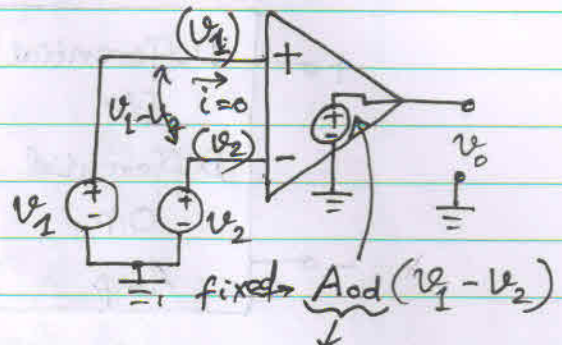
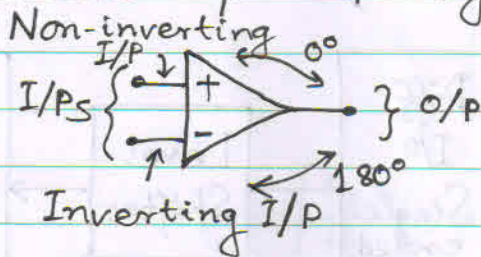
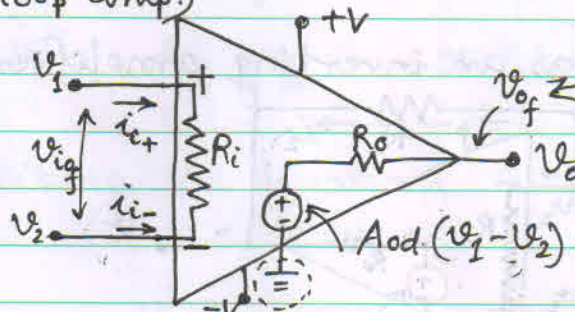


(Op-Amps) Operational Amplifiers

1. Ideal op-amp: Symbol & operation



2. Practical op-amp: (Open loop amp.)



Open-loop (OL) differential gain (V)

$$A_{cm} \approx 0$$

$$CMRR = \frac{A_{od}}{A_{cm}}$$

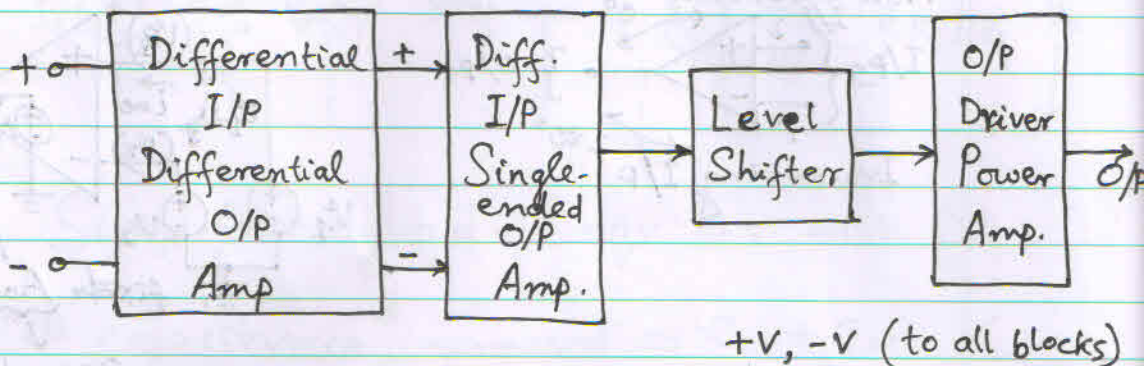
When, $V_1 = V_2 (\neq 0) \Rightarrow V_1$ & V_2 are common-mode I/P
 $\therefore V_0 = 0$ for an ideal case } Common-mode
 $V_0 \neq 0$ for a practical case } O/P.

3. Op-amp parameters:

	<u>Ideal</u>	<u>Practical</u>
a) I/P resistance (R_i)	∞	Very high (M/G Ω)
b) O/P resistance (R_o)	0	Low or Very Low (Ω)
c) OL diff. gain (A_{od})	∞	Very high (L+M)
d) O/P V-Swing/range	∞	$< [+V - (-V)]$
e) O/P current (max.)	∞	Depends on design
f) Common Mode Rejection Ratio (CMRR)	∞	Very high or high
g) Power Supply Rejection Ratio (PSRR)	∞	" " " "
h) I/P offset V (V_{io})	0	Very Low (mV)
i) O/P offset V (V_{of})	0	Very Low (mV)
j) I/P bias I (i_{i+} or i_{i-})	0	Very low
k) O/P offset I	0	Very low (R_i dep.)
l) Slew rate & GBW	∞	Very high

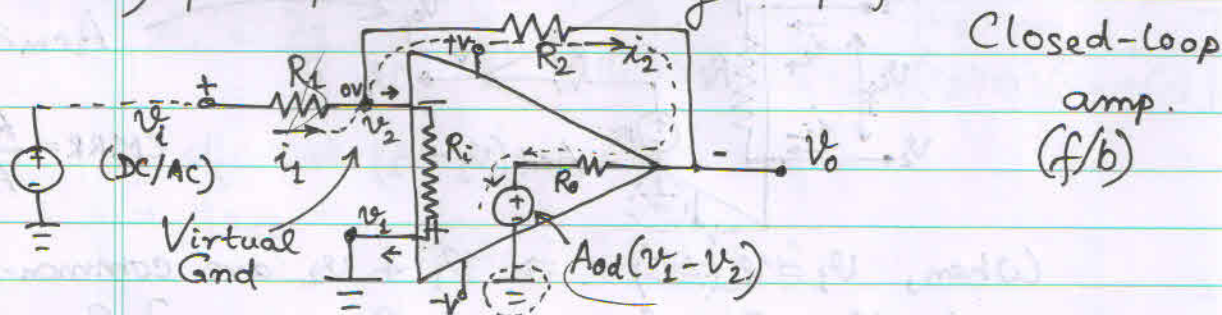
Noise handling

4. Block Diagram (generic)



BJTs/MOSFETs/FETs (other)

5. a) Op-Amp as an inverting amplifier:



Assume, $R_i \approx \infty$ & $R_o \approx 0$, R_1 or $R_2 \ll R_i$

$$i_1 \approx i_2$$

($\because i_{R_i} \approx 0 \Rightarrow V_1 = V_2$ for this closed-loop circuit)

$$\frac{V_i}{R_1} = -\frac{V_o}{R_2}$$

Now, $V_1 = V_2 = 0V$

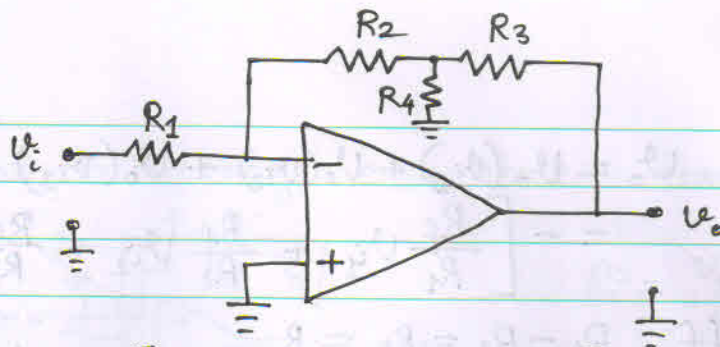
\therefore We say V_2 is at a virtual ground

$$\Rightarrow V_o = -\left(\frac{R_2}{R_1}\right) \cdot V_i$$

(I/Ps '+' & '-' are at same potential)

b) If you need a ^{very} large gain (e.g. > 1000), increasing R_2 results in noise injection in the circuit, since, $V_{n(rms)} = \sqrt{4k \cdot T \cdot R \cdot \Delta f}$

Instead, use a T-network replacement for R_2 .



$$V_o = - \left[\frac{R_2}{R_1} \left(1 + \frac{R_3}{R_4} + \frac{R_3}{R_2} \right) \right] V_i$$

c) How about the effect of A_{od} ?

Derivation with a single R_2 :

$$i_1 = \frac{V_i - V_2}{R_1}$$

$$i_2 = \frac{V_2 - V_o}{R_2}$$

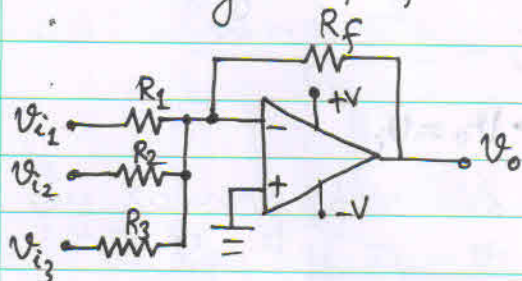
$$\therefore V_o = -A_{od} \cdot V_2 \Rightarrow V_2 = -\frac{V_o}{A_{od}}$$

Combining the equations,
 $i_1 = i_2$

$$\Rightarrow \frac{V_i + \frac{V_o}{A_{od}}}{R_1} = \frac{-\frac{V_o}{A_{od}} - V_o}{R_2}$$

$$\Rightarrow V_o = - \left[\frac{R_2}{R_1} \frac{1}{1 + \frac{1}{A_{od}} \left(1 + \frac{R_2}{R_1} \right)} \right] \cdot V_i$$

6. Summing amplifier: VAdder



By using Superposition Theorem based analysis:

$$V_o(V_{i1}) = - \frac{R_F}{R_1} \cdot V_{i1}$$

$$V_o(V_{i2}) = - \frac{R_F}{R_2} \cdot V_{i2}$$

$$V_o(V_{i3}) = - \frac{R_F}{R_3} \cdot V_{i3}$$

$$\therefore V_o = V_o(V_{i1}) + V_o(V_{i2}) + V_o(V_{i3})$$

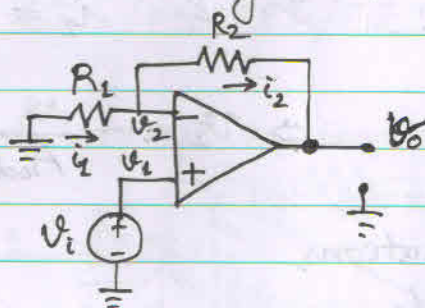
$$= - \left[\frac{R_f}{R_1} V_{i1} + \frac{R_f}{R_2} V_{i2} + \frac{R_f}{R_3} V_{i3} \right]$$

If, $R_1 = R_2 = R_3 = R$

$$V_o = - \frac{R_f}{R} (V_{i1} + V_{i2} + V_{i3})$$

7. Non-inverting amplifier:

a)



$$i_1 = \frac{-V_o}{R_1} = - \frac{V_o}{R_1}$$

$$i_2 = \frac{V_o - V_i}{R_2} = \frac{V_o - V_i}{R_2}$$

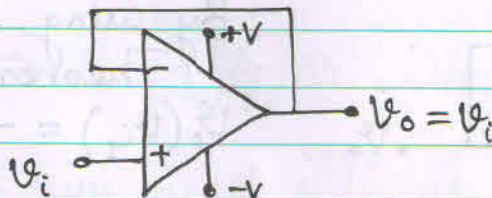
$$\therefore i_1 = i_2$$

$$- \frac{V_o}{R_1} = \frac{V_o - V_i}{R_2}$$

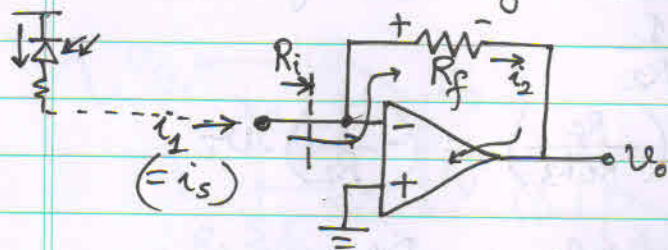
$$\Rightarrow V_o = \left(1 + \frac{R_2}{R_1} \right) V_i$$

b) Voltage follower/buffer/impedance transformer:
In a non-inverting amplifier, if $R_2 = 0$, $\frac{V_o}{V_i} = 1$. However, that will increase power.

A simplified circuit achieving the same is:



8 Current to voltage converter: Trans-impedance amp.

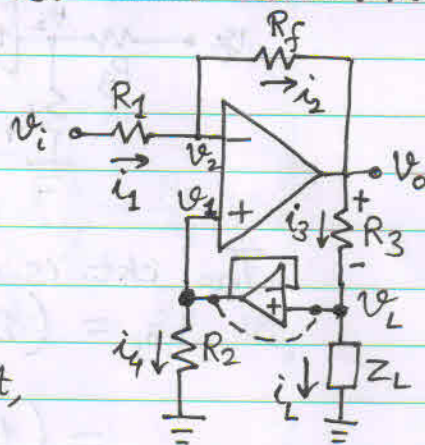
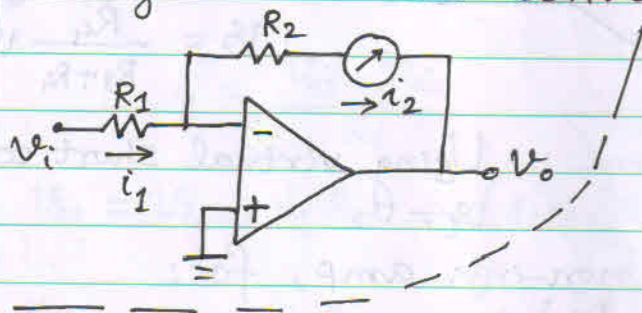


$$R_i = \frac{V_i}{i_i} \approx 0$$

$$i_2 = i_1 = i_s \leftarrow \text{Signal } I$$

$$\therefore V_o = -i_2 \cdot R_f = -i_s \cdot R_f$$

9. Voltage to current converter: Trans-conductance amp.



Using virtual short concept,
 $V_1 = V_2 = Z_L \cdot i_L = V_L$

Since, $i_1 = i_2$

$$\frac{V_i - i_L Z_L}{R_1} = \frac{i_L Z_L - V_o}{R_f}$$

Using KCL at the '+' I/P of the op-amp,

$$\frac{V_o - i_L Z_L}{R_3} = i_L + \frac{i_L Z_L}{R_2}$$

(Considering the dotted line as short)

By solving for $(V_o - i_L Z_L)$, or V across R_3

$$\frac{R_f}{R_1} \cdot \frac{V_o - V_i}{R_3} = i_L + \frac{i_L Z_L}{R_2}$$

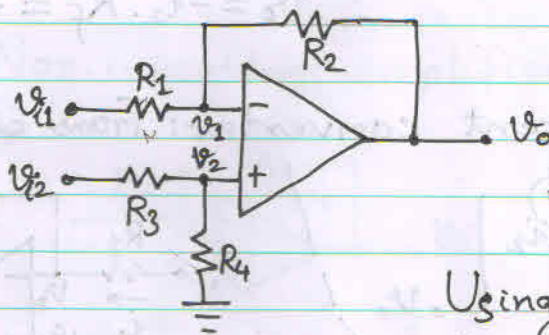
$$\Rightarrow i_L \left(\frac{R_f Z_L}{R_1 R_3} - 1 - \frac{Z_L}{R_2} \right) = V_i \left(\frac{R_f}{R_1 R_3} \right)$$

For making i_L independent of Z_L ,

$$\frac{R_f}{R_1 R_3} = \frac{1}{R_2}$$

$$\therefore i_L = -V_i \left(\frac{R_f}{R_1 R_3} \right) = \left(-\frac{1}{R_2} \right) \cdot V_i$$

10. Difference amplifier: DA



Case 1: $V_{i1} = 1$, $V_{i2} = 0V$

$$V_{o1} = -\frac{R_2}{R_1} V_{i1}$$

Case 2: $V_{i2} = 1$, $V_{i1} = 0V$

$$V_{o2} = \frac{R_4}{R_3 + R_4} V_{i2}$$

Using virtual short concept,
 $V_1 = V_2$

The ckt. is a non-inv. amp, for:

$$V_{o2} = \left(1 + \frac{R_2}{R_1} \right) V_2$$

$$= \left(1 + \frac{R_2}{R_1} \right) \left(\frac{R_4}{R_3 + R_4} \right) V_{i2}$$

$$= \left(1 + \frac{R_2}{R_1} \right) \left(\frac{\frac{R_4}{R_3}}{1 + \frac{R_4}{R_3}} \right) V_{i2}$$

$$V_o = V_{o1} + V_{o2} = \left(1 + \frac{R_2}{R_1} \right) \left(\frac{\frac{R_4}{R_3}}{1 + \frac{R_4}{R_3}} \right) V_{i2} - \frac{R_2}{R_1} V_{i1}$$

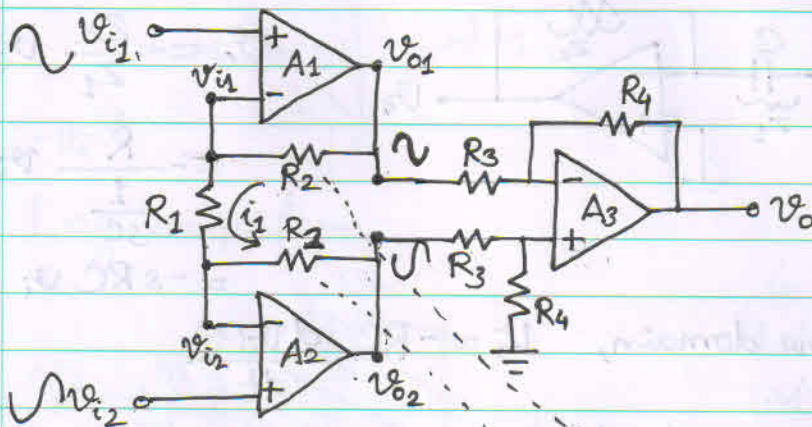
If, $\frac{R_4}{R_3} = \frac{R_2}{R_1}$ (or $R_1 = R_3$ & $R_2 = R_4$)

$$V_o = \frac{R_2}{R_1} (V_{i2} - V_{i1})$$

Drawbacks: 1) Low I/P impedance/resistance.

2) Matching of $\frac{R_2}{R_1}$ & $\frac{R_4}{R_3}$ is critical for a good CMRR.

11. Instrumentation amplifier: IA



$$i_1 = \frac{v_{i1} - v_{i2}}{R_1}$$

$$v_{o1} = v_{i1} + i_1 R_2 = \left(1 + \frac{R_2}{R_1}\right) v_{i1} - \frac{R_2}{R_1} v_{i2}$$

$$v_{o2} = v_{i2} - i_1 R_2 = \left(1 + \frac{R_2}{R_1}\right) v_{i2} - \frac{R_2}{R_1} v_{i1}$$

Also,

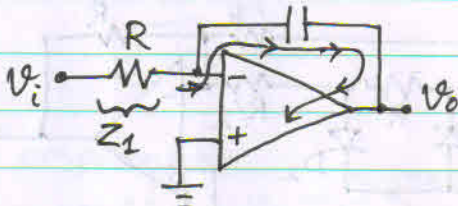
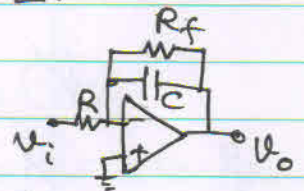
$$v_o = \frac{R_4}{R_3} (v_{o2} - v_{o1})$$

$$= \frac{R_4}{R_3} \left(1 + \frac{2R_2}{R_1}\right) (v_{i2} - v_{i1})$$

Note: Solves the problem of DA's with high I/P Z.

12. Op-amp as filters (active filters):

a) Integrator (Low pass filter)



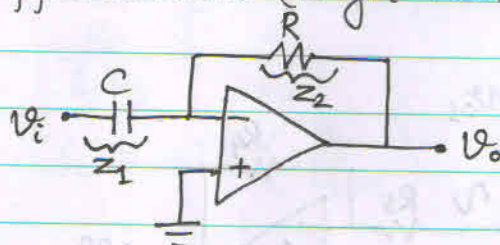
$$v_o = -\frac{Z_2}{Z_1} v_i$$

$$= -\frac{1}{sC} \frac{1}{R} v_i = -\frac{1}{sRC} v_i$$

In time domain,
$$v_o = -\frac{1}{RC} \int_0^{t_1} v_i(t) dt$$



b. Differentiator (High Pass Filter):



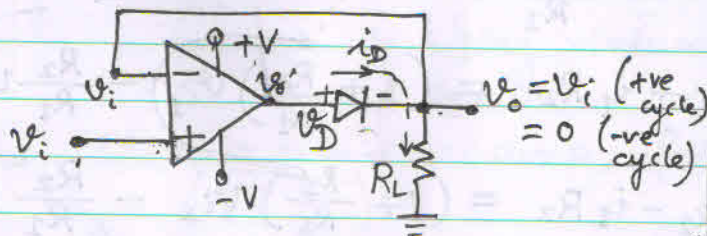
$$V_o = -\frac{Z_2}{Z_1} V_i$$

$$= -\frac{R}{\frac{1}{sC}} V_i$$

$$= -sRC V_i$$

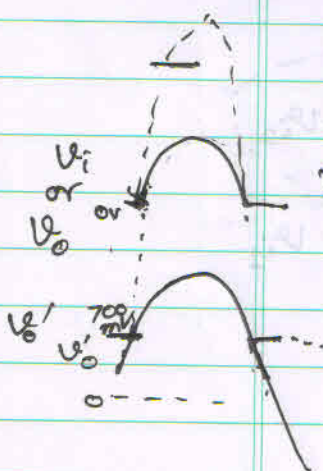
In time domain, $V_o = -RC \frac{dV_i(t)}{dt}$

13. Precision half-wave rectifier: Active rectifier (+ve V_o)

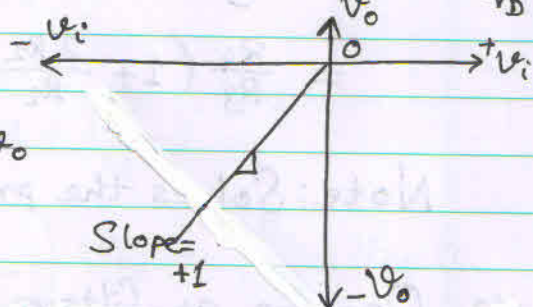
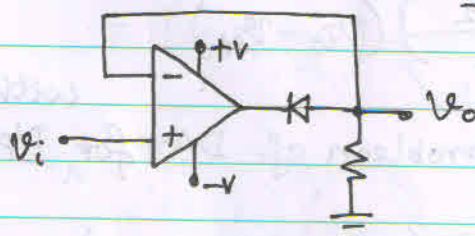
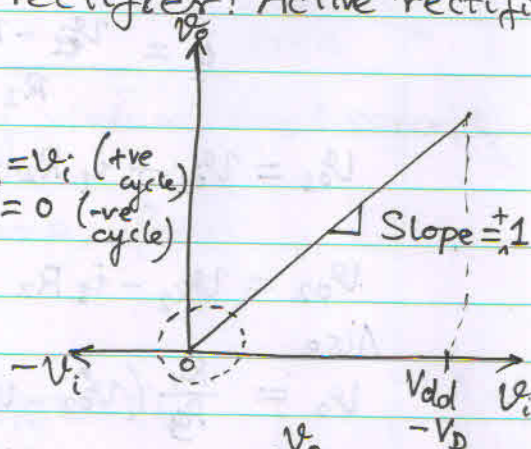


$$V_o = V_i \text{ (+ve cycle)}$$

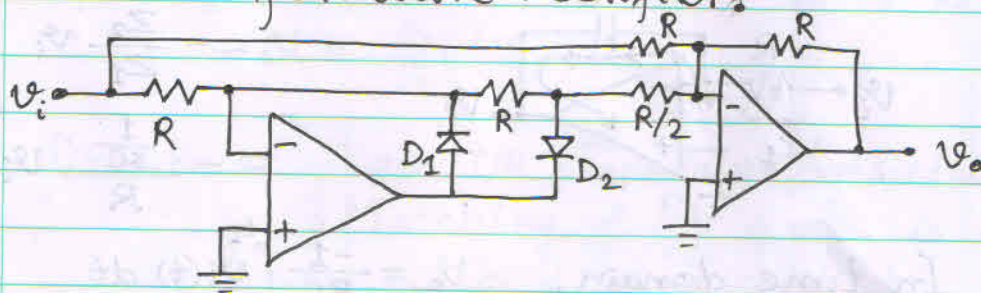
$$= 0 \text{ (-ve cycle)}$$



(-ve V_o)

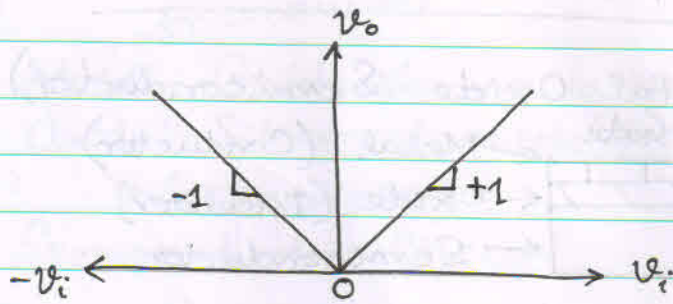


14. Precision full-wave rectifier:

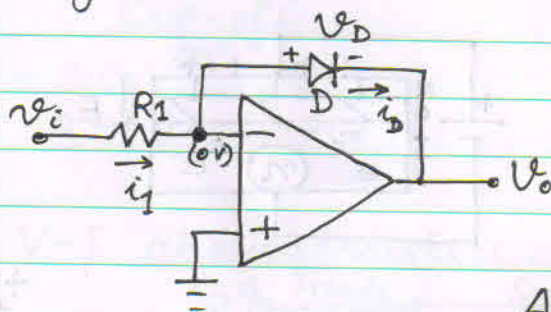


$$= \left. \frac{dV_o}{dt} \right|_{\max}$$

$$\text{Slew rate} \geq 2\pi f V_{\max}$$



14. Log amplifier:



$i_D = I_S \left(e^{\frac{V_D}{V_T}} - 1 \right)$
while D is sufficiently
in F.B.

$$i_D = I_S e^{\frac{V_D}{V_T}}$$

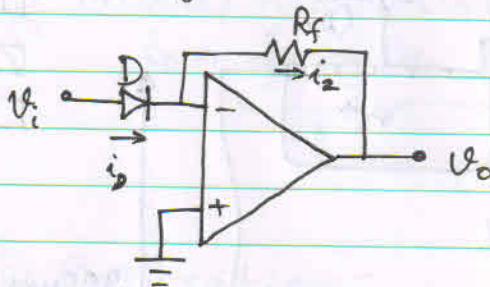
Also, $i_1 = \frac{v_i}{R_1}$ & $v_o = -V_D$

$$\therefore i_1 = i_D \approx I_S e^{\left(\frac{-v_o}{V_T} \right)} = \frac{v_i}{R_1}$$

Taking natural log on both sides,
 $\ln \left(\frac{v_i}{I_S R_1} \right) = -\frac{v_o}{V_T}$

$$\Rightarrow v_o = -V_T \ln \left[\frac{v_i}{I_S R_1} \right]$$

15. Anti-log amplifier:

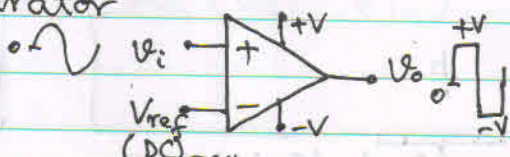


$$i_D \approx I_S e^{\left(\frac{V_D}{V_T} \right)}$$

$$\& v_o = -i_2 R_f = -i_D R_f$$

$$\Rightarrow v_o = -I_S R_f e^{\left(\frac{v_i}{V_T} \right)}$$

16. V Comparator



$$v_o = +V - V_{\text{drop}} \quad (\text{if } v_i > V_{\text{ref}})$$

$$v_o = -V + V_{\text{drop}} \quad (\text{if } v_i \leq V_{\text{ref}})$$