Expt. No. __3___

Page No. 27

	Experiment - 3
	Aim 5- To study working and various parameters of Hertification using half wave and full-wave hertifier and regulation using capacital filter, Xener diode and IC 7806.
2.	
	Descriptions- Rectifier is a device that can constant alternating current (AC) to direct current, 1.e, the process of ane-way flow of current and this process is called rectification. Rectifier can be of shapes of several different physical forms such as solid state diodes, vaccum-tube diodes, mercury—are values, silvian controlled rectifiers.
	Half wave sectifier: 3-It sectifies only half cycle of waveform HAT consist a step down transformer, a diode connected to the transformer and a boad sexistance connected to controls and of diode Main supply voltage is reduced by
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Date	

Expt. No. _____Page No.

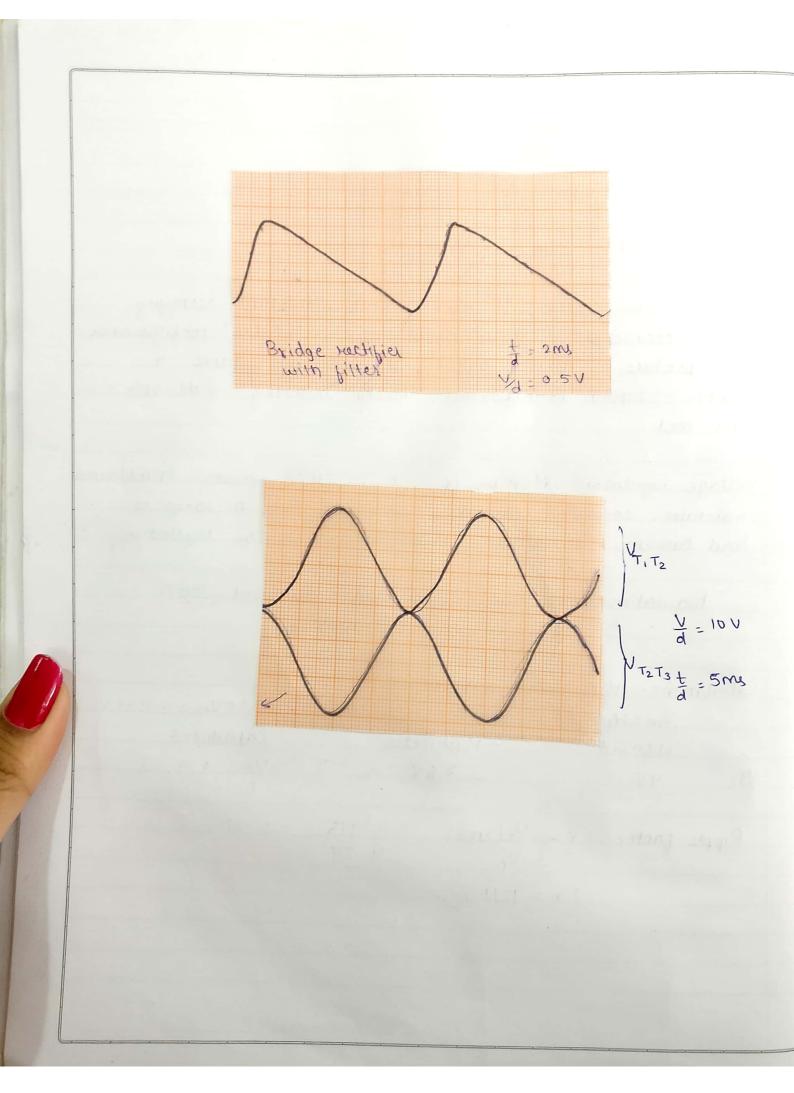
Step down transformer. Decreased AC voltage is given to the diade which is connected social to the secondary winding of transformer. Diode is an electric component which allows only forward biased surrent and not reverse biased surrent. From the diode, we will get pulsating DC at the wad resistance. efficiency, n = Pdc Pac sipple factor it: Valsons) Full wave sectifies: - It converts an AC voltage: unto a pulsating DC voltage using both half cycles of the applied Ac voltage. It use two diodes of which one conducts during one half cycle while the other conducts during the other half cycle of the applied ac voltage. for single phase Ac, it the transformer is centre taped, then two diodes back to back forms full-wave ractifies. It converts the input waveform to one of constant polarity by reversing the negative portions of the auternating current waveform. ending to double the bequerey of the output waveform Teacher's Signature: _

Half wave 4.58 4.49 4.53 4.50 0.0178 > 1. Full wave 9.16 8.99 9.09 8.95 0.234 > 2. Full wave bridge 8.54 8.34 8.42 8.24 0.364 - 3.		Solt	age Reg	reitalu		
Full wave 9.16 8.99 9.09 8.95 0.234 \Rightarrow 2. Full wave 8.54 8.34 8.42 8.24 0.364 \Rightarrow 3. with Zenov 4.61 4.56 4.59 4.54 0.1154 \Rightarrow 1. with 1C+806 4.97 4.53 4.92 4.36 0.1399 \Rightarrow 1	Revifie	VnL	VRL	VRL2	VENILZ	VR = (VML - VFL)
Full wave 8.54 8.34 8.42 8.24 $0.364 \rightarrow 3$ with Zenoy 4.61 4.56 4.59 4.54 $0.154 \rightarrow 1$ with 1C+806 4.97 4.53 4.92 4.36 $0.1399 \rightarrow 1$	Half wave	4.58	4.49	4.53	4.50	0.0178 -> 1.
Full wave 8.54 8.34 8.42 8.24 $0.364 \rightarrow 3$. with Zenoy 4.61 4.56 4.59 4.54 $0.054 \rightarrow 1$. with 1C7806 4.97 4.53 4.42 4.36 $0.1399 \rightarrow 1$. CHANGE	Full wave	9.16				0.234 -2.
with 1C7806 4.47 4.53 4.42 4.36 0.1399 \rightarrow 1	Full wave bridge	8.54				0.364 - 3.
$V_{2}(b-b)=5.8$	with Zenoy	4.61	4.56	4.59	4.54	0.1154 -> 1.
$\int_{\mathbb{R}^{n}} \int_{\mathbb{R}^{n}} \int_{$	with 107806	4.97	4.53	4,92	4.36	0.1399 -> 1
		Harb	wave seg		i smsi	Ĺ, t

VDC 4.29

Teacher's Signature: _

1.8 = 121.1 /.



Page No. Expt. No. Half mane Rictifier with capacitor filter:-VLCP-P) = \$42.8 V Vrms = Vrcp-P) = 0.808 V Vdc = 8₹ 9.17 (multimeter) Full-wave rectifier 3-Vm= 15714V Vx(1ms) = 0.308 Vm = 4.312V Observed Multimeter Calculated Vdc 11.68 1289.1 2Vm 8.9126V Ripple factor, 1 = Vx(2ms) = 0.484 Vdc 1.8 = 48.41. Full wave sectifies with capacitor filter Vr(p-p) = 14 V Vr(rms) = Vr(p-p) = 0.4,04V Vdc = 128 V , 13.7V (multimeter) Full wave Bridge Roctifier Um = 13 V Ur (xms) = 0.308 Vm = 4.004 V Observed Multimeter Calculated

Vdc → 8V 8.5V 8.276V

Ripple factor, r = Vylxms) = 0.4838 x Vdc

Teacher's Signature : ____

	- , = = 7 -		N.	Da	te
Expt. No.				Page N	0
`	· Y = 48.38	7.			
Full wave Baid V2(P-P) = 1.31 Vdc = 12V	J	V _A	paritor (rms) =	Vr(p-p) 253	= 0.375 V
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Expt. No.

Page No. 32

E	xpt. No
	Discussions: By Chelsi Raheja (16 CS10013)
1	Half wave and full wave filters made using diodus supply loss voltage than input as there will be alound voltage drop across the diodus which will be alound at in voltage may lead to deviations from ideal state
2	During reversed. Dies State, voltage has to be maintained below zenow breakdown voltage.
3.	yell in case of full wave rectifier was observed to be around double the case of half wave rectifier because both cycles may contribute to output whereas only has cycle is contributing to output in half wave rortifier.
4.	Observed and calculated values of Vdc were in emperimental
5	time constant of the enparitor filter should be quite crough so that capacitor doesn't dischange auithin short range of time
6.	Bridge sertifier loss of input voltage is around double that the case of loss in full wave sortifier but it trepures harf of secondary windings and less cost as compared to bull wave sortifier.
	Teacher's Signature:

Expt. No.

Page No. 33

4.	Slightly inclined baselines were observed in AC coupled output because of presence of capacitor in oscilloscope to pass AC components in series with custinit
	Maximum load applied was munimum of L, 89 L. Ni shoot boo at eldissed ton som the meth so. Neither dias of seises
	A Konor diode and 10+806 did not differ much in their output but the difference is zener diode Veltage drop varies significantly with rument, temperatu Whereare TC has four stages to work an a voltage reference an error amplifier series pass transistor and feedback circuit when output veltage drops of increase, the feedback circuit conses the change and send that change to error amplifies who inverses the voltage and drives the soiles pass element in opposite disentern (9.5)
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DISCUSSIONS:

SWASTIKA DUTTA

16CS10060.

Rectifiers:

· Half ware and full wave rectified can be formed wing diodes In half wave reclifier, we are able to tap only positive eyele of

input nortage while we tap both polarities and convert

them to single polarity in full wave rectified.

The maximum of output voltage shall be approximately Maximum in input voltage - diode cut in voltage owing to

voltage drop a cross tre diode, in case of centre-lapped rectifier or ray warre nectifier. In case of bridge reclifier, mere are

two diodes for each wint in forward bias, thus,

Voiman = VI, man - 2 Vr.

The de equiralent of half-wave rectifier is:

Vac = 12 T Vm sindd = Vm/T.

while in case of full-wave rectifier, it is 27m/ = 2 Vdc

Similarly ac part of nay-wome he clipier = \[127 \ \maximode

= Vm/2

while for full-war it is Vm/50

considering efficiency: n = Pdc

= 0.812 (Juli-warre)

Thus, New-wave 2. nhay-wave.

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0	Mont use small company the doe Hertilier and water
<u> </u>	Next, we small compare bridge rectifier and univertapped
	rectifier: for bridge; we mane Vo, man = Or Vimon - 2V2
	while for untre-tapped, Voman = Vinan - Vo
	However for unive-tapped rectifier, DIV of diodes required
	is aproximately 2 vs., while it is vs. for bridge-rectifier.
	also very accurate livens ratio is required for centre-tapping.
	Thus, pridge rectifiers are more practical.
9	Fillow:
	a capacitor attached parallel to road voltage acts as
	rectifice. De In an ideal case, the time constant of the
	resulting RC circuit Mould be zero men that ripple voltage
	(Vo) >0. trowever per practical cases in just-boidge rectifiers,
	we assume $V_T = \frac{V_m}{2f RC}$, where $R \to 10ad$ $V_m \to V_{output}$ maximum
	→ source frequency.
	These results have been observed and concluded from
	this experiments, within experimental ranges of everor.
	we and observe seignote, inclined baselines in CRO, due to
	presence of capacitos required to pass AC components in
	series with wruit.
•	Voltage Regulation:
	In this experiement, we have used Jener didde regulation
	and 10 7806 regulations using Bridge rectifier. In our experiment.
	the two results did not differ significantly. Als
	Also for proper ng regulation, v. applied avon sener
	mould be above V2 all the time (to exadicate Vx or Vx >0).
	-
To Bear	Teacher's Signature :

	Date
Expt. No.	Page No. 36
use colculated regulation usin	ng V.R = VNL - VFL
ture the full load case is or	nanimum of load 1 oh load 2 cion to connect them in series.
jasty, we realise that IC 7806 Zener diode regulation sence son to temperature changes and curr of resistance in practical cases), wh circuit that helps to maintain	ret did de regulation is muro ptible rent changes avrons didde (presence rile 107806 has a feedback
	10) HAP
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