

Module 1 : Combinational Logic Circuits.

Assignment 2 Date: 21.01.2019

2. (a) Design a circuit to compute $x \pmod{7}$, where x is a BCD number as 4-bit input
(b) Display the output on a 7-segment display using BCD-to-7-segment decoder driver (chip).

For example, if $x = 1001$, "2" will be displayed, if $x = 0101$, "5" will be displayed etc.