Module 1 : Combinational Logic Circuits

Assignment 1   Date: 14.01.2019

1. Realize the following function

$f(w, x, y, z) = \sum m(0, 1, 2, 3, 5, 7, 8, 9, 10, 12, 13)$

(a)   using NAND gates

(b) using 8-to-1 MUX (MUX - Multiplexer). We have an 8-to-1 MUX (74151) with an active low enable input (EN'), and both active high and active low outputs.