

1. Description

1.1. Project

| Project Name | Chongs Cool STM32 Board |
|-----------------|-------------------------|
| Board Name | custom |
| Generated with: | STM32CubeMX 6.12.0 |
| Date | 09/15/2024 |

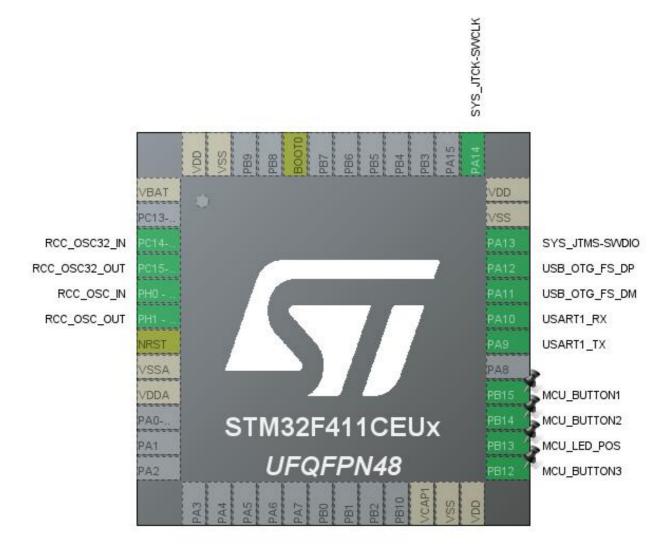
1.2. MCU

| MCU Series | STM32F4 |
|----------------|---------------|
| MCU Line | STM32F411 |
| MCU name | STM32F411CEUx |
| MCU Package | UFQFPN48 |
| MCU Pin number | 48 |

1.3. Core(s) information

| Core(s) | Arm Cortex-M4 |
|---------|---------------|

2. Pinout Configuration

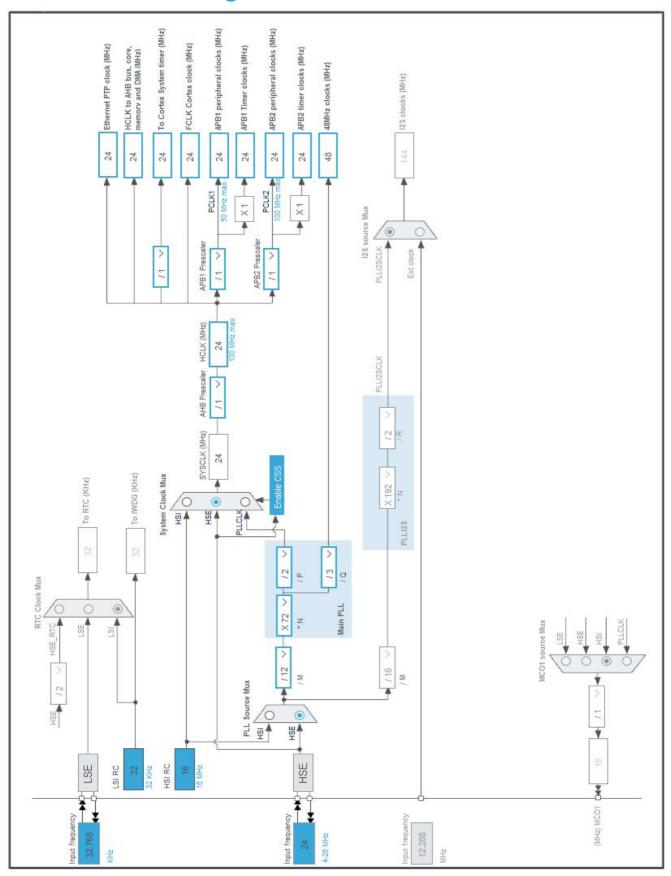


3. Pins Configuration

| Pin Number UFQFPN48 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|------------------------|---------------------------------------|----------|--------------------------|-------------|
| 1 | VBAT | Power | | |
| 3 | PC14-OSC32_IN | I/O | RCC_OSC32_IN | |
| 4 | PC15-OSC32_OUT | I/O | RCC_OSC32_OUT | |
| 5 | PH0 - OSC_IN | I/O | RCC_OSC_IN | |
| 6 | PH1 - OSC_OUT | I/O | RCC_OSC_OUT | |
| 7 | NRST | Reset | | |
| 8 | VSSA | Power | | |
| 9 | VDDA | Power | | |
| 22 | VCAP1 | Power | | |
| 23 | VSS | Power | | |
| 24 | VDD | Power | | |
| 25 | PB12 * | I/O | GPIO_Input | MCU_BUTTON3 |
| 26 | PB13 * | I/O | GPIO_Output | MCU_LED_POS |
| 27 | PB14 * | I/O | GPIO_Input | MCU_BUTTON2 |
| 28 | PB15 * | I/O | GPIO_Input | MCU_BUTTON1 |
| 30 | PA9 | I/O | USART1_TX | |
| 31 | PA10 | I/O | USART1_RX | |
| 32 | PA11 | I/O | USB_OTG_FS_DM | |
| 33 | PA12 | I/O | USB_OTG_FS_DP | |
| 34 | PA13 | I/O | SYS_JTMS-SWDIO | |
| 35 | VSS | Power | | |
| 36 | VDD | Power | | |
| 37 | PA14 | I/O | SYS_JTCK-SWCLK | |
| 44 | воото | Boot | | |
| 47 | VSS | Power | | |
| 48 | VDD | Power | | |

^{*} The pin is affected with an I/O function

4. Clock Tree Configuration



Page 4

1. Power Consumption Calculator report

1.1. Microcontroller Selection

| Series | STM32F4 |
|-----------|---------------|
| Line | STM32F411 |
| мси | STM32F411CEUx |
| Datasheet | DS10314_Rev6 |

1.2. Parameter Selection

| Temperature | 25 |
|-------------|-----|
| Vdd | 1.7 |

1.3. Battery Selection

| Battery | Li-SOCL2(A3400) |
|-------------------|-----------------|
| Capacity | 3400.0 mAh |
| Self Discharge | 0.08 %/month |
| Nominal Voltage | 3.6 V |
| Max Cont Current | 100.0 mA |
| Max Pulse Current | 200.0 mA |
| Cells in series | 1 |
| Cells in parallel | 1 |

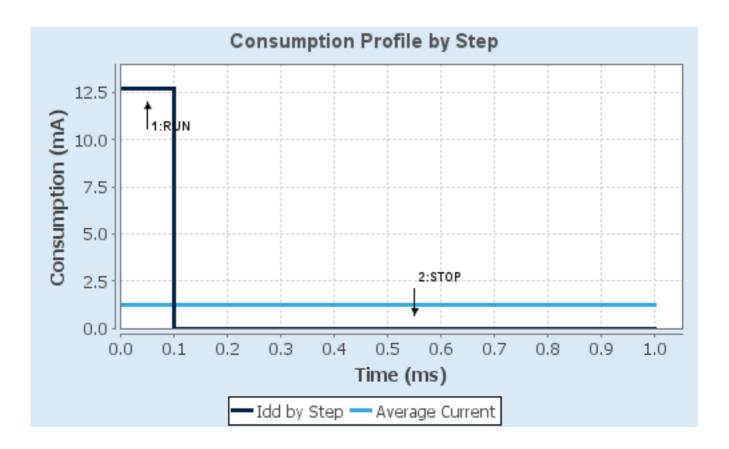
1.4. Sequence

| Step | Step1 | Step2 |
|------------------------|-------------|-----------------------|
| Mode | RUN | STOP |
| Vdd | 1.7 | 1.7 |
| Voltage Source | Battery | Battery |
| Range | Scale1-High | No Scale |
| Fetch Type | SRAM | n/a |
| CPU Frequency | 100 MHz | 0 Hz |
| Clock Configuration | HSE PLL | Regulator_LPLV Flash- |
| | | PwrDwn |
| Clock Source Frequency | 4 MHz | 0 Hz |
| Peripherals | | |
| Additional Cons. | 0 mA | 0 mA |
| Average Current | 12.7 mA | 9 μΑ |
| Duration | 0.1 ms | 0.9 ms |
| DMIPS | 125.0 | 0.0 |
| Ta Max | 104.31 | 105 |
| Category | In DS Table | In DS Table |

1.5. Results

| Sequence Time | 1 ms | Average Current | 1.28 mA |
|---------------|---------------|-----------------|-------------|
| Battery Life | 3 months, 19 | Average DMIPS | 125.0 DMIPS |
| | days, 6 hours | - | |

1.6. Chart



2. Software Project

2.1. Project Settings

| Name | Value |
|-----------------------------------|--|
| Project Name | Chongs Cool STM32 Board |
| Project Folder | C:\Users\user\Documents\Altium\Chongs-Cool-STM32-Board\CubeIDE Stuff |
| Toolchain / IDE | STM32CubeIDE |
| Firmware Package Name and Version | STM32Cube FW_F4 V1.28.1 |
| Application Structure | Advanced |
| Generate Under Root | Yes |
| Do not generate the main() | No |
| Minimum Heap Size | 0x200 |
| Minimum Stack Size | 0x400 |

2.2. Code Generation Settings

| Name | Value |
|---|---------------------------------------|
| STM32Cube MCU packages and embedded software | Copy only the necessary library files |
| Generate peripheral initialization as a pair of '.c/.h' files | No |
| Backup previously generated files when re-generating | No |
| Keep User Code when re-generating | Yes |
| Delete previously generated files when not re-generated | Yes |
| Set all free pins as analog (to optimize the power | No |
| consumption) | |
| Enable Full Assert | No |

2.3. Advanced Settings - Generated Function Calls

| Rank | Function Name | Peripheral Instance Name |
|------|------------------------|--------------------------|
| 1 | SystemClock_Config | RCC |
| 2 | MX_GPIO_Init | GPIO |
| 3 | MX_USART1_UART_Init | USART1 |
| 4 | MX_USB_OTG_FS_PCD_Init | USB_OTG_FS |

3. Peripherals and Middlewares Configuration

3.1. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator Low Speed Clock (LSE): Crystal/Ceramic Resonator

3.1.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Enabled
Data Cache Enabled

Flash Latency(WS) 0 WS (1 CPU cycle)

RCC Parameters:

HSI Calibration Value 16

TIM Prescaler Selection Disabled

HSE Startup Timout Value (ms) 100

LSE Startup Timout Value (ms) 5000

Power Parameters:

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

3.2. SYS

Debug: Serial Wire

Timebase Source: SysTick

3.3. **USART1**

Mode: Asynchronous 3.3.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

3.4. USB_OTG_FS

Mode: Device_Only

3.4.1. Parameter Settings:

Speed Device Full Speed 12MBit/s

Low powerDisabledLink Power ManagementDisabledVBUS sensingDisabledSignal start of frameDisabled

^{*} User modified value

4. System Configuration

4.1. GPIO configuration

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull down | Max Speed | User Label |
|----------------|------------------------|--------------------|------------------------------|-----------------------------|----------------|-------------|
| RCC | PC14- OSC32_IN | RCC_OSC32_IN | n/a | n/a | n/a | |
| | PC15- OSC32_OU T | RCC_OSC32_O UT | n/a | n/a | n/a | |
| | PH0 - OSC_IN | RCC_OSC_IN | n/a | n/a | n/a | |
| | PH1 - OSC_OUT | RCC_OSC_OUT | n/a | n/a | n/a | |
| SYS | PA13 | SYS_JTMS- SWDIO | n/a | n/a | n/a | |
| | PA14 | SYS_JTCK- SWCLK | n/a | n/a | n/a | |
| USART1 | PA9 | USART1_TX | Alternate Function Push Pull | No pull-up and no pull-down | Very High * | |
| | PA10 | USART1_RX | Alternate Function Push Pull | No pull-up and no pull-down | Very High | |
| USB_OTG_ FS | PA11 | USB_OTG_FS_ DM | Alternate Function Push Pull | No pull-up and no pull-down | Very High | |
| | PA12 | USB_OTG_FS_ DP | Alternate Function Push Pull | No pull-up and no pull-down | Very High | |
| GPIO | PB12 | GPIO_Input | Input mode | No pull-up and no pull-down | n/a | MCU_BUTTON3 |
| | PB13 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | MCU_LED_POS |
| | PB14 | GPIO_Input | Input mode | No pull-up and no pull-down | n/a | MCU_BUTTON2 |
| | PB15 | GPIO_Input | Input mode | No pull-up and no pull-down | n/a | MCU_BUTTON1 |

4.2. DMA configuration

nothing configured in DMA service

4.3. NVIC configuration

4.3.1. NVIC

| Interrupt Table | Enable | Preenmption Priority | SubPriority | | |
|---|--------|----------------------|-------------|--|--|
| Non maskable interrupt | true | 0 | 0 | | |
| Hard fault interrupt | true | 0 | 0 | | |
| Memory management fault | true | 0 | 0 | | |
| Pre-fetch fault, memory access fault | true | 0 | 0 | | |
| Undefined instruction or illegal state | true | 0 | 0 | | |
| System service call via SWI instruction | true | 0 | 0 | | |
| Debug monitor | true | 0 | 0 | | |
| Pendable request for system service | true | 0 | 0 | | |
| System tick timer | true | 15 | 0 | | |
| PVD interrupt through EXTI line 16 | unused | | | | |
| Flash global interrupt | unused | | | | |
| RCC global interrupt | unused | | | | |
| USART1 global interrupt | unused | | | | |
| USB On The Go FS global interrupt | unused | | | | |
| FPU global interrupt | unused | | | | |

4.3.2. NVIC Code generation

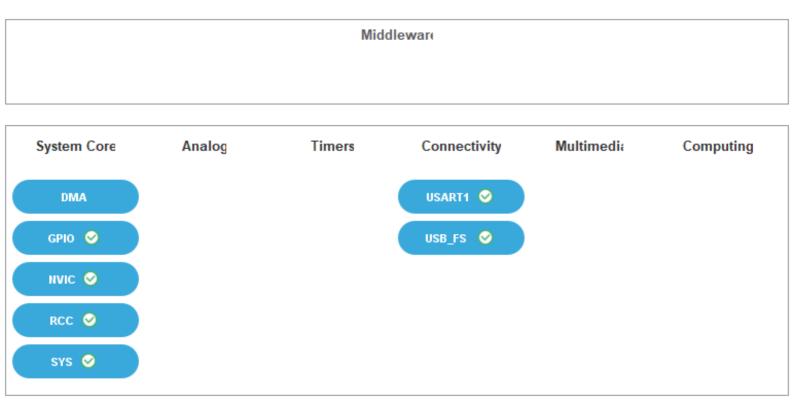
| Enabled interrupt Table | Select for init | Generate IRQ | Call HAL handler |
|---|-------------------|--------------|------------------|
| | sequence ordering | handler | |
| Non maskable interrupt | false | true | false |
| Hard fault interrupt | false | true | false |
| Memory management fault | false | true | false |
| Pre-fetch fault, memory access fault | false | true | false |
| Undefined instruction or illegal state | false | true | false |
| System service call via SWI instruction | false | true | false |
| Debug monitor | false | true | false |
| Pendable request for system service | false | true | false |
| System tick timer | false | true | true |

* User modified value

5. System Views

5.1. Category view

5.1.1. Current



6. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl_model/stm32f411_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis_model/stm32f411_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f4-svd.zip

Description

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