

a pc out[7:0]

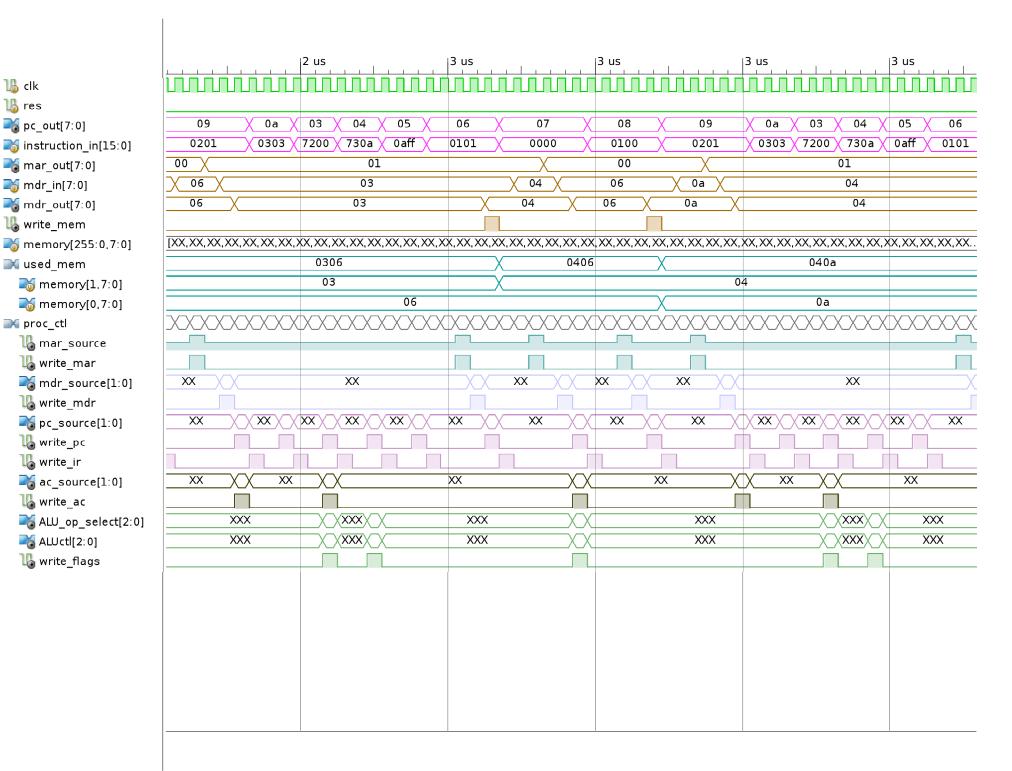
¼ write_mem

used mem

m proc_ctl

₩ write_pc **¼** write_ir

 $ule{1}$ write $ule{1}$ ac



a pc out[7:0]

mdr_in[7:0]

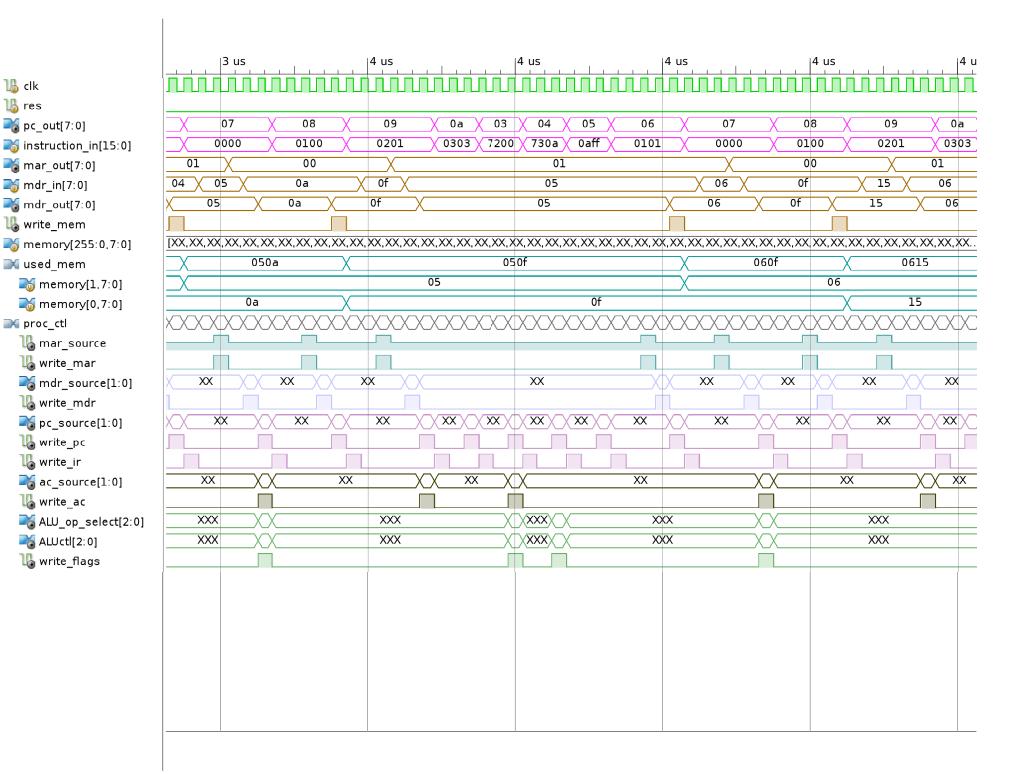
¼ write_mem

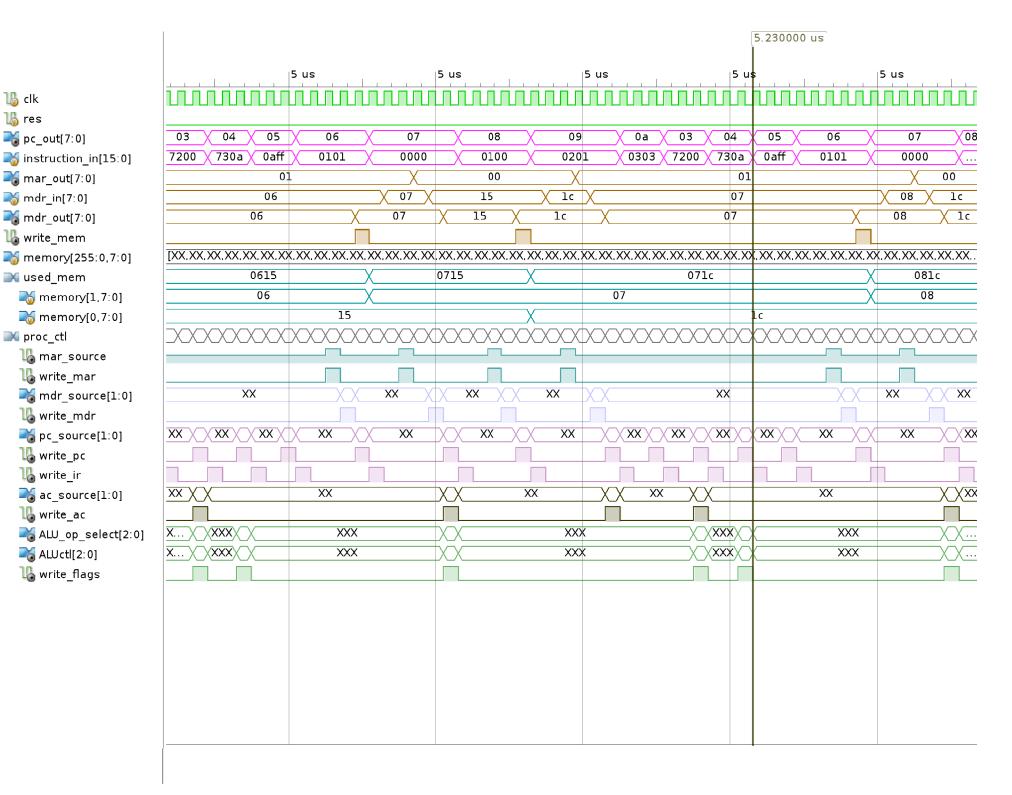
used mem

m proc_ctl

₩ write_pc **¼** write_ir

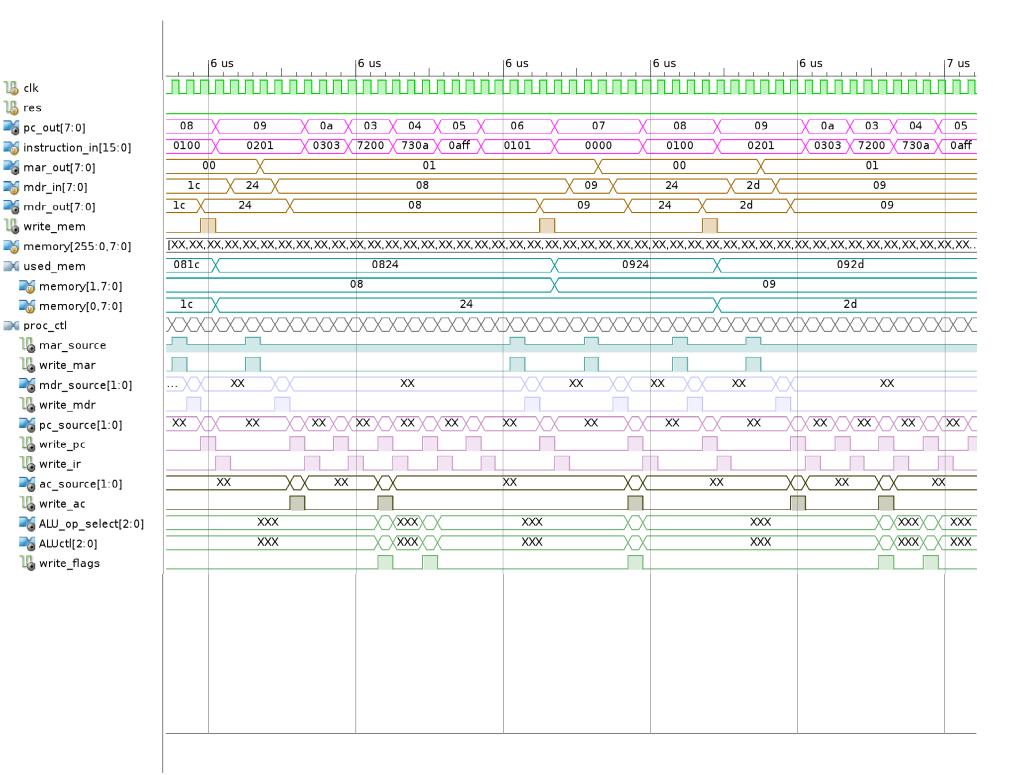
U write ac





₩ clk 🔚 res

m proc_ctl



a pc out[7:0]

mdr_in[7:0]

¼ write_mem

used mem

m proc_ctl

₩ write_pc **¼** write_ir

U write ac