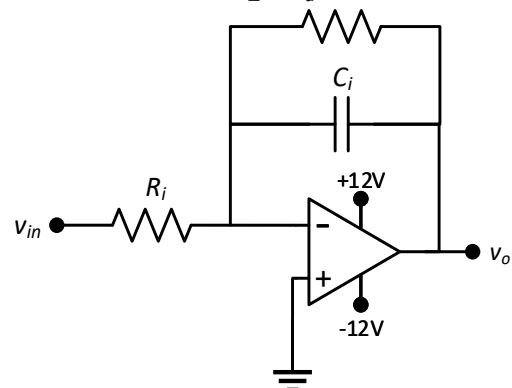
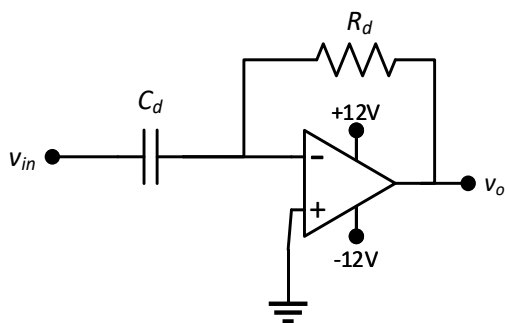
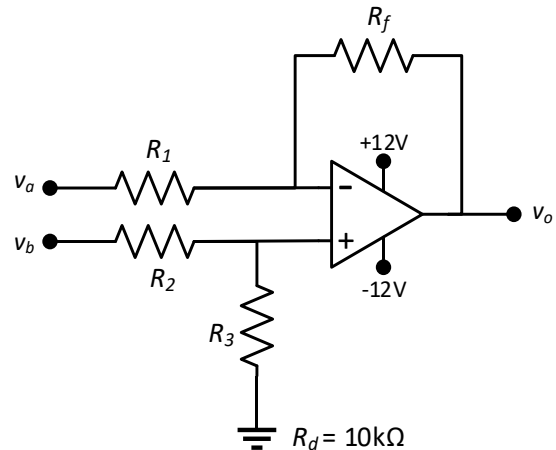
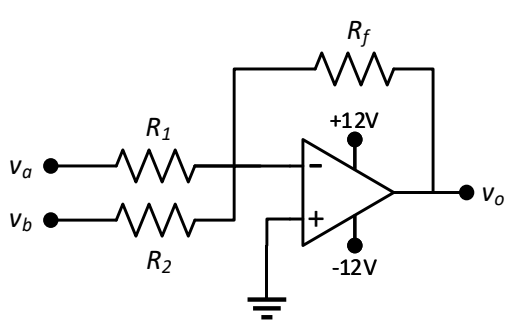


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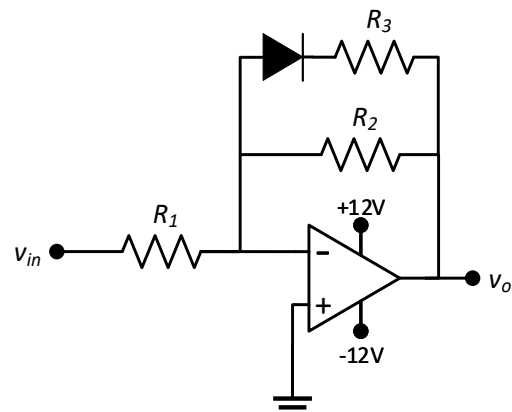
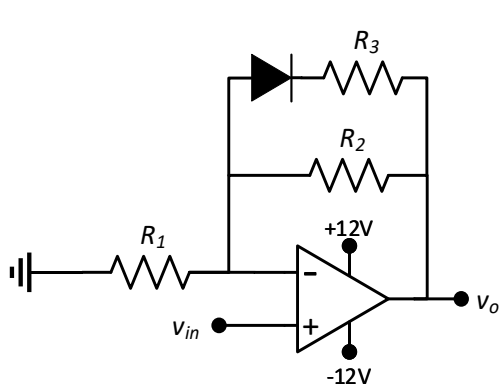
Fall 2025-2026
Duration: 110 Minutes

EE 213 Practical Exam Tuesday Morning

Some Operational Amplifier Topologies



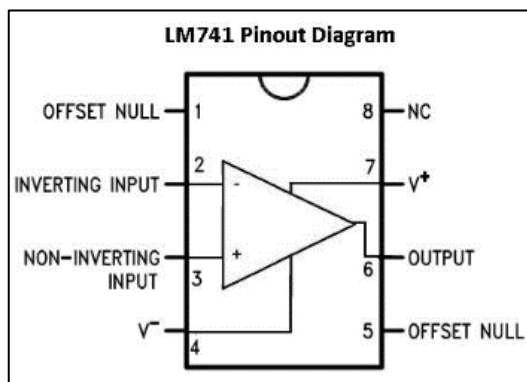
You may assume $V_{D,on}=0V$ during design



Please read the exam regulations at the back of this page.

Practical Exam Regulations

- **Write your name on all odd numbered pages.**
- Your phones will be collected at the beginning of each session. You are not allowed to take them back until you are told so. Any case failing to comply with this rule will be considered as a cheating attempt.
- Please check the oscilloscope by using “Probe Comp” output; you should see a square wave of 1 kHz frequency and 2.5 V_{p-p} amplitude. Adjust the signal generator to give a square wave and check the output using the oscilloscope. Do not forget to operate the signal generator in High-Z mode (Utility→Output Setup→High-Z).
- Digital multimeters in the laboratory accept probes from both front and rear panels. Please check “Rear/Front” button of the digital multimeters and be sure that the front option is activated.
- Do not forget to make output **ON** for the DC supply and the signal generator whenever you need them. This item may seem redundant to state, but there is a large number of students that forget this during the exam.
- Do not forget to limit the output currents to **100 mA**.
- During the exam, you are required to show all the plots on your exam paper to the assistant while the result is available on the oscilloscope screen. **Figures that are not presented to the assistant during the exam will not be accepted.** If you want to make a change on a plot, call the assistant to show your revised plot.
- The assistants will not answer any of your questions during the exam. The sole exception to this rule is that you can ask the assistant to check your device settings **only for once** so that a minor error will not result in you not performing the task fully.
- All necessary equipment will be given to you in a box. You may ask for components or probes in case of any problem.
- You may ask for changing your table; however, **you need to justify the reason** for your request.



Components for Design:

5 x 1k Ω , two components for each one of the following set: (1.2 k Ω , 1.5 k Ω , 1.8 k Ω , 2.2 k Ω , 2.7 k Ω , 3.3 k Ω , 3.9 k Ω , 4.7 k Ω , 5.6 k Ω , 6.8 k Ω , 8.2 k Ω , 10 k Ω)

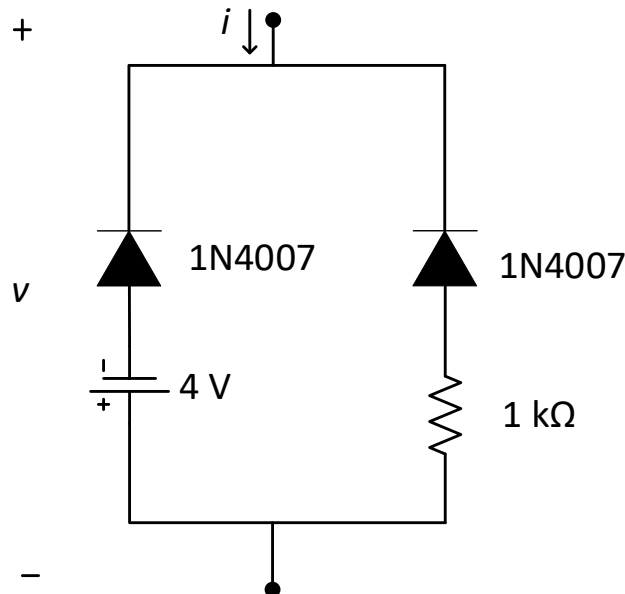
1 μ F, 2 x 100 nF

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Practical Exam TuM Questions

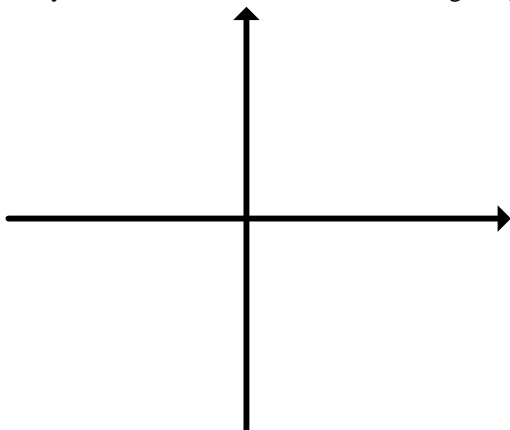
1. Find the i - v characteristics of the nonlinear device shown below using DSO and function generator (whose frequency is set to 30 Hz). Give the circuit configuration and show the probe connections and DSO settings. Clearly explain the method you used. Plot the result you obtained giving **all the details**. If you **can't** find the i - v characteristics experimentally, you may plot the theoretical results you expected.



Circuit Configuration & Explanation:

$V_{in}(t) = A * \sin(\omega t)$	
$A =$	$\omega =$

Results: (Ideally, give your experimental results with theoretical justifications. Otherwise, for theoretical result, assume that the diode opening voltage is 0.6V and the LED opening voltage is 1.6V.) **Do not forget** to show your DSO screen to the assistant using the given channel resolutions.



Ch1: 1V/div Ch2: 2V/div

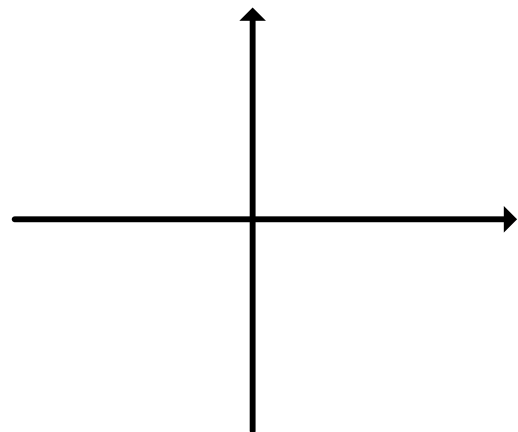
2. In this question, you are required to design an operational amplifier circuit that adds two sinusoidal waveforms with a gain of $-K$ where $K \cong 7$ (range: $6.3 < K < 7.7$). That is, you are required to construct a circuit where $v_o(t) = -K * (v_1(t) + v_2(t))$, where $v_1(t) = 2v_2(t) = 0.5 \sin(1000\pi t)$ V.

- a) Construct a circuit to obtain $v_2(t)$ from $v_1(t)$ (**Hint:** A voltage divider and a buffer) and indicate it below.

First design:

- b) Draw your circuit configuration below (you do not have to redraw your buffer circuit). Calculate the required resistor/capacitor values by analyzing the circuit by assuming that the operational amplifier is in linear region. What is the **name** of this circuit?

Circuit analysis and design:



Ch v_{in} : 0.5V/div | Ch v_{out} : 2V/div | t: 1ms/div

- c) Obtain and plot $v_2(t)$ and $v_o(t)$ vs. t , labeling each signal, on the same figure using DSO. Use the space above for your plot and **do not forget** to show the DSO screen to the assistant using the given channel resolutions.

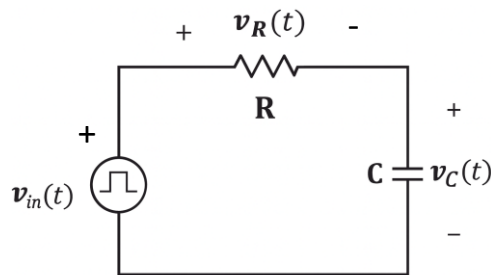
Name:
ID:

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3. (Bonus) You are given MATLAB data files containing the time vector t (in seconds) and the corresponding voltage waveforms of an RC circuit shown below: the input voltage v_{in} (in volts), the resistor voltage v_R (in volts), and the capacitor voltage v_C (in volts).

Using these data, **plot all three waveforms on the same graph in MATLAB**. Your plot must satisfy the following requirements:

1. **Each waveform must be clearly distinguishable** by including a **legend** indicating which curve corresponds to
 - $v_{in}(t)$
 - $v_R(t)$
 - $v_C(t)$
2. Properly **label** the axes including units:
3. Add an appropriate **title** to the plot.
4. Ensure the plot is clear and readable (e.g., use grid lines).



(Show the assistant the MATLAB figure which includes your plot.)