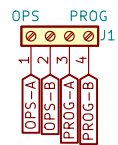
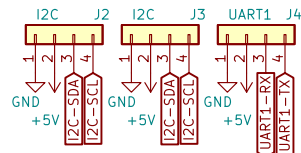


The factory reset jumper can be used to clear all persistent configuration data during startup. It is not recommended to keep this jumper in place during normal operation.

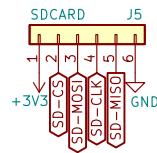


Command Station expansion ports



I2C is only used today for OLED/LCD devices. UART1 currently unused but will be in the future.

SD Module connection

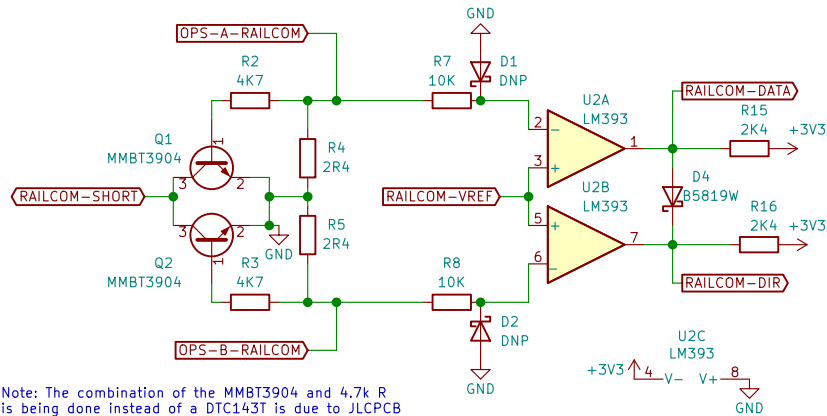


When using a DevKit-C (or compatible) ESP32 module it is recommended to use an SD card SPI module. This is to reduce the wear on the on-board FLASH and to allow updating the CS in the future. For the TTGO-T1 this is not used since it has this on-board.



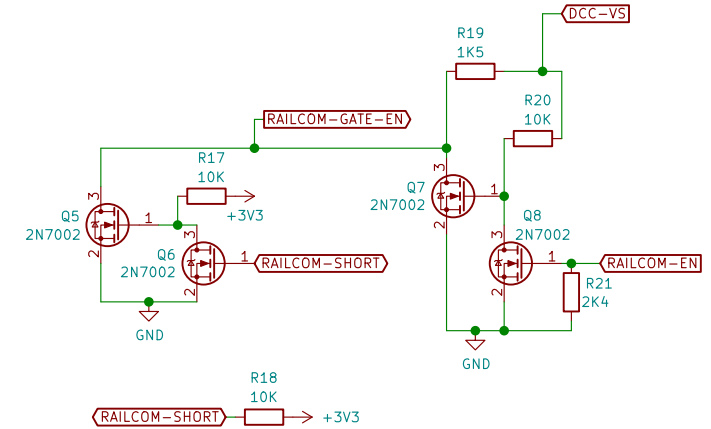
Sheet: /		
File: pcb.sch		
Title: ESP32 Command Station with LCC and RailCom		
Size: B	Date: 2020-06-21	Rev: 1.3
KiCad E.D.A. kicad 5.1.6	Id: 1/8	

OPS Track RailCom detector circuit



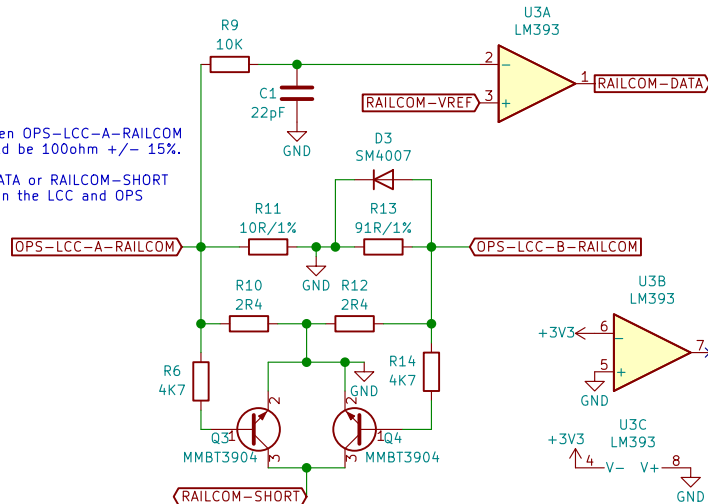
Note: The combination of the MMBT3904 and 4.7k R is being done instead of a DTC143T is due to JLCPCB having the DTC143T as an "extended part" whereas MMBT3904 is a "basic part"

RailCom FET gate enable circuit



Force RailCom short to be HIGH by default.

Power Station Feedback Interface via LCC ports



Note: The total resistance between OPS-LCC-A-RAILCOM and OPS-LCC-B-RAILCOM should be 100ohm +/- 15%.

No P/U is added to RAILCOM-DATA or RAILCOM-SHORT as these nets are shared between the LCC and OPS detectors.

Note: The combination of the MMBT3904 and 4.7k R is being done instead of a DTC143T is due to JLCPCB having the DTC143T as an "extended part" whereas MMBT3904 is a "basic part"

TP1 RAILCOM-EN
○ RAILCOM-EN
TP2 RAILCOM-DATA
○ RAILCOM-DATA
TP3 RAILCOM-SHORT
○ RAILCOM-SHORT

NOTE: The usage of two LM393 instead of one LM339 is due to JLCPCB having LM339 as an "extended part" and LM393 as a "basic part"

Sheet: /RailCom Detector/
File: pcb-railcom.sch

Title:

Size: A4

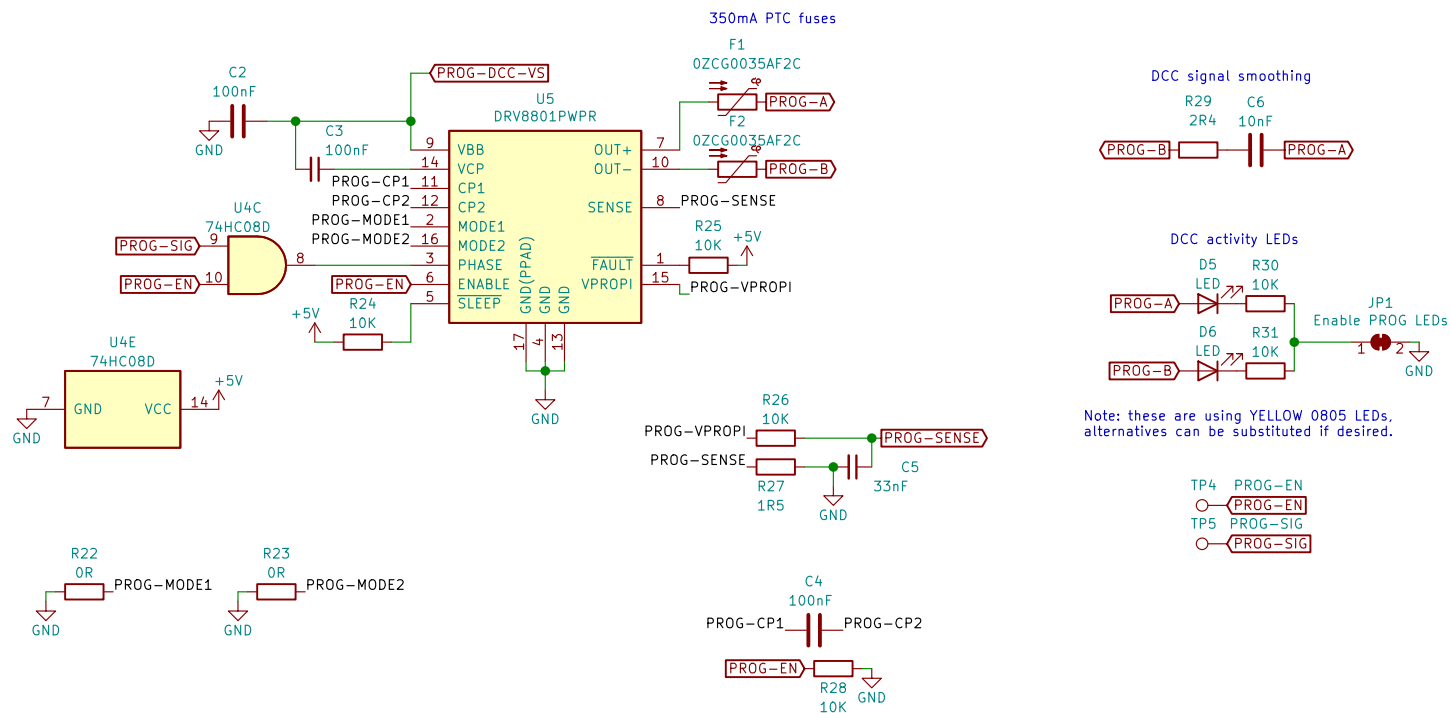
Date:

KiCad E.D.A. kicad 5.1.6

Rev:

Id: 2/8

DCC signal generator for PROG track



Sheet: /Programming Track DCC/
File: pcb-prog-dcc.sch

Title:

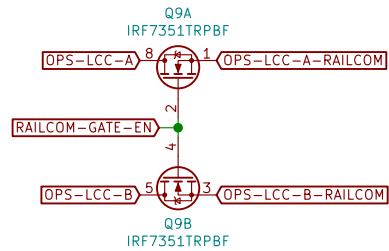
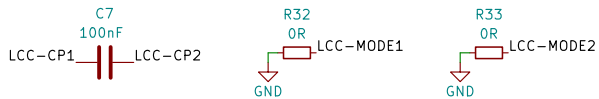
Size: A4

Date:

KiCad E.D.A. kicad 5.1.6

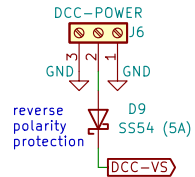
Rev:

Id: 3/8



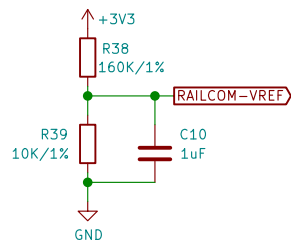
Id: 4/8

PCB and DCC power supply connection



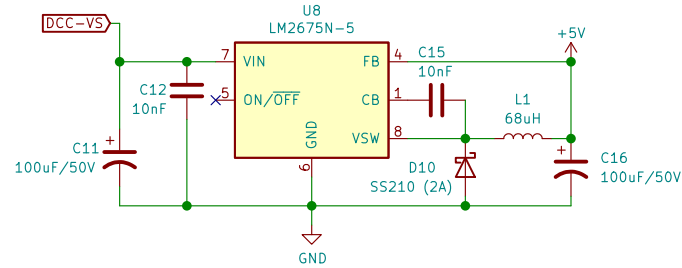
Note: Either DCC-POWER pin 1 or 3 should be used as BOOSTER-COMMON

RailCom Reference Voltage

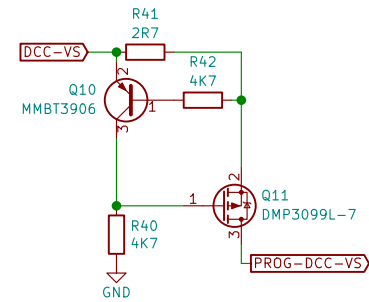


$$\begin{aligned} \text{RAILCOM-VREF} &= (3\text{v3} \times \text{R38}) / (\text{R37} + \text{R38}) \\ \text{RAILCOM-VREF} &= (3.3 \times 10000) / (160000 + 10000) \\ \text{RAILCOM-VREF} &= \text{approx } 19\text{mv} \end{aligned}$$

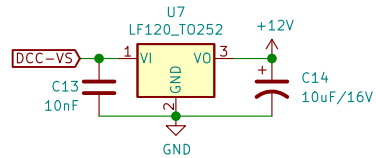
5V 1A switching step-down converter



Programming track DCC current limited voltage supply (approx 250mA)



12V 500mA step-down converter for LCC-DCC signal output



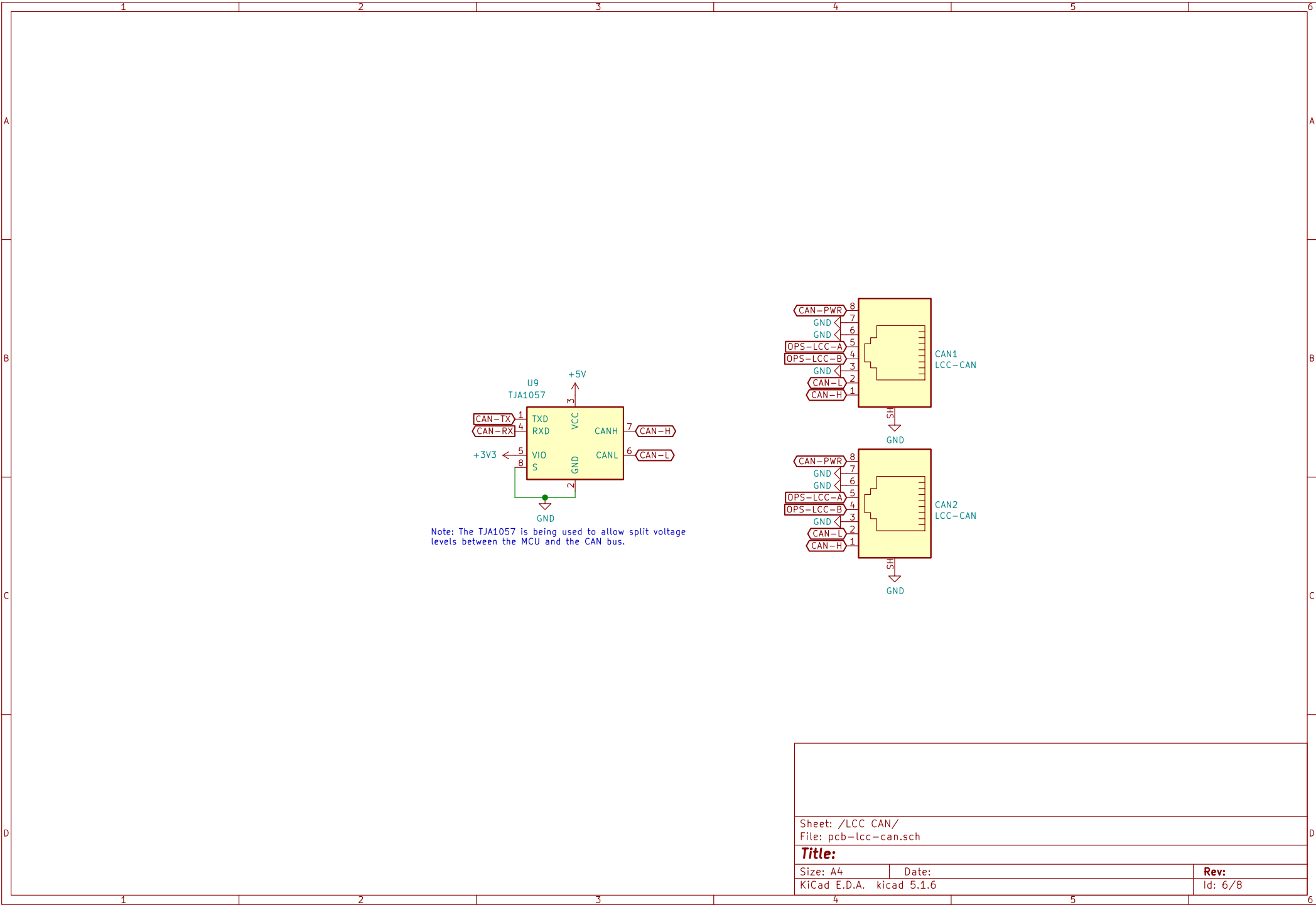
Sheet: /Power/
File: pcb-power.sch

Title:

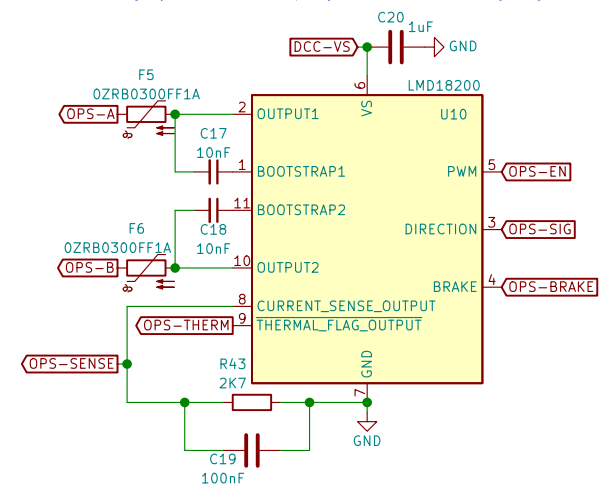
Size: A4
KiCad E.D.A. kicad 5.1.6

Date:

Rev:
Id: 5/8

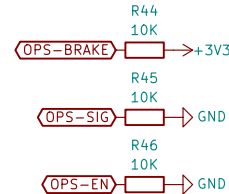


LMD18200 h-bridge (3A continous, 6A peak) for OPS track DCC signal generation.



Note: The CURRENT_SENSE_OUTPUT is an INLINE output from the h-bridge. The PWM and DIR have a PULL-DOWN and BRAKE has a PULL-UP to force the LMD18200 into a known state on startup (IE: OFF)

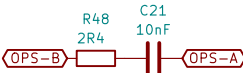
P/U and P/D to force LMD18200 into a known state on startup.



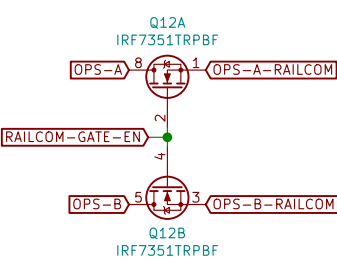
Thermal alert LED (RED), when ON the LMD18200 is detecting a temperature of at least 145C.



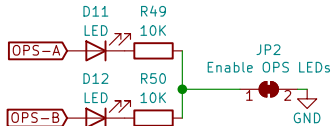
DCC Signal smoothing



RailCom FET bridge for OPS track



DCC activity LEDs



Note: these are using YELLOW 0805 LEDs, alternatives can be substituted if desired.

- TP6 OPS-SIG
- TP7 OPS-EN
- TP8 OPS-BRAKE

Sheet: /OPS Track DCC/
File: pcb-ops-dcc.sch

Title:

Size: A4
KiCad E.D.A. kicad 5.1.6

Date:

Rev:

Id: 7/8

