0CPU

* Data path
  + Control Unit
  + Decoder 5x32
  + Decoder 2x4
  + Ram
  + 16x1 mux, 32 bit
  + 2x1 mux, 32 bit
  + 32x1 mux, 32bit
  + 4x1 mux, 32 bit
  + 8x1 mux, 32 bit
  + 32 bit register
  + Register windows
  + ALU















