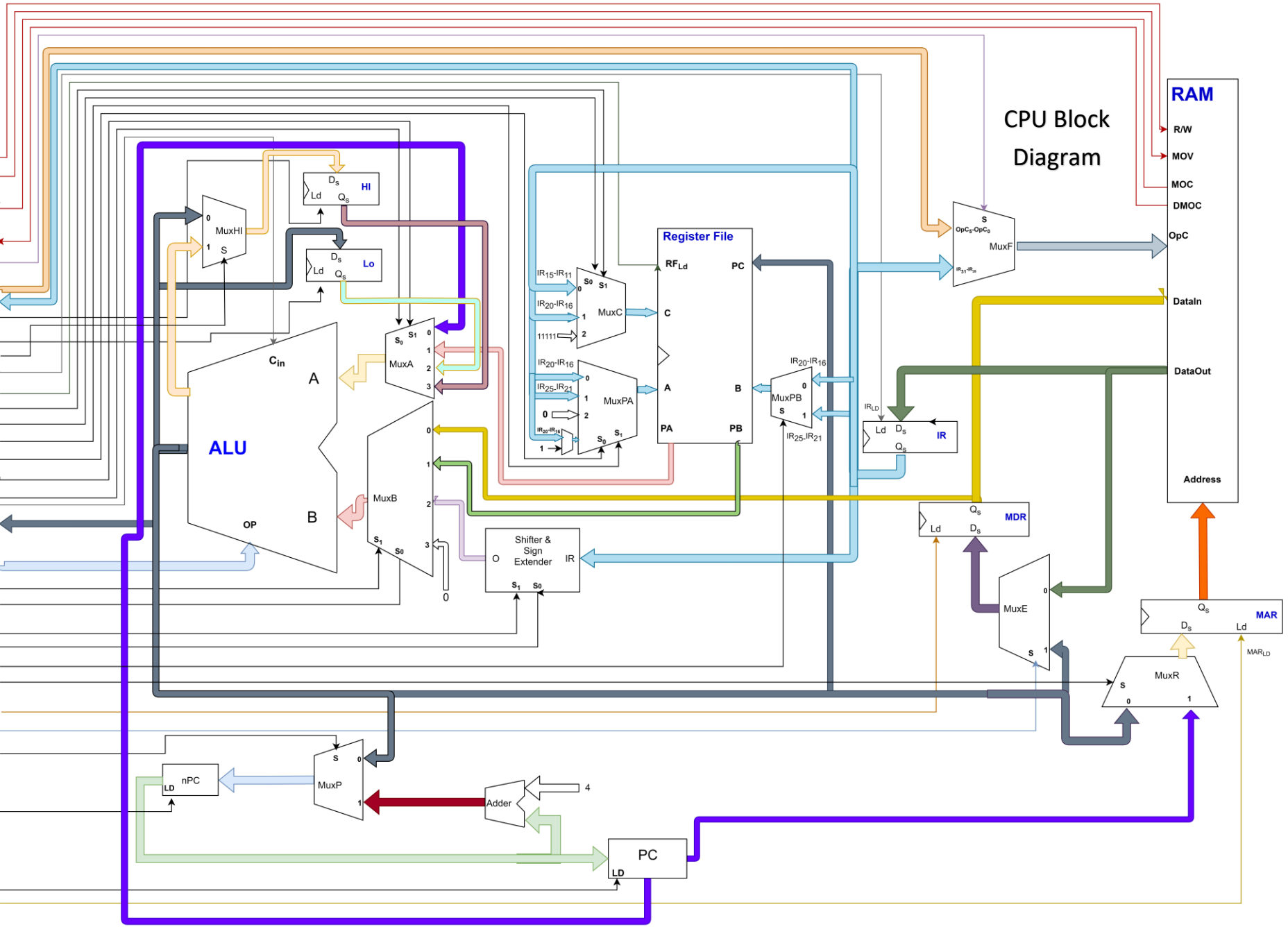
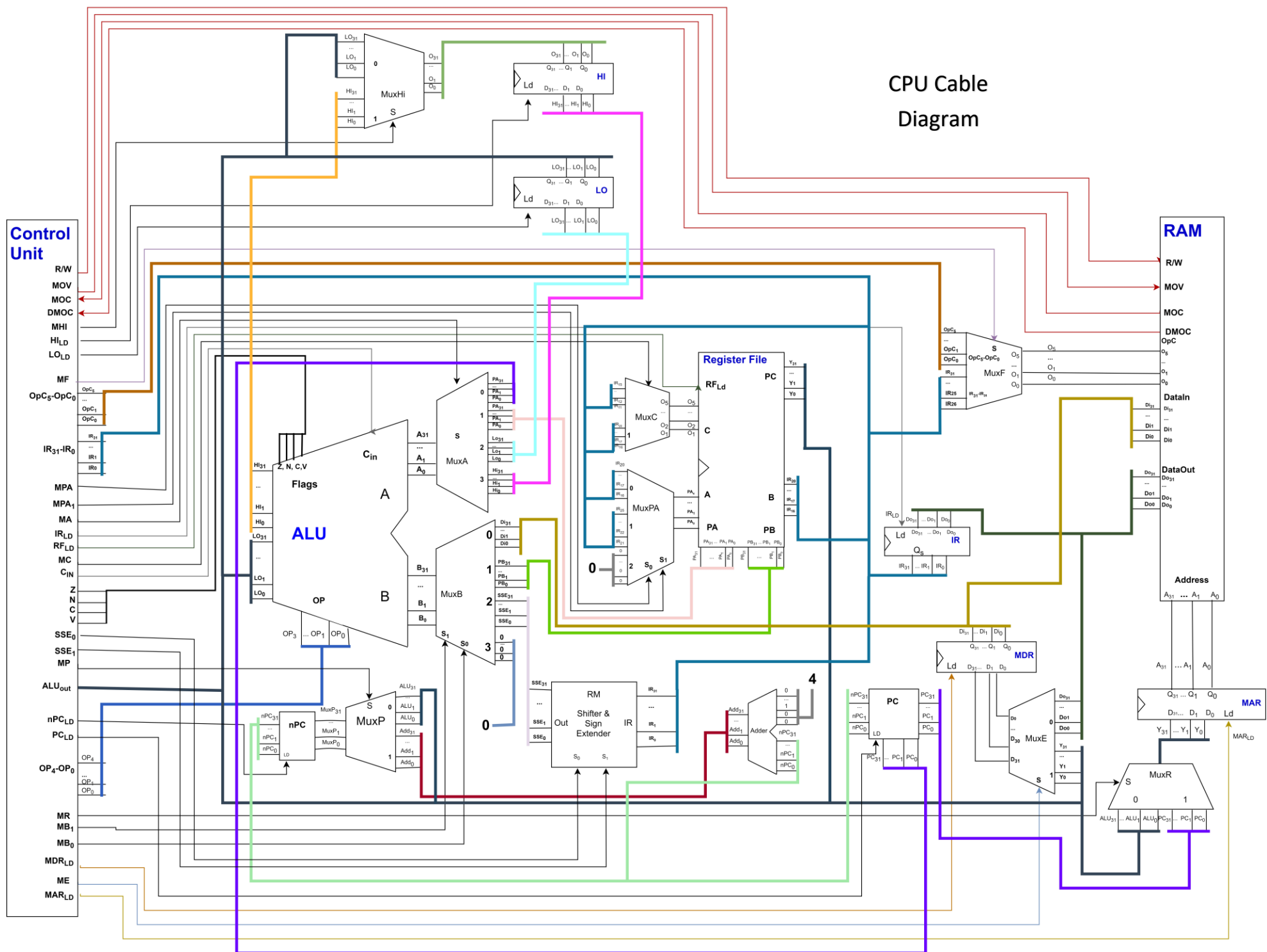
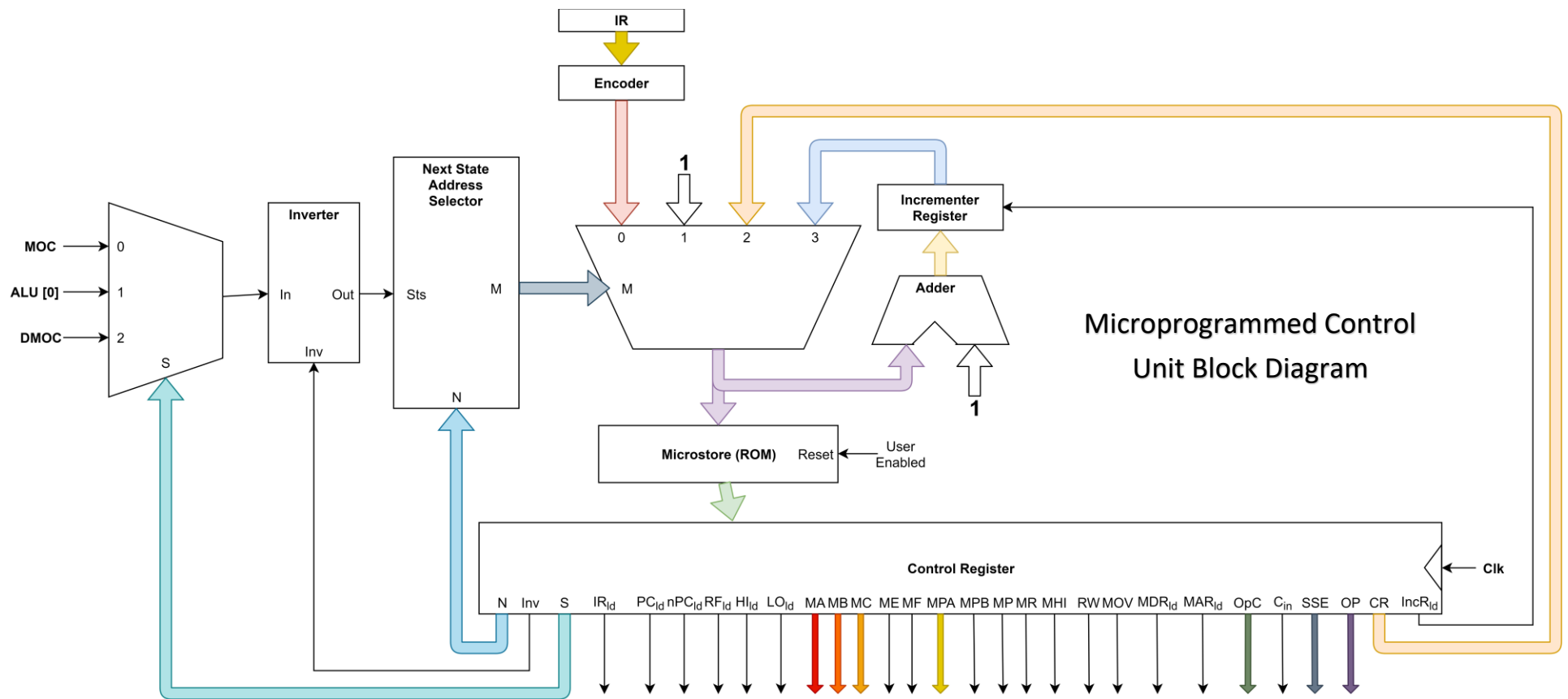


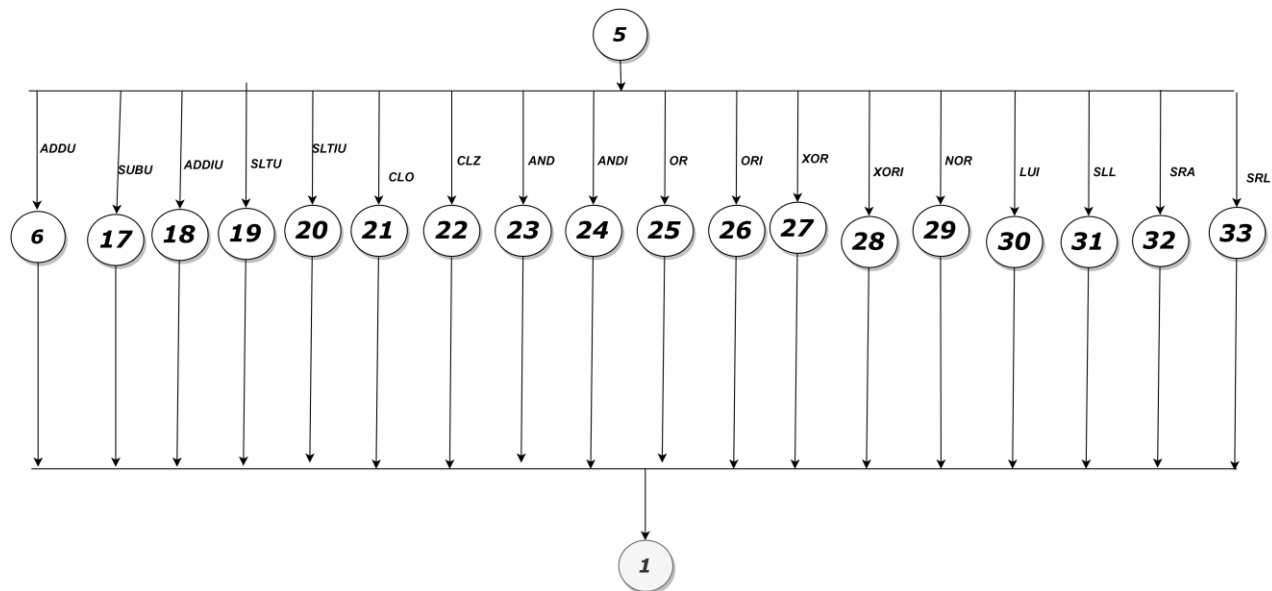
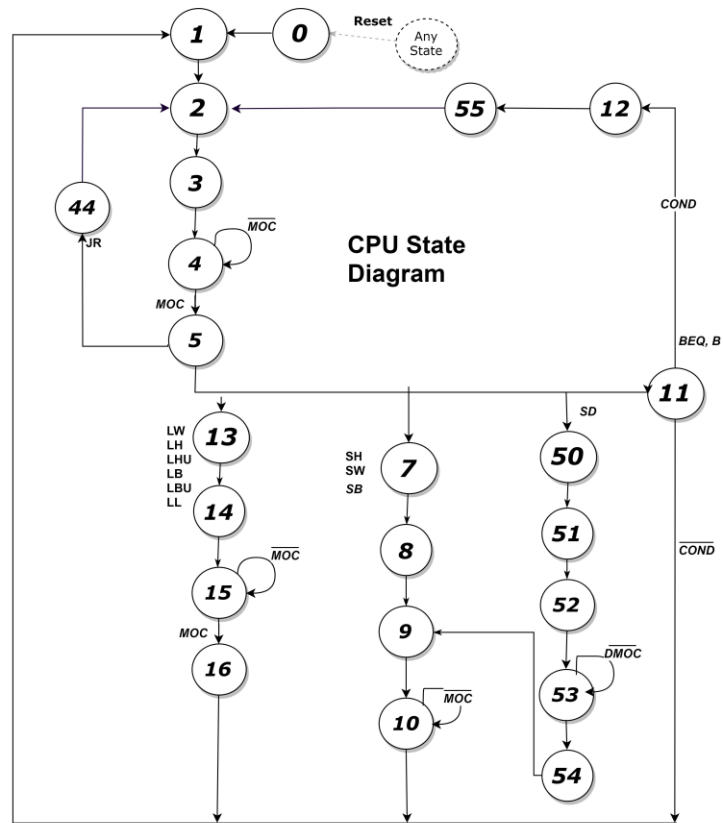
Control Unit

R/W
 MOV
 MOC
 DMOC
 MF
 OpC₅-OpC₀
 IR₃₁-IR₀
 HI_{LD}
 LO_{LD}
 MHI
 IR_{LD}
 RF_{LD}
 MC₁
 MC₀
 MPA₁
 MPA₀
 MA₁
 MA₀
 C_{IN}
 ALU_{out}
 OP₃-OP₀
 MB₁
 MB₀
 SSE₁
 SSE₀
 MPB
 MR
 MDR_{LD}
 ME
 MP
 nPC_{LD}
 PC_{LD}
 MAR_{LD}









CPU State Diagram Continued

