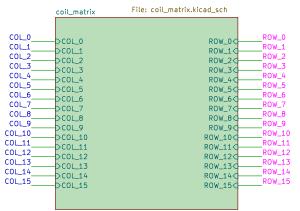
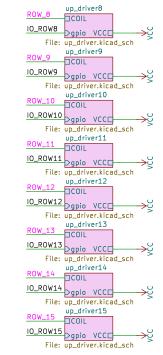


COIL MATRIX



IO_ROW0 ->gpio VCC□ File: up_driver.kicad_sch up_driver1 COIL IO_ROW1 ⊃gpio VCC□ File: up_driver.kicad_sch up_driver2 COIL IO_ROW2 ⊃gpio VCC□ File: up_driver.kicad_sch up_driver3 COIL IO_ROW3 ⊃gpio VCC□ File: up_driver.kicad_sch up_driver4 COIL IO_ROW4 ⊃gpio VCC□ File: up_driver.kicad_sch up_driver5 COIL IO_ROW5 ⊃gpio VCC□ File: up_driver.kicad_sch up_driver6 COIL IO_ROW6 ⊃gpio VCC□ File: up_driver.kicad_sch up_driver7 COIL IO_ROW7 ⊃gpio VCC□ File: up_driver.kicad_sch

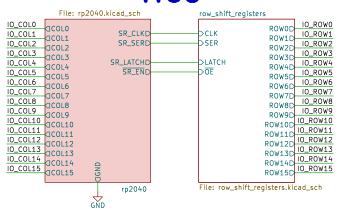


COLUMNS

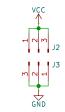


Back EMF Protections

MCU

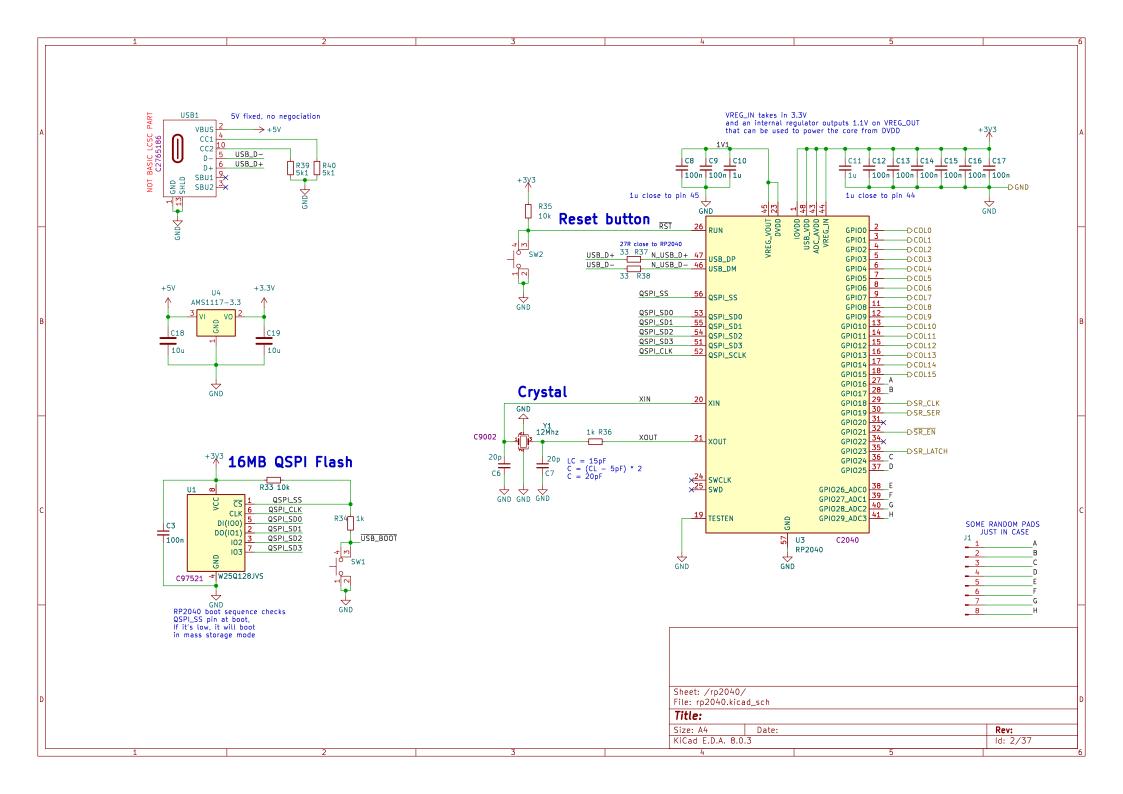


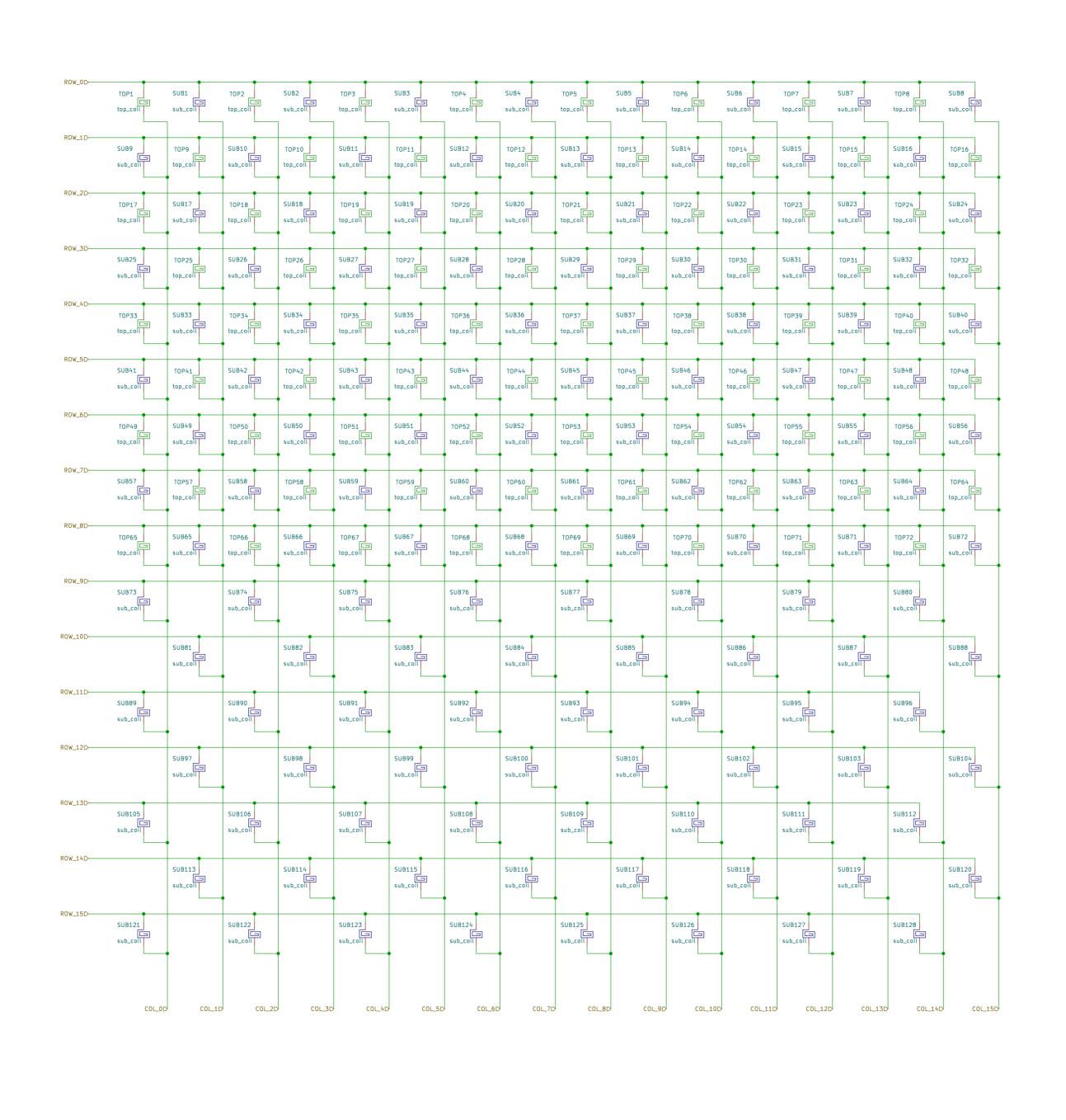
ROWS



POWER INPUT

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Title:						
Size: A4	Date: 20	24-07-23			Rev:	
KiCad E.D.A. 8.0.3						
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Jonas Stirnema	ann.	
Sheet: /coil_m File: coil_matri	atrix/	
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Size: A2	Date: 2024-07-28	Rev: 0.1
KiCad E.D.A. 8	ld: 3/37	
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