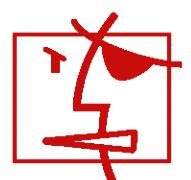
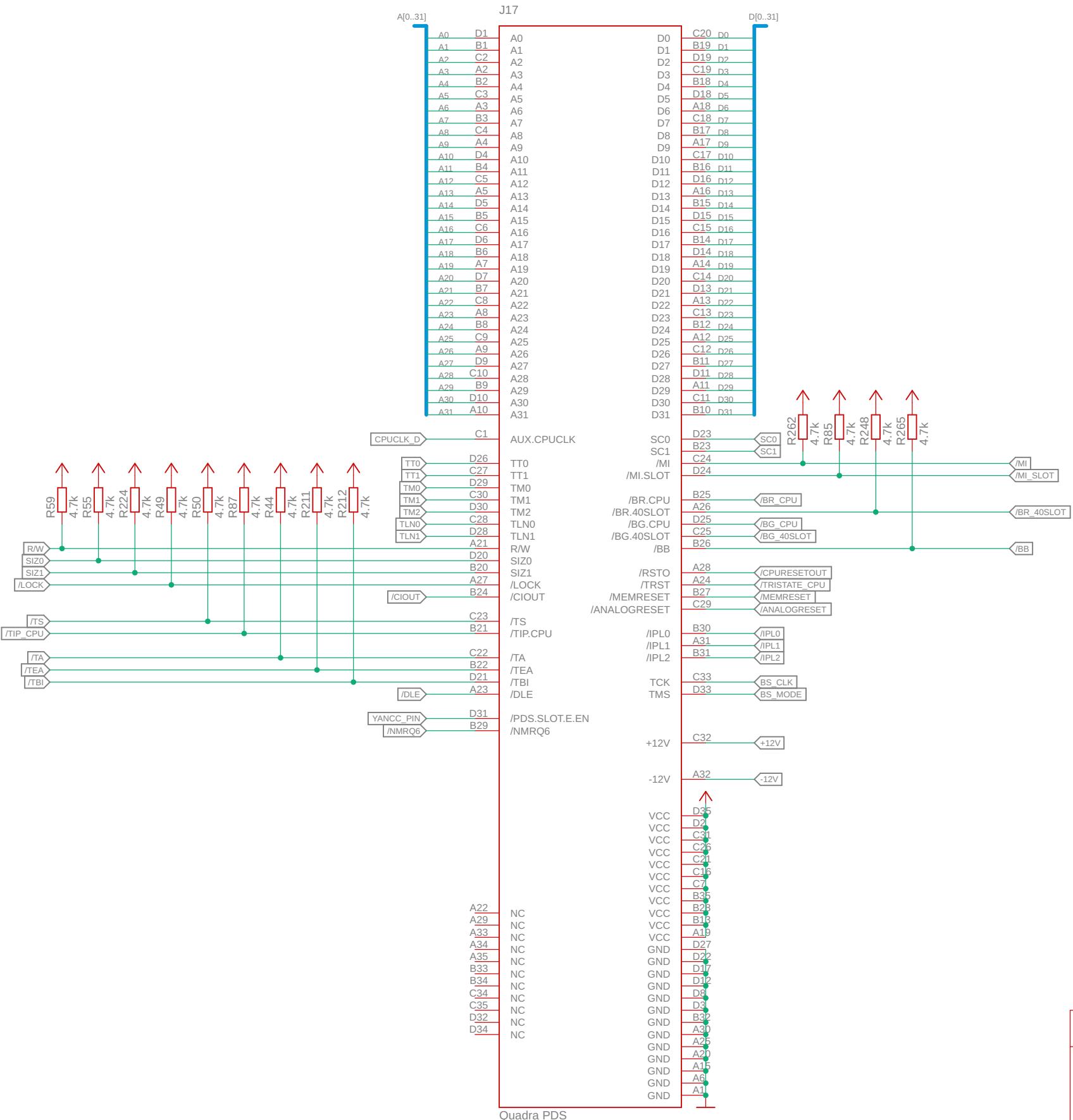


Macintosh Quadra 950 logicboard

68040
Memory data bus buffers
Memory data bus term.

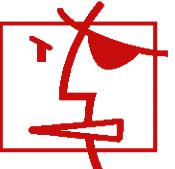


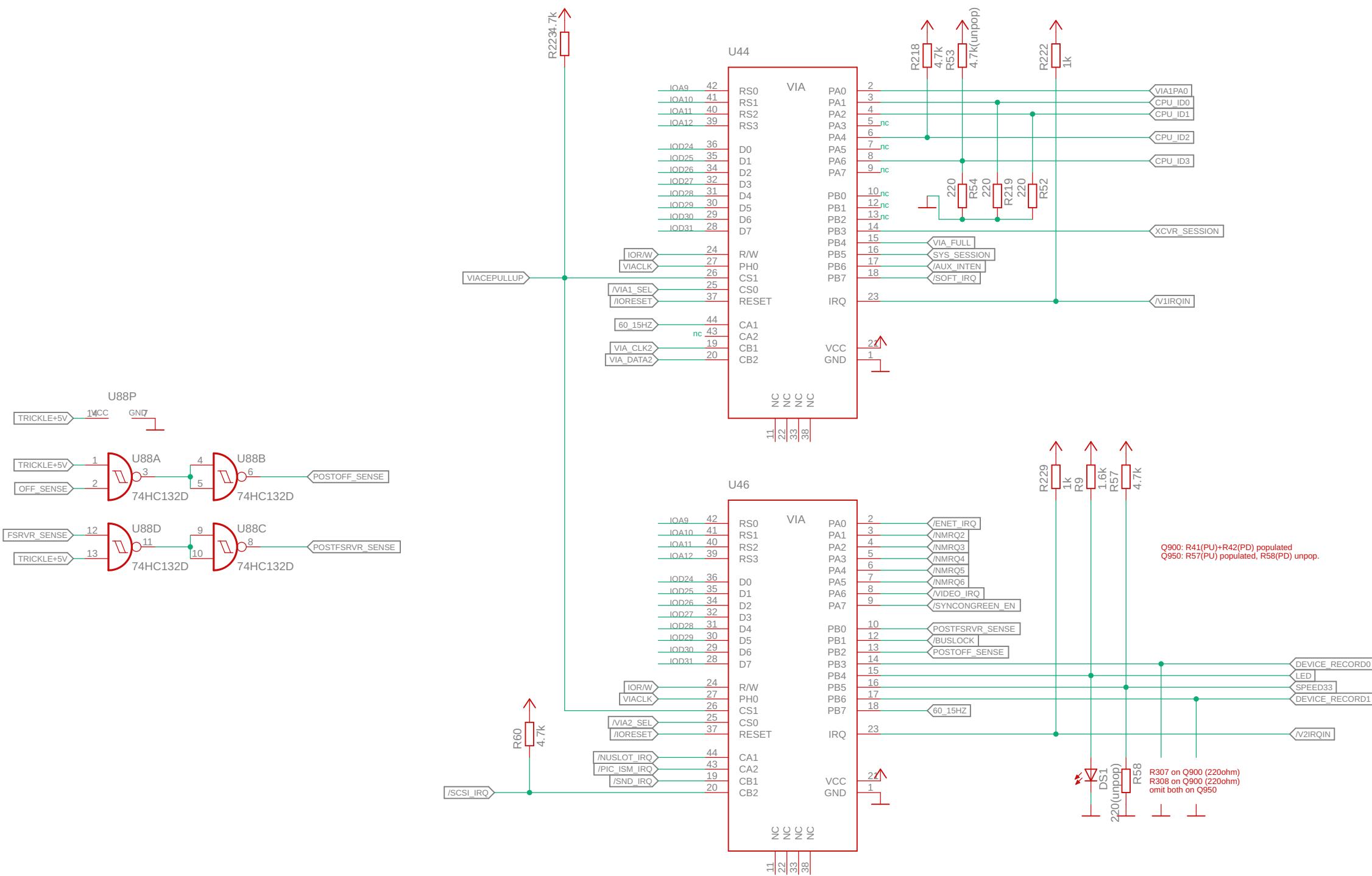


Macintosh Quadra 950 logicboard

PDS

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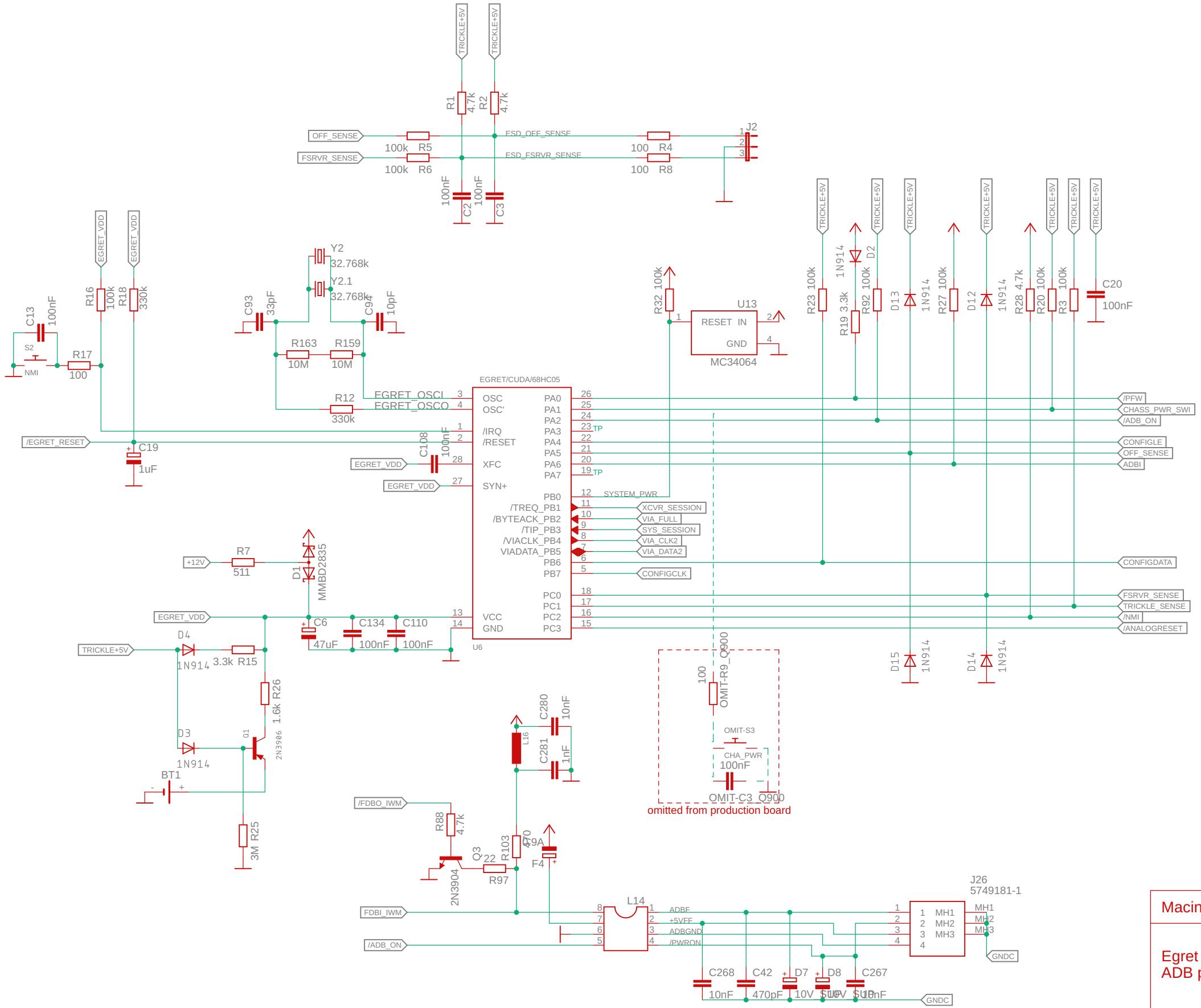




Macintosh Quadra 950 logicboard

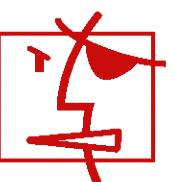
VIA
Power sense

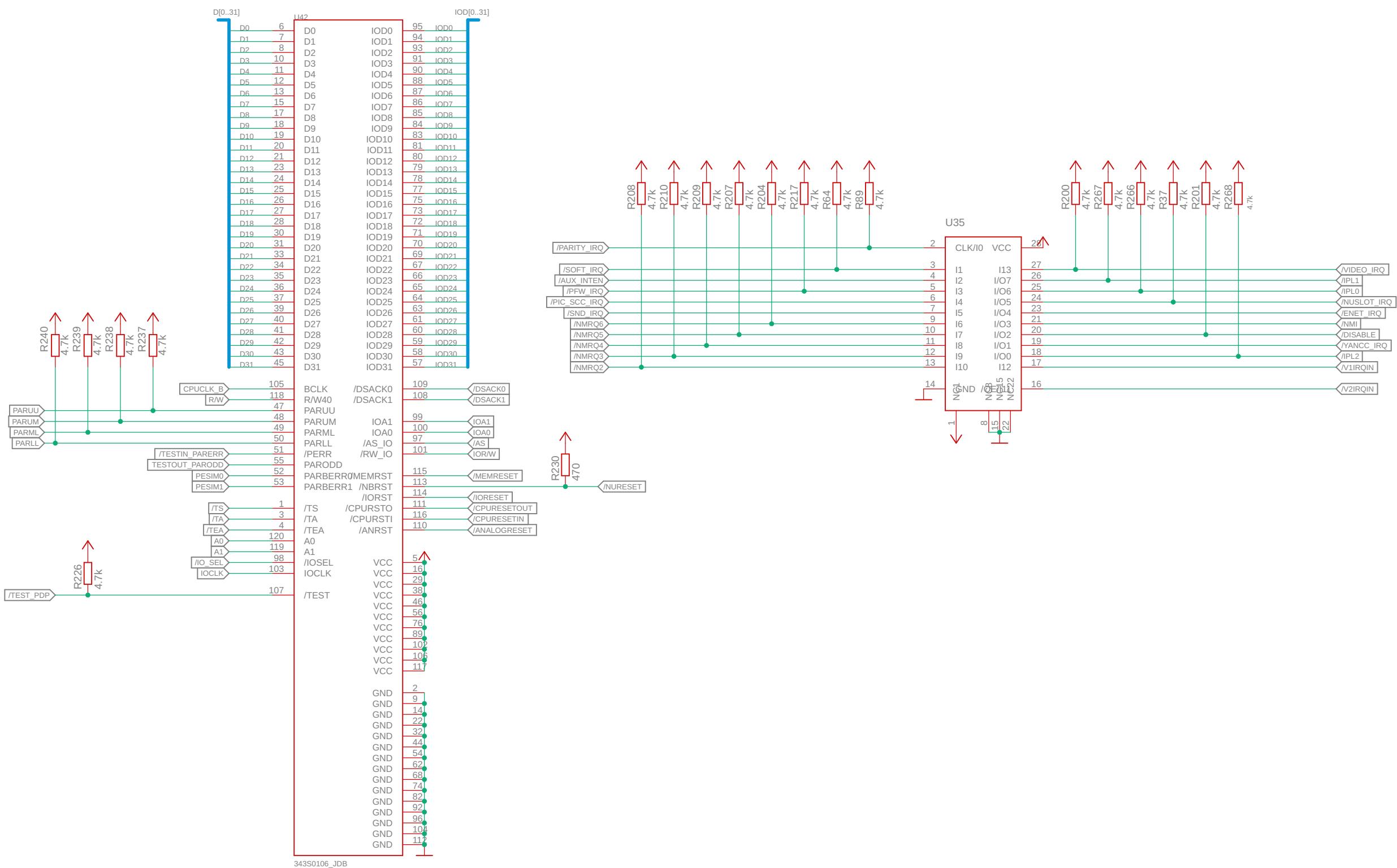


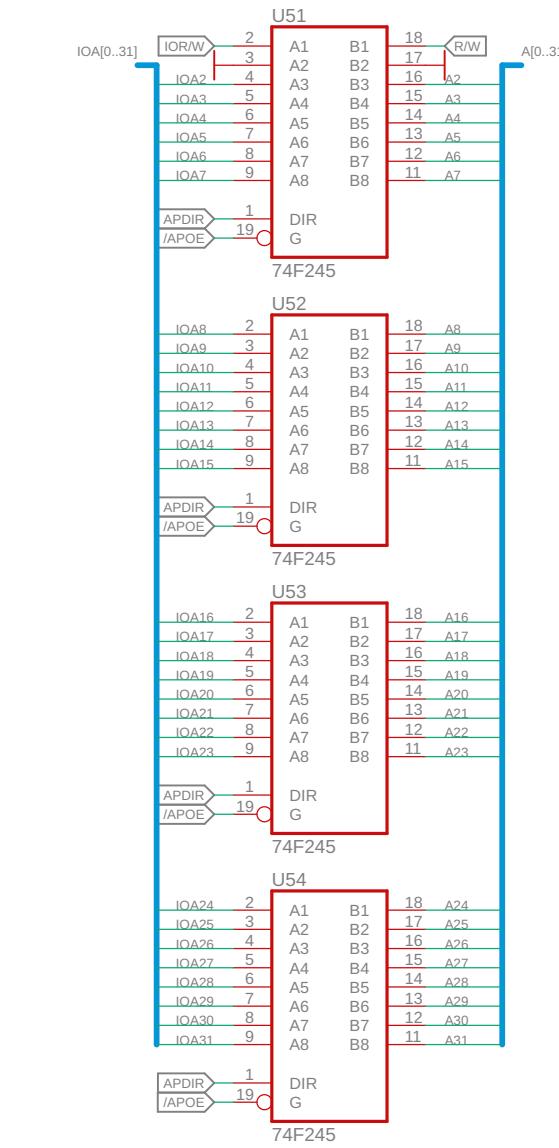
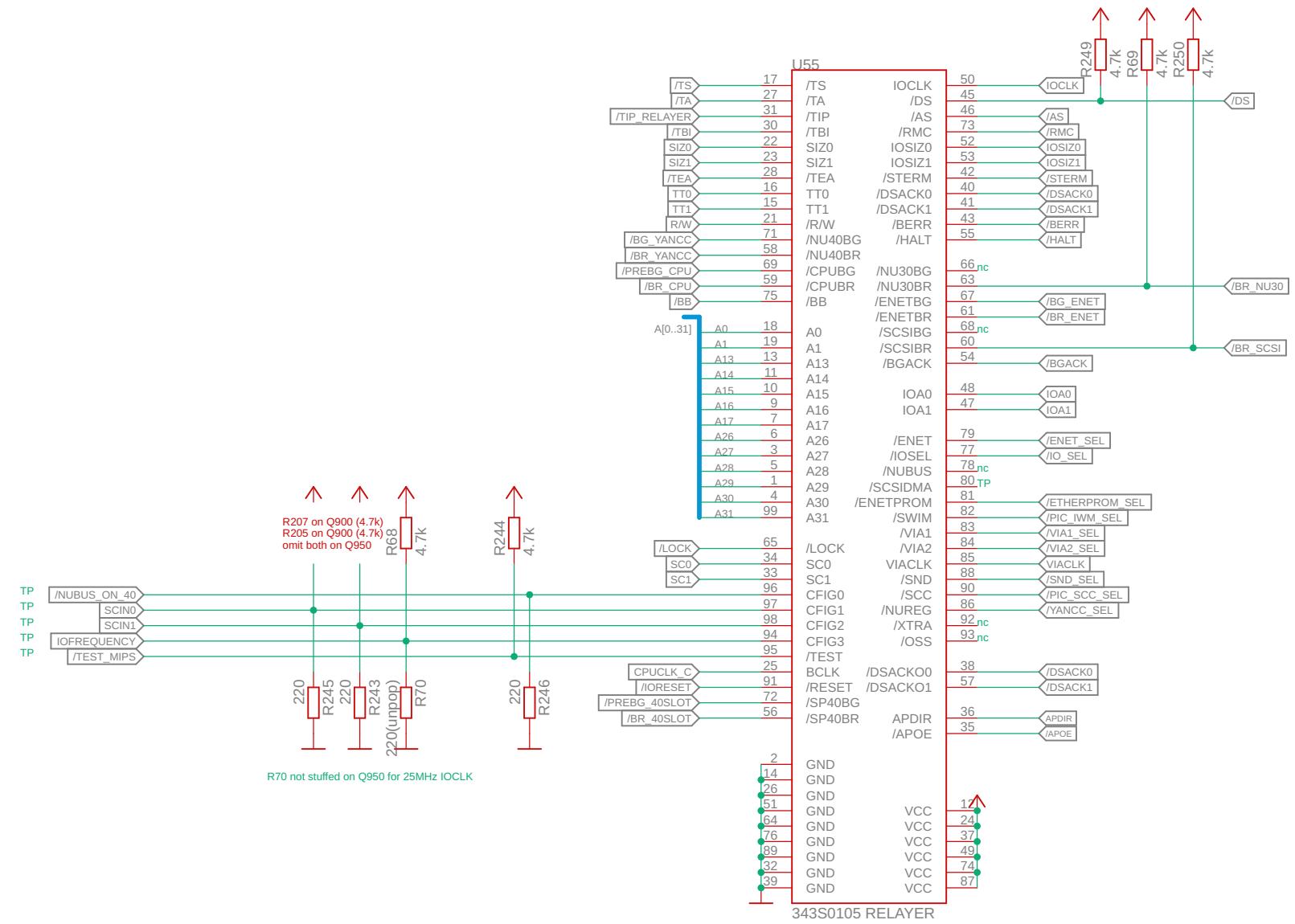


Macintosh Quadra 950 logicboard

Egret ADB port

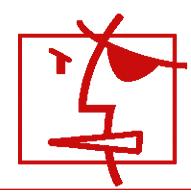


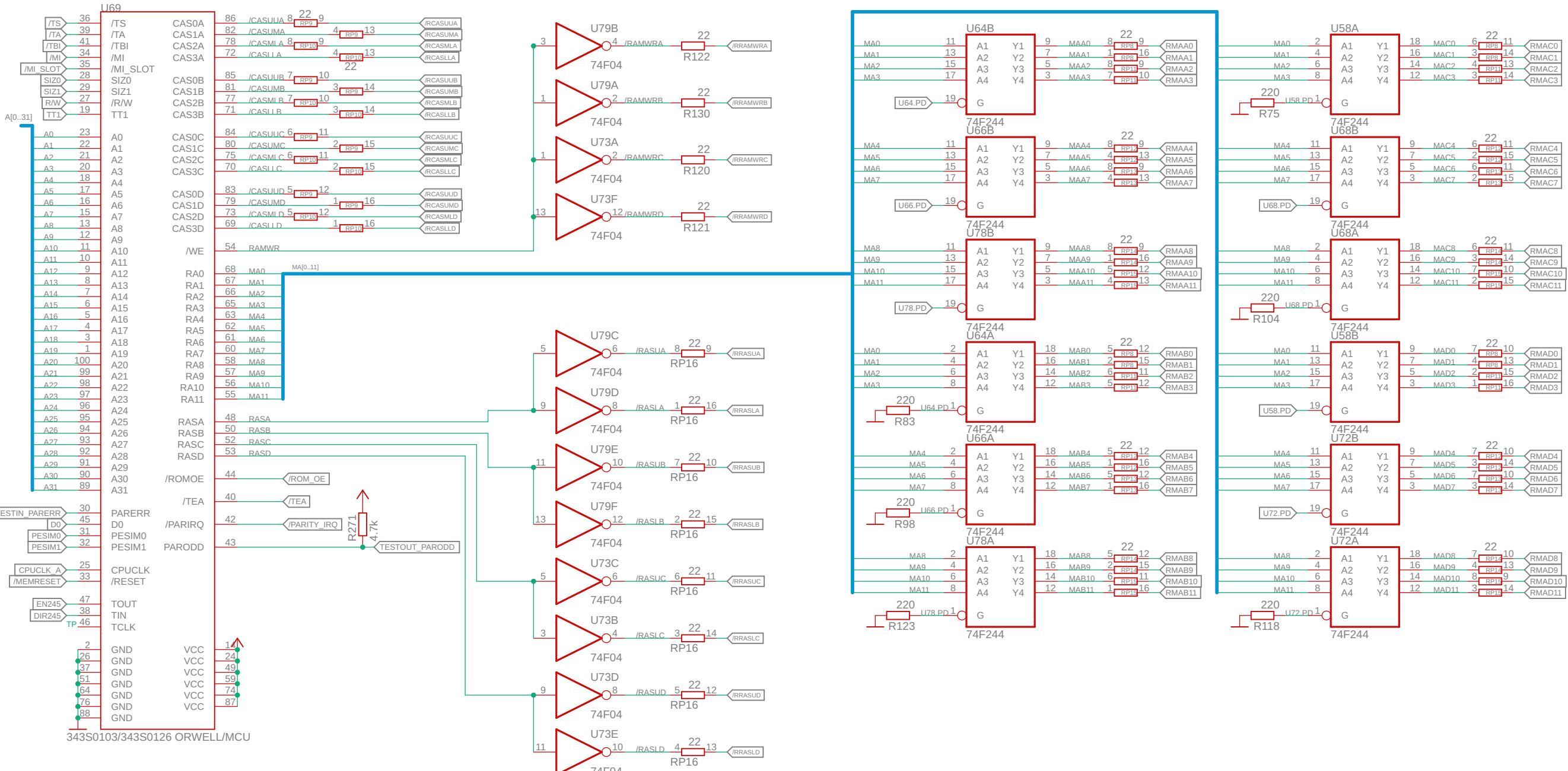




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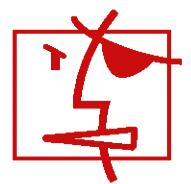
Relayer
IO Address bus buffers

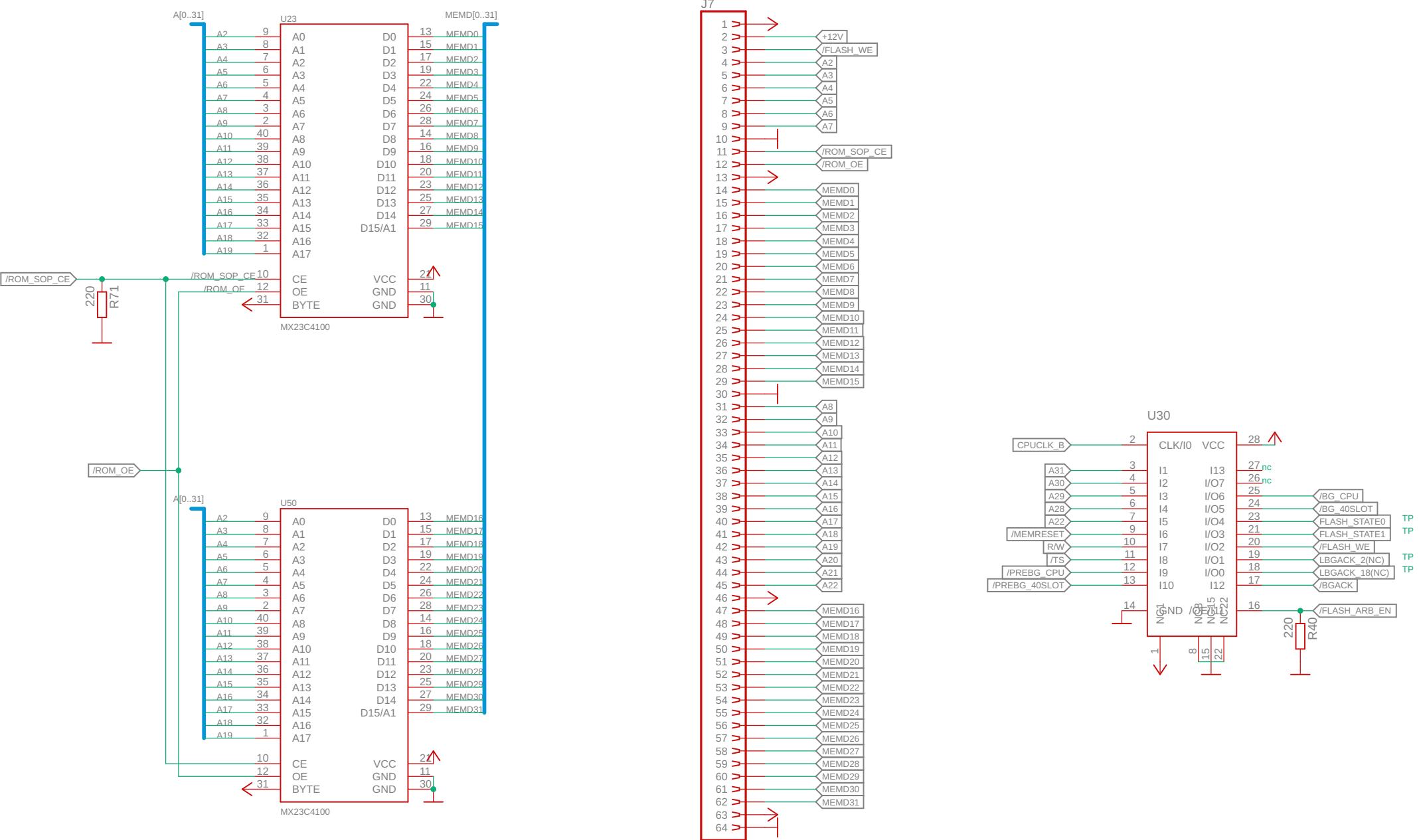




Macintosh Quadra 950 logicboard

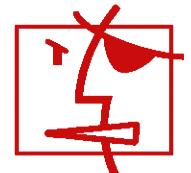
Memory controller
RAM Address buffers

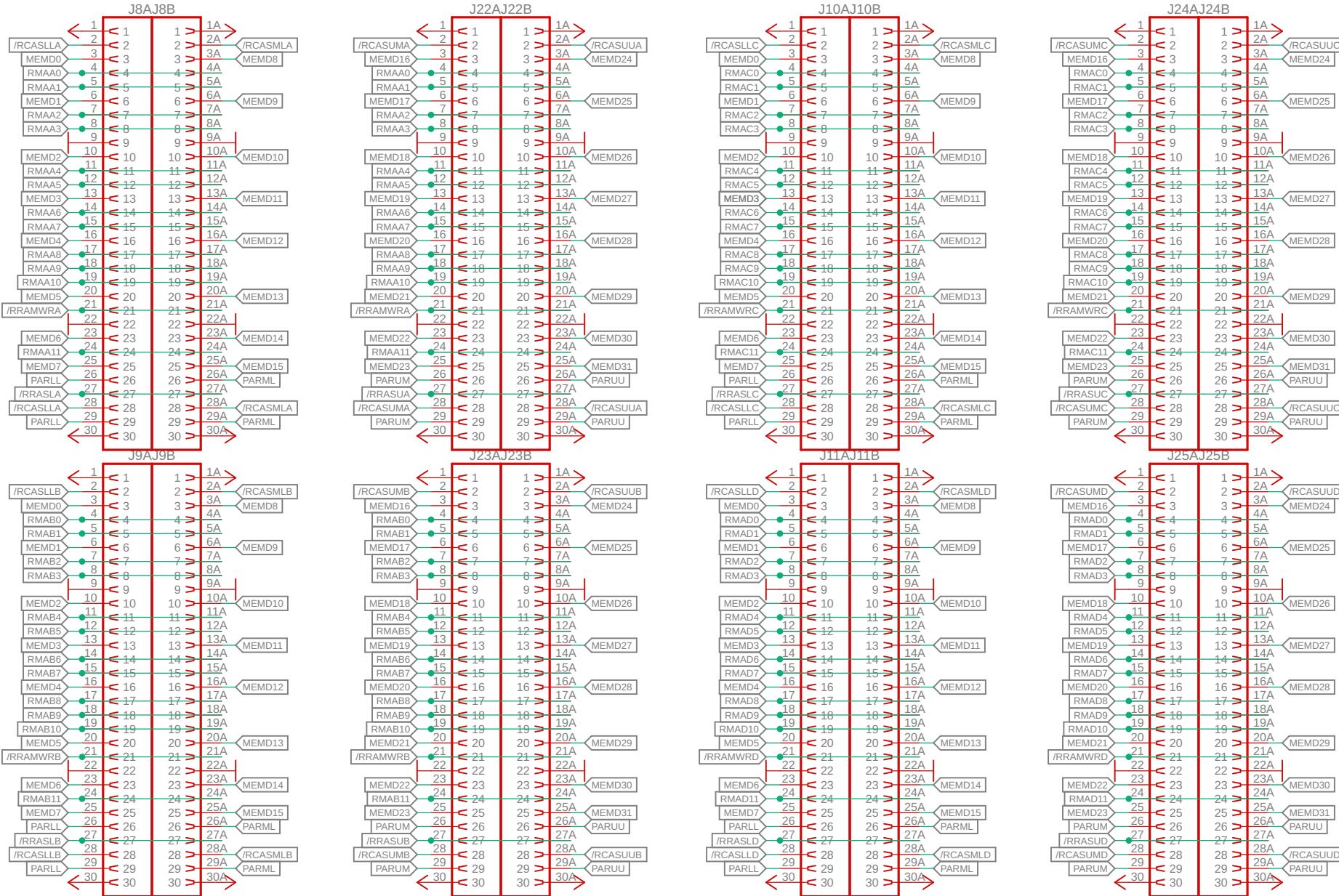




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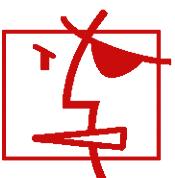
ROM ICs
ROM SIMM
ROM programming PAL

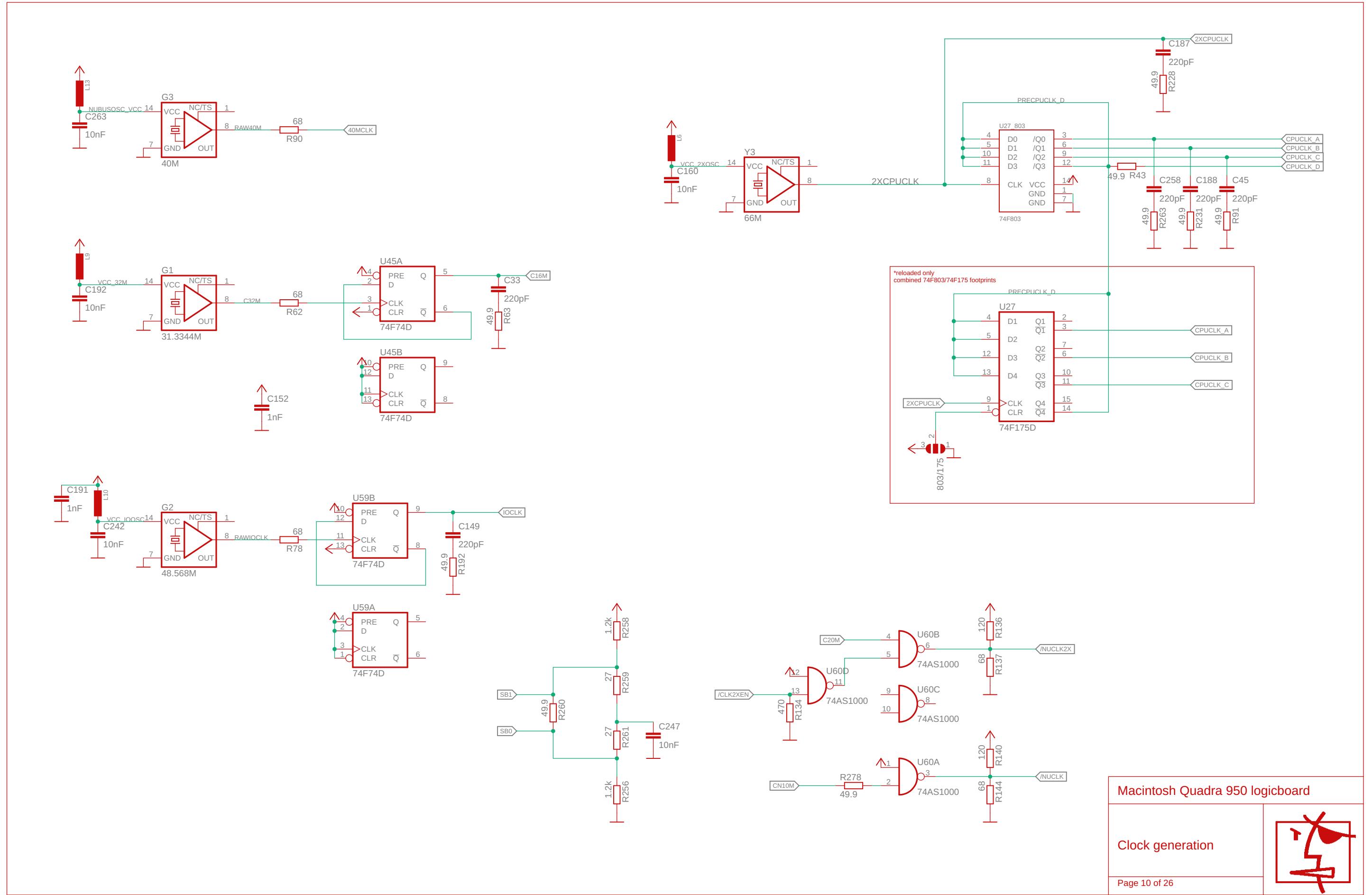


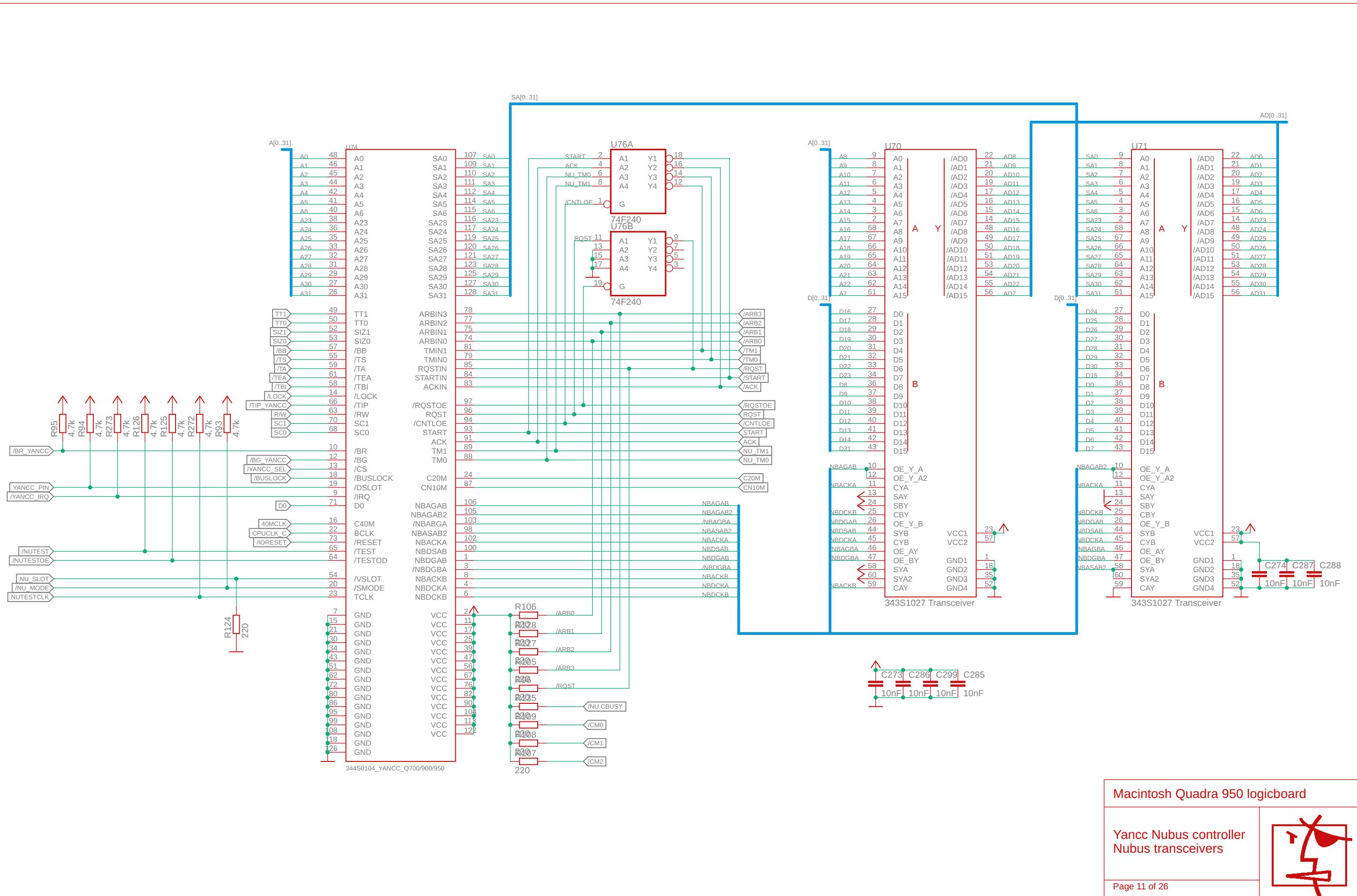


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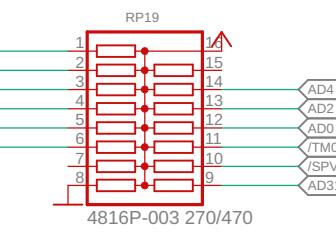
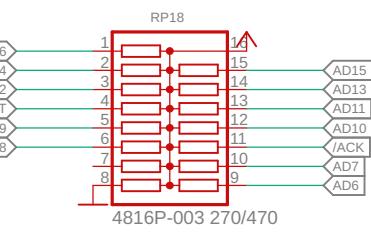
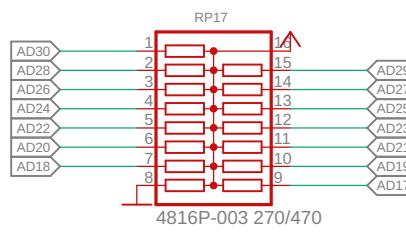
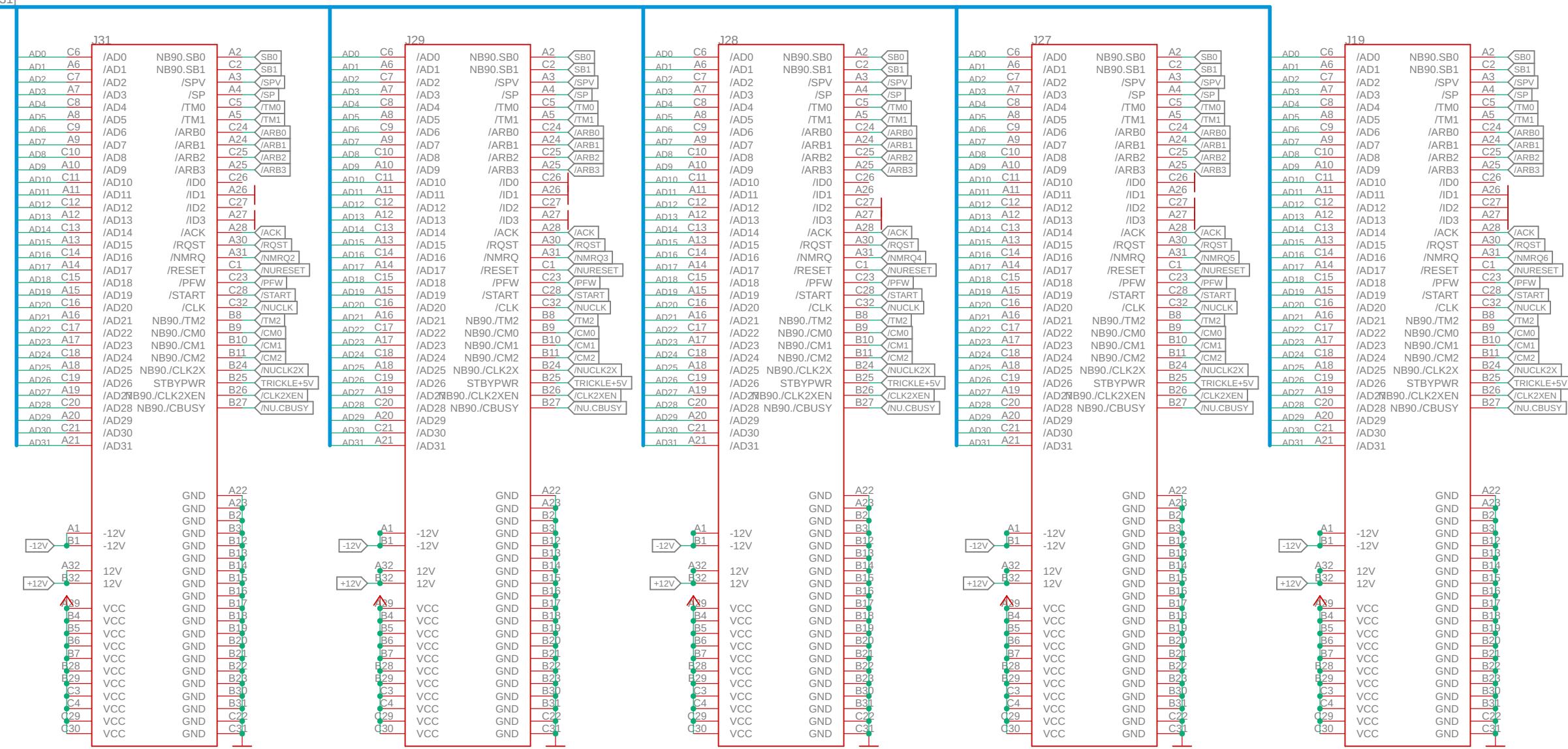
RAM SIMMs





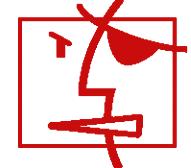


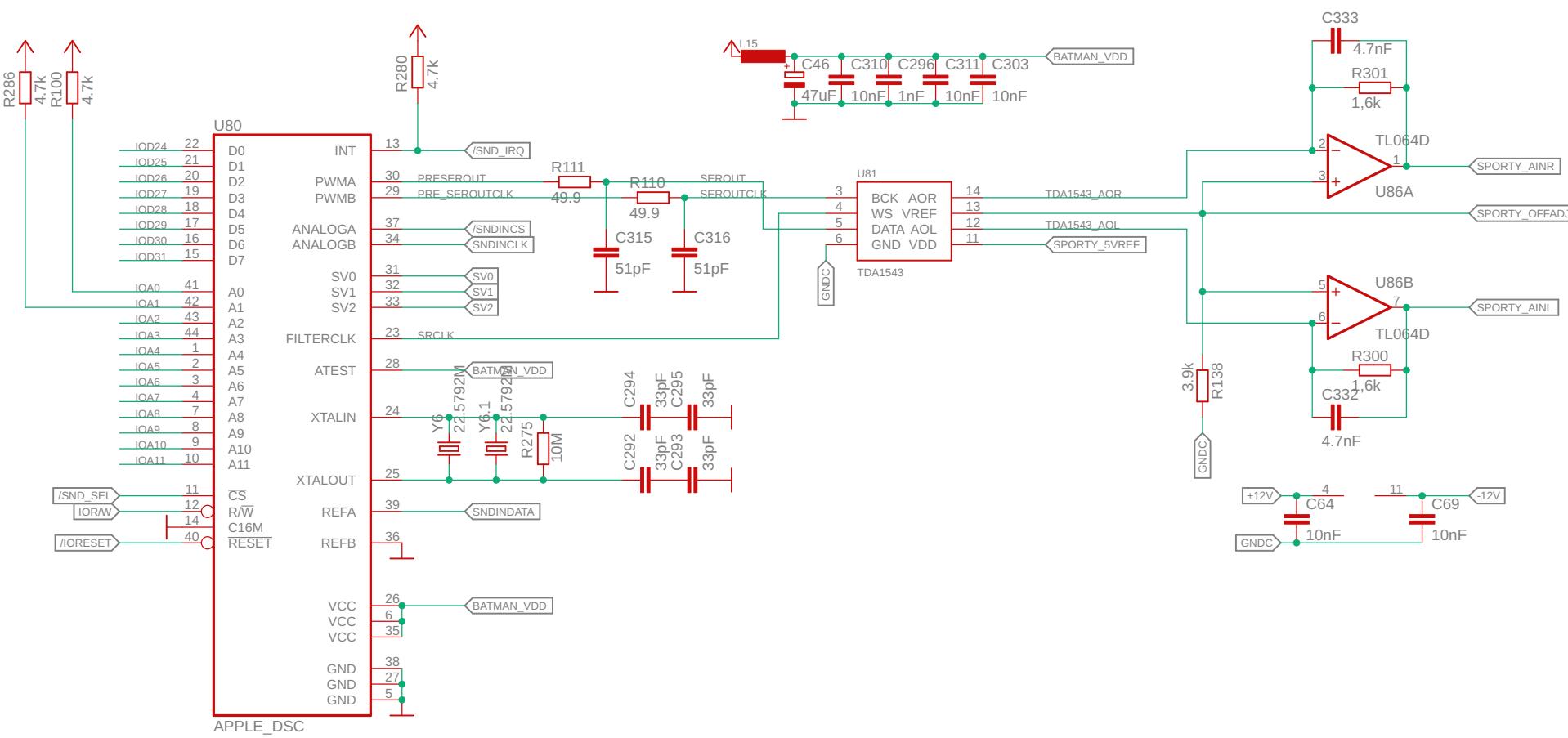
AD[0..31]



Macintosh Quadra 950 logicboard

Nubus slots
Nubus signal termination

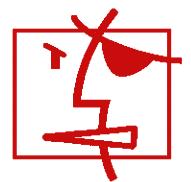


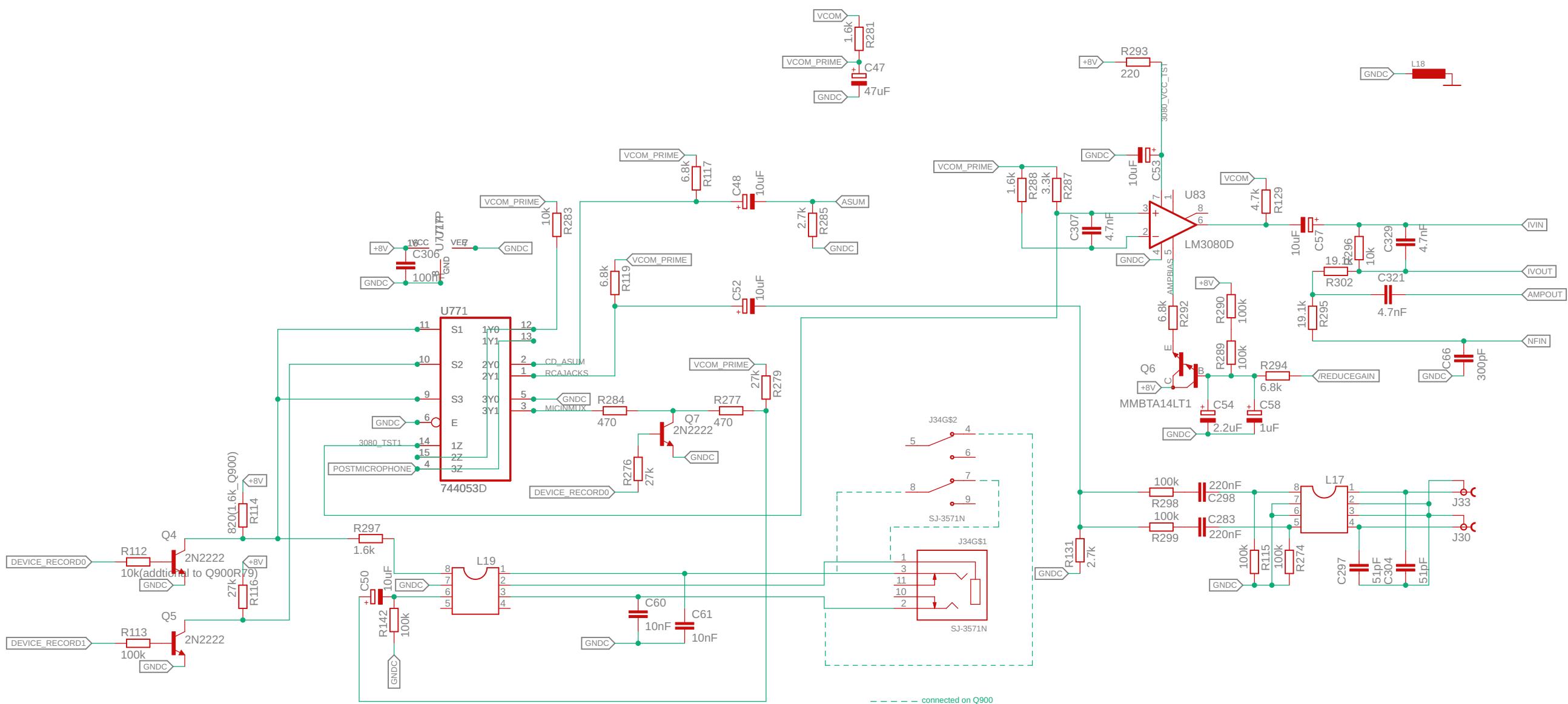


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EASC

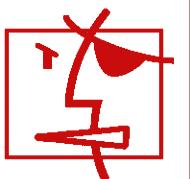
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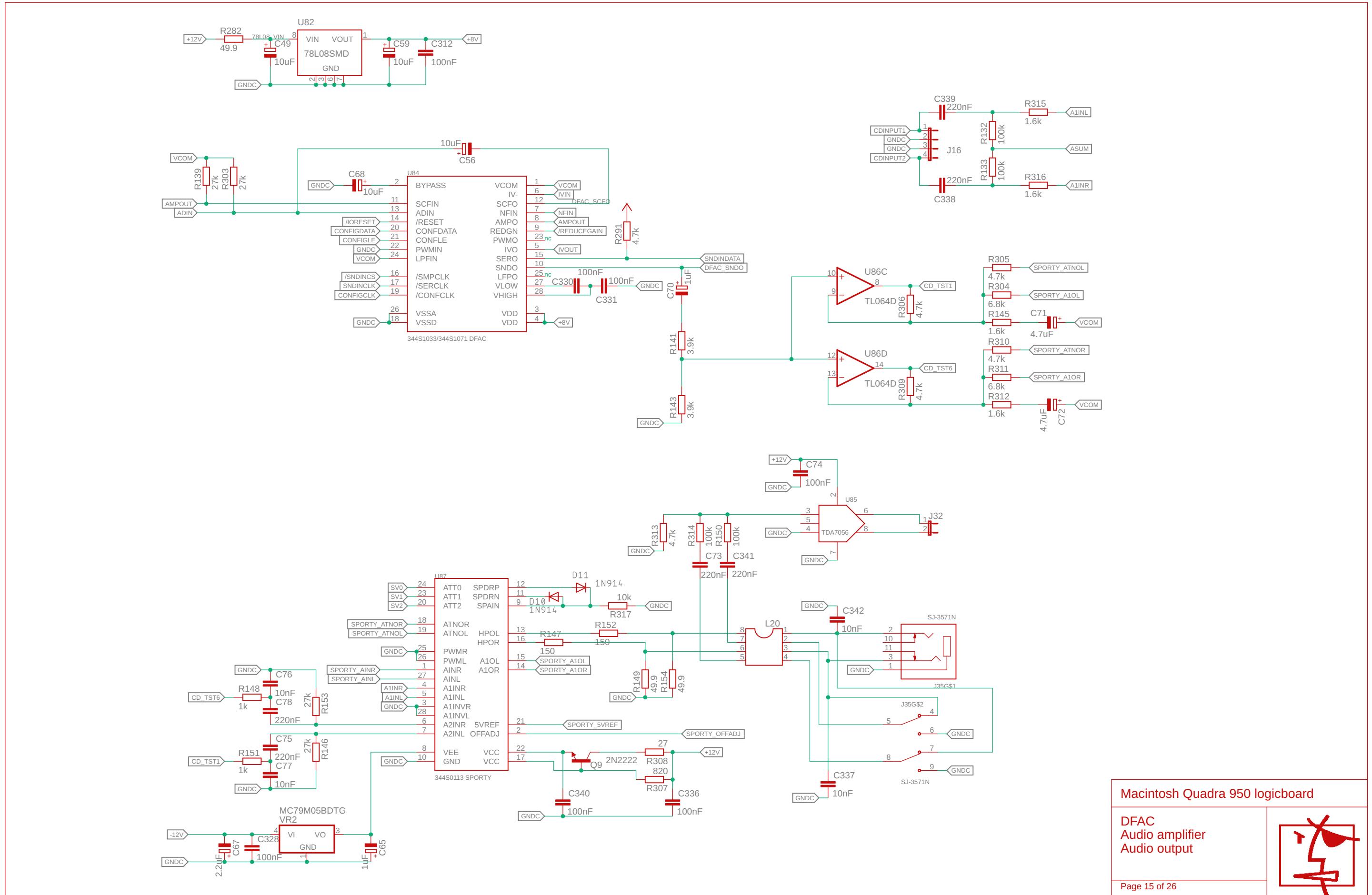


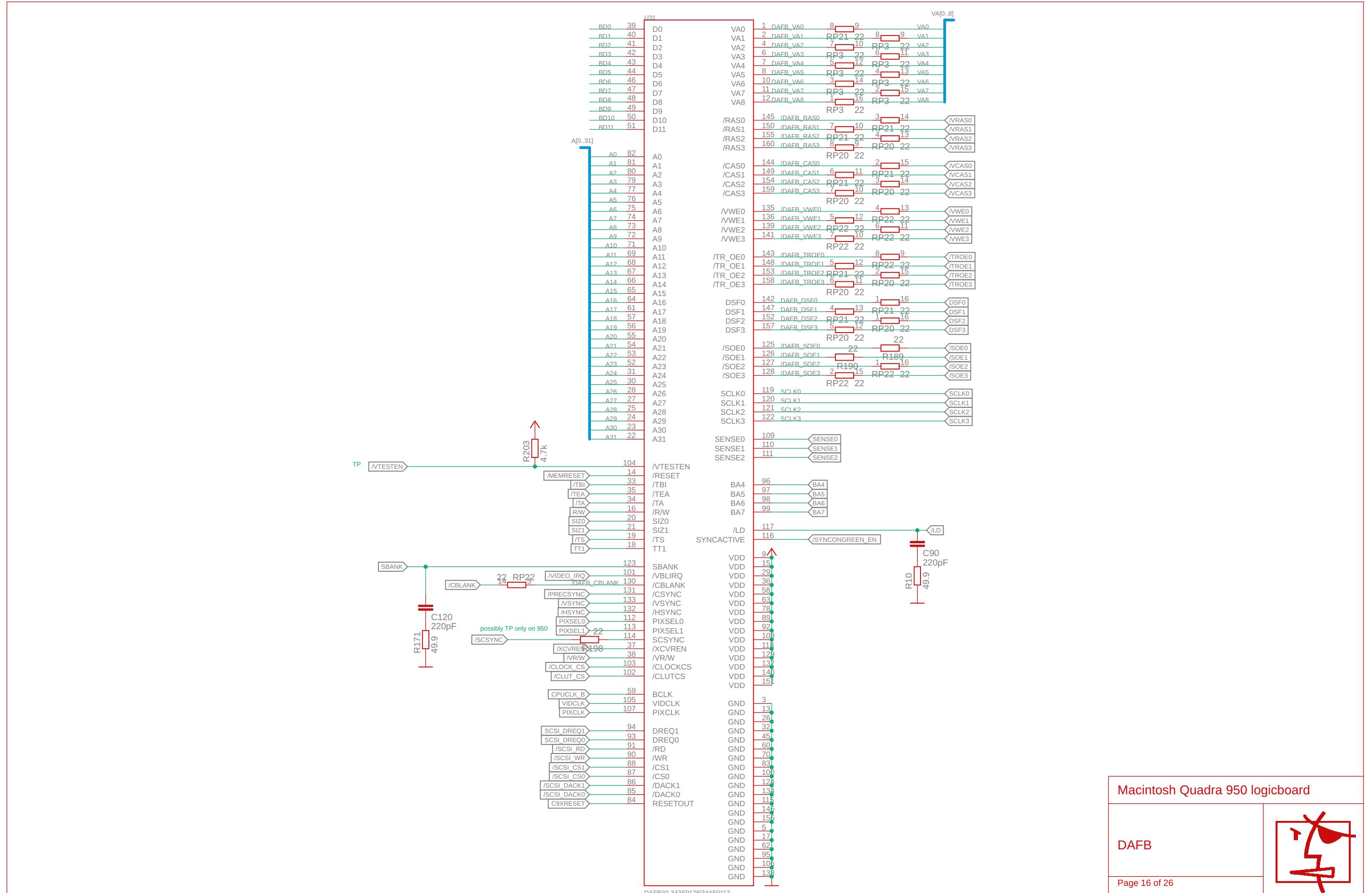


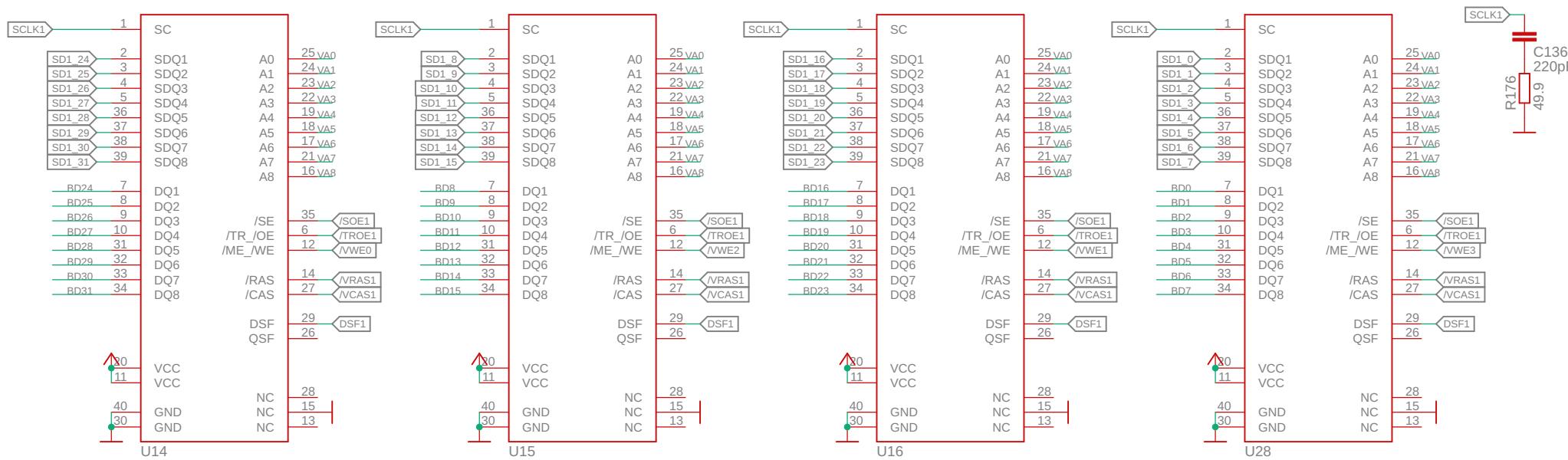
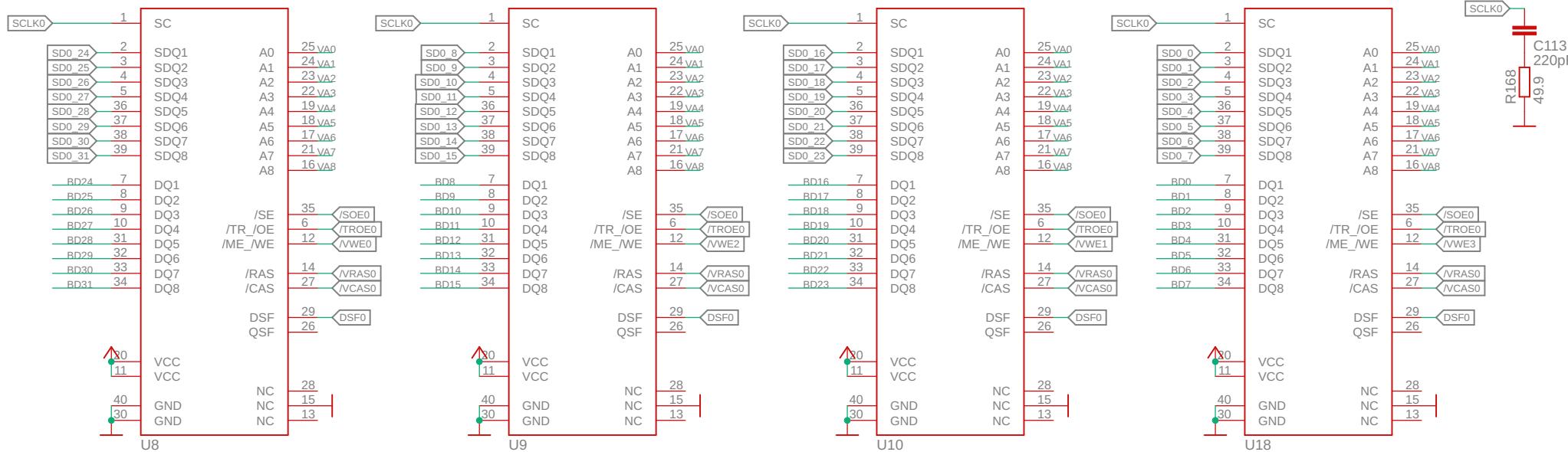
Macintosh Quadra 950 logicboard

Audio input



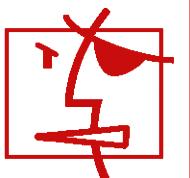


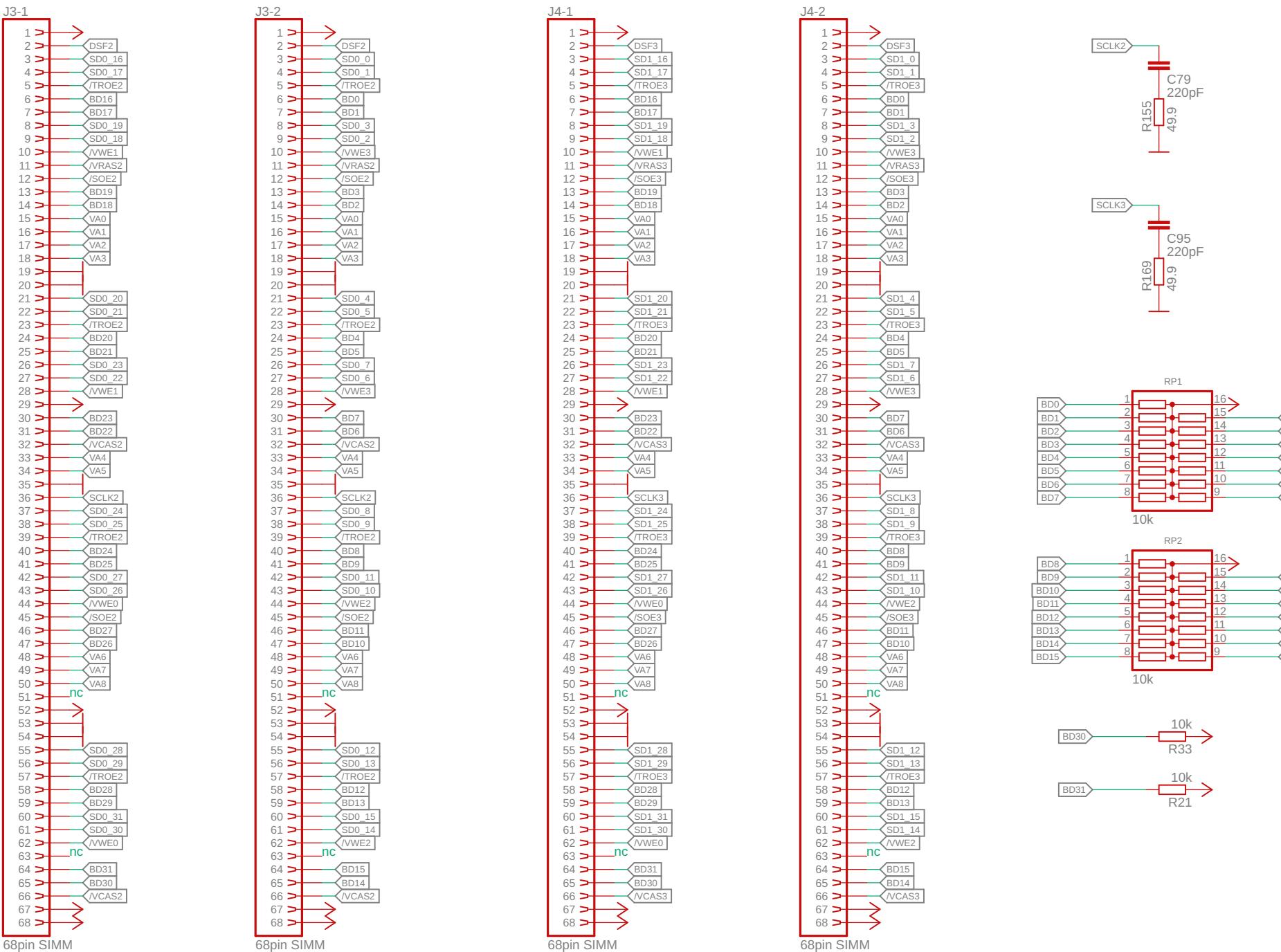




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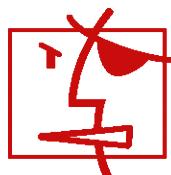
VRAM

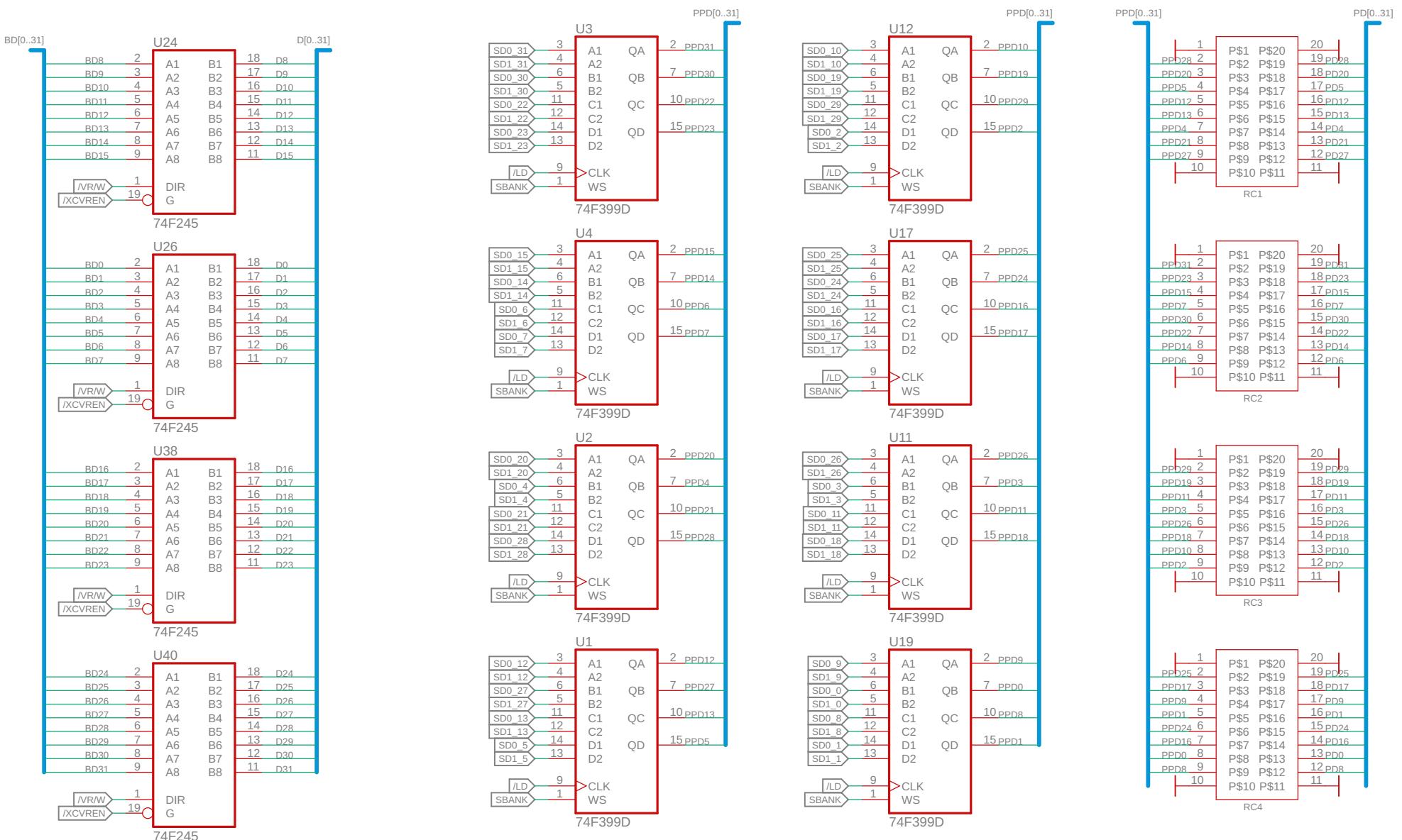




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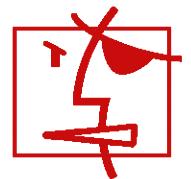
VRAM SIMMs
Video data bus termination

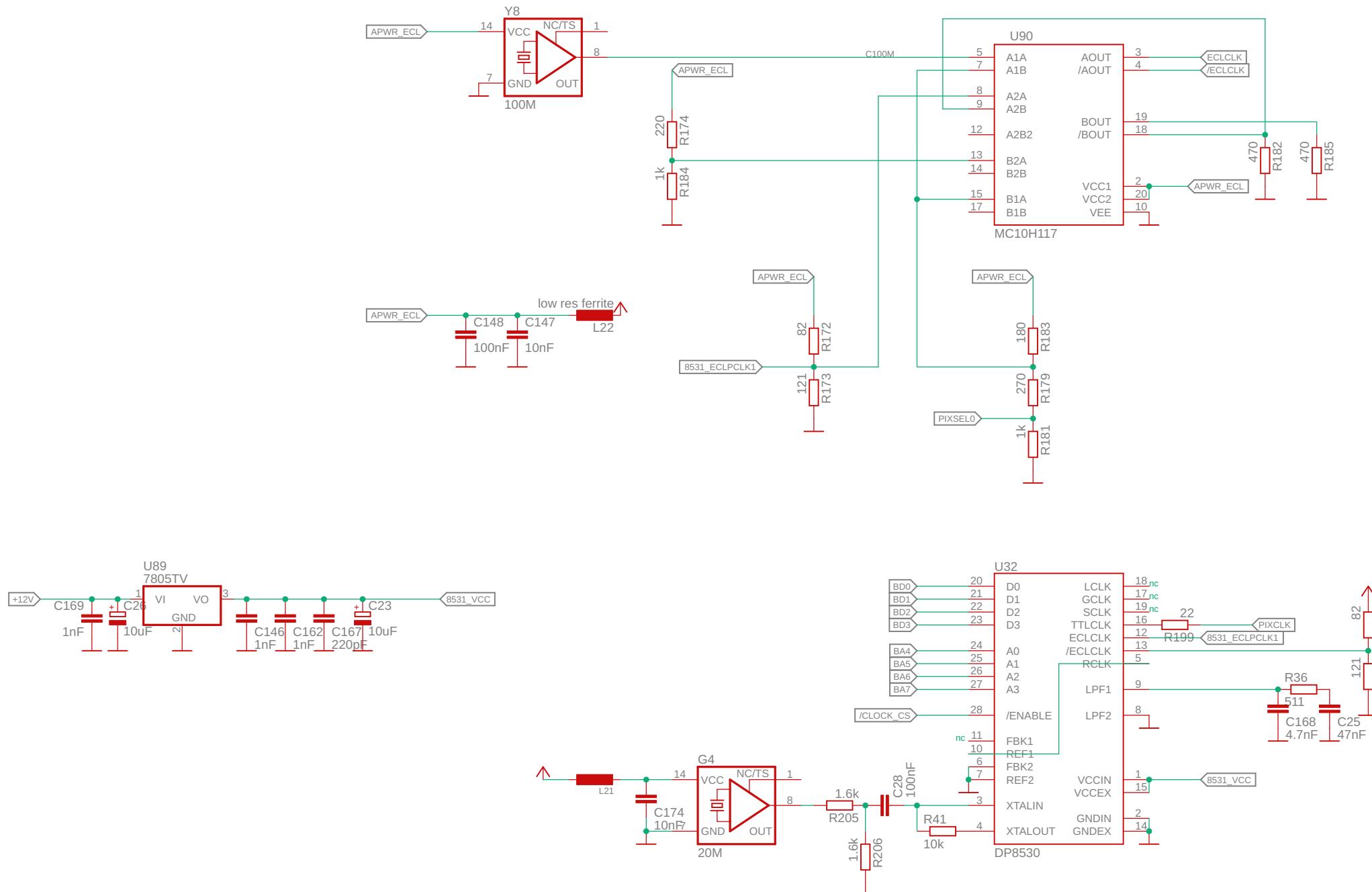




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pixel data muxes

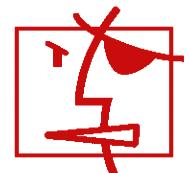


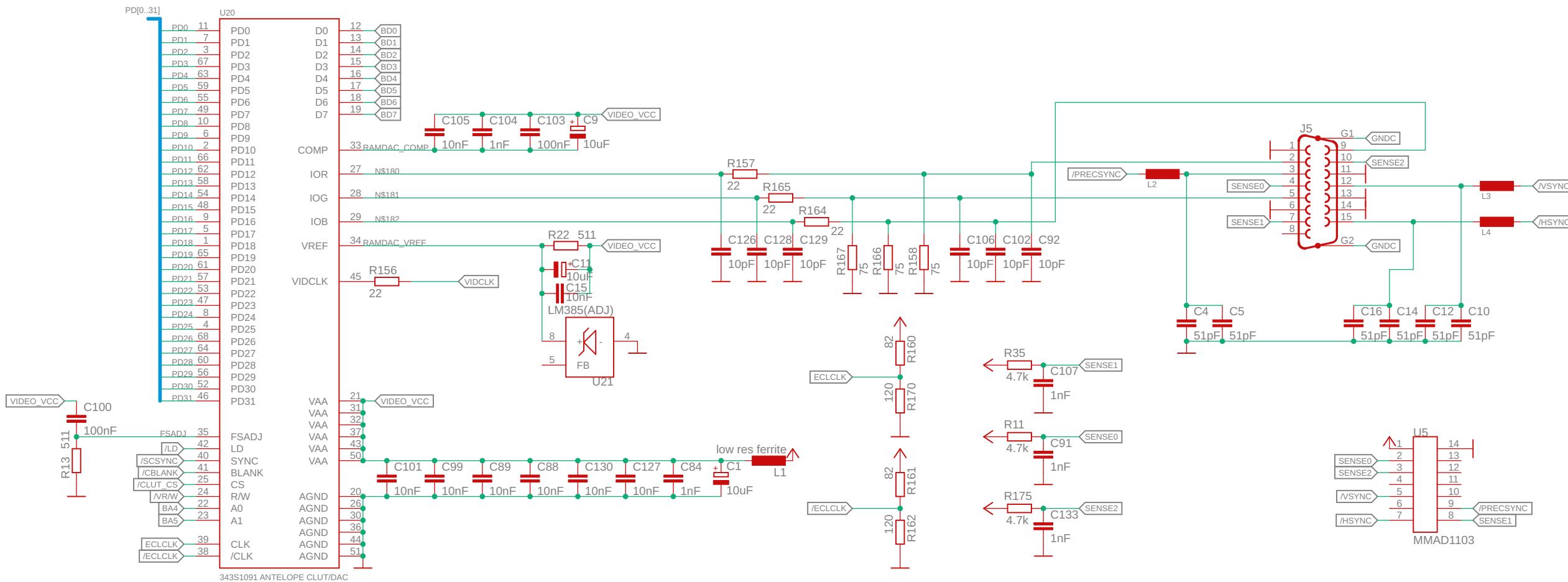


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pixel clock generation

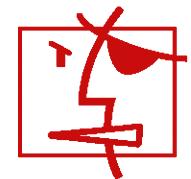
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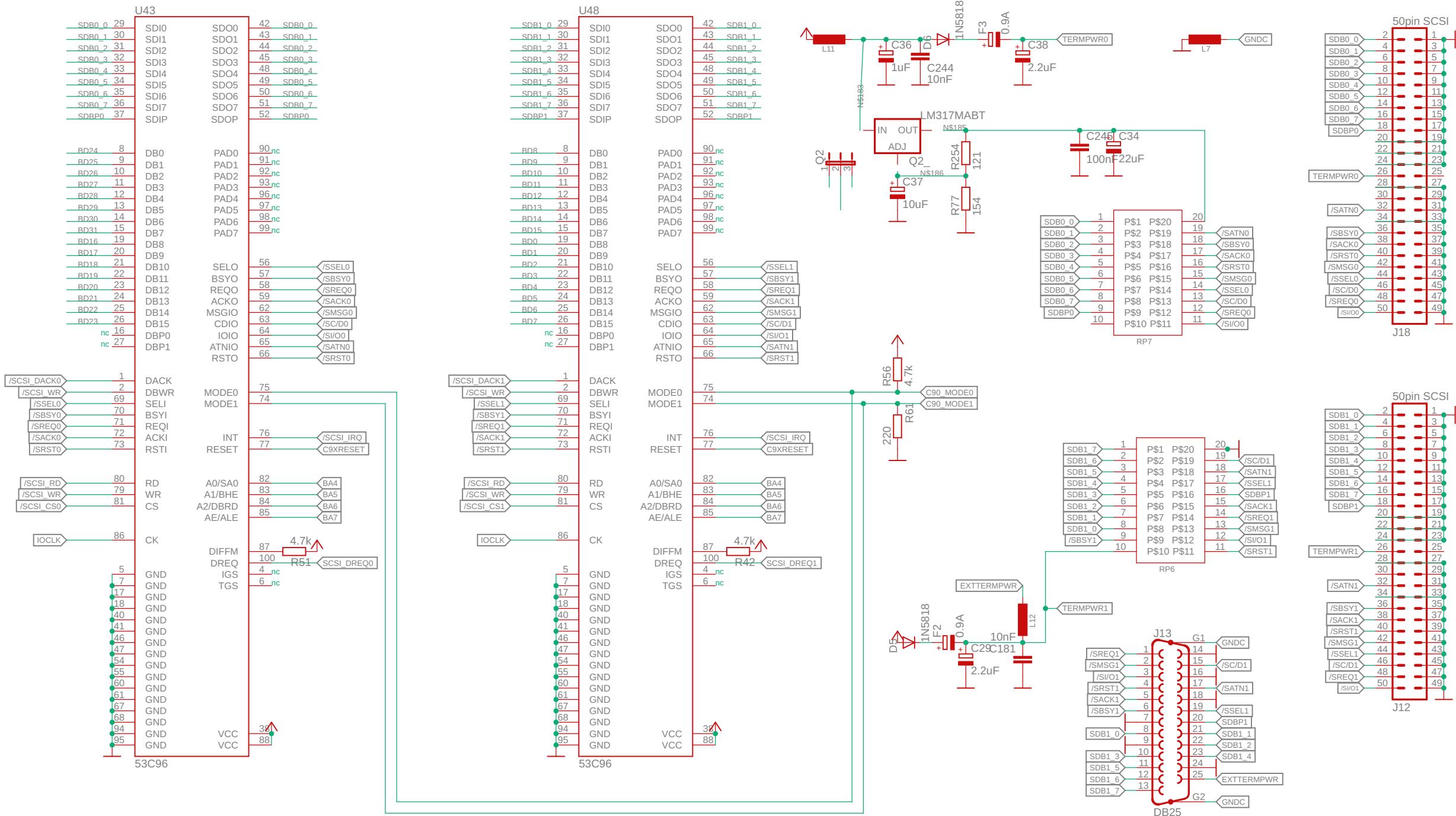




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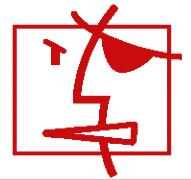
CLUT/DAC
video output

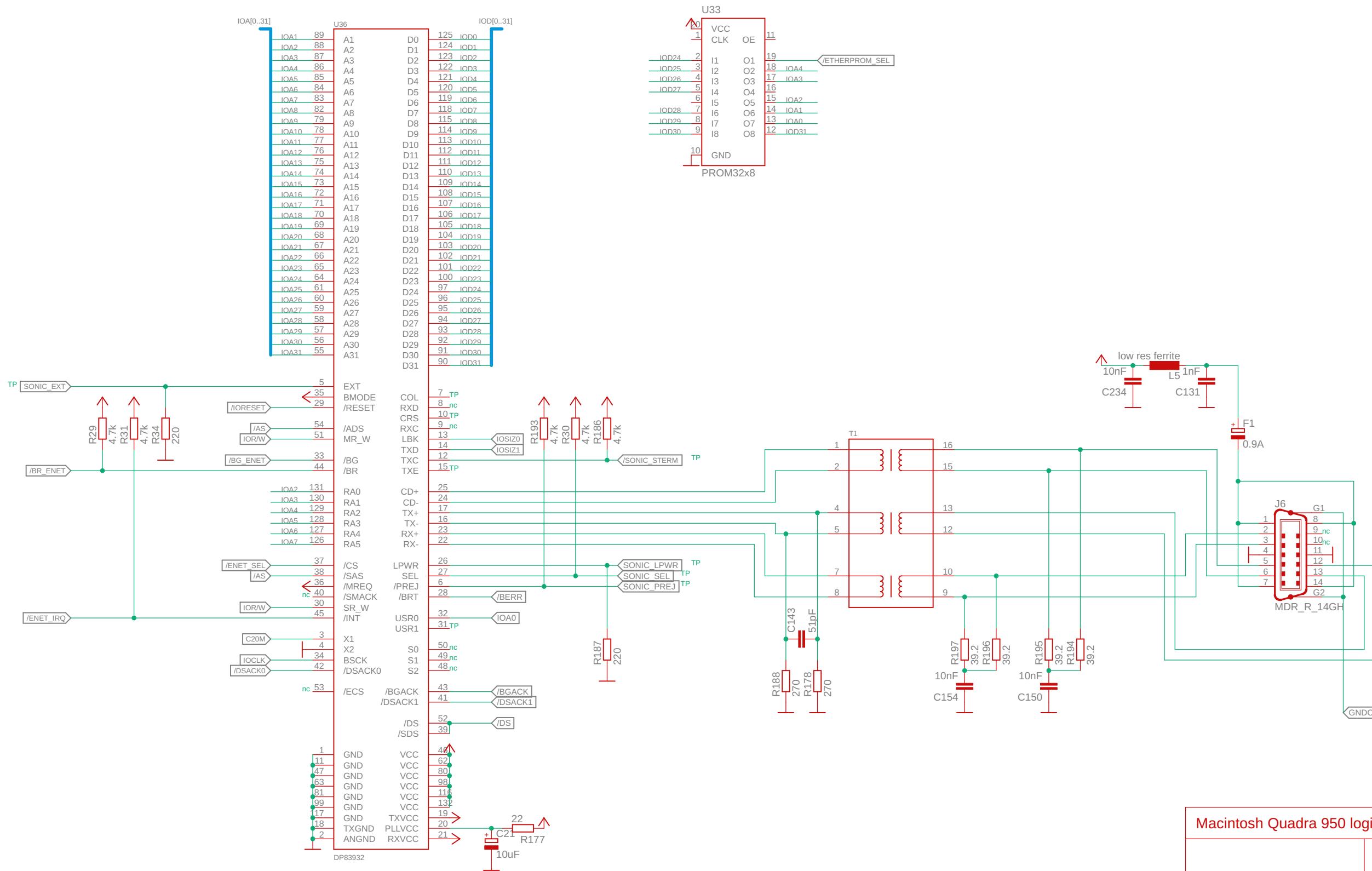




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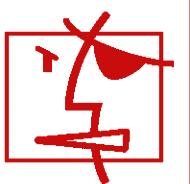
SCSI





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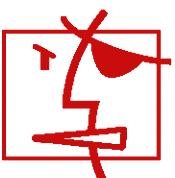
SONIC ethernet

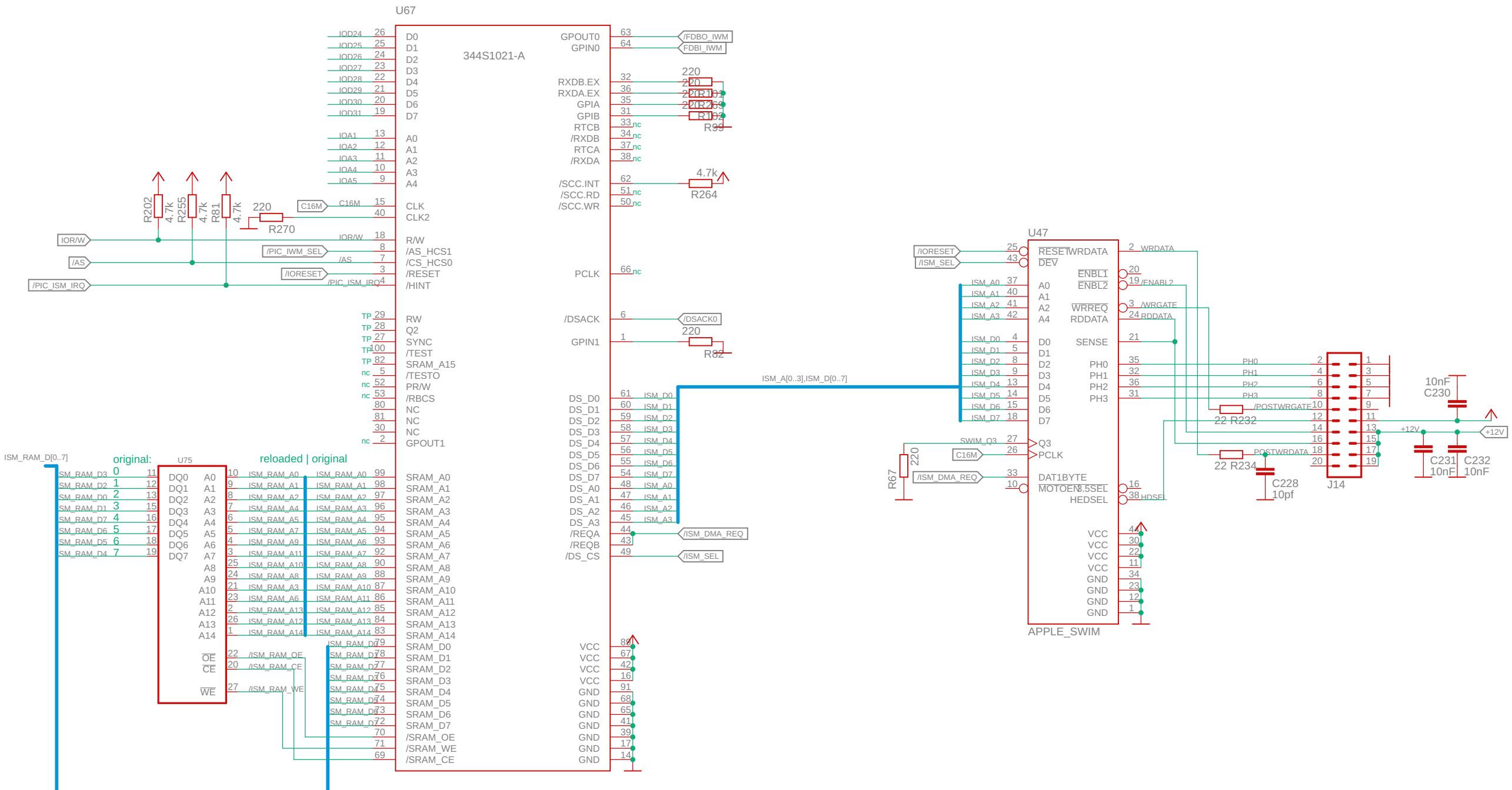




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power connector
-5V regulator
bulk/decoupling caps





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SWIM IOP
SWIM
floppy interface

