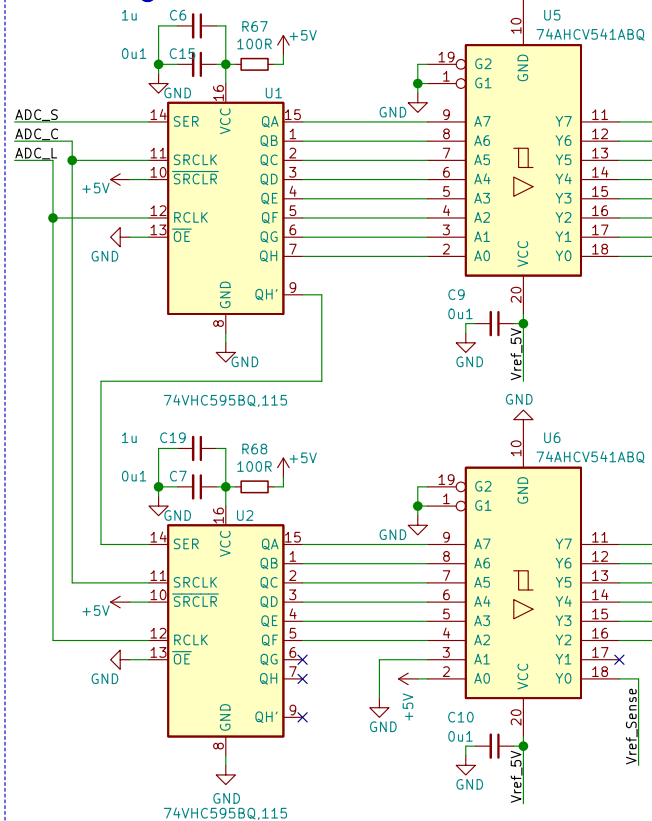
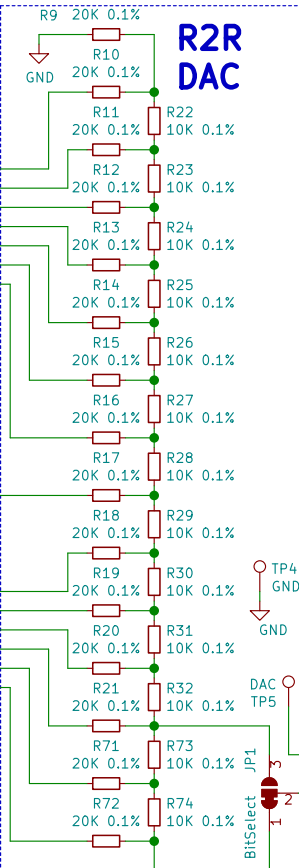


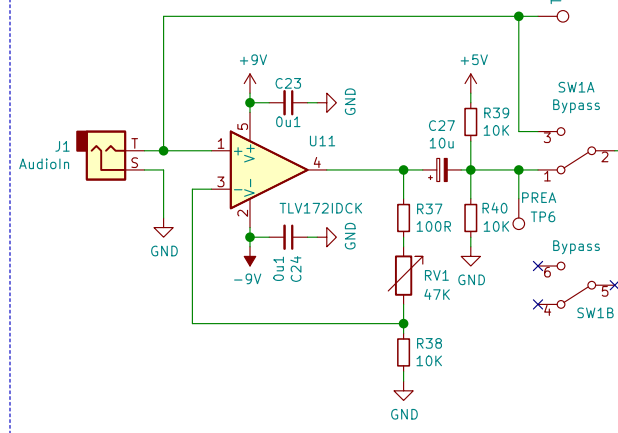
DAC Logic Interface



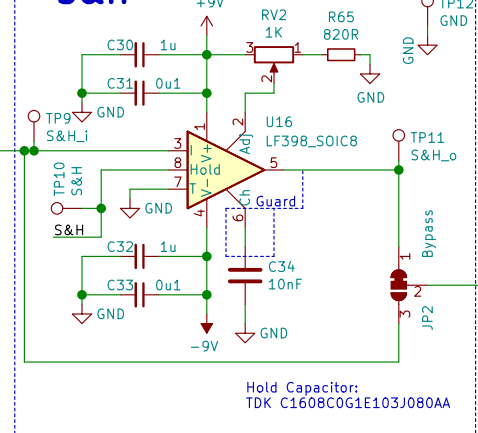
R2R DAC



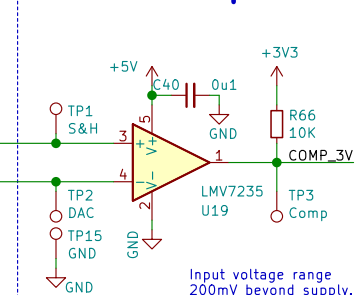
AUDIO PREAMP



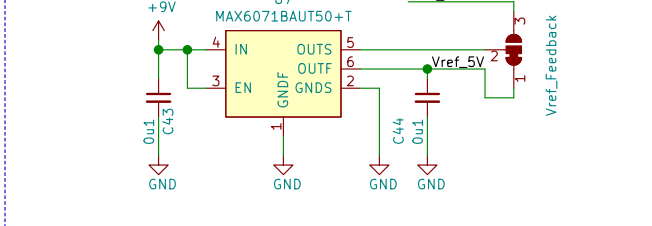
S&H



Comparator

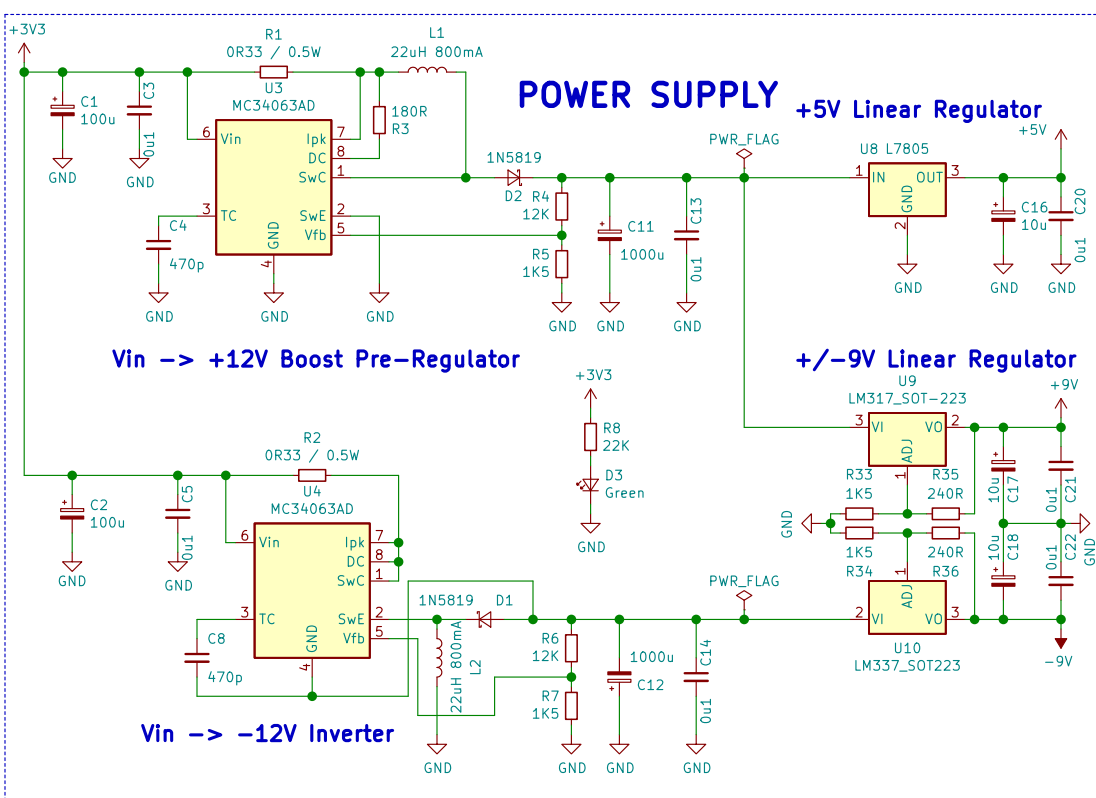


5V Ref

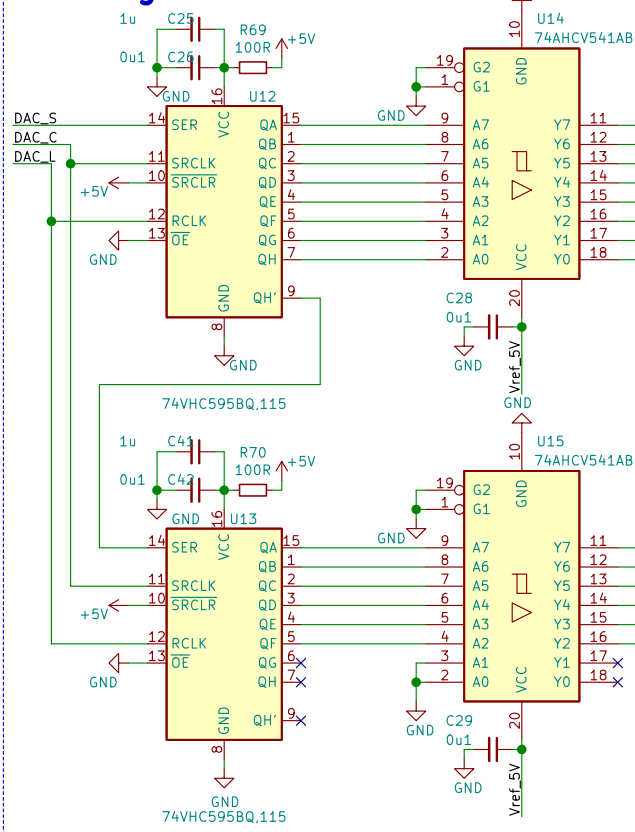


Note [2]: 5V Ref Compensation:
Not sure if good idea to include buffer
in feedback path of 5V reference. Use jumper
to select.

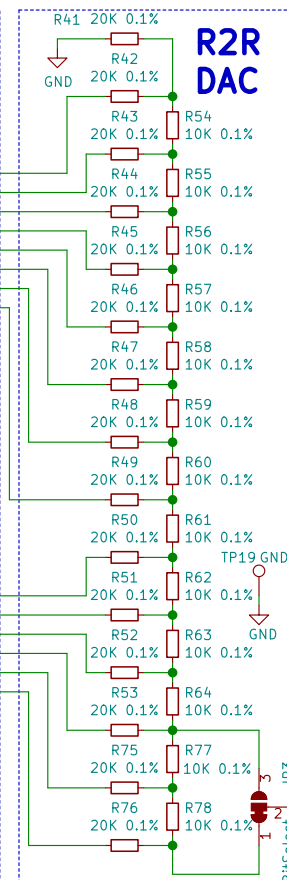
POWER SUPPLY +5V Linear Regulator



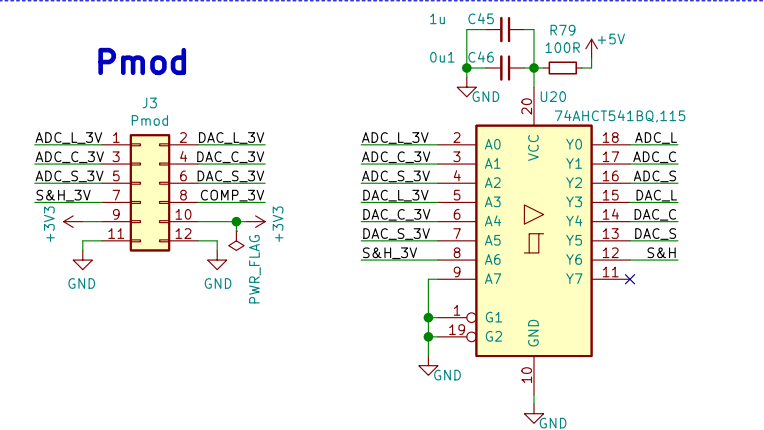
DAC Logic Interface



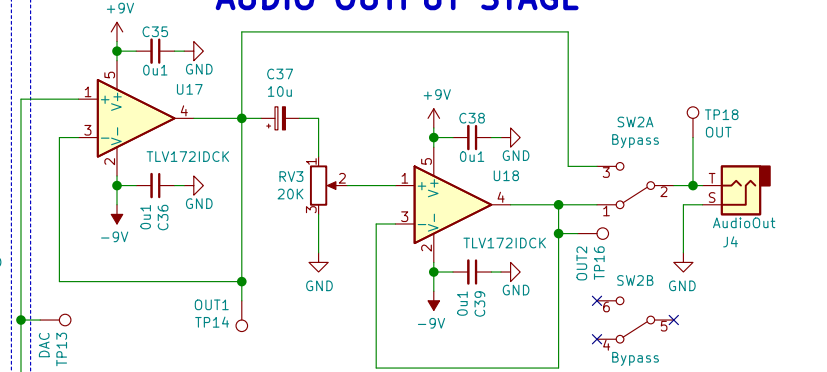
R2R DAC



Pmod



AUDIO OUTPUT STAGE



Philipp Schilk

Sheet: /

File: PmodADC.sch

Title: PmodADC

Size: A3 Date: 2020-10-12

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Rev: v0.1

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