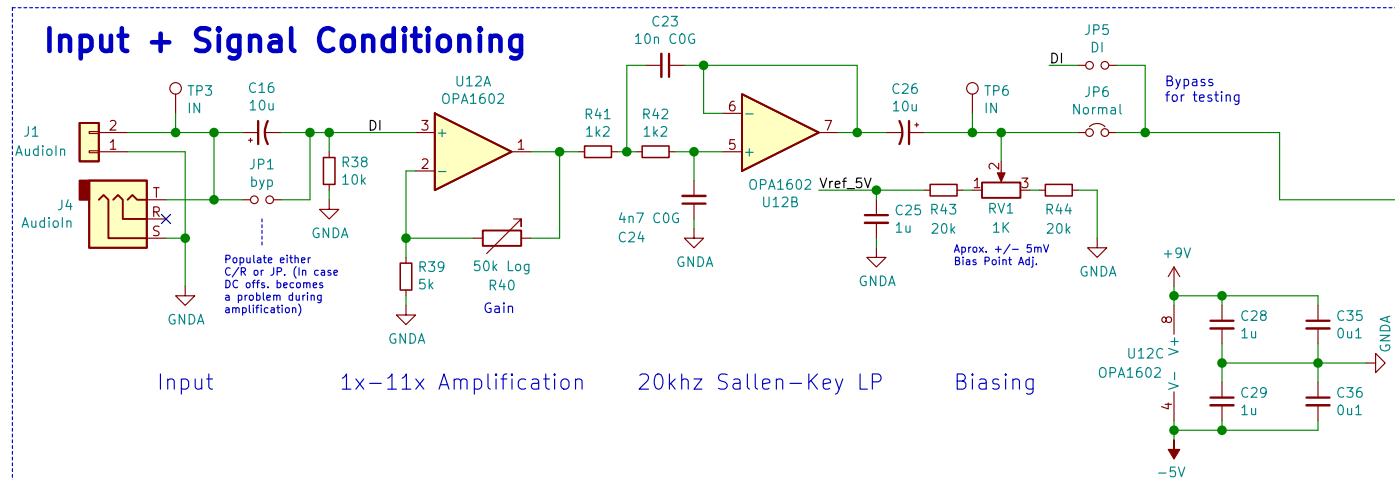
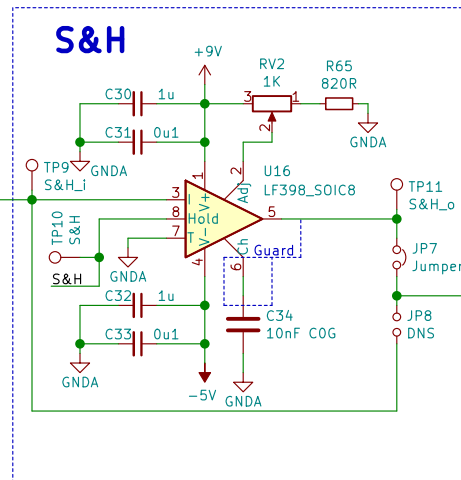


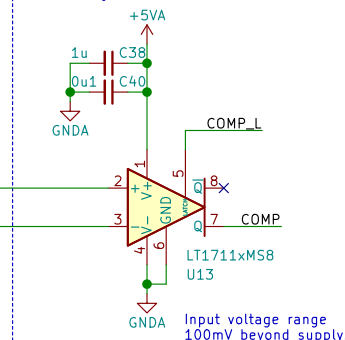
Input + Signal Conditioning



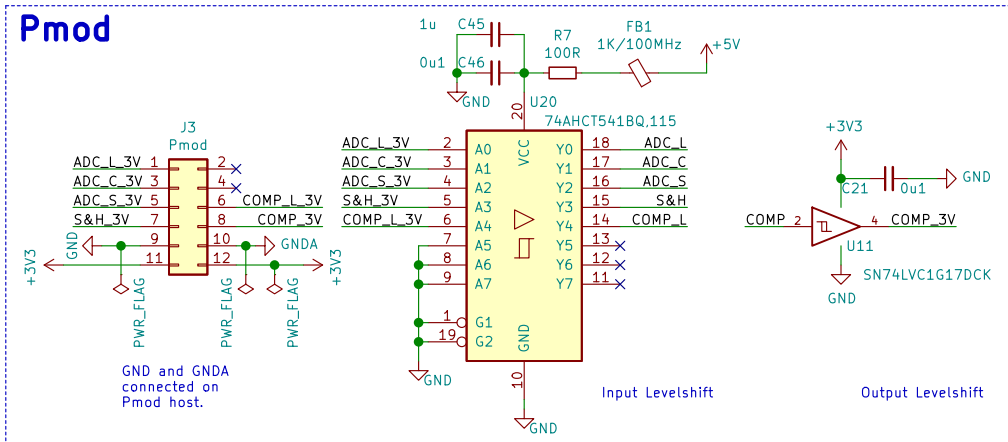
S&H



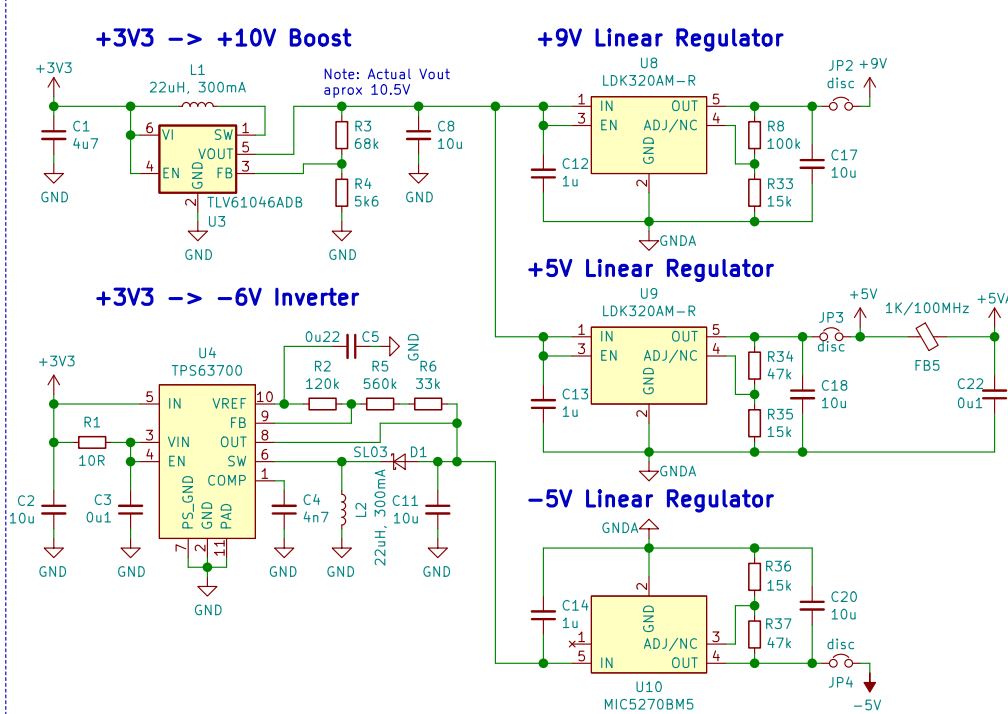
Comparator



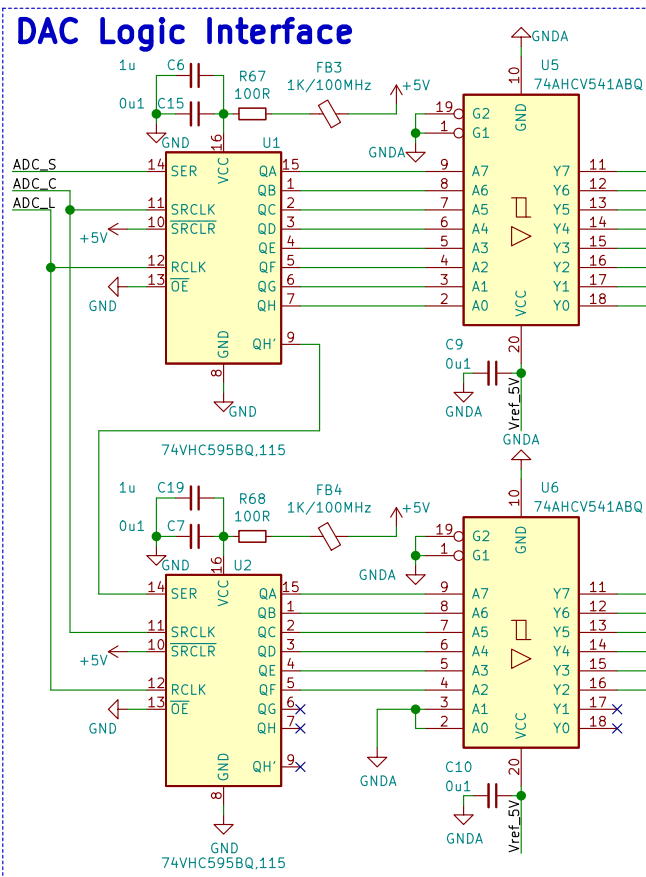
Pmod



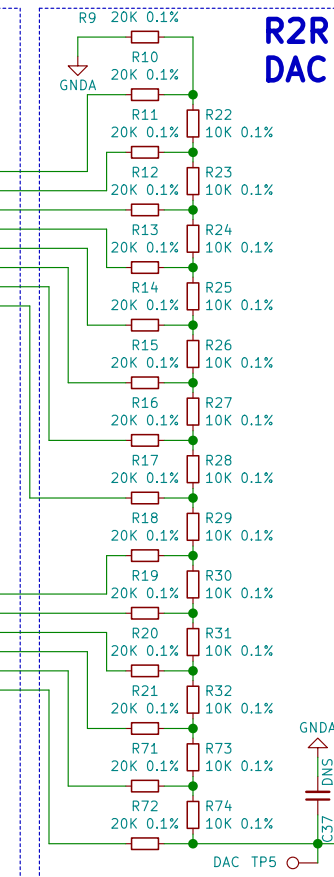
POWER SUPPLY



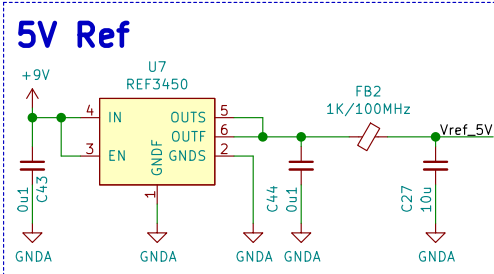
DAC Logic Interface



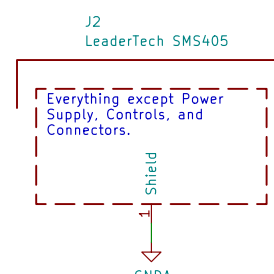
R2R DAC



5V Ref



J2 LeaderTech SMS405



<https://github.com/TheSchilk/PmodADC>

Philipp Schilk

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Rev: v0.2
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