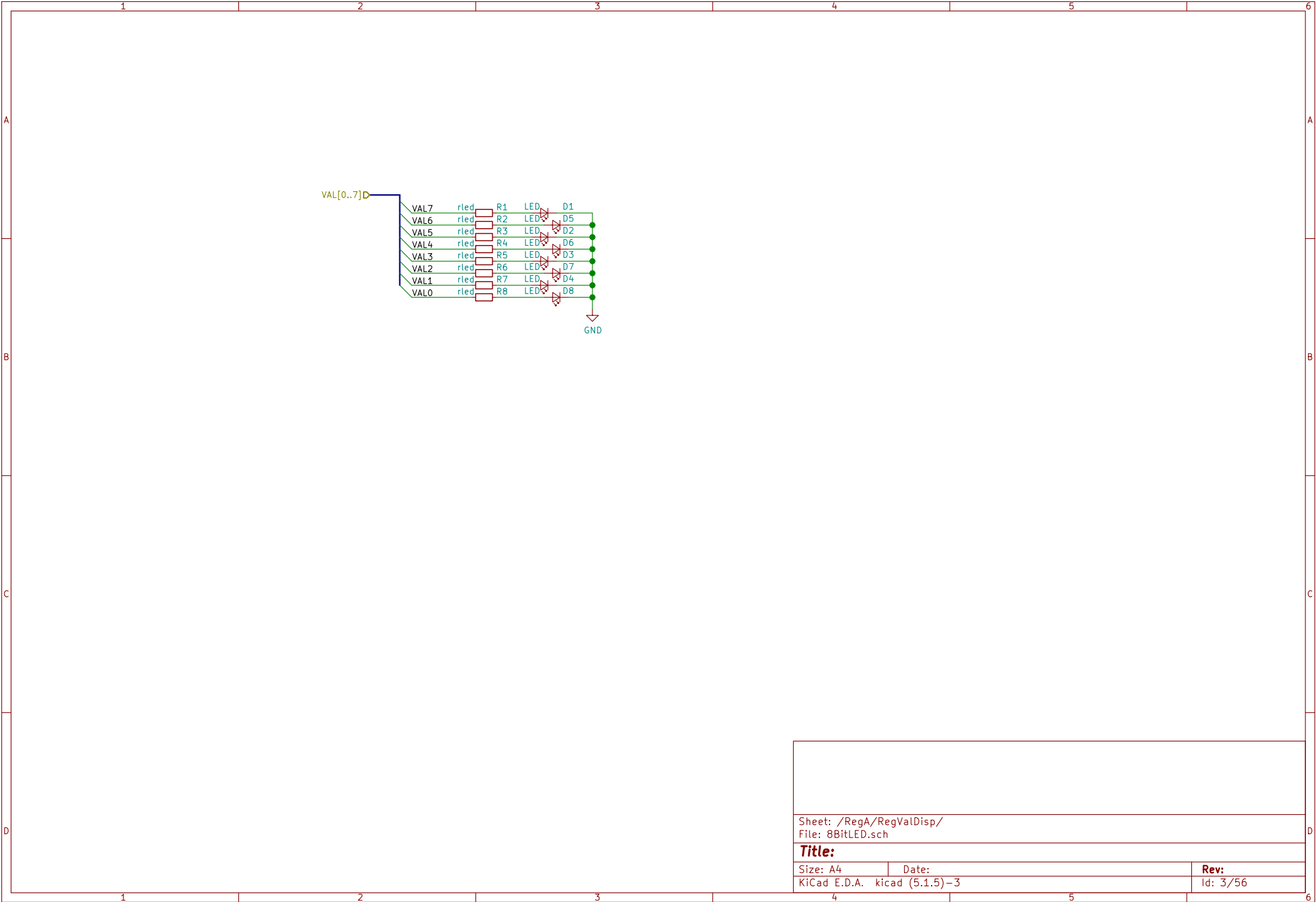
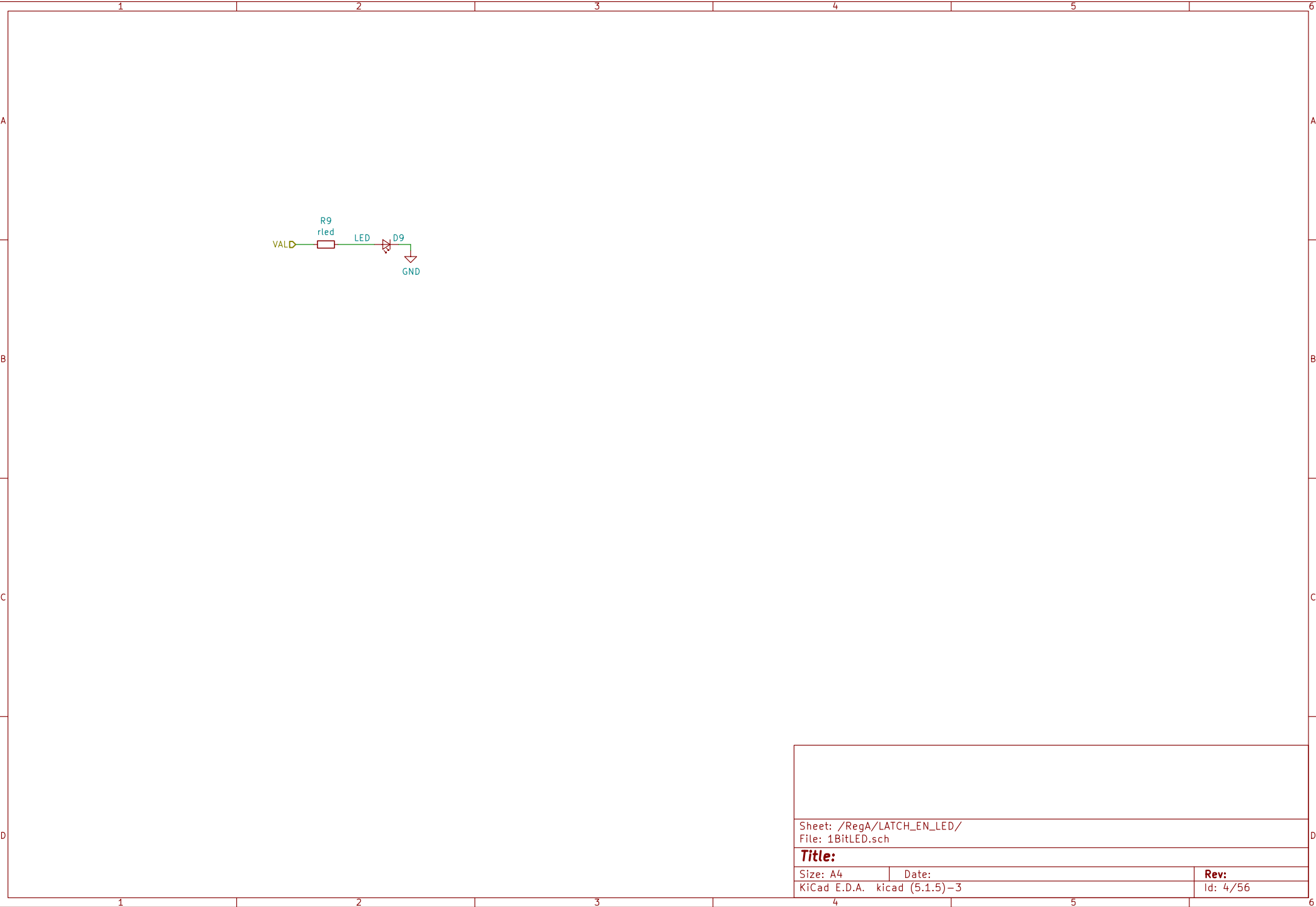


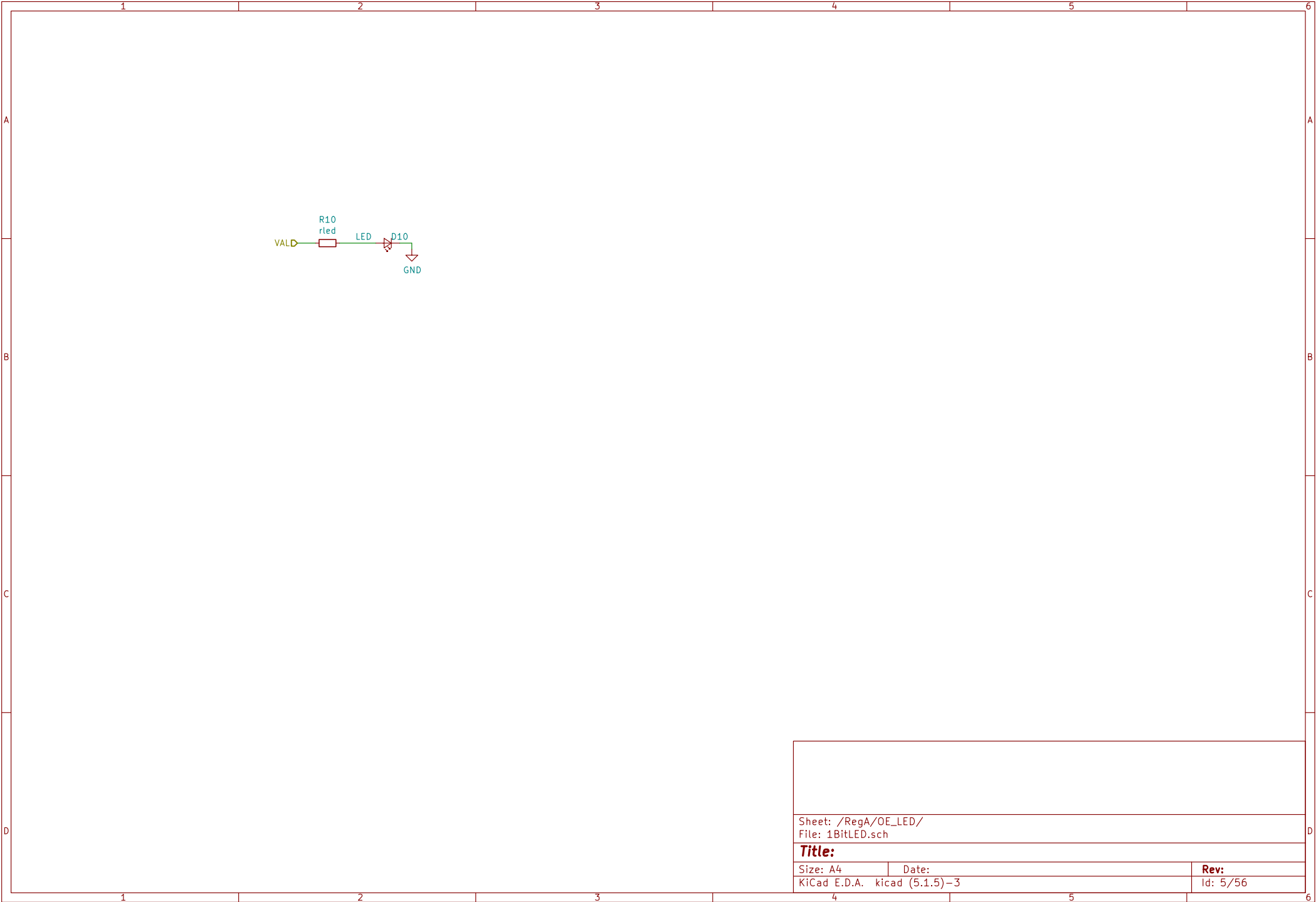
Sheet: /RegA/ File: RegA.sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)-3		Id: 2/56



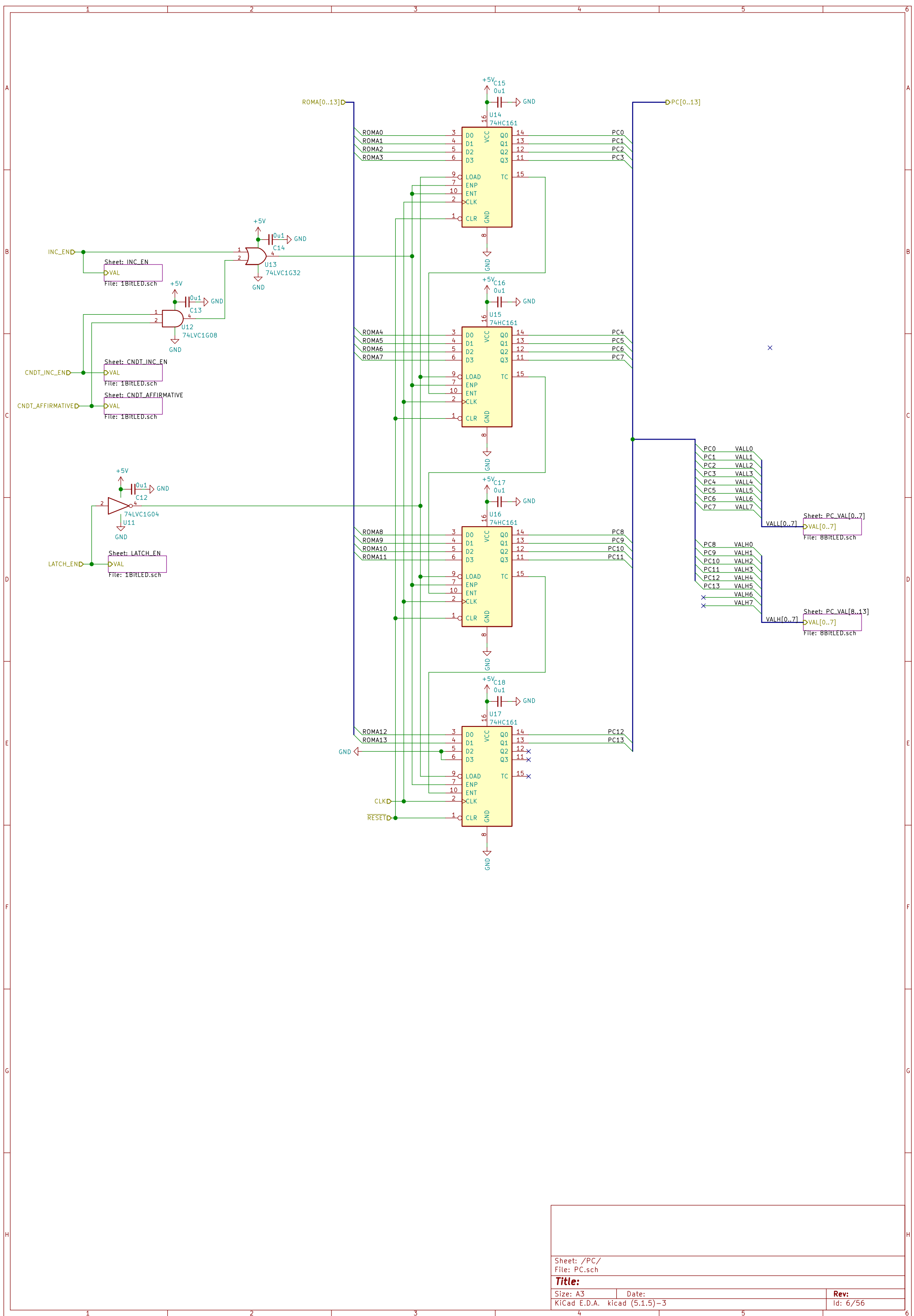
Sheet: /RegA/RegValDisp/ File: 8BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)-3		Id: 3/56

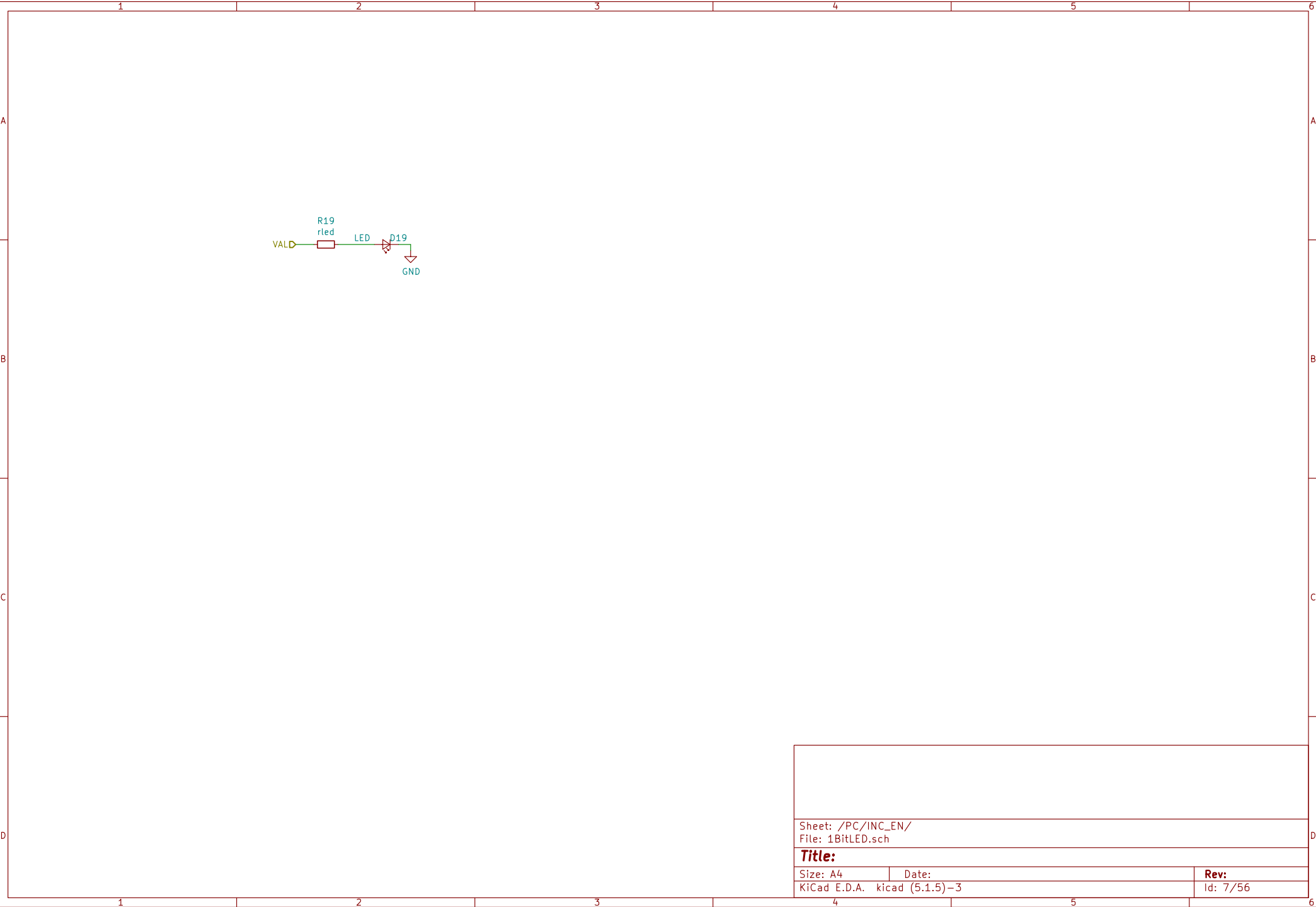


Sheet: /RegA/LATCH_EN_LED/ File: 1BitLED.sch		
<b>Title:</b>		
Size: A4	Date:	<b>Rev:</b>
KiCad E.D.A. kicad (5.1.5)–3		Id: 4/56

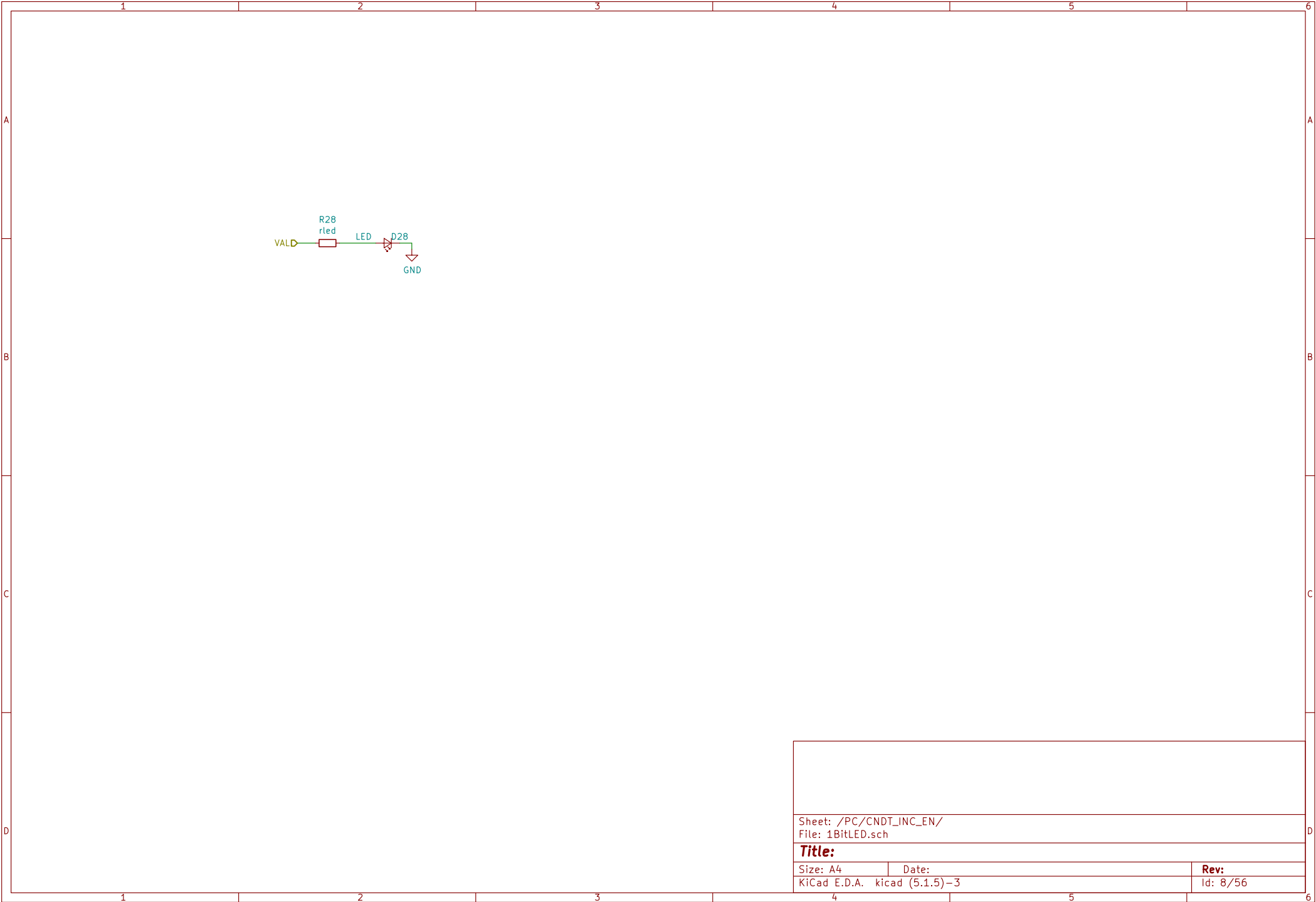


Sheet: /RegA/OE_LED/ File: 1BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)-3	Id: 5/56	



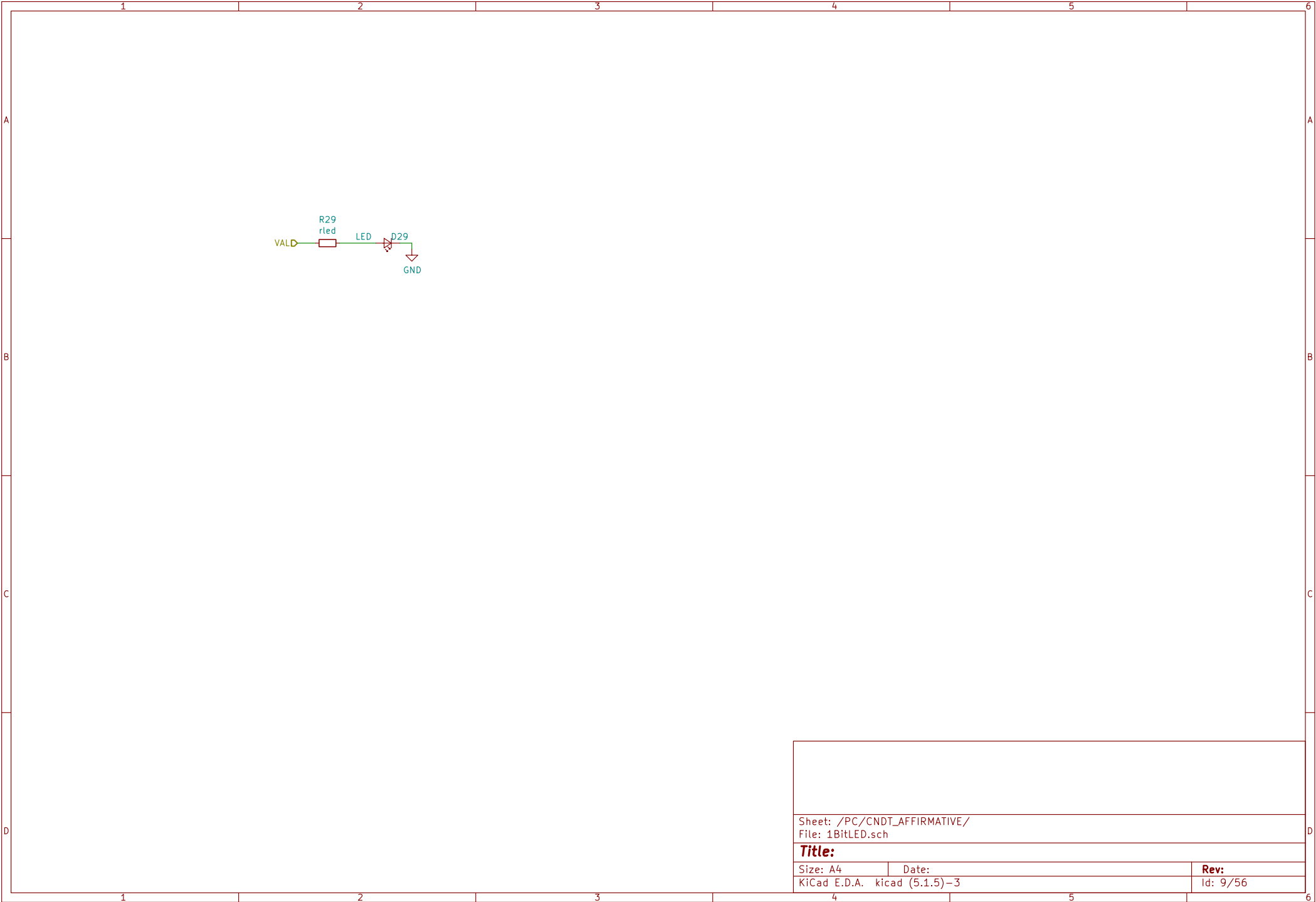


Sheet: /PC/INC_EN/ File: 1BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)–3		Id: 7/56

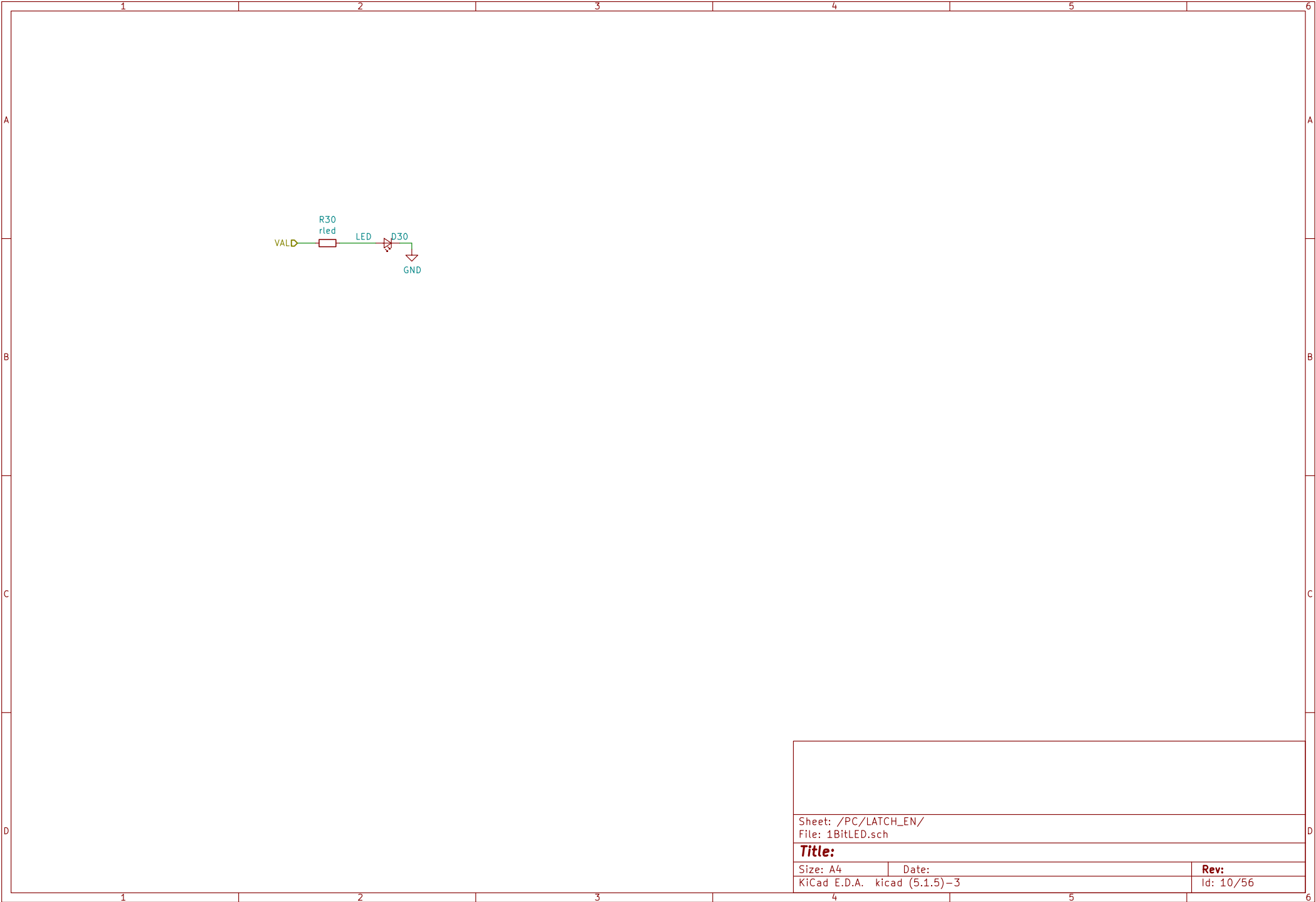


Sheet: /PC/CNDT_INC_EN/ File: 1BitLED.sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)-3		Id: 8/56

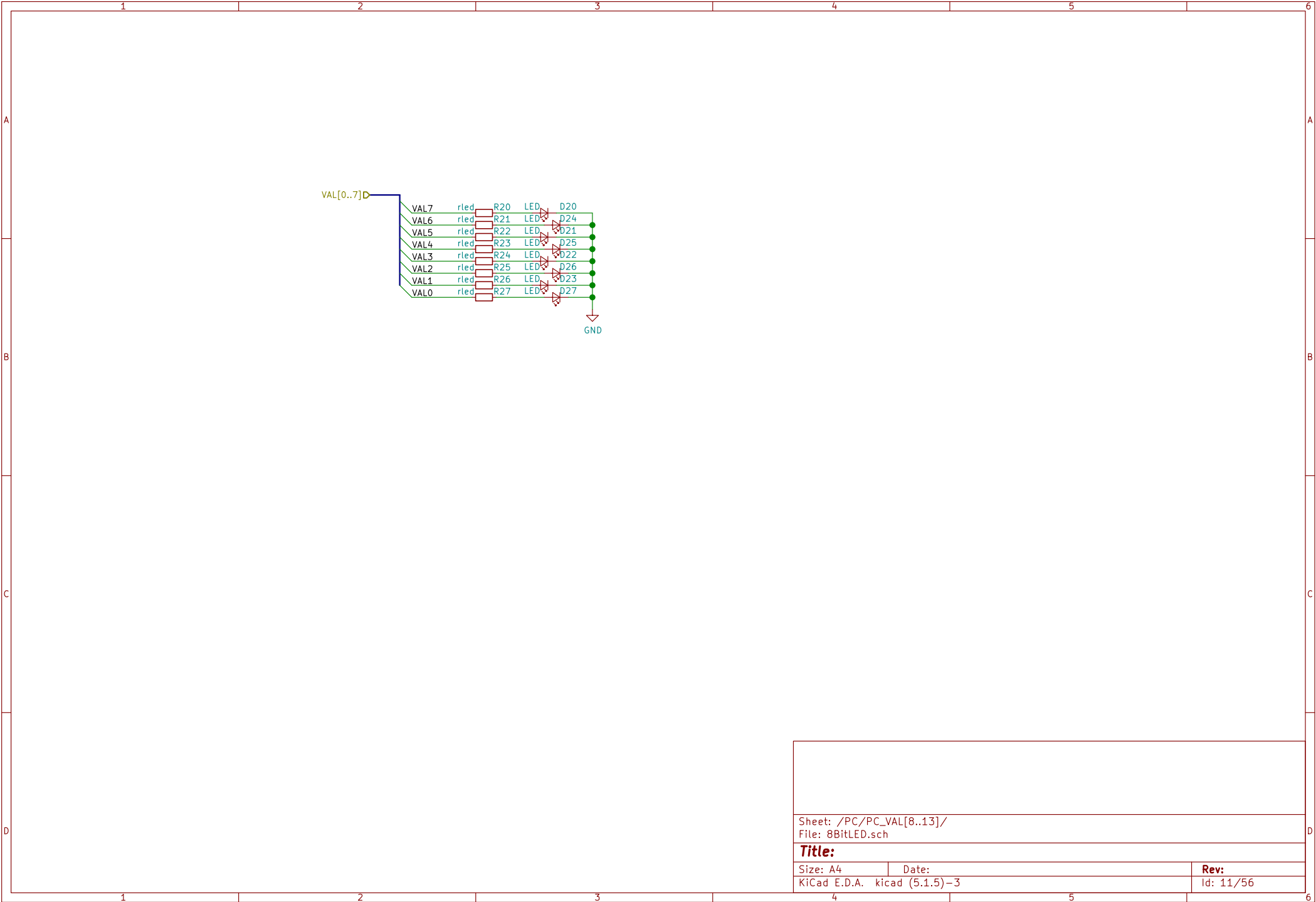




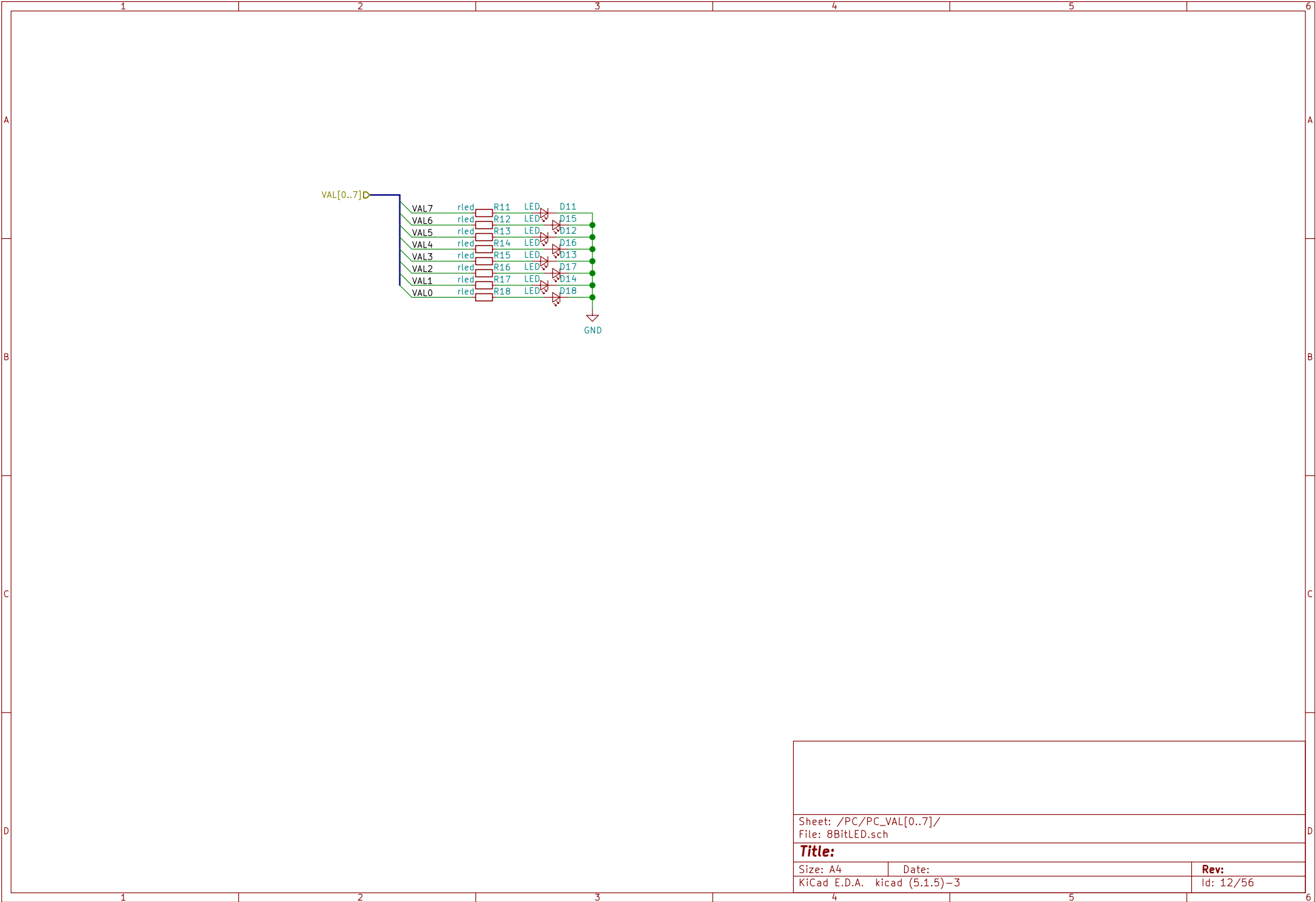
Sheet: /PC/CNDT_AFFIRMATIVE/		
File: 1BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)-3		Id: 9/56

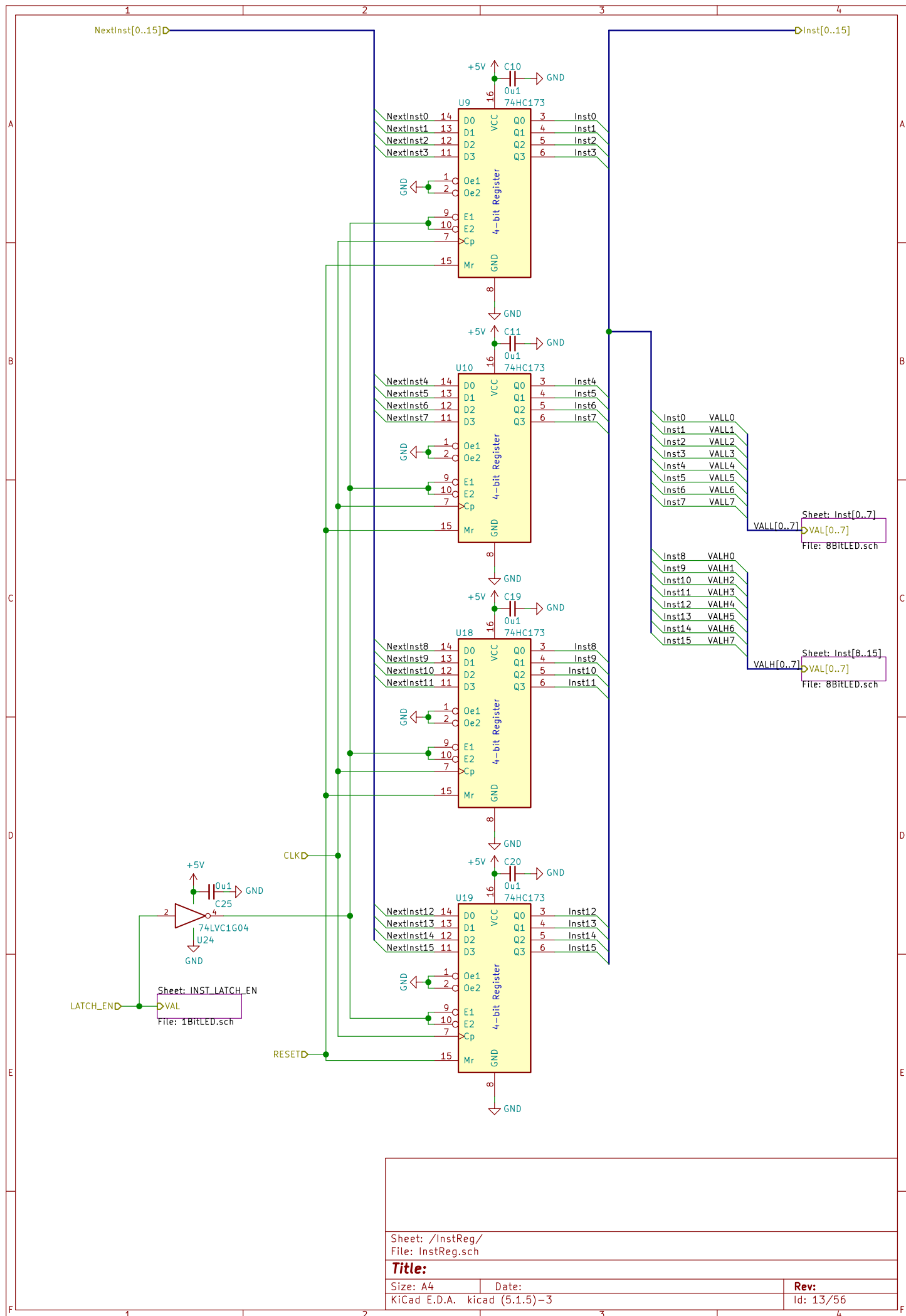


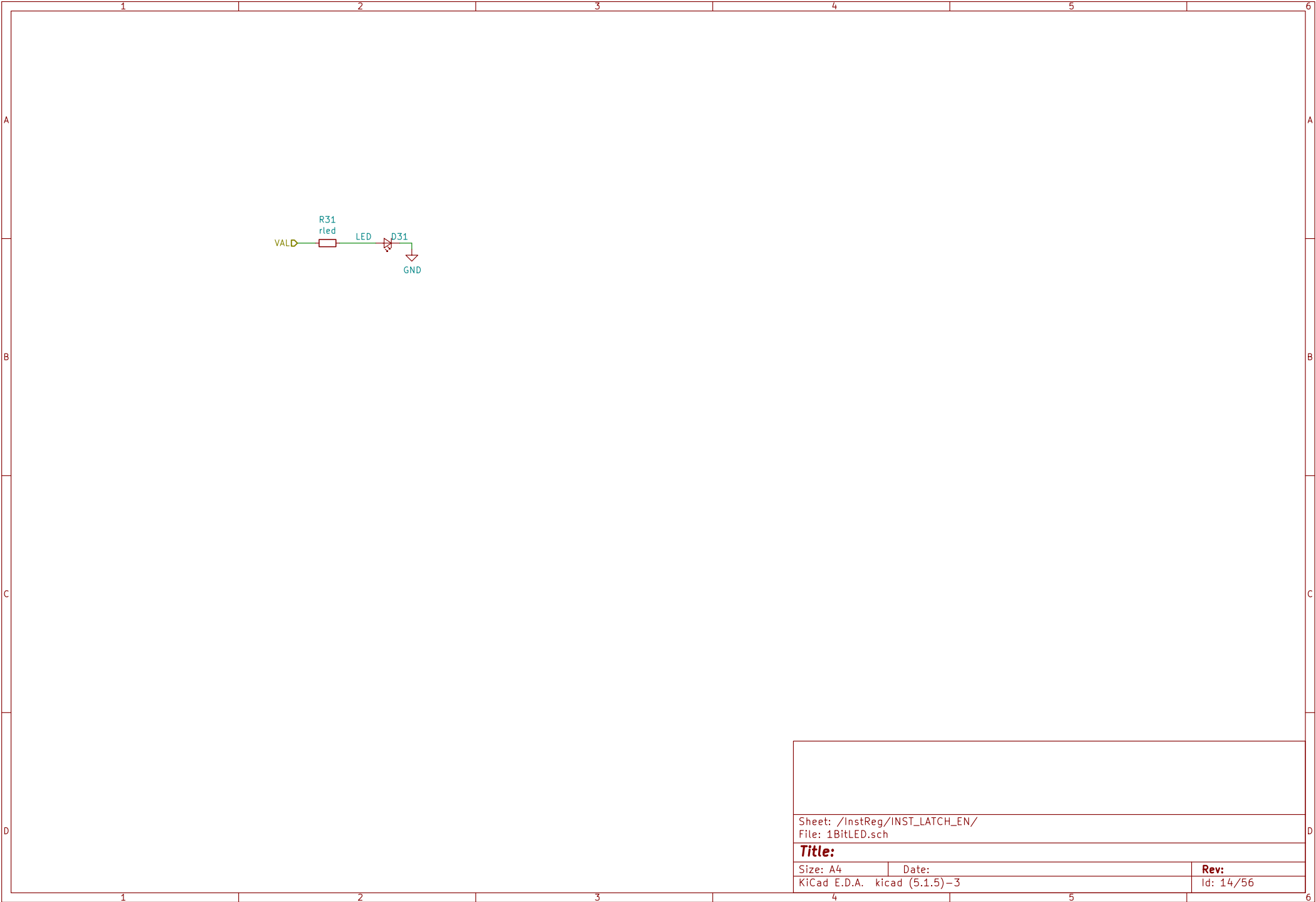
Sheet: /PC/LATCH_EN/ File: 1BitLED.sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)–3		Id: 10/56



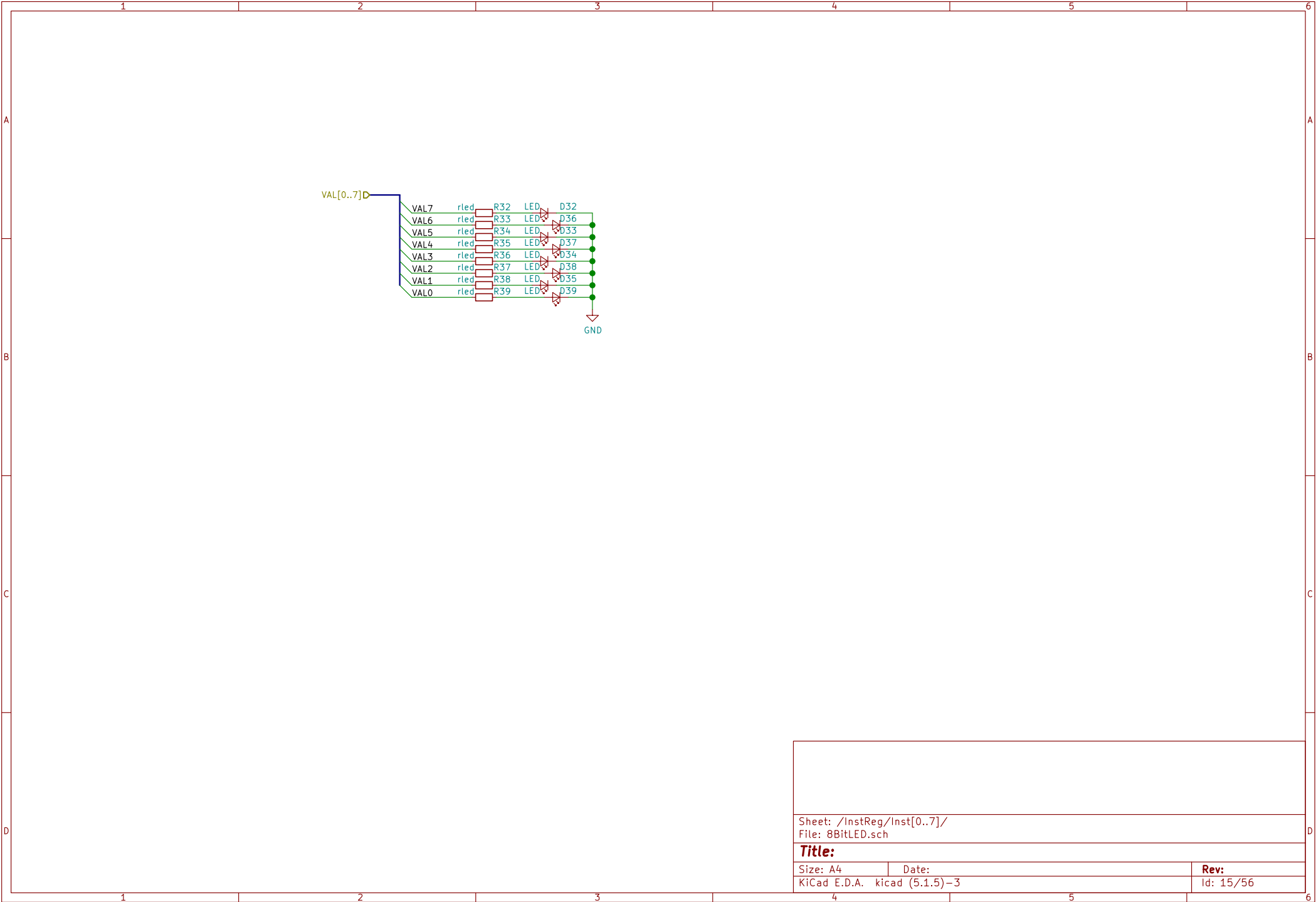
Sheet: /PC/PC_VAL[8..13]/		
File: 8BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)-3		Id: 11/56



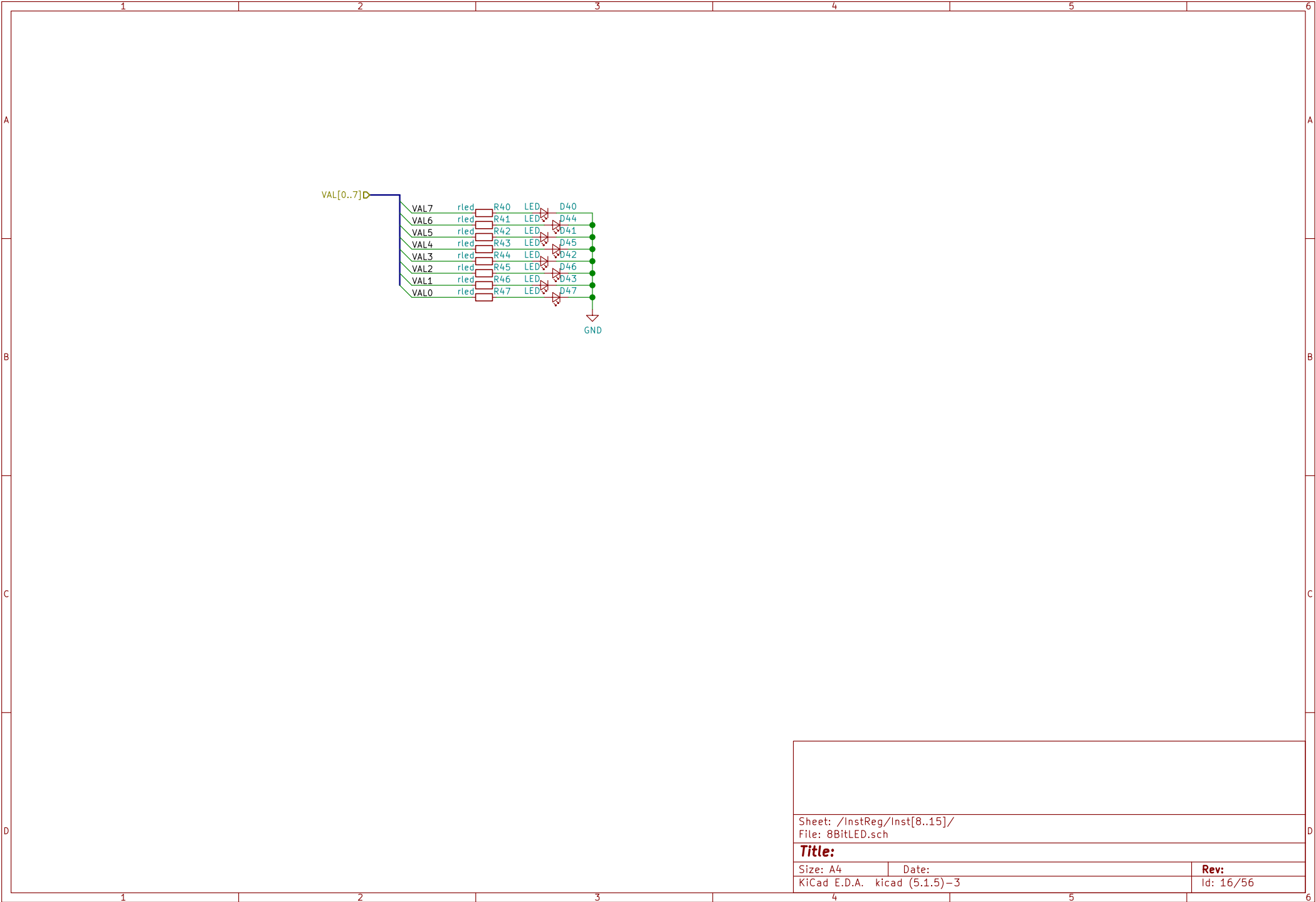




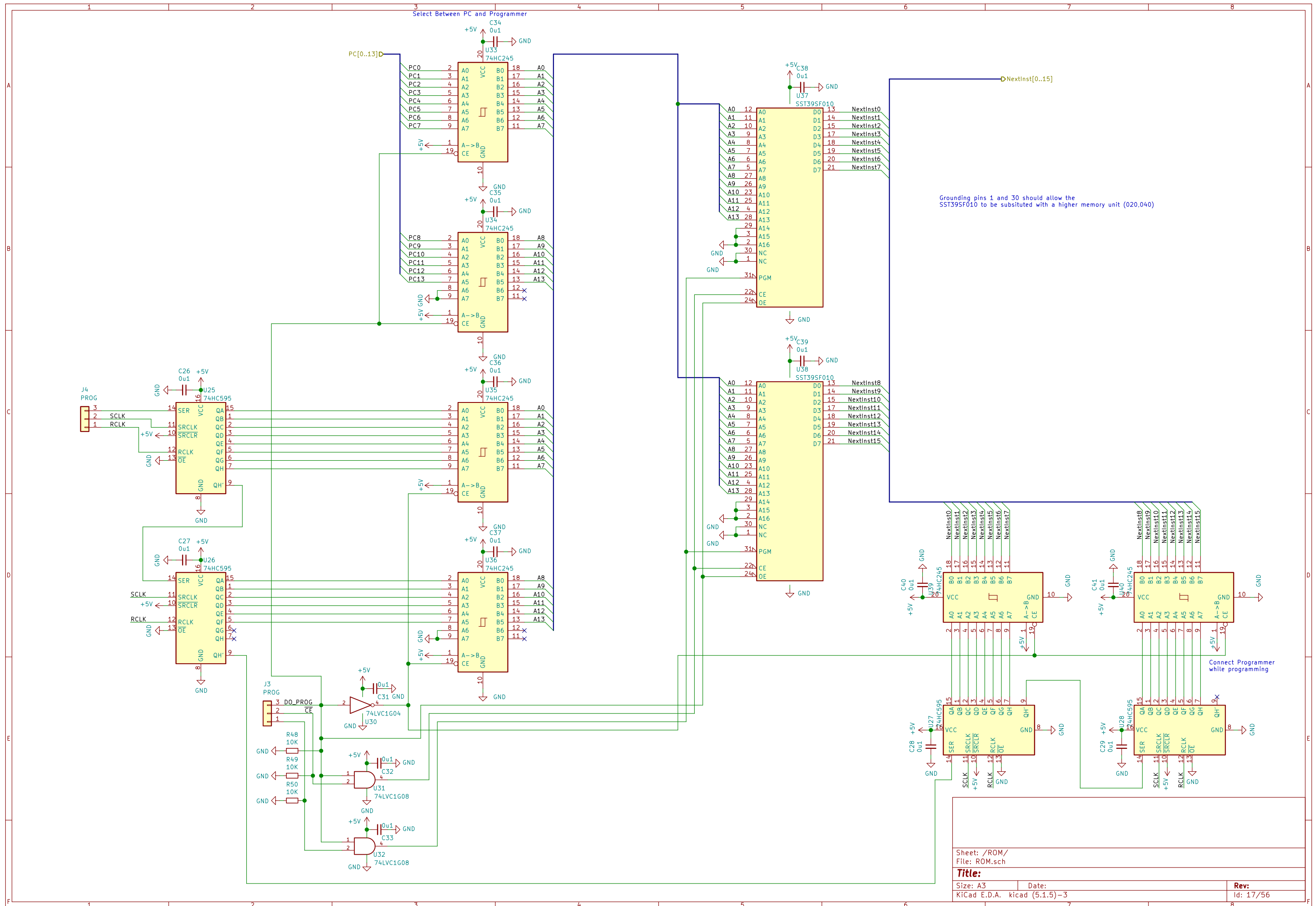
Sheet: /InstReg/INST_LATCH_EN/ File: 1BitLED.sch		
<b>Title:</b>		
Size: A4	Date:	<b>Rev:</b>
KiCad E.D.A. kicad (5.1.5)–3		Id: 14/56



Sheet: /InstReg/Inst[0..7]/		
File: 8BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)-3		Id: 15/56







T-FF with enable.  
Divides MClock by 2 to generate System Clock.  
If enabled by the Clock-Step-Control Logic

Sheet: MClock\_OSC  
File: MClock\_OSC.sch

D-FF with enable.  
Controls Stepping of Clock

Sheet: ClockCtrlUI  
File: ClockCtrlUI.sch

Sheet: sheet5EECC2BB  
File: 1BitLED.sch

Sheet: /ClockCtrl/  
File: ClockCtrl.sch

**Title:**

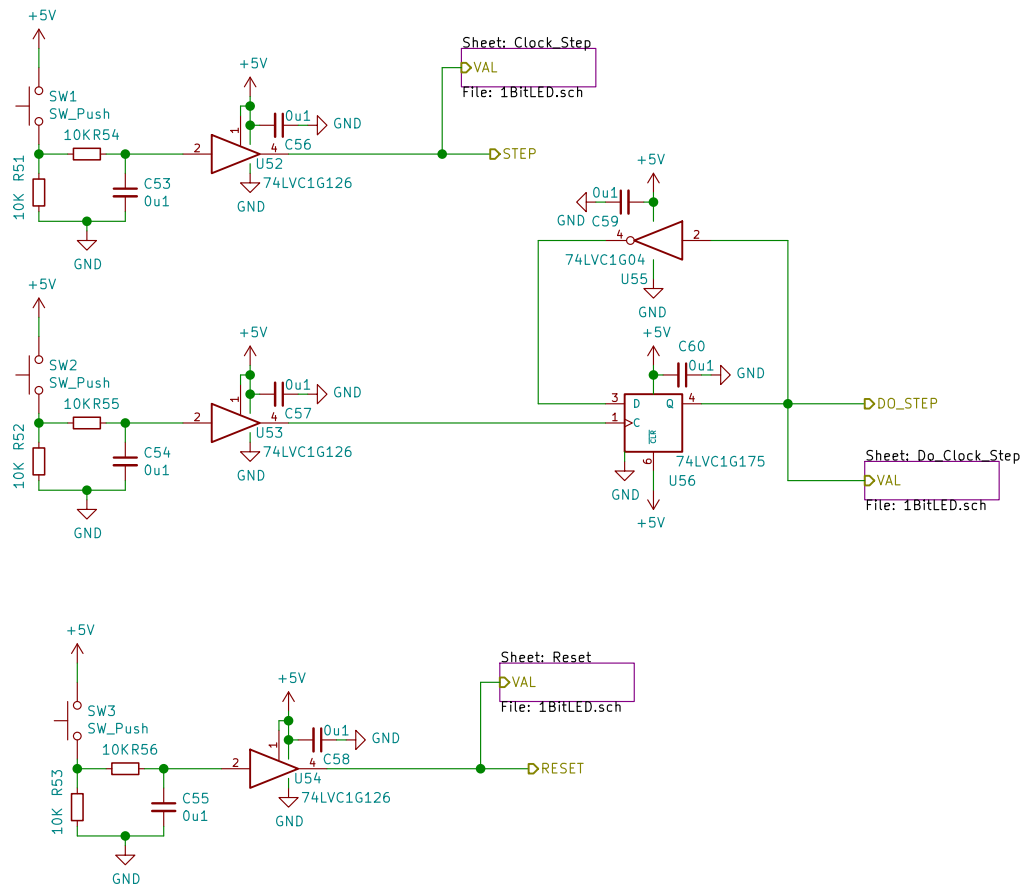
Size: A4

Date:

KiCad E.D.A. kicad (5.1.5)-3

**Rev:**

Id: 18/56



Sheet: /ClockCtrl/ClockCtrlUI/  
File: ClockCtrlUI.sch

**Title:**

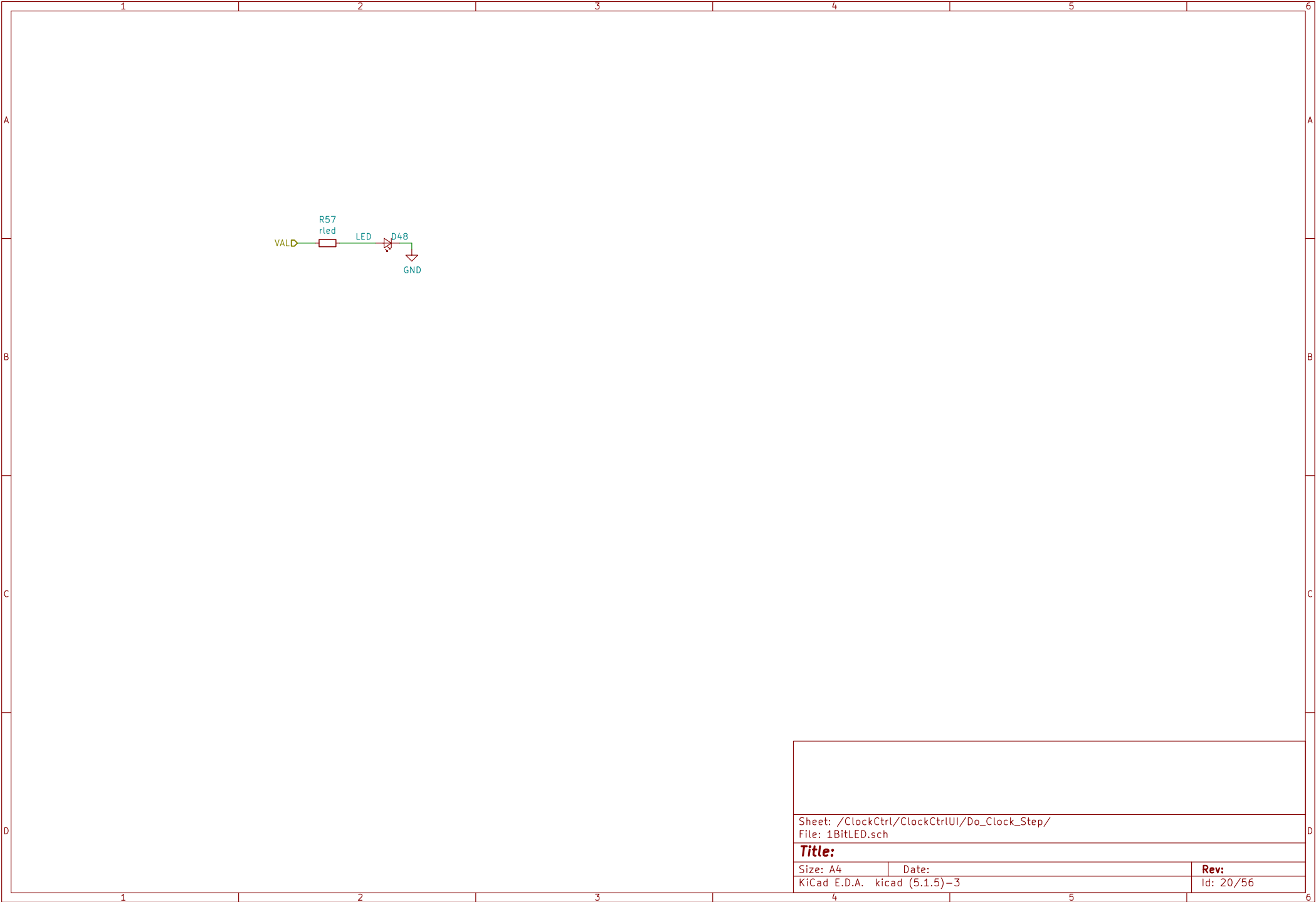
Size: A4

Date:

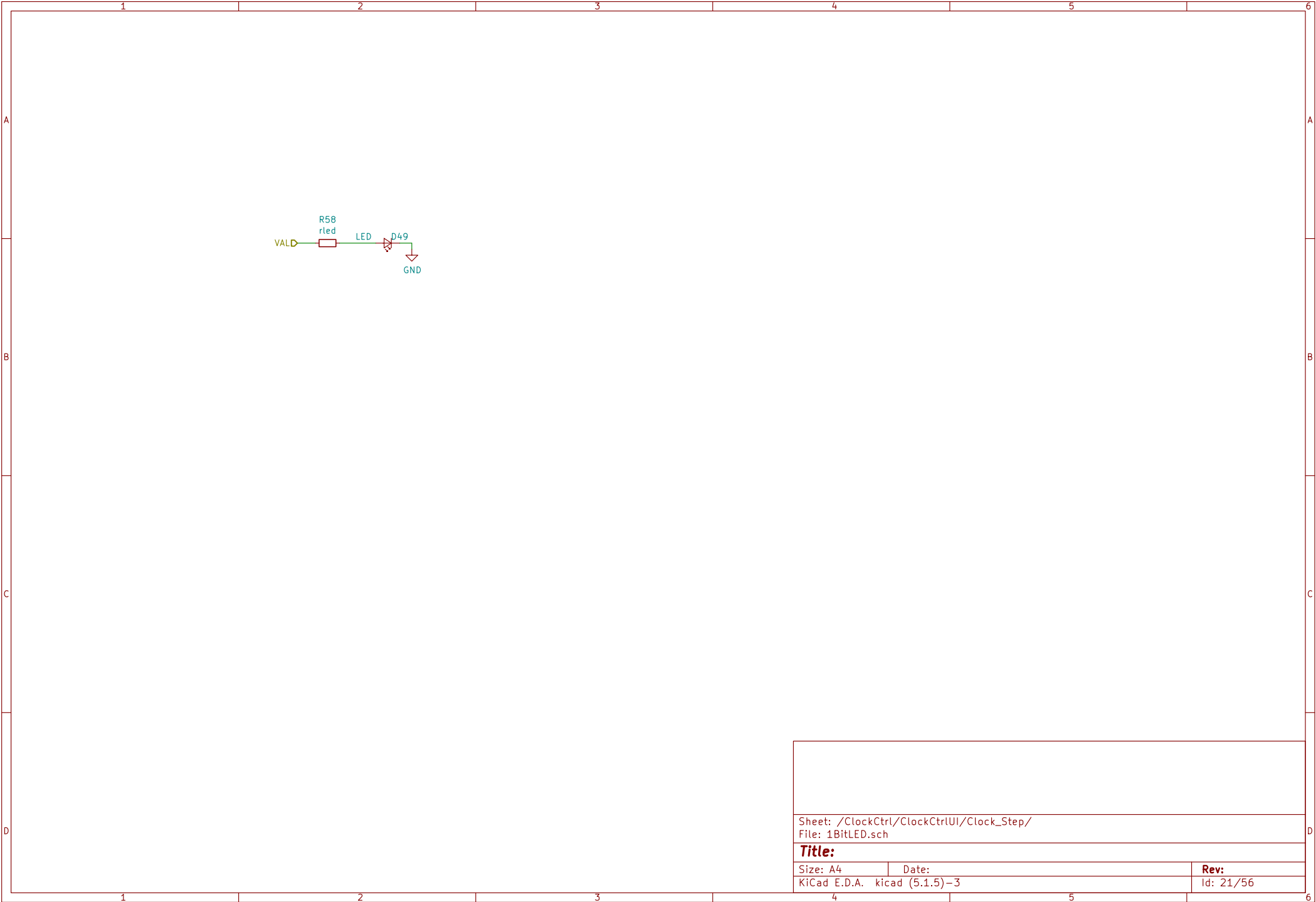
KiCad E.D.A. kicad (5.1.5)-3

**Rev:**

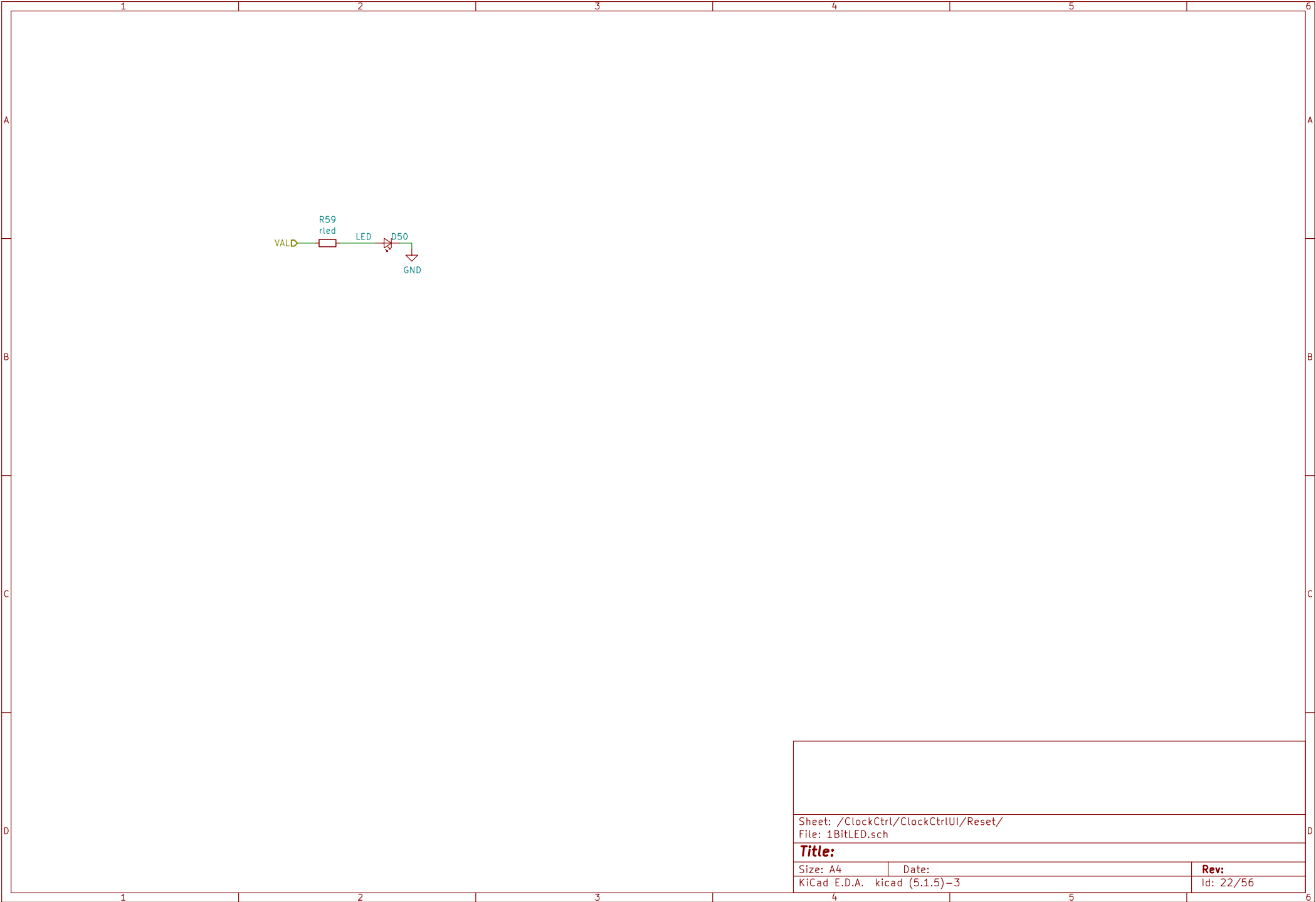
Id: 19/56



Sheet: /ClockCtrl/ClockCtrlUI/Do_Clock_Step/ File: 1BitLED.sch		
<b>Title:</b>		
Size: A4	Date:	<b>Rev:</b>
KiCad E.D.A. kicad (5.1.5)–3		Id: 20/56



Sheet: /ClockCtrl/ClockCtrlUI/Clock_Step/ File: 1BitLED.sch		
<b>Title:</b>		
Size: A4	Date:	<b>Rev:</b>
KiCad E.D.A. kicad (5.1.5)–3		Id: 21/56



Sheet: /ClockCtrl/ClockCtrlUI/Reset/  
File: 1BitLED.sch

**Title:**

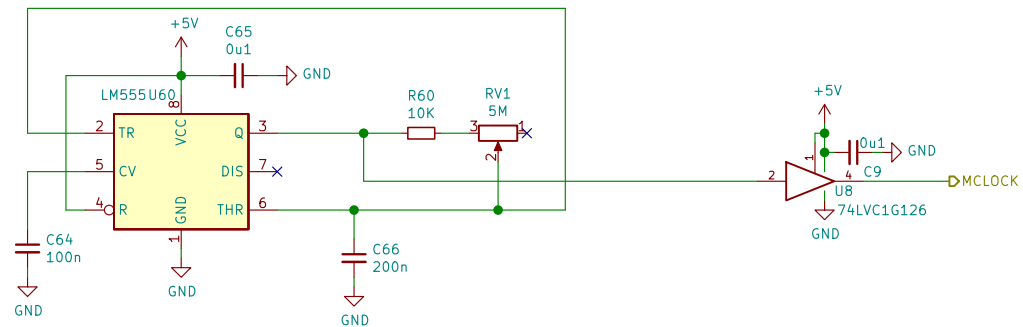
Size: A4

Date:

KiCad E.D.A. kicad (5.1.5)-3

**Rev:**

Id: 22/56



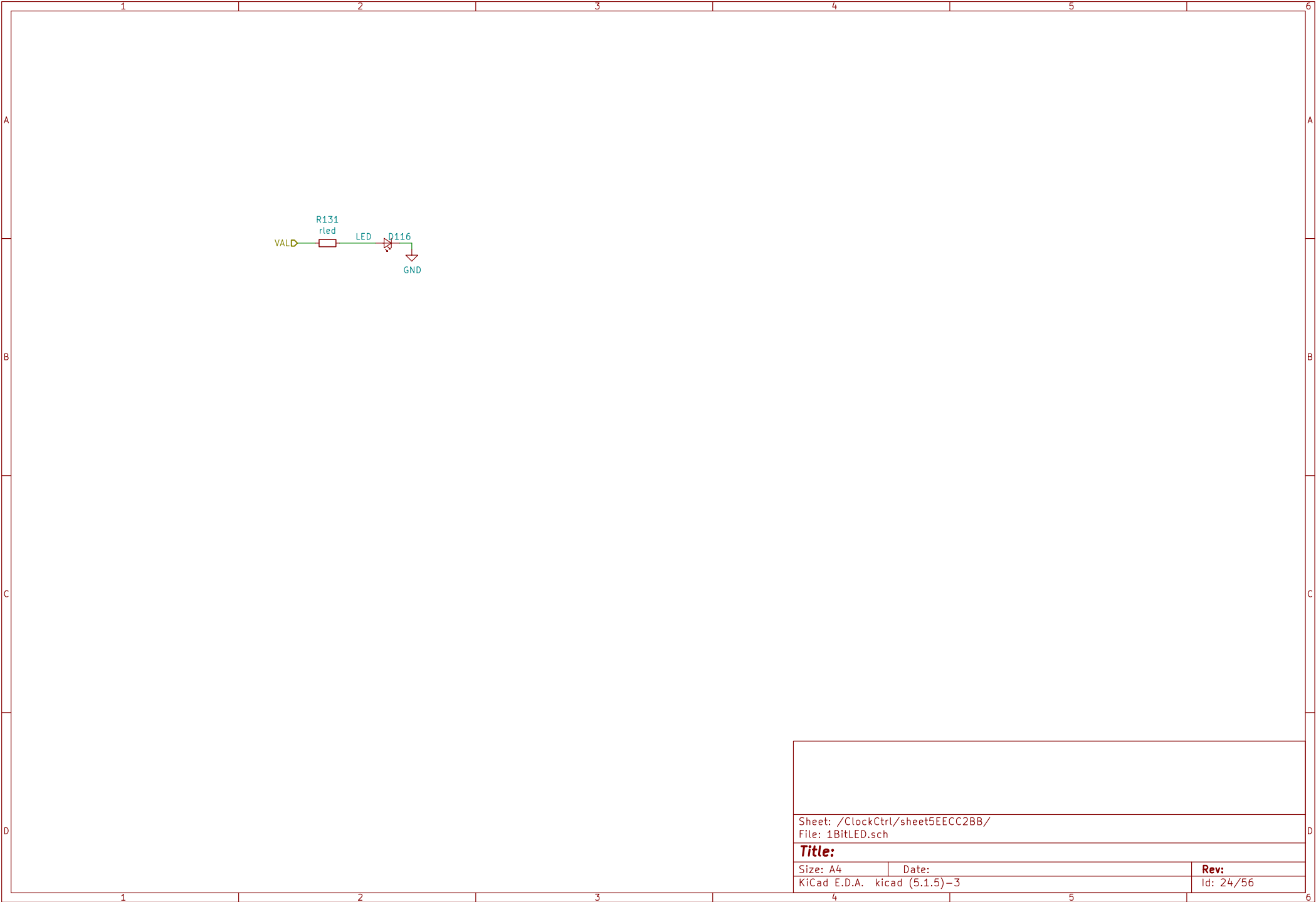
Sheet: /ClockCtrl/MClock\_OSC/  
File: MClock\_OSC.sch

**Title:**

Size: A4  
KiCad E.D.A. kicad (5.1.5)-3

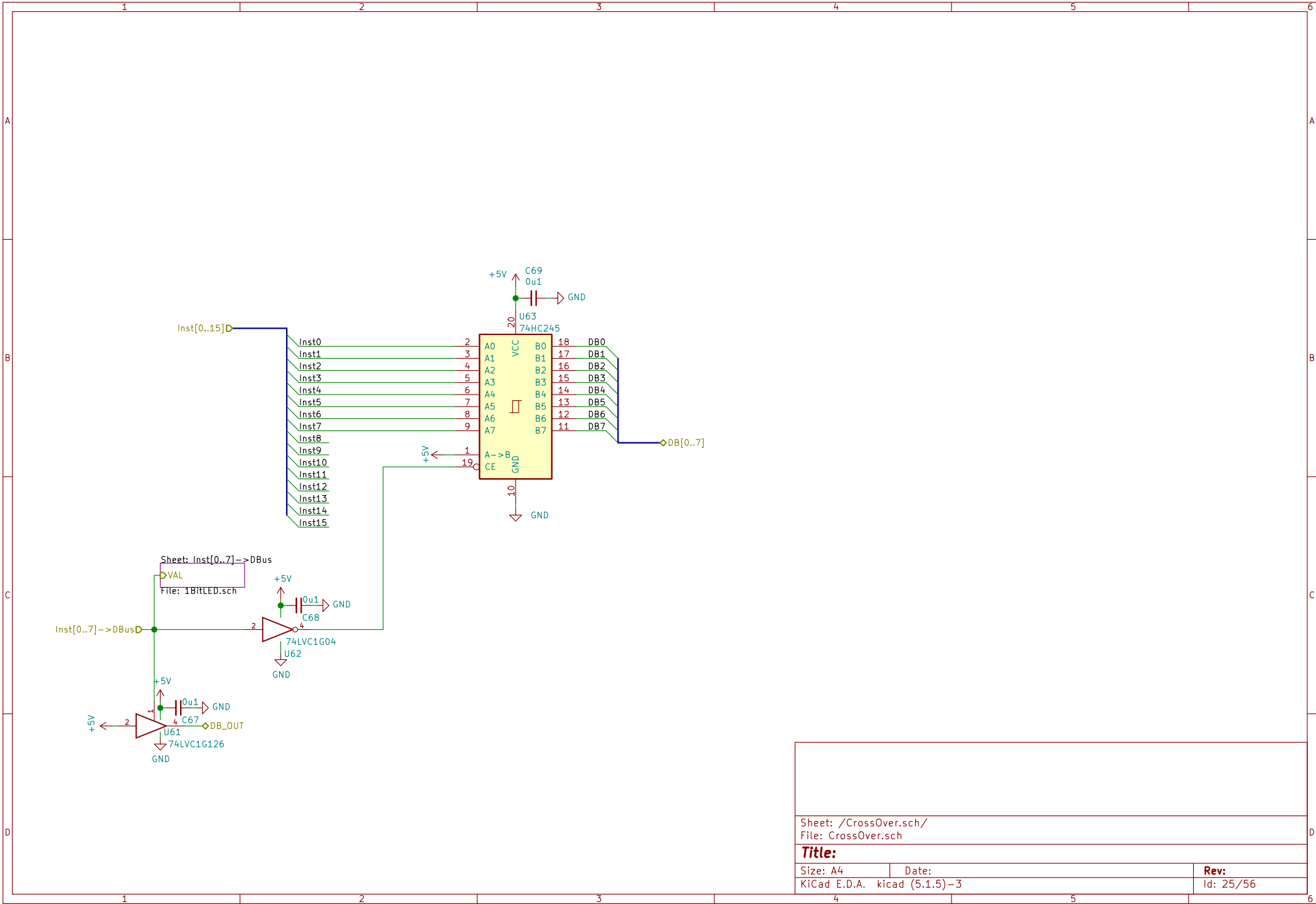
Date:

Rev:  
Id: 23/56



Sheet: /ClockCtrl/sheet5EECC2BB/ File: 1BitLED.sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)–3		Id: 24/56





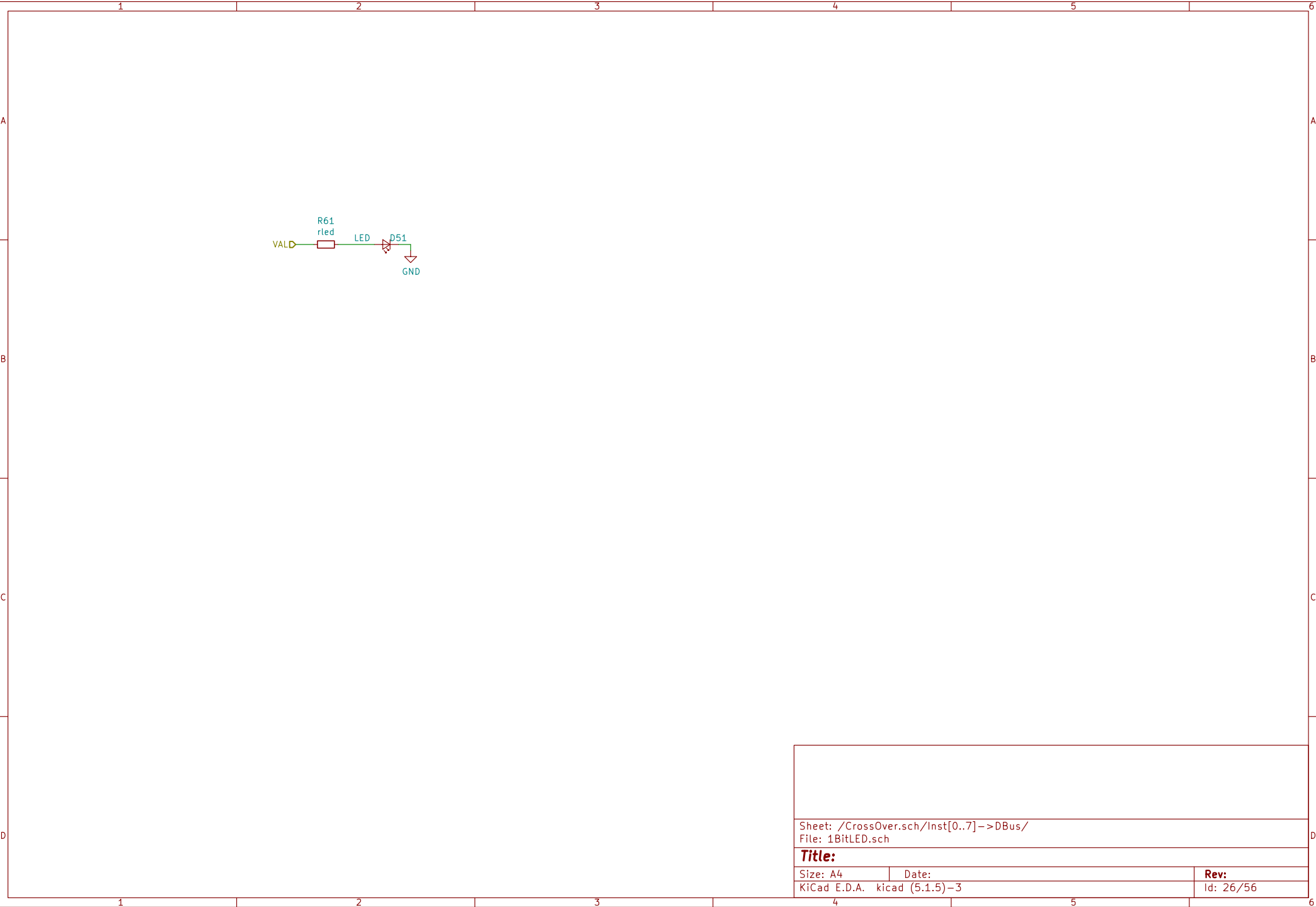
Sheet: /CrossOver.sch/  
File: CrossOver.sch

**Title:**

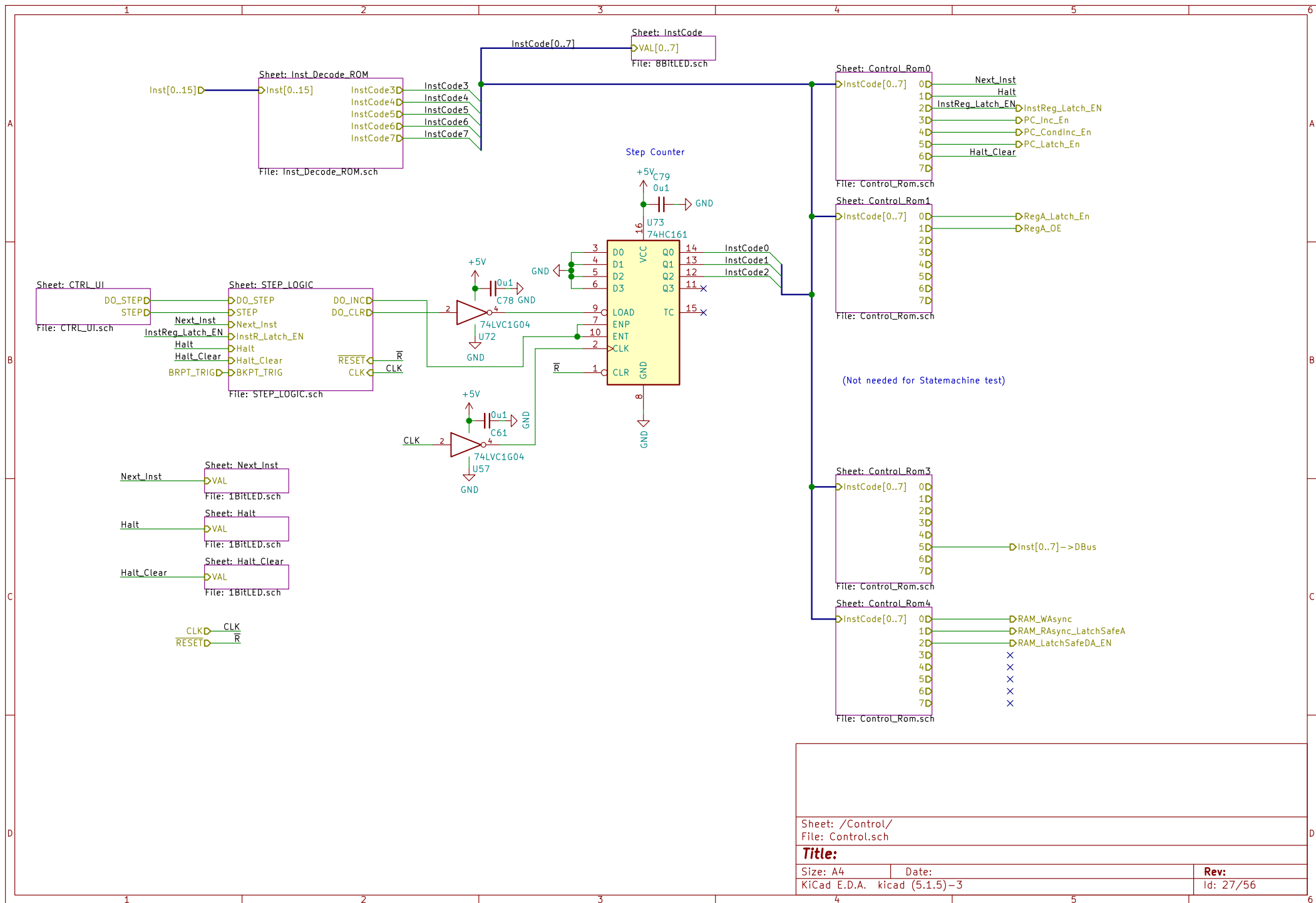
Size: A4  
KiCad E.D.A. kicad (5.1.5)-3

Date:

Rev:  
Id: 25/56



Sheet: /CrossOver.sch/Inst[0..7]->DBus/ File: 1BitLED.sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)-3		Id: 26/56



Sheet: /Control/  
File: Control.sch

# Title:

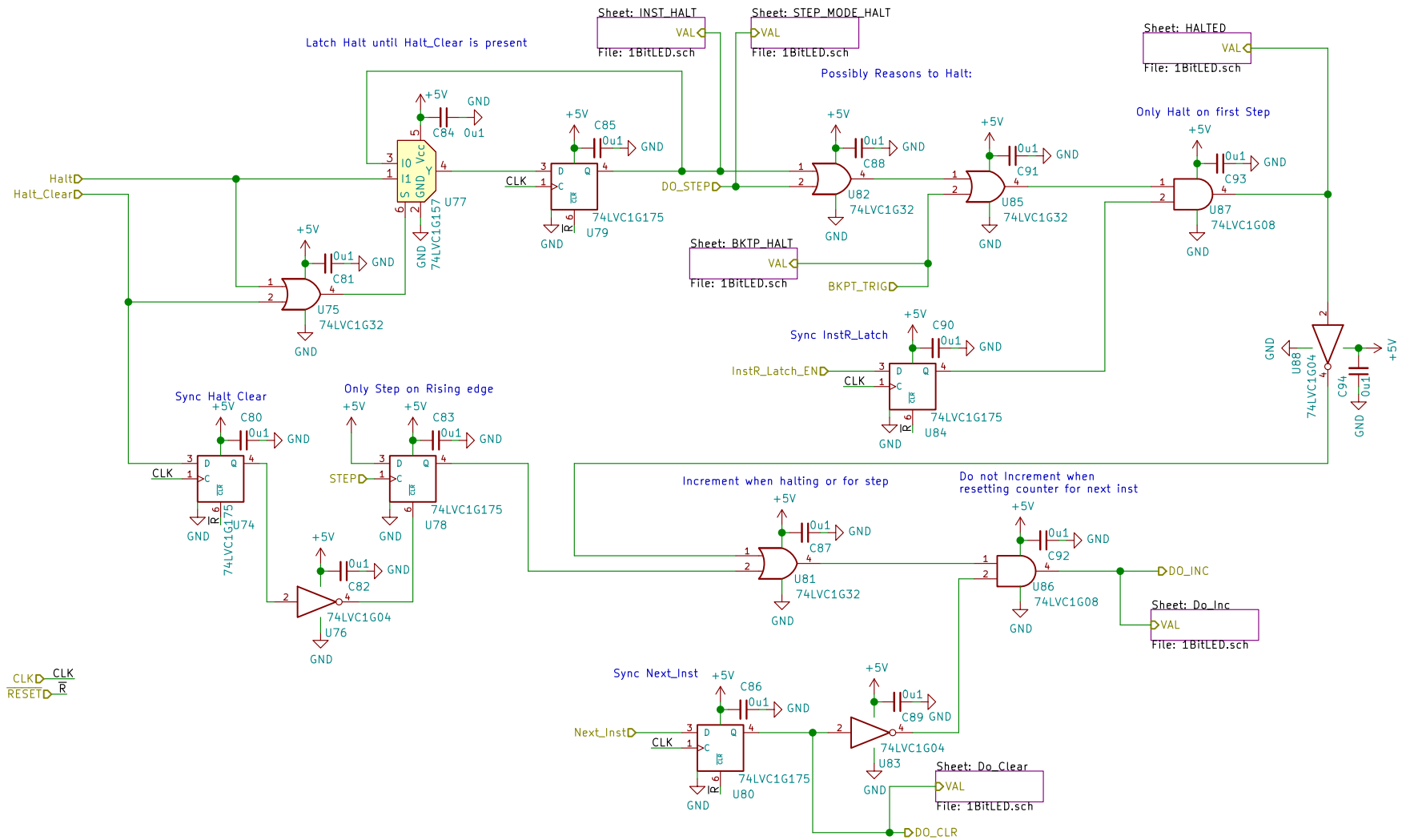
Size: A4

Date:

KiCad E.D.A. kicad (5.1.5) - 3

Rev:

Id: 27/56



Sheet: /Control/STEP\_LOGIC/  
File: STEP\_LOGIC.sch

# Title:

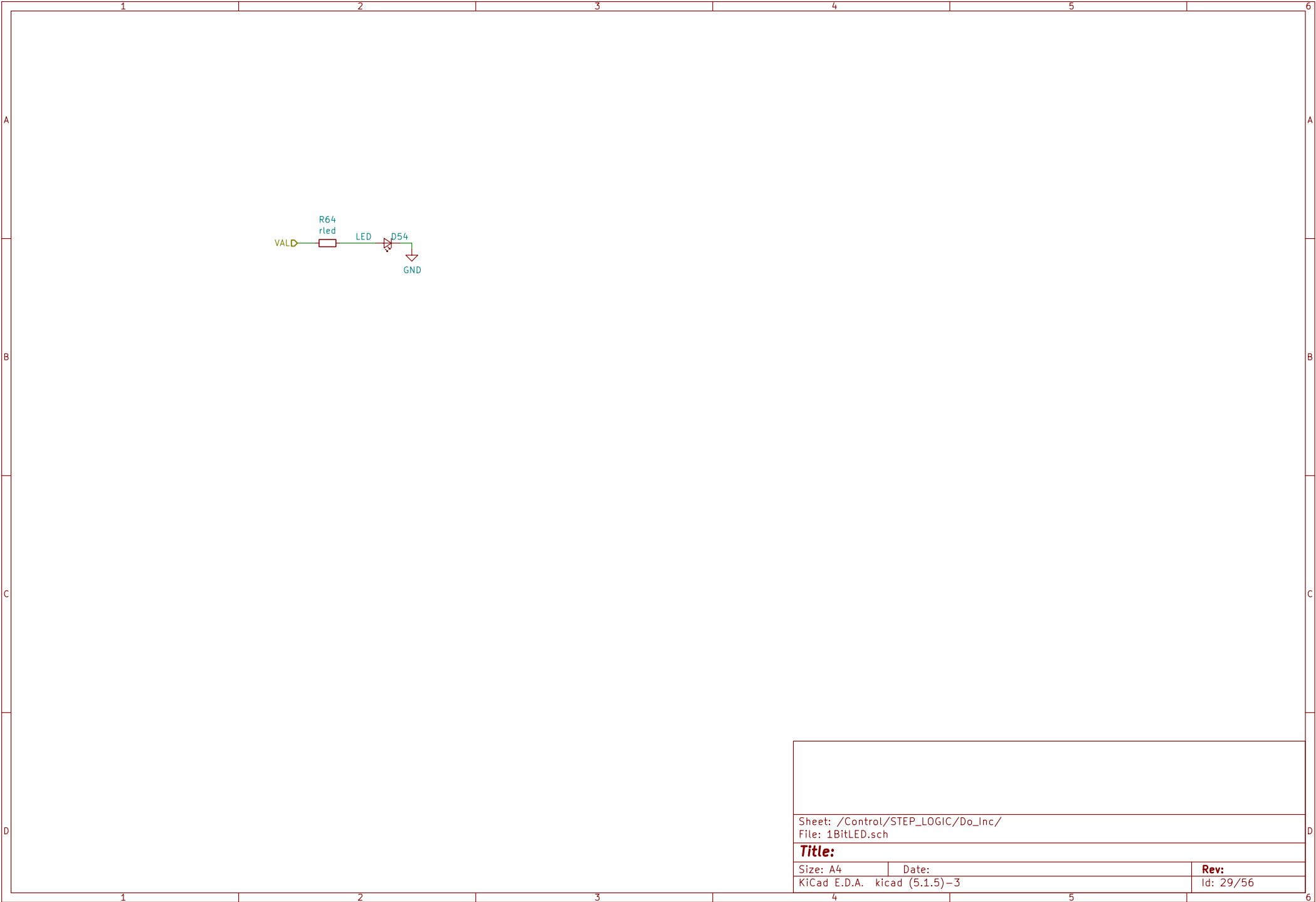
Size: A4

Date:

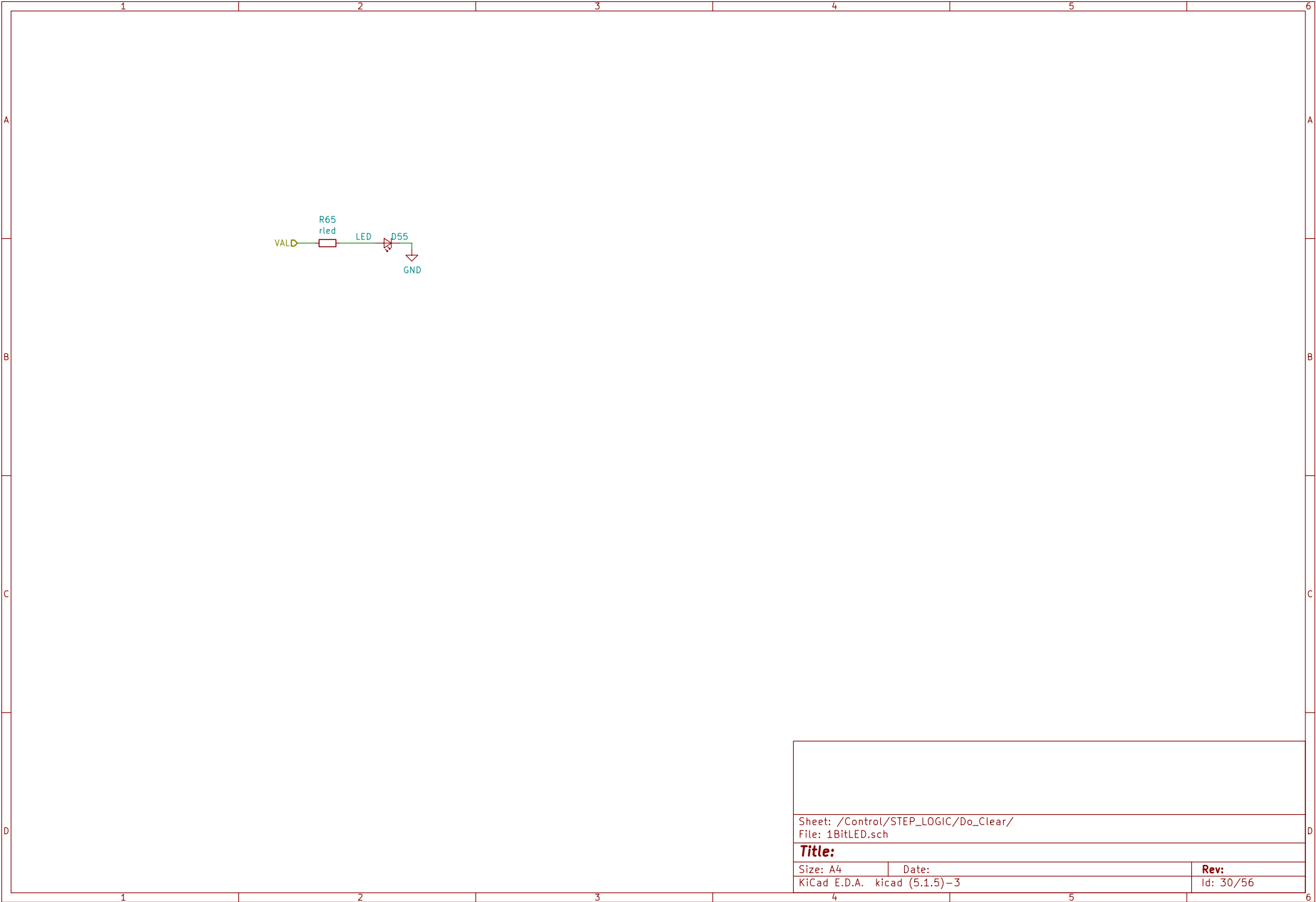
KiCad E.D.A. kicad (5.1.5)-3

Rev:

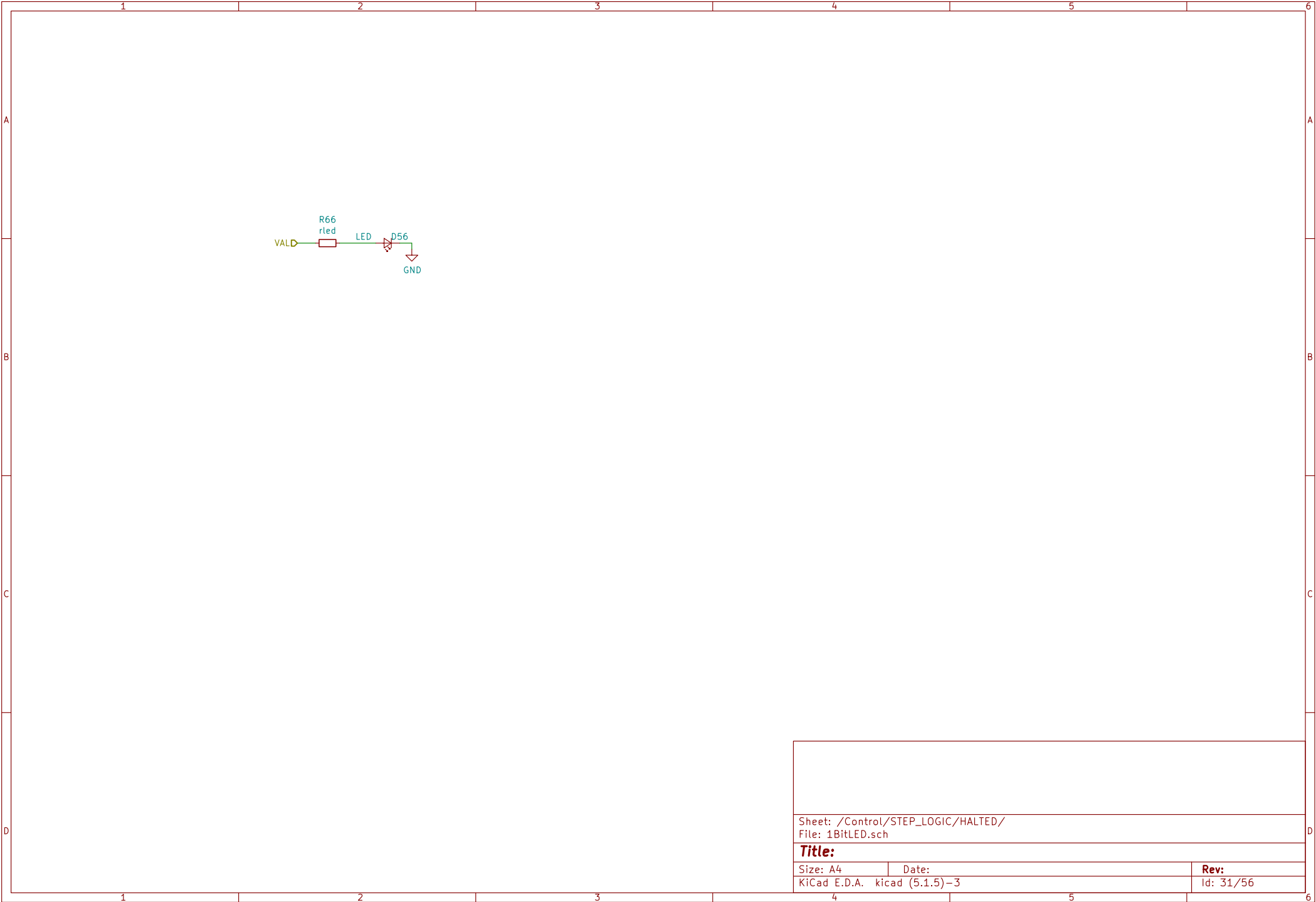
Id: 28/56



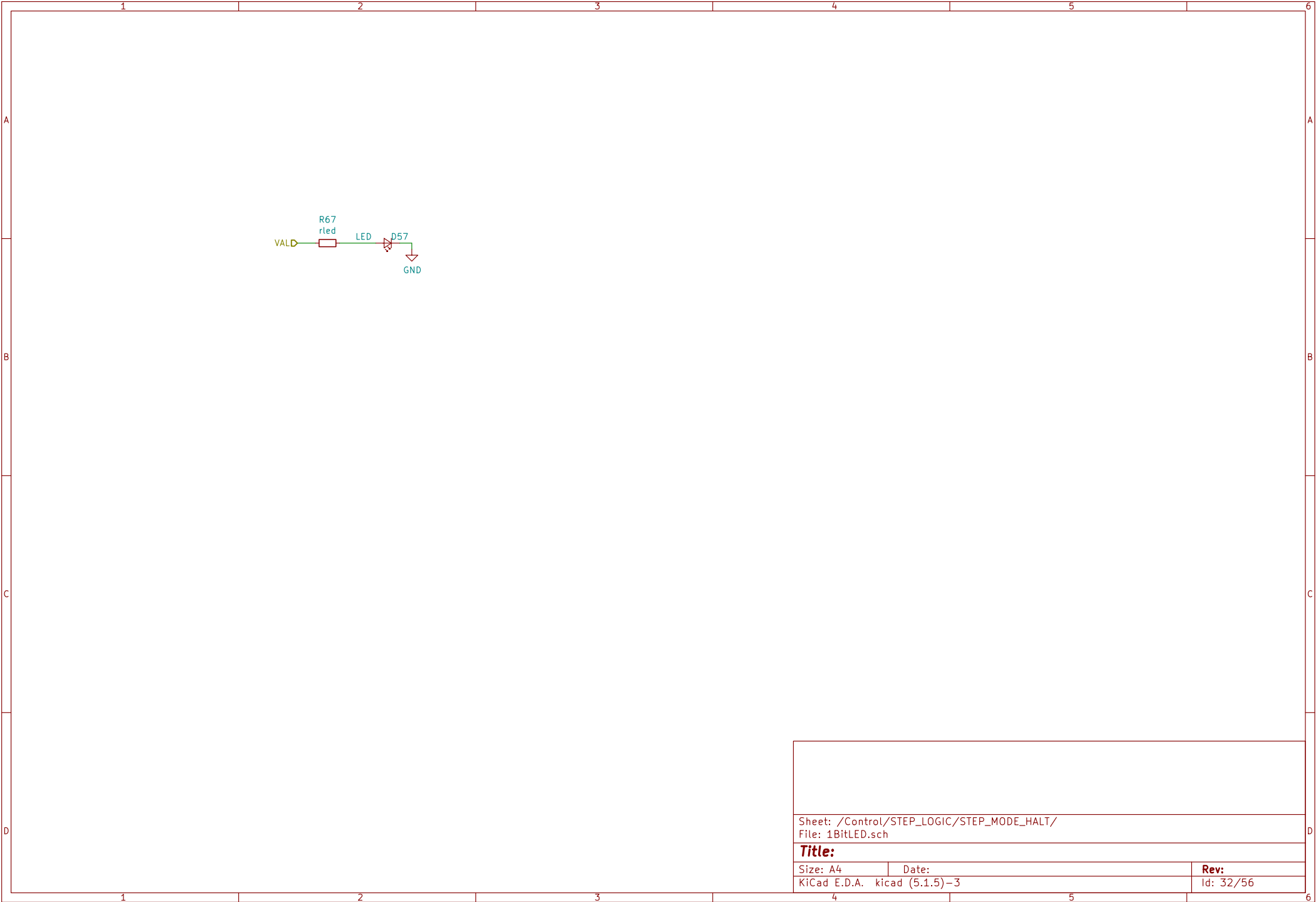
Sheet: /Control/STEP_LOGIC/Do_Inc/		
File: 1BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)-3	Id: 29/56	



Sheet: /Control/STEP_LOGIC/Do_Clear/ File: 1BitLED.sch		
<b>Title:</b>		
Size: A4	Date:	<b>Rev:</b>
KiCad E.D.A. kicad (5.1.5)–3		Id: 30/56

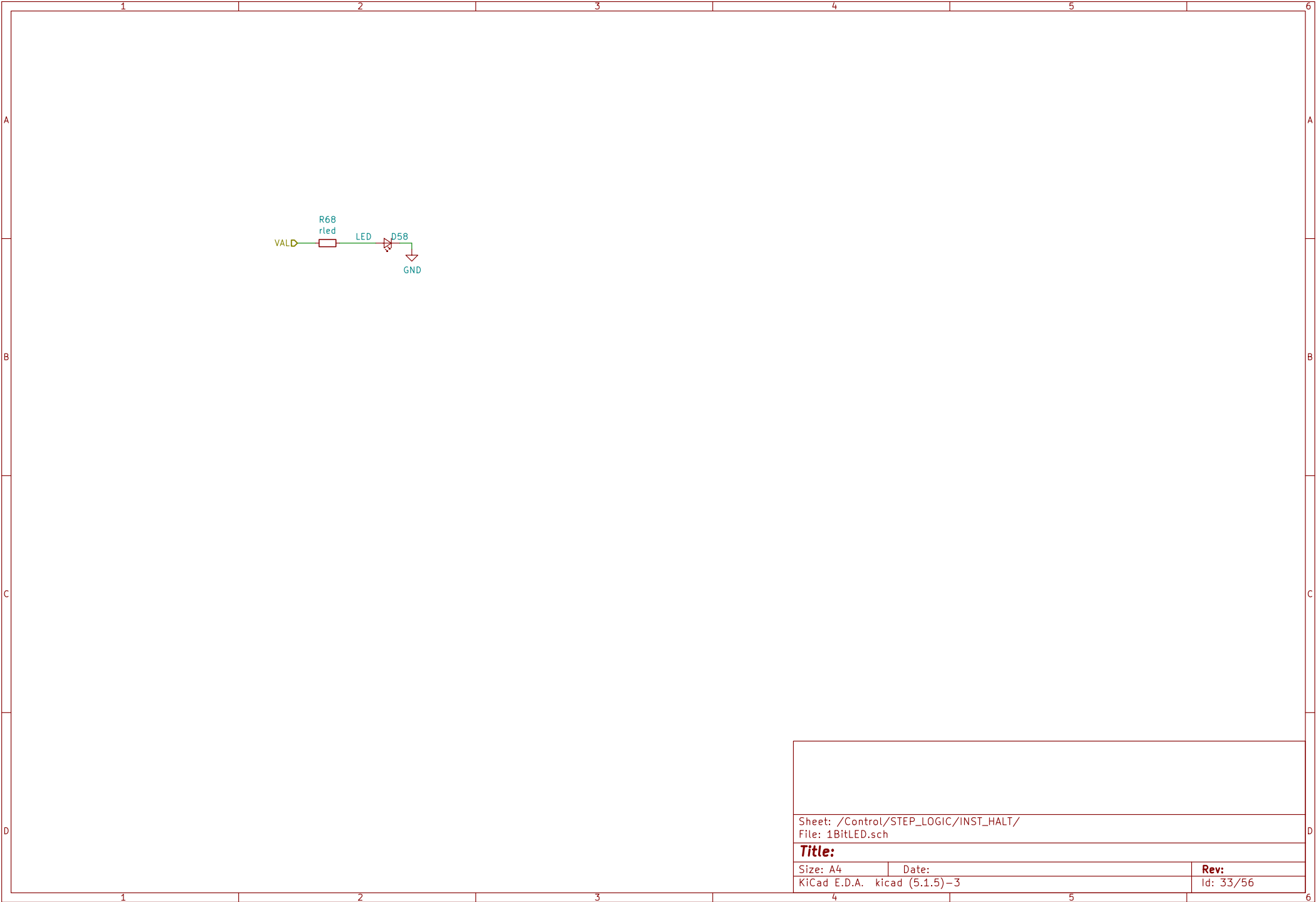


Sheet: /Control/STEP_LOGIC/HALTED/ File: 1BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)–3		Id: 31/56

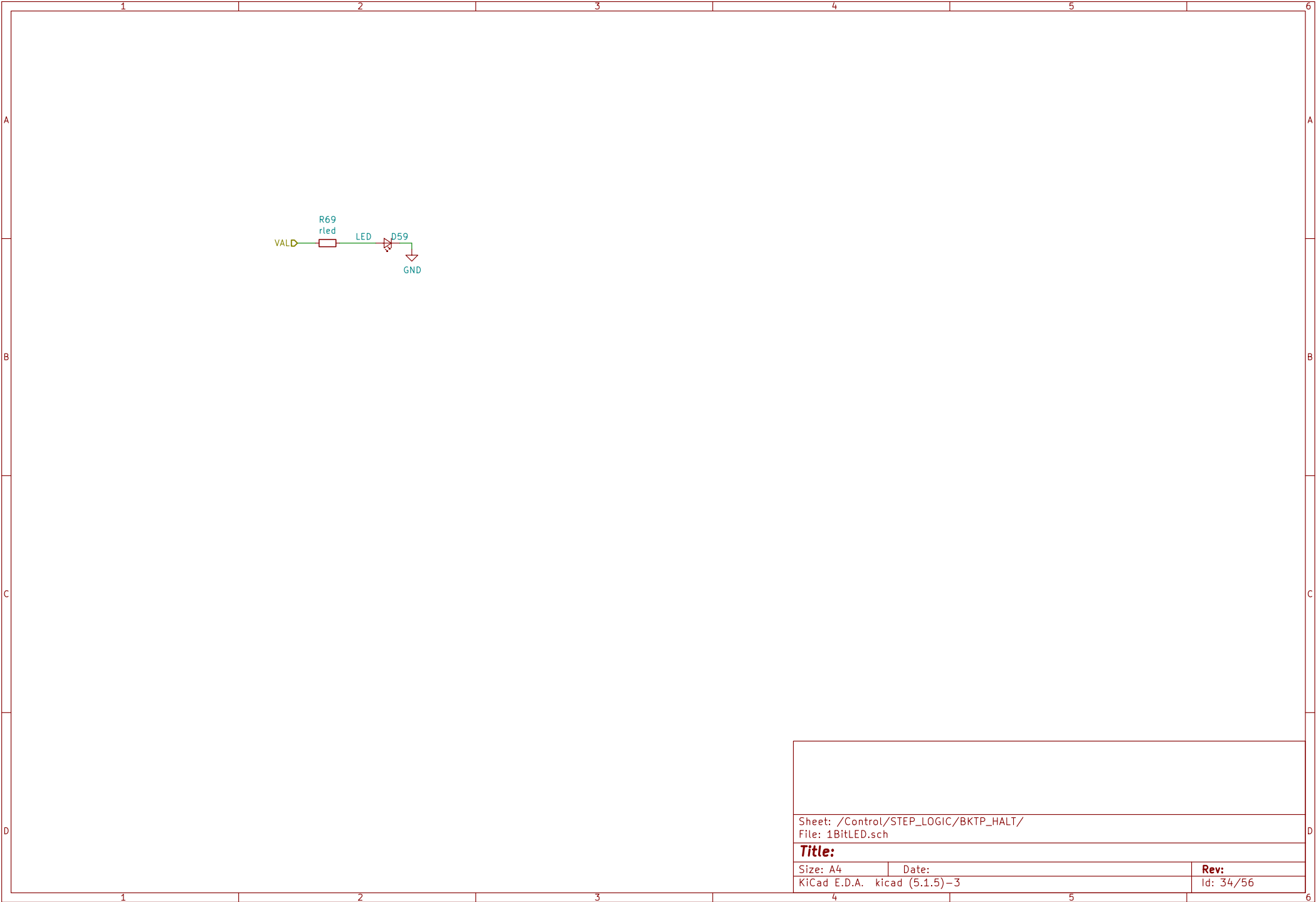


Sheet: /Control/STEP_LOGIC/STEP_MODE_HALT/ File: 1BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)–3		Id: 32/56

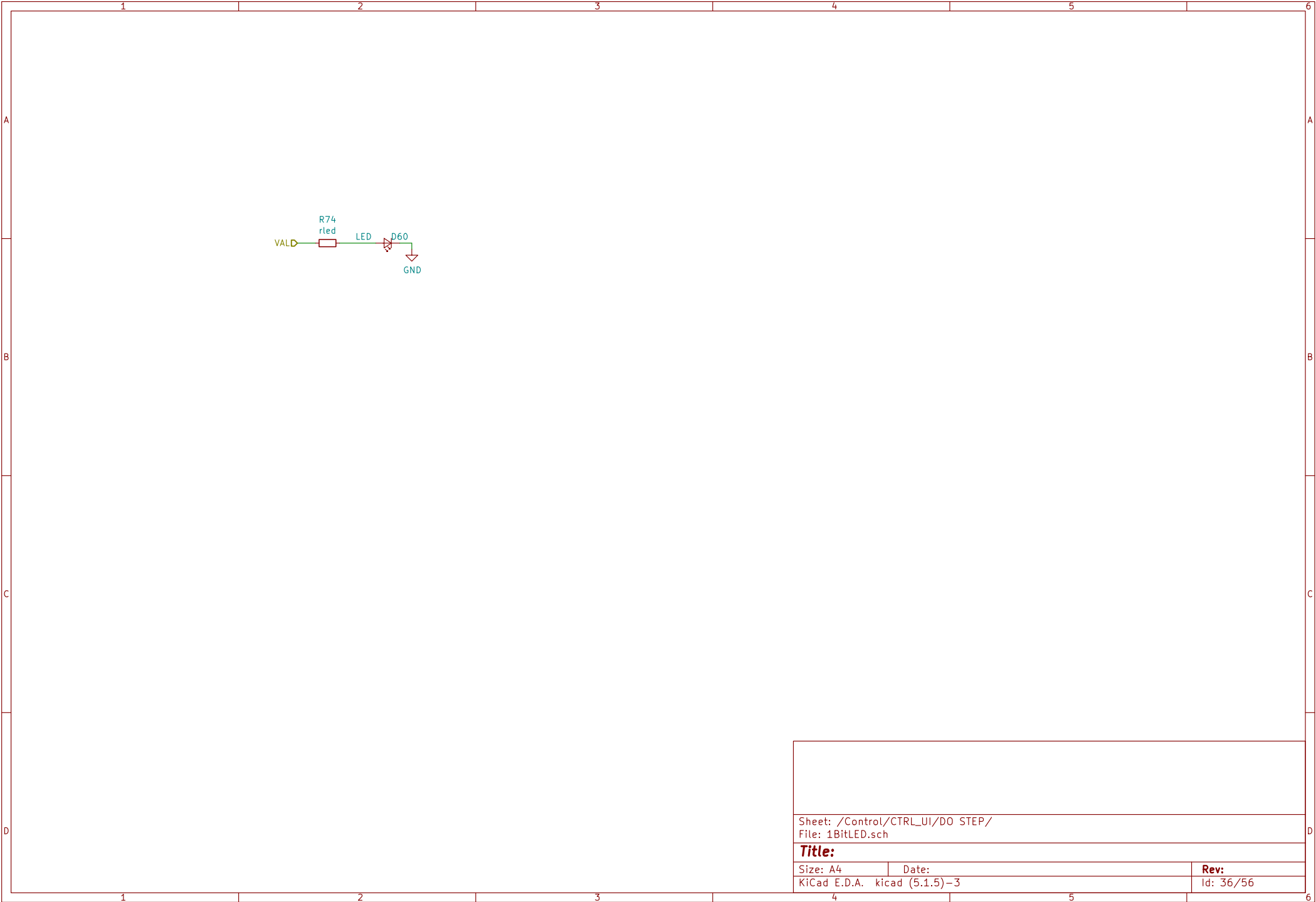




Sheet: /Control/STEP_LOGIC/INST_HALT/ File: 1BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)-3		Id: 33/56







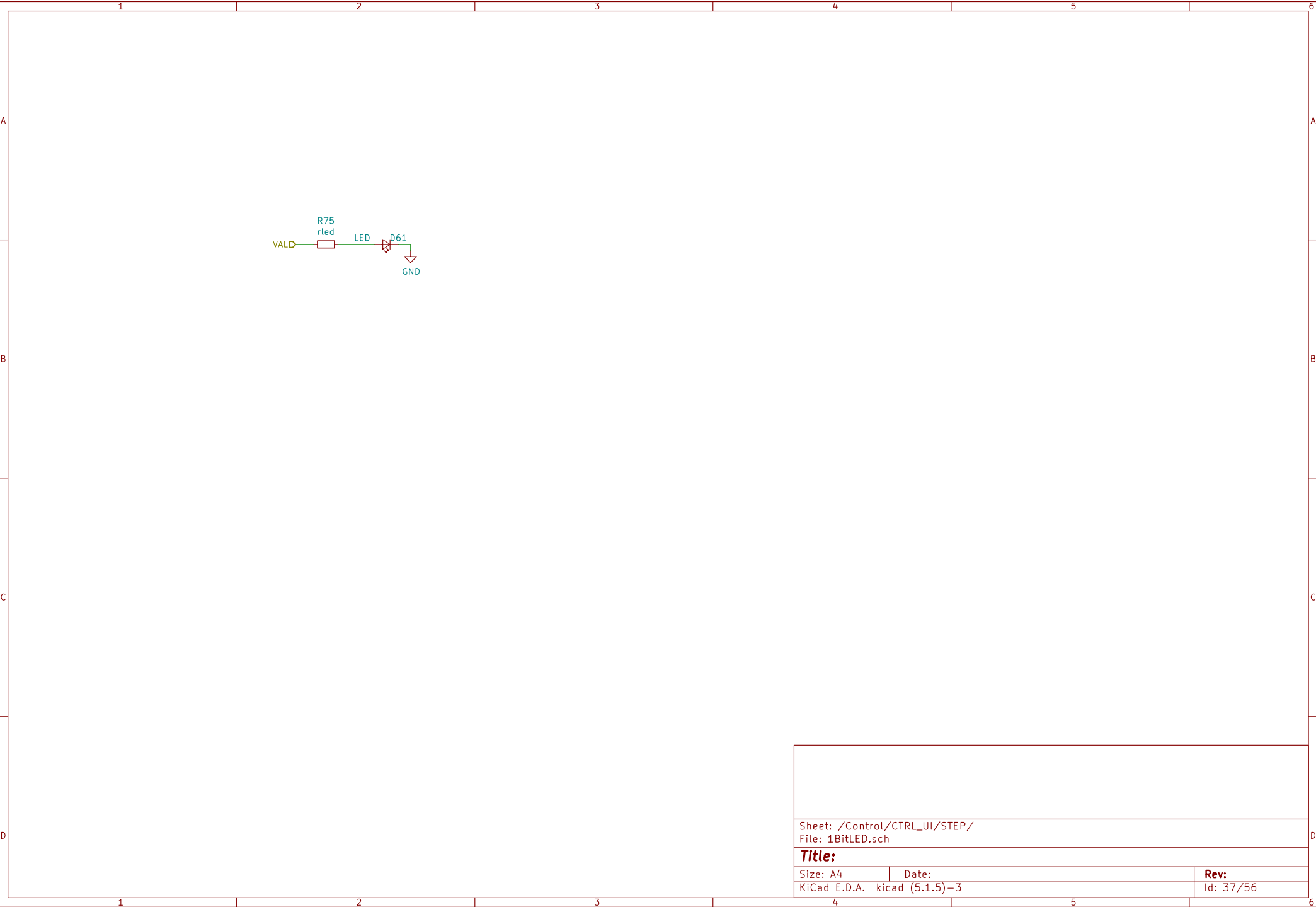
Sheet: /Control/CTRL\_UI/DO STEP/  
File: 1BitLED.sch

**Title:**

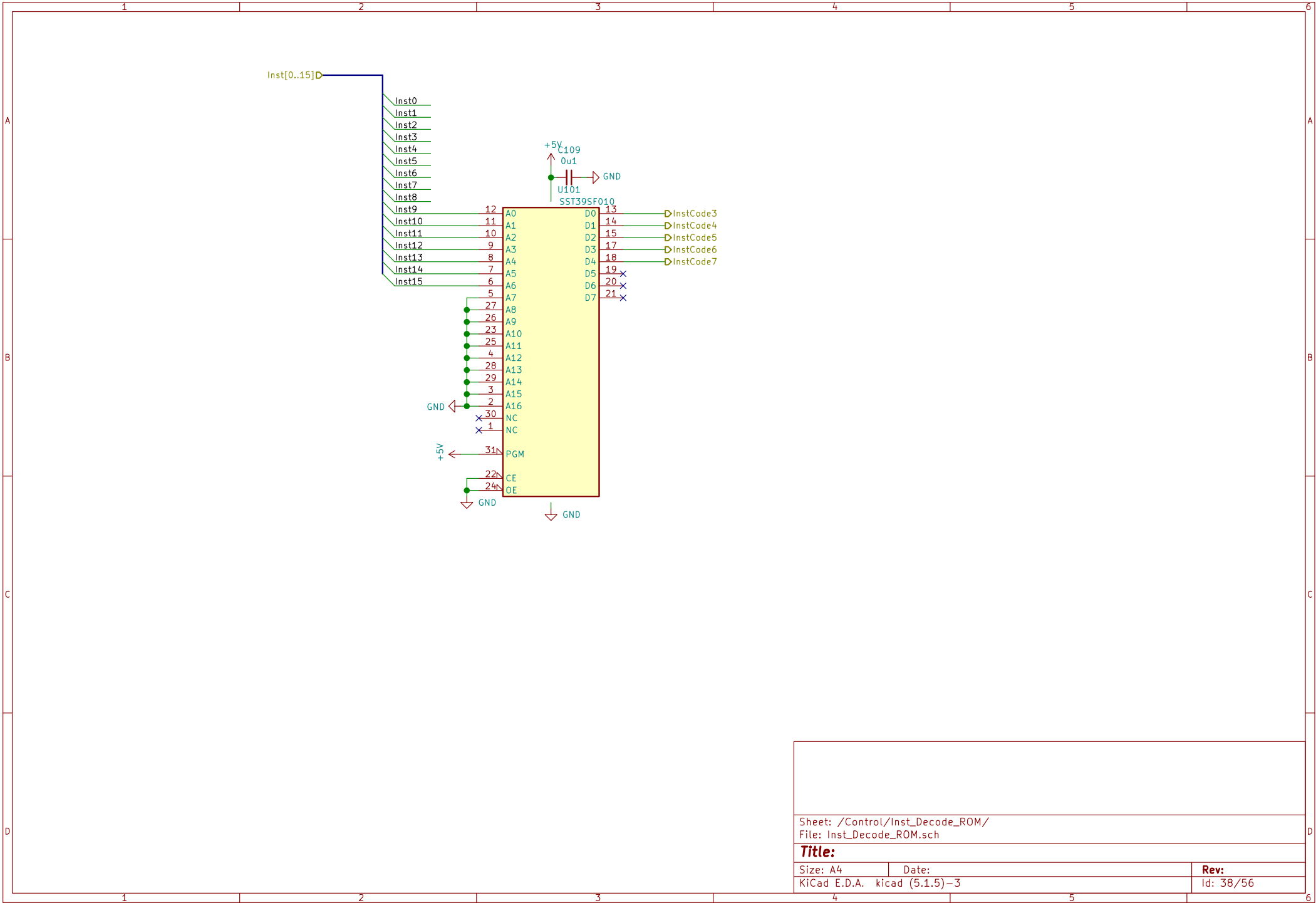
Size: A4  
KiCad E.D.A. kicad (5.1.5)–3

Date:

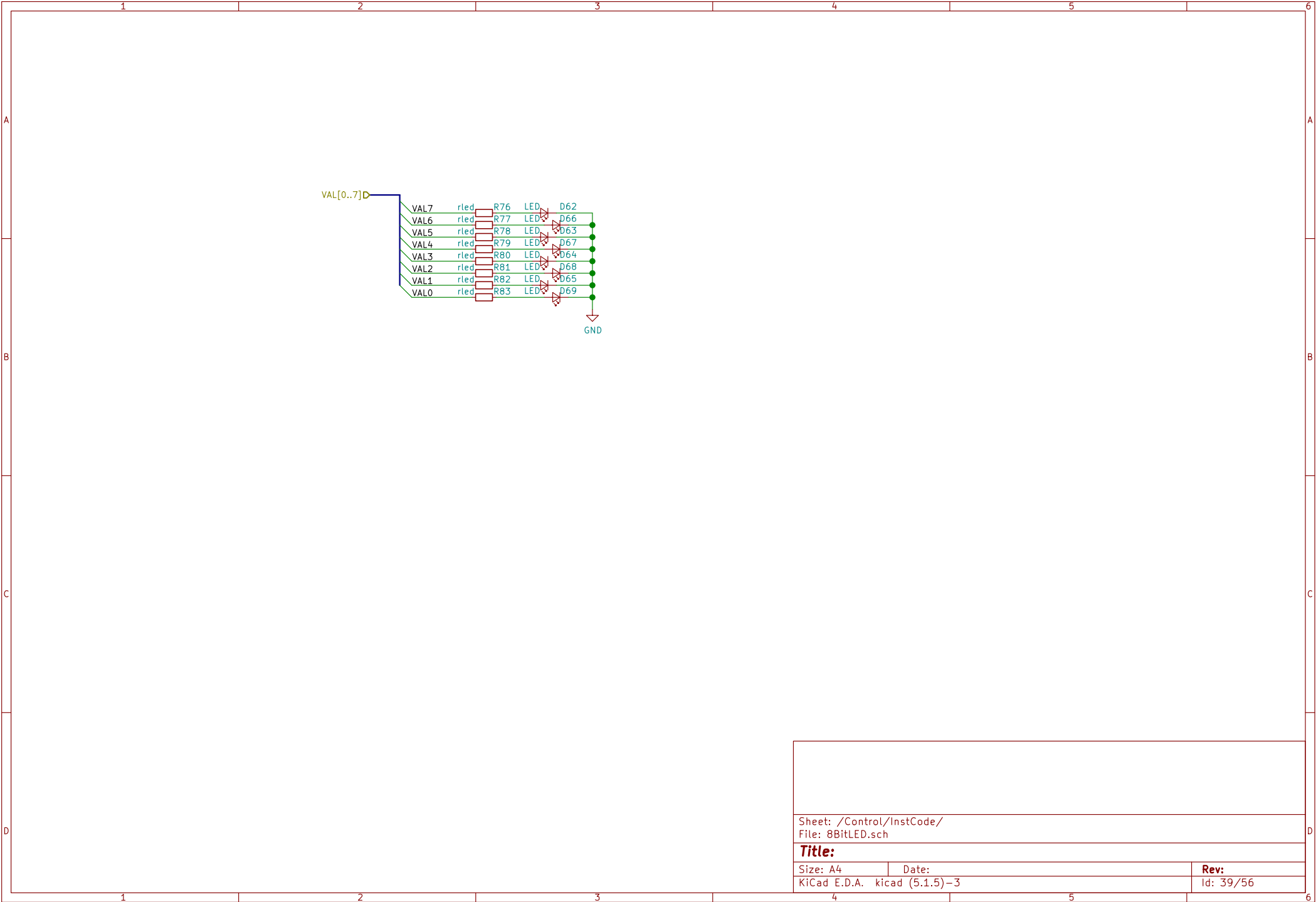
Rev:  
Id: 36/56

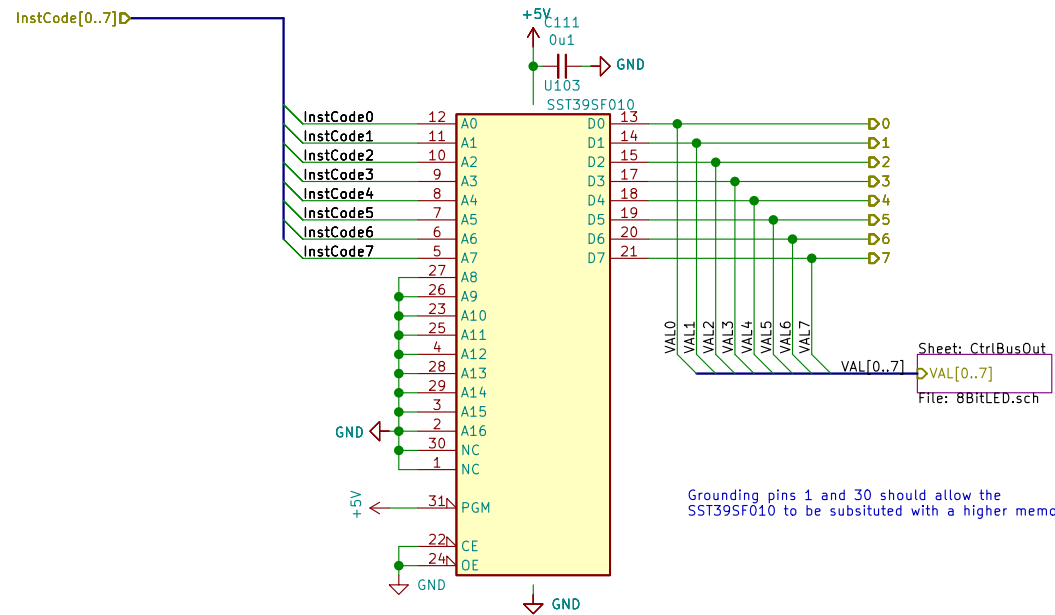


Sheet: /Control/CTRL_UI/STEP/ File: 1BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)–3		Id: 37/56



Sheet: /Control/Inst_Decode_ROM/ File: Inst_Decode_ROM.sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)-3		Id: 38/56





Grounding pins 1 and 30 should allow the  
SST39SF010 to be subsituted with a higher memory unit (020,040)

Sheet: /Control/Control\_Rom0/  
File: Control\_Rom.sch

**Title:**

Size: A4

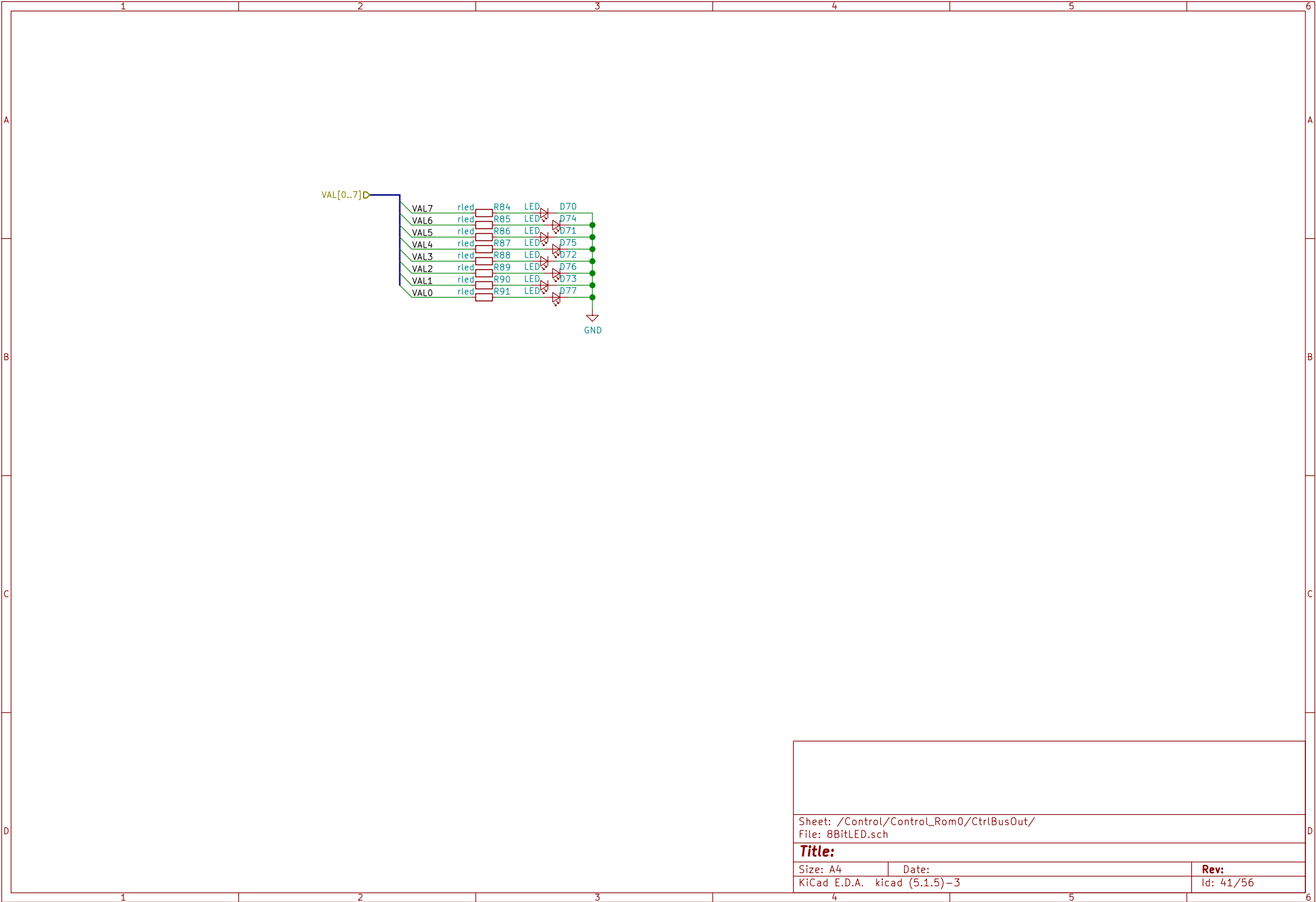
Date:

KiCad E.D.A. kicad (5.1.5)-3

**Rev:**

Id: 40/56





Sheet: /Control/Control\_Rom0/CtrlBusOut/  
File: 8BitLED.sch

**Title:**

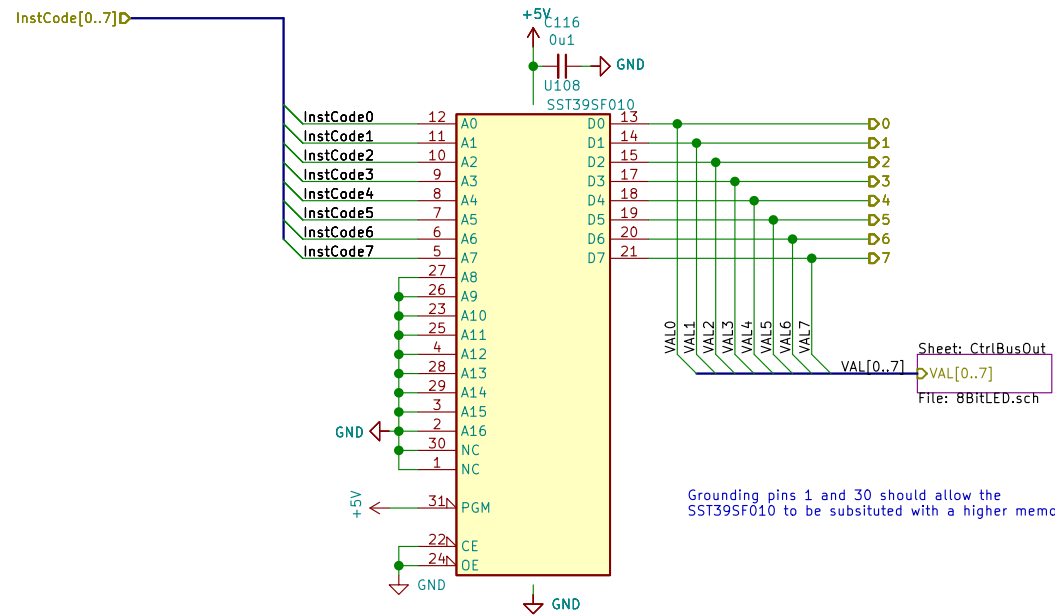
Size: A4

Date:

KiCad E.D.A. kicad (5.1.5)-3

**Rev:**

Id: 41/56



Sheet: /Control/Control\_Rom1/  
File: Control\_Rom.sch

**Title:**

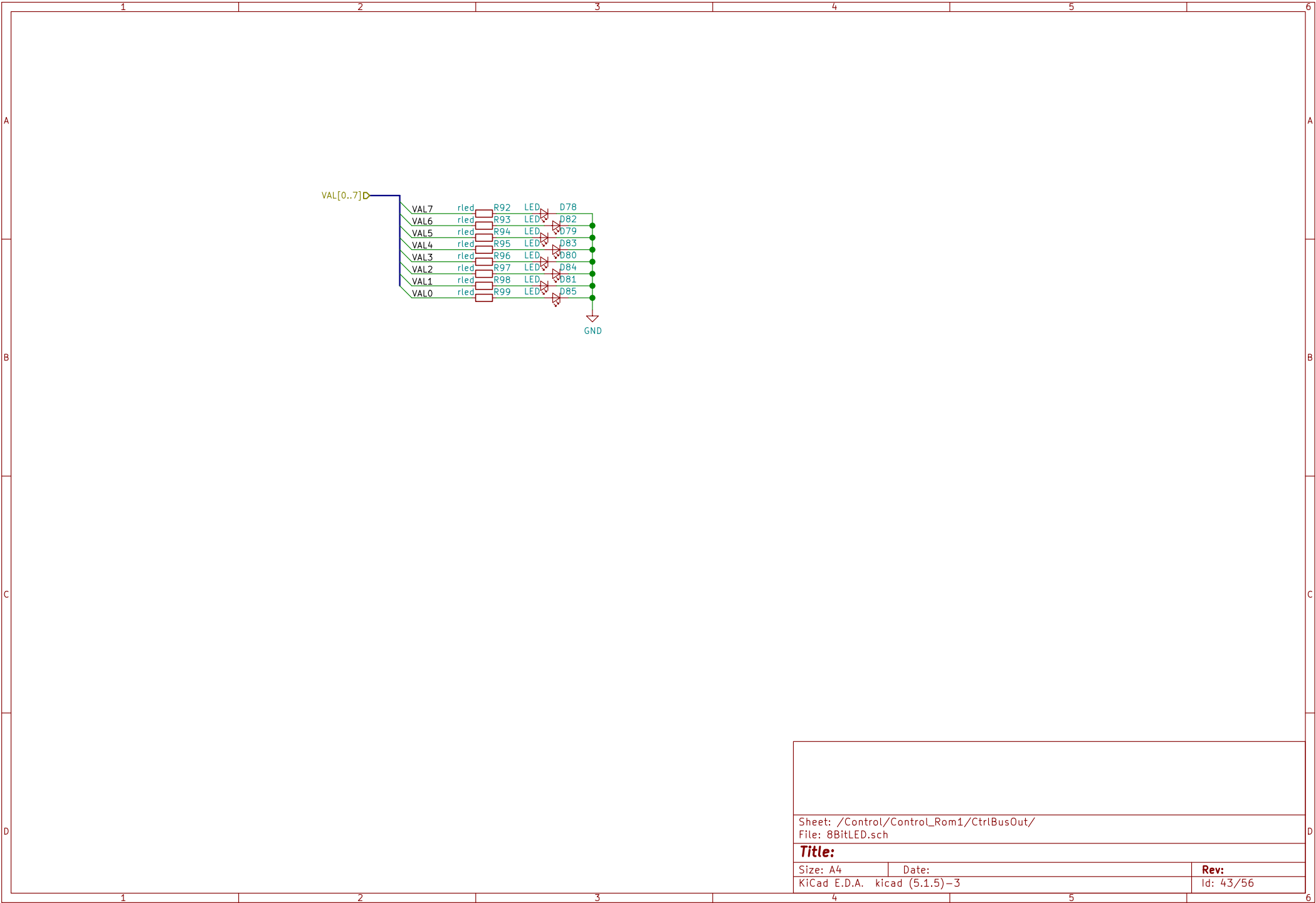
Size: A4

Date:

KiCad E.D.A. kicad (5.1.5)-3

**Rev:**

Id: 42/56



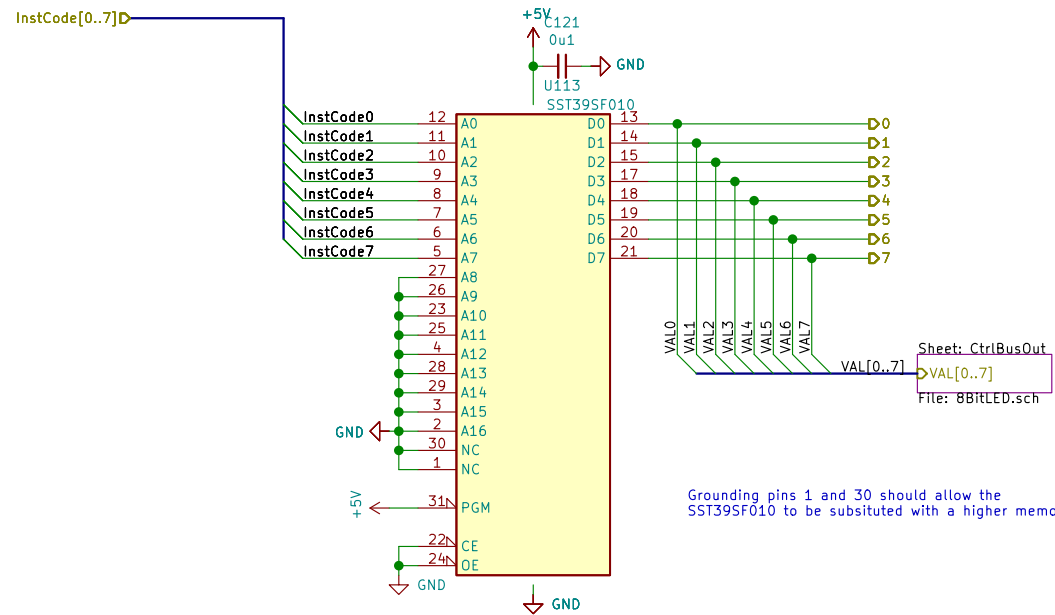
Sheet: /Control/Control\_Rom1/CtrlBusOut/  
File: 8BitLED.sch

**Title:**

Size: A4  
KiCad E.D.A. kicad (5.1.5)-3

Date:

Rev:  
Id: 43/56



Grounding pins 1 and 30 should allow the  
SST39SF010 to be subsituted with a higher memory unit (020,040)

Sheet: /Control/Control\_Rom3/  
File: Control\_Rom.sch

**Title:**

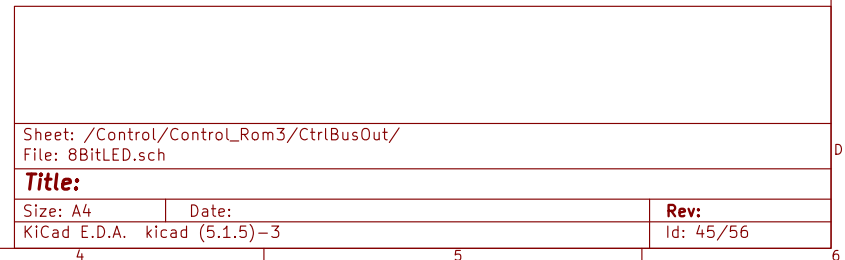
Size: A4

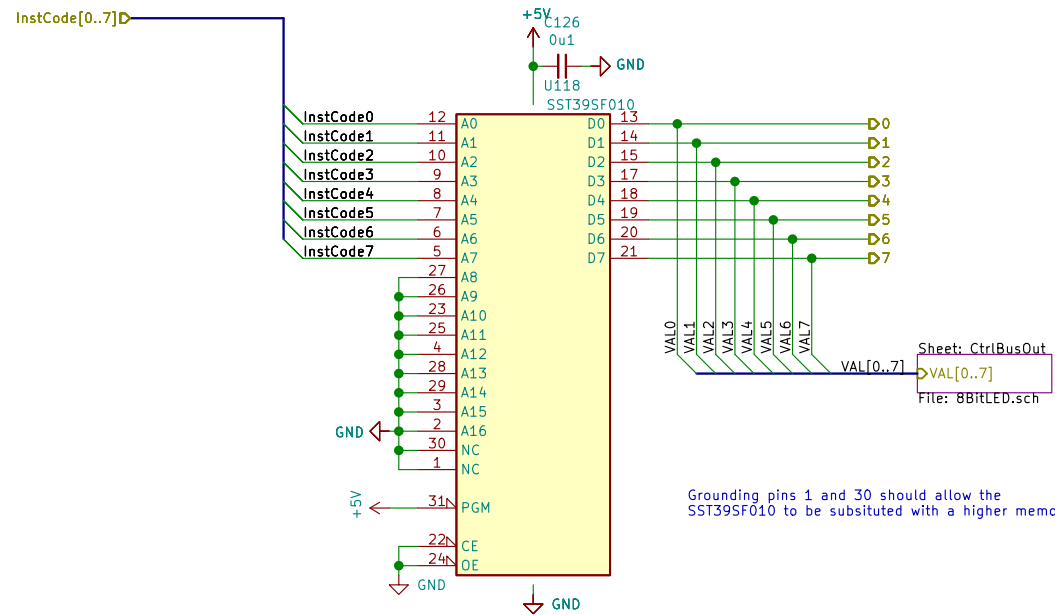
Date:

KiCad E.D.A. kicad (5.1.5)-3

**Rev:**

Id: 44/56





Sheet: /Control/Control\_Rom4/  
File: Control\_Rom.sch

**Title:**

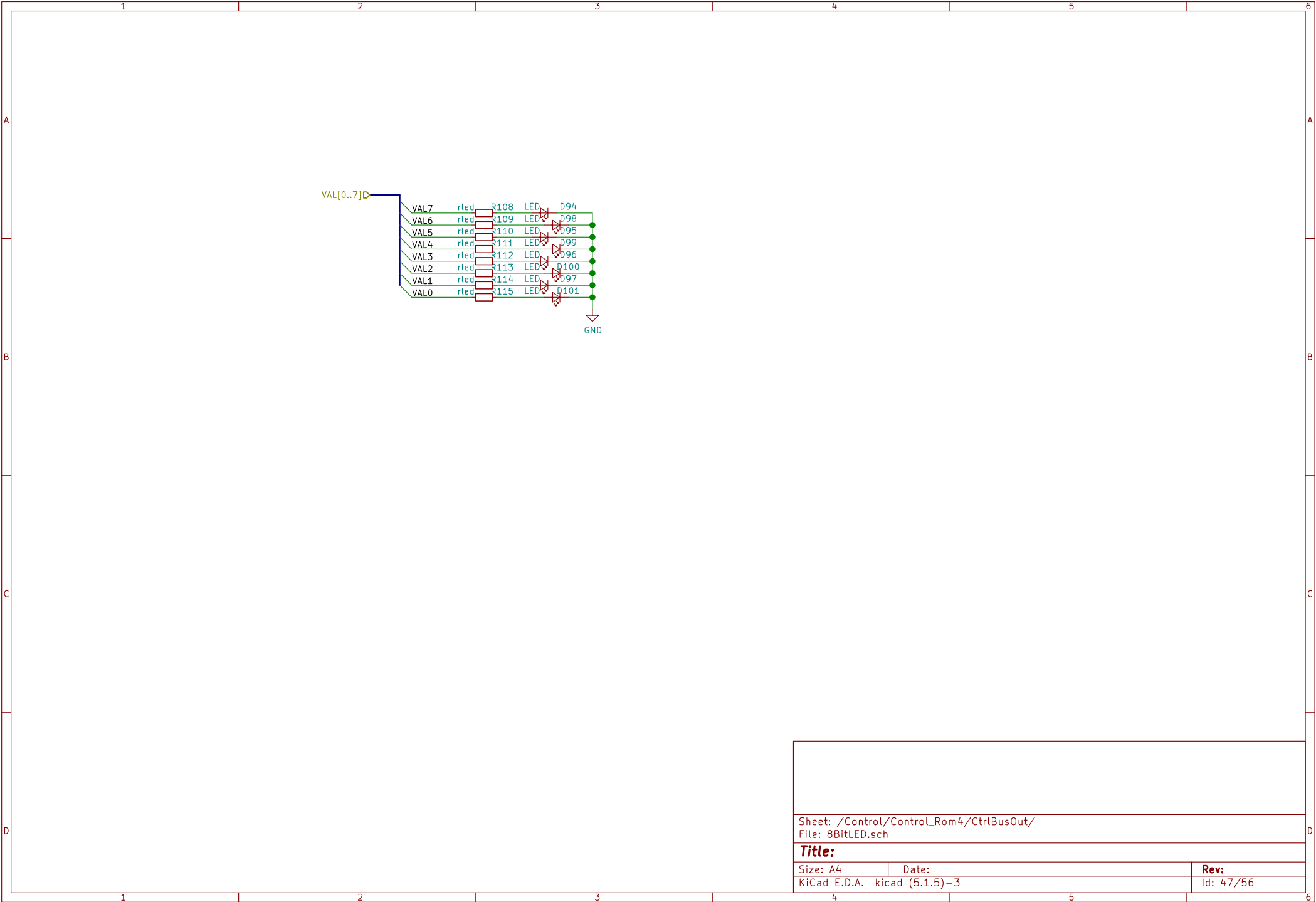
Size: A4

Date:

KiCad E.D.A. kicad (5.1.5)-3

**Rev:**

Id: 46/56



Sheet: /Control/Control\_Rom4/CtrlBusOut/  
File: 8BitLED.sch

**Title:**

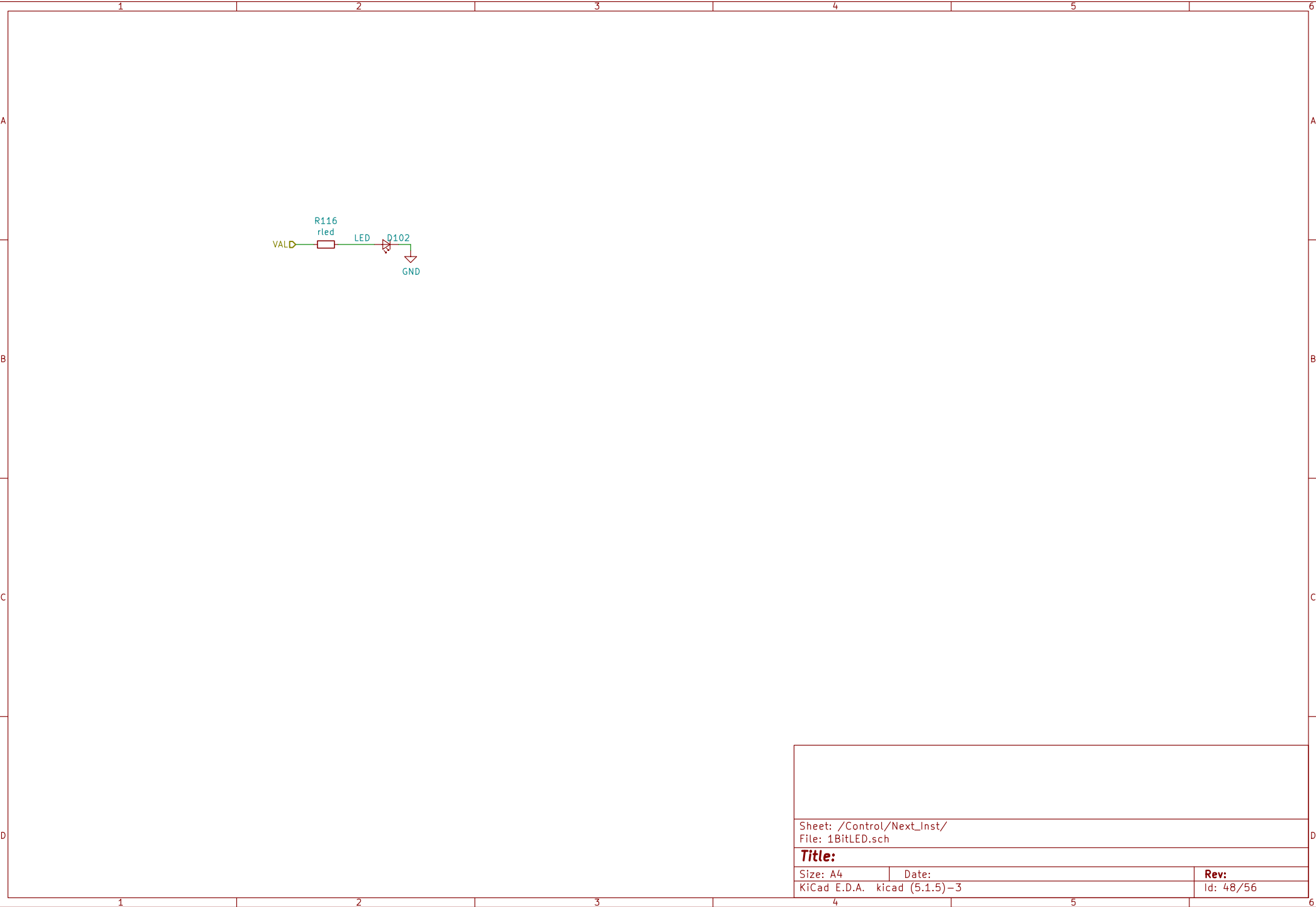
Size: A4

Date:

KiCad E.D.A. kicad (5.1.5)-3

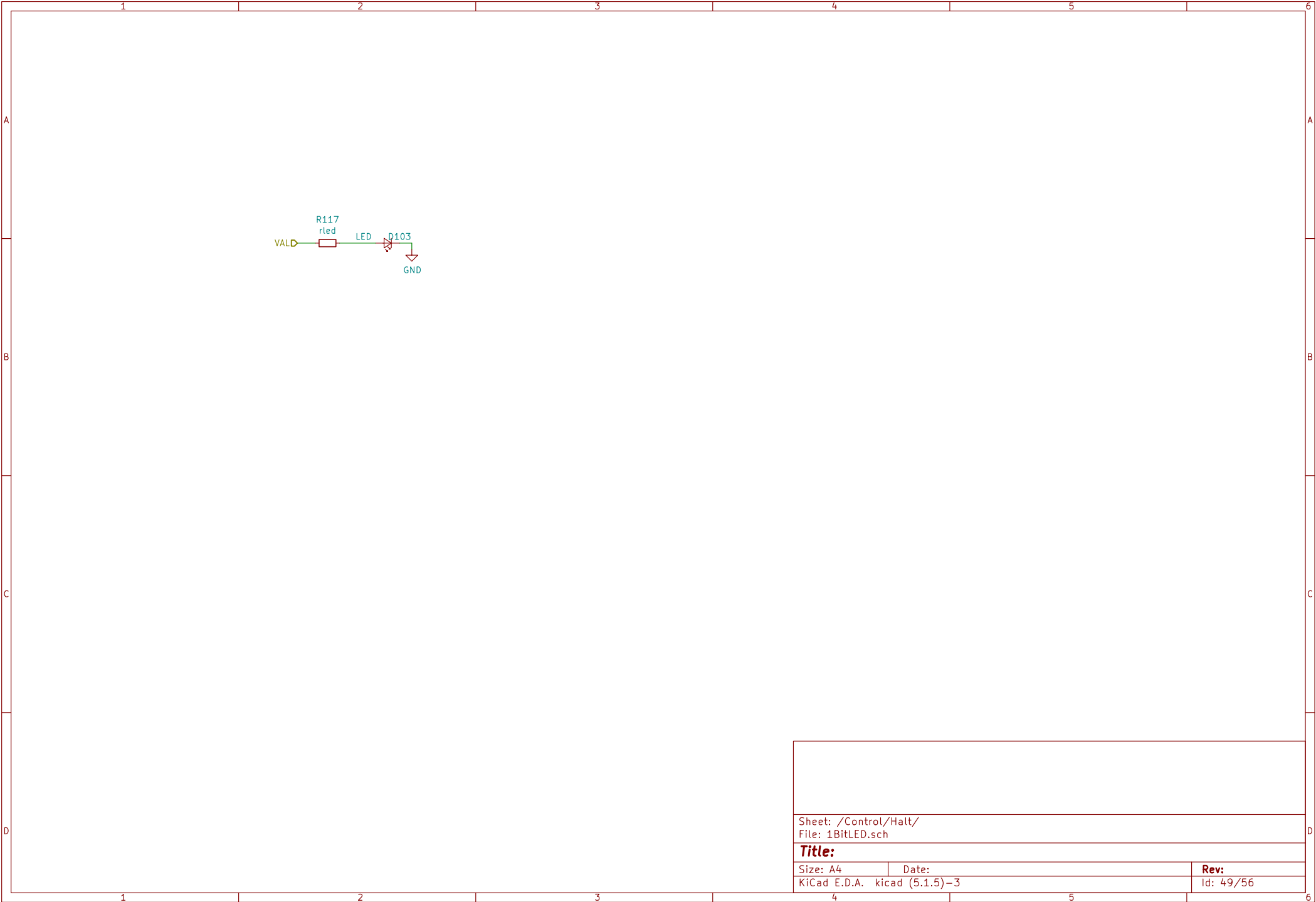
**Rev:**

Id: 47/56

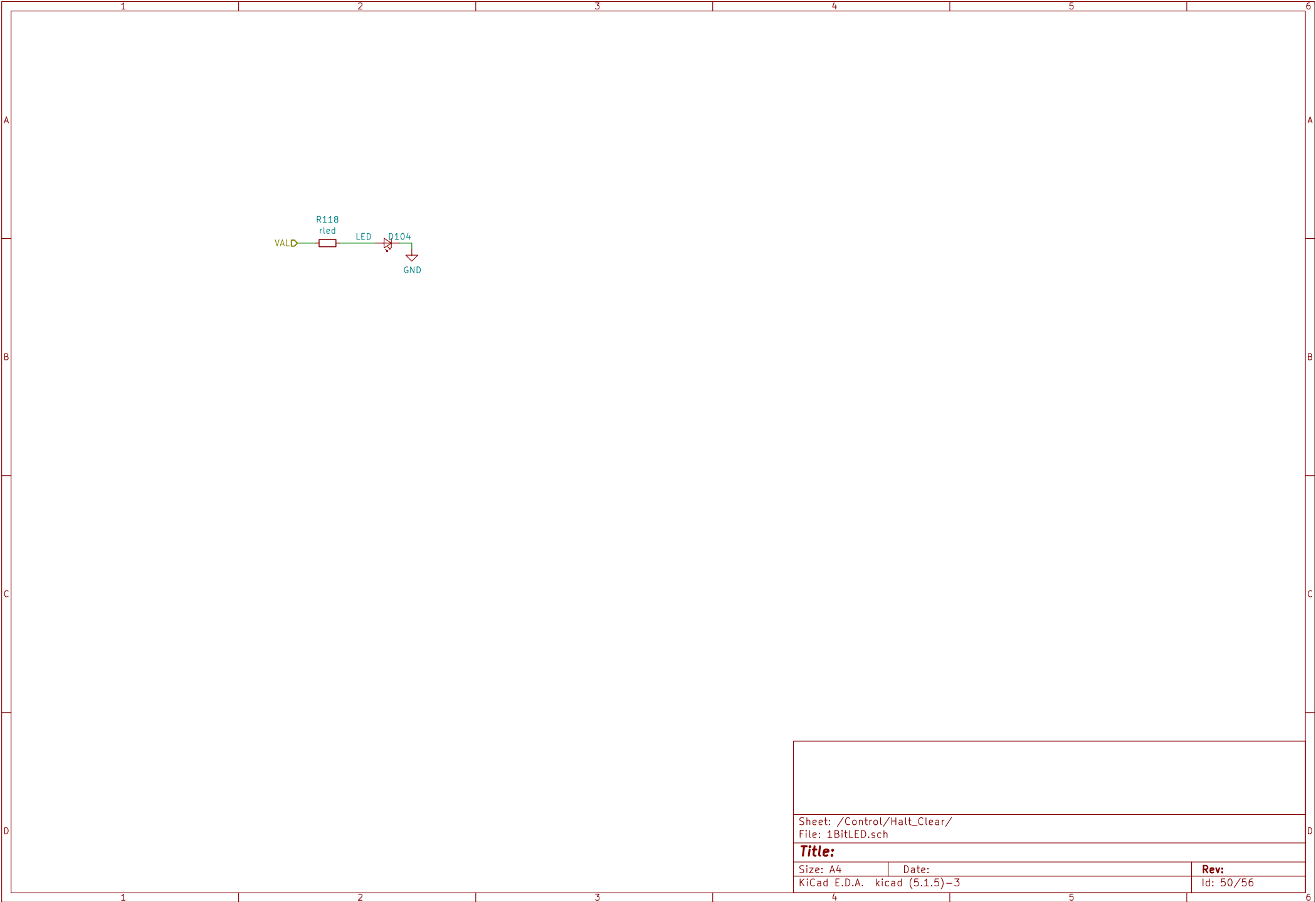


Sheet: /Control/Next_Inst/ File: 1BitLED.sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)–3		Id: 48/56

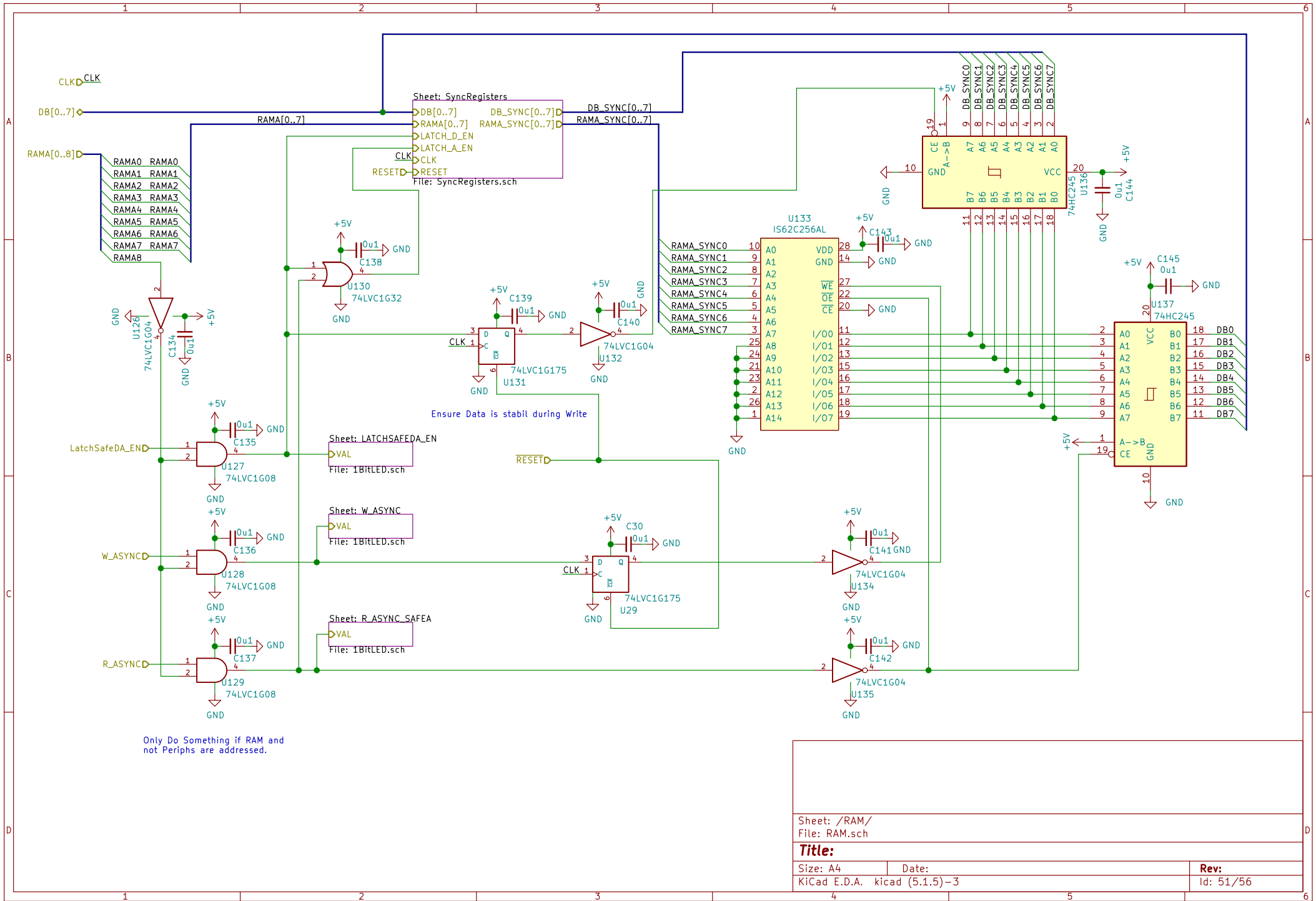


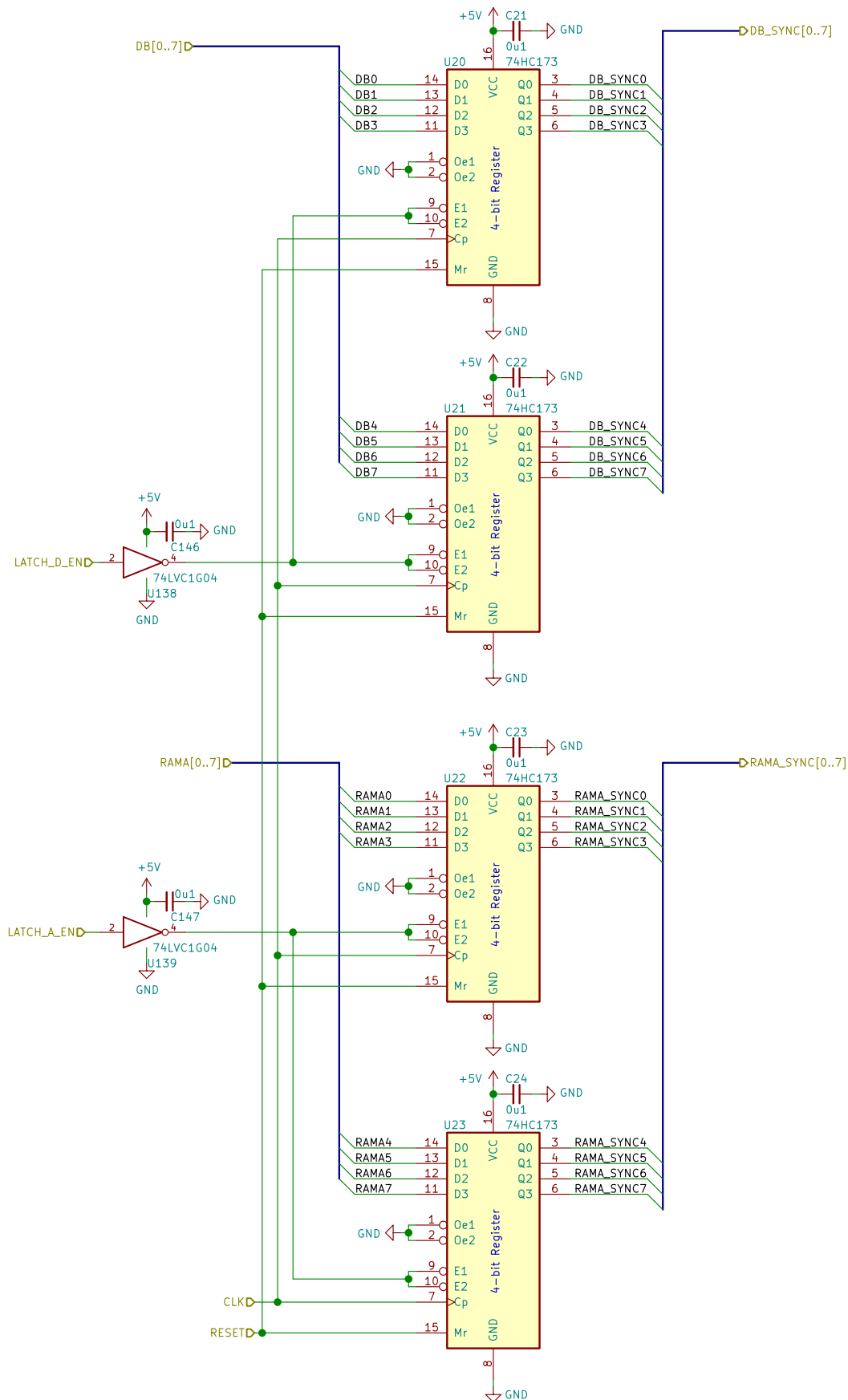


Sheet: /Control/Halt/ File: 1BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)–3		Id: 49/56



Sheet: /Control/Halt_Clear/ File: 1BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)–3		Id: 50/56



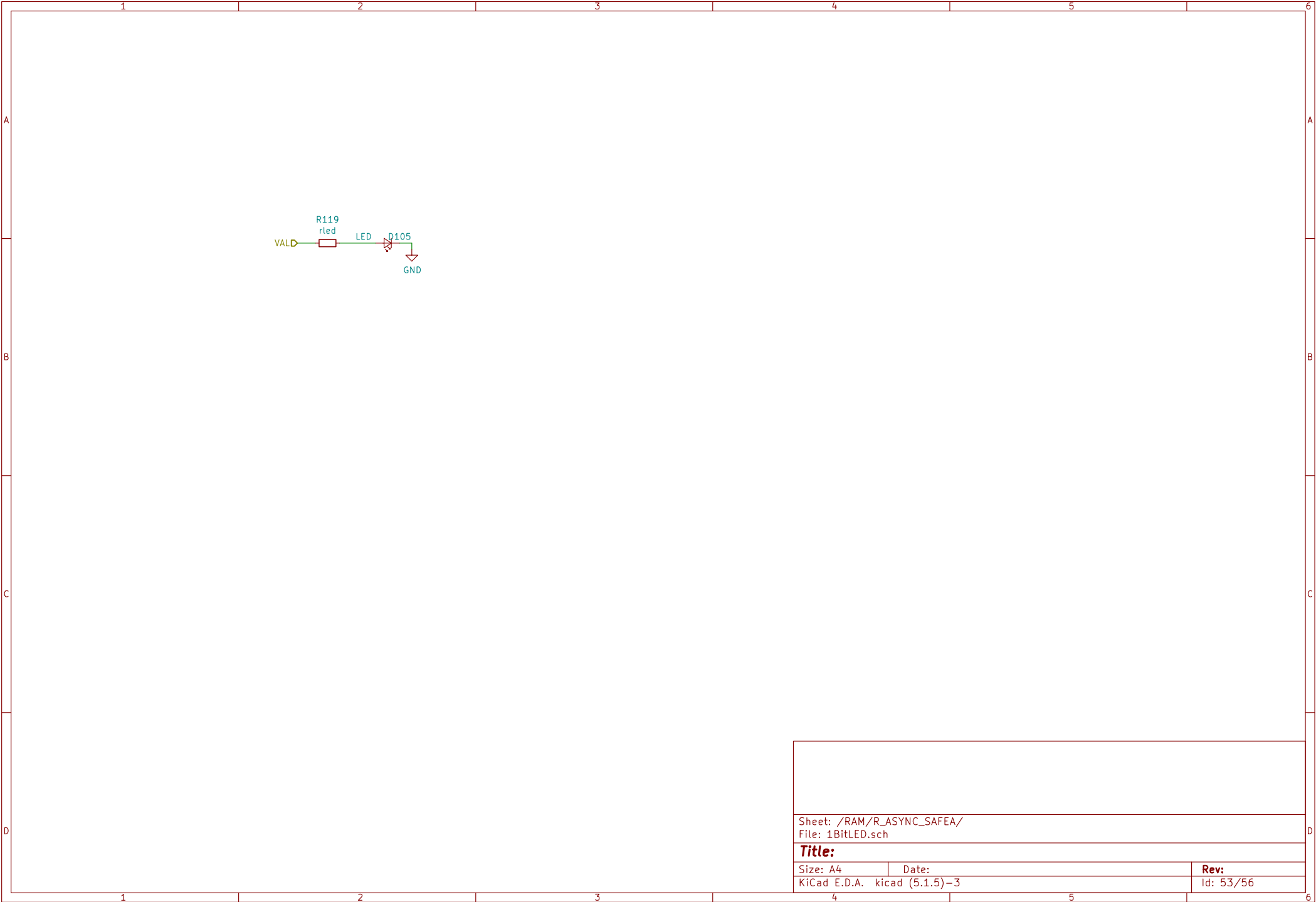


Sheet: /RAM/SyncRegisters/  
File: SyncRegisters.sch

**Title:**

Size: A4 Date:  
KiCad E.D.A. kicad (5.1.5) - 3

**Rev:**  
Id: 52/56



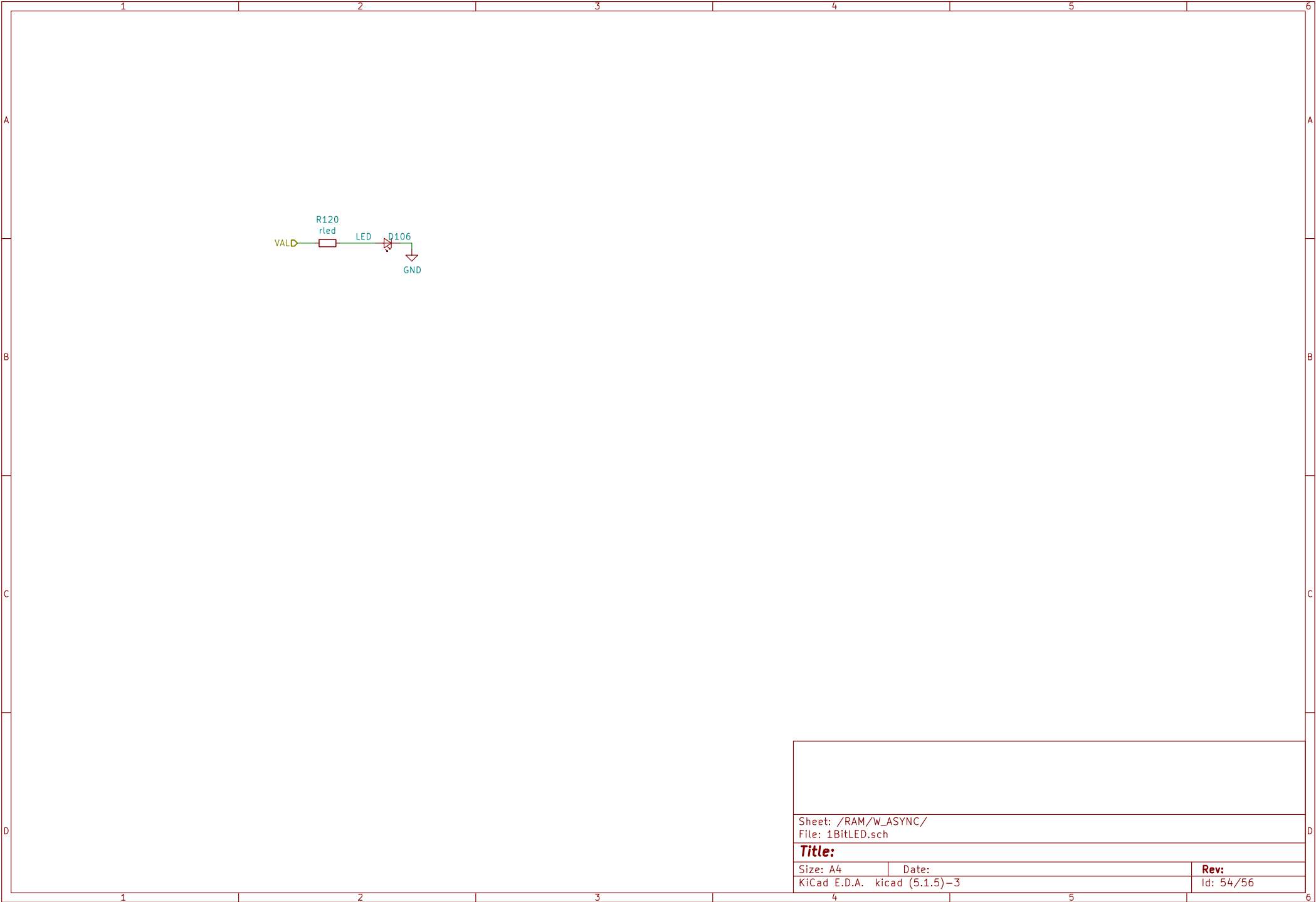
Sheet: /RAM/R\_ASYNC\_SAFEA/  
File: 1BitLED.sch

**Title:**

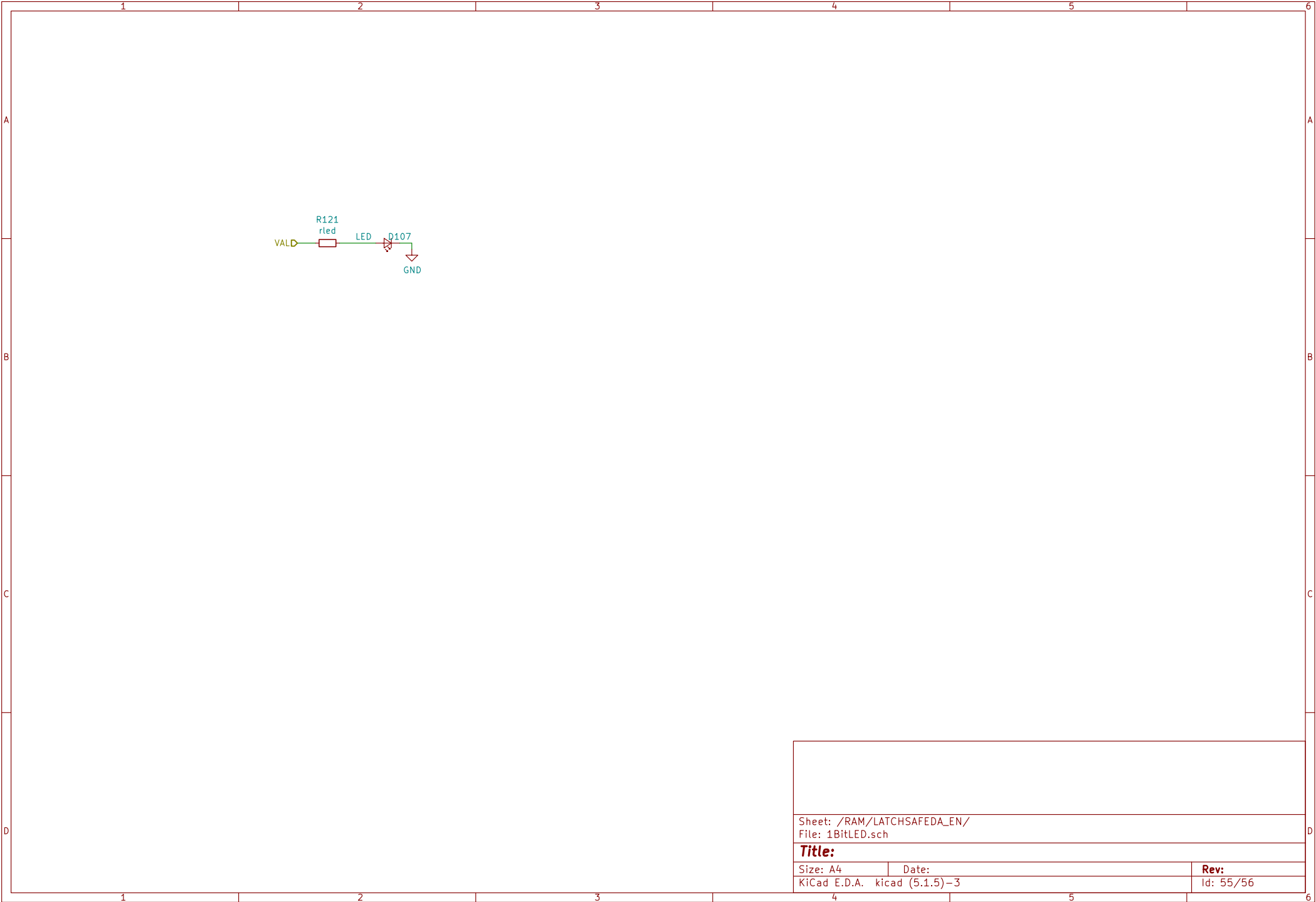
Size: A4  
KiCad E.D.A. kicad (5.1.5)–3

Date:

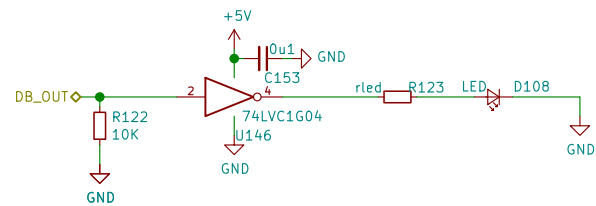
Rev:  
Id: 53/56



Sheet: /RAM/W_ASYNC/ File: 1BitLED.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.5)–3		Id: 54/56



Sheet: /RAM/LATCHSAFEDA_EN/ File: 1BitLED.sch		
<b>Title:</b>		
Size: A4	Date:	<b>Rev:</b>
KiCad E.D.A. kicad (5.1.5)–3		Id: 55/56



Sheet: /DBUS\_DISPLAY.sch/  
File: DBUS\_DISPLAY.sch

**Title:**

Size: A4  
KiCad E.D.A. kicad (5.1.5)-3

Date:

Rev:  
Id: 56/56