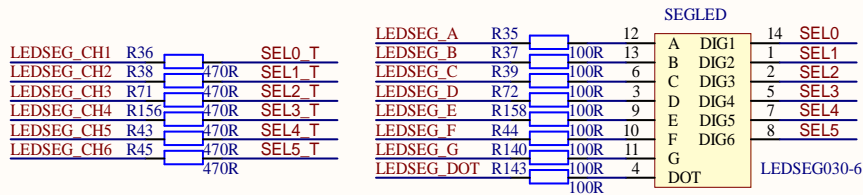
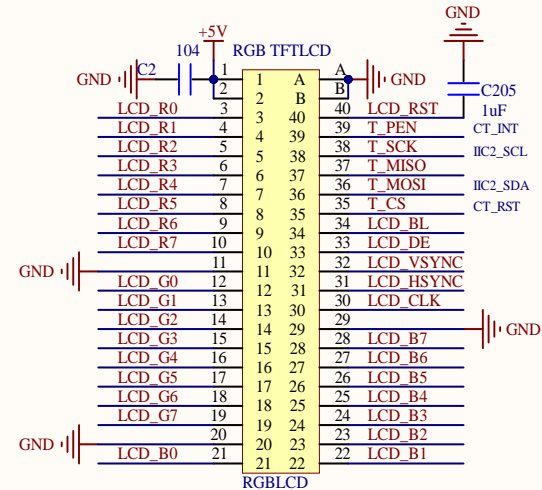


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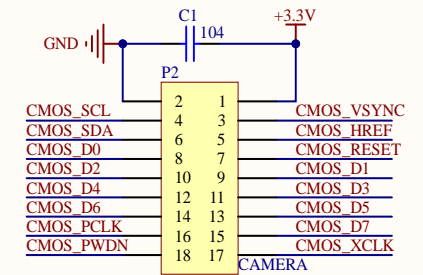
SEGLED



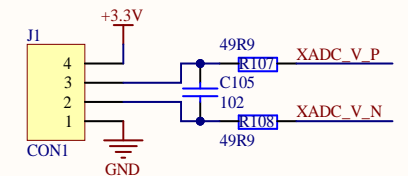
RGB LCD



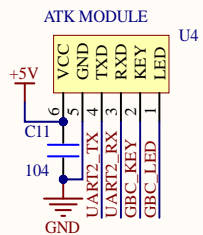
OLED&CAMERA



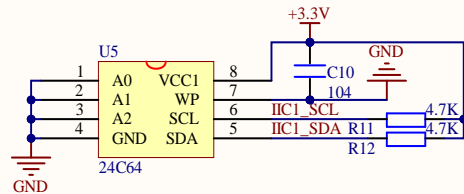
XADC



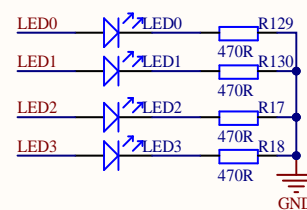
ATK MODULE



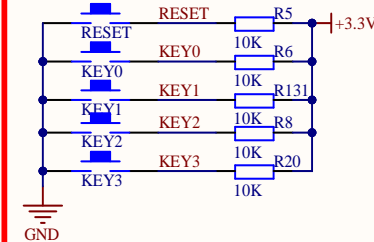
EEPROM



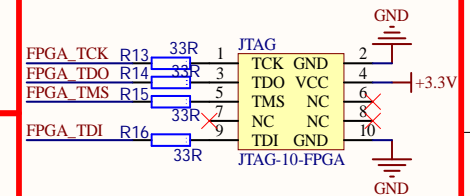
LED



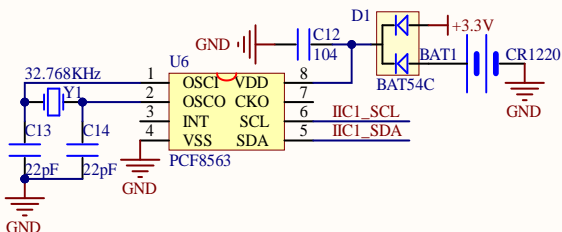
KEY



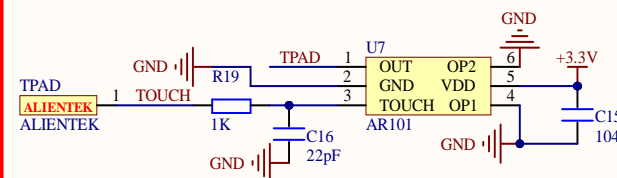
JTAG



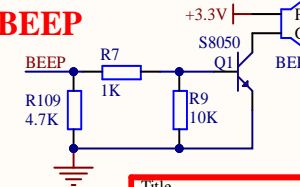
RTC



TOUCH KEY

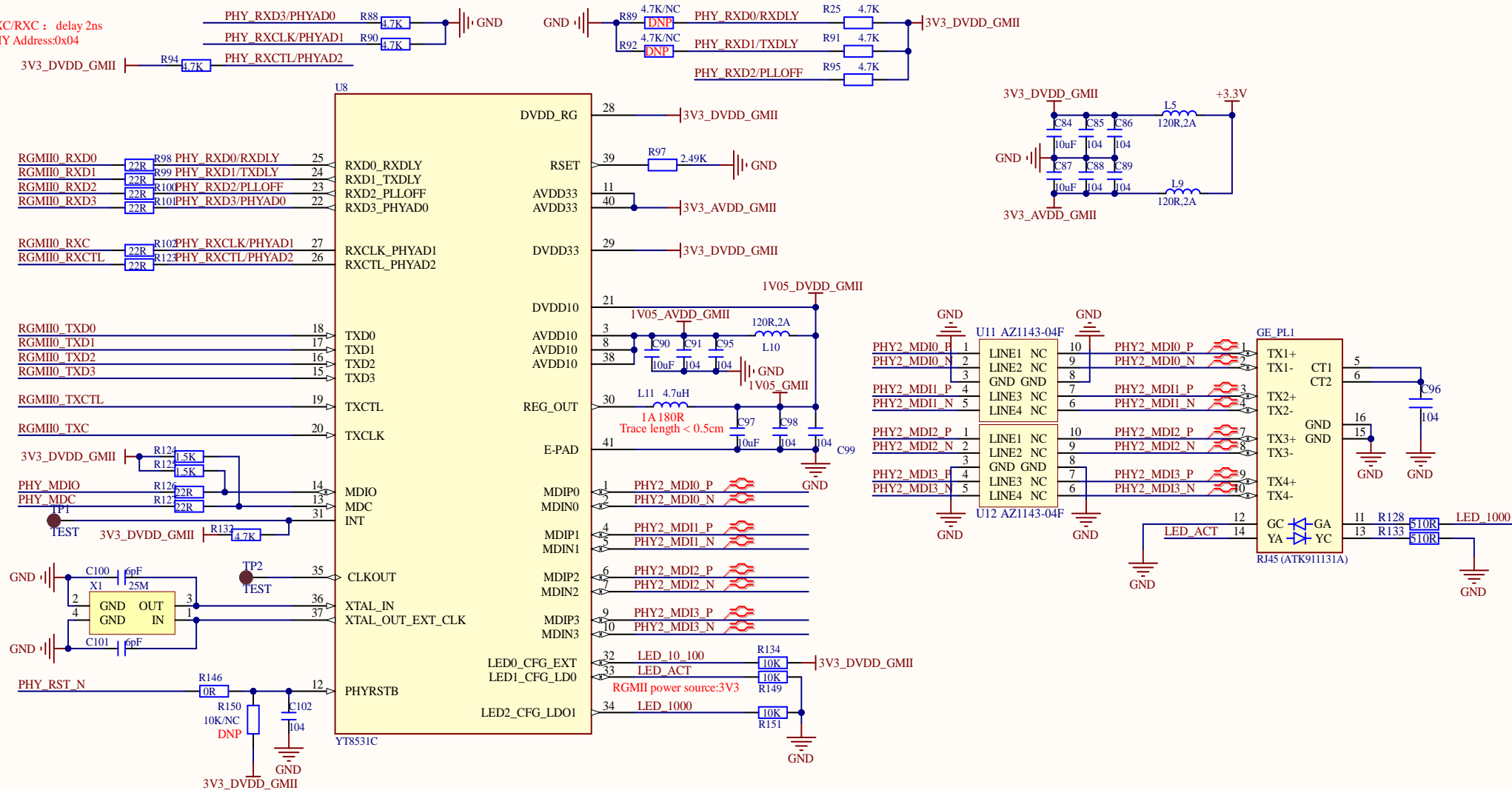


BEEP



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TXC/RXC : delay 2ns
PHY Address:0x04



Title: ATK-DF7A35 V2.1.PrjPCB	
Author: ALIENTEK	Size: A4
Date: 2023/3/13	File: ETH.SchDoc
Revision:	Version: V1.6



U3A

BANK 13

IO_0_13 Y17
IO_L1P_T0_13 Y16
IO_L1N_T0_13 AA16
IO_L2P_T0_13 AB16
IO_L2N_T0_13 AB17
IO_L3P_T0_DQS_13 AA13
IO_L3N_T0_DQS_13 AB13
IO_L4P_T0_13 AA15
IO_L4N_T0_13 AB15
IO_L5P_T0_13 Y13
IO_L5N_T0_13 AA14
IO_L6P_T0_13 W14
IO_L6N_T0_VREF_13 Y14
IO_L7P_T1_13 AB11
IO_L7N_T1_13 AB12
IO_L8P_T1_13 AA9
IO_L8N_T1_13 AB10
IO_L9P_T1_DQS_13 AA10
IO_L9N_T1_DQS_13 AA11
IO_L10P_T1_13 V10
IO_L10N_T1_13 W10
IO_L11P_T1_SRCC_13 Y12
IO_L11N_T1_SRCC_13 W11
IO_L12P_T1_MRCC_13 W12
IO_L12N_T1_MRCC_13 V13
IO_L13P_T2_MRCC_13 V14
IO_L13N_T2_MRCC_13 U15
IO_L14P_T2_SRCC_13 V15
IO_L14N_T2_SRCC_13 T14
IO_L15P_T2_DQS_13 T15
IO_L15N_T2_DQS_13 W15
IO_L16P_T2_13 W16
IO_L16N_T2_13 T16
IO_L17P_T2_13 U16
IO_L17N_T2_13

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U3H

F6 MGTREFCLK0P_216 MGTREF_216 F8 GND
E6 MGTREFCLK0N_216
F10 MGTREFCLK1P_216
E10 MGTREFCLK1N_216
B8 MGTPRX0_216 MGTPXP0_216 B4
A8 MGTPRXN0_216 MGTPTXN0_216 A4
D11 MGTPRX1_216 MGTPXP1_216 D5
C11 MGTPRXN1_216 MGTPTXN1_216 C5
B10 MGTPRX2_216 MGTPXP2_216 B6
A10 MGTPRXN2_216 MGTPTXN2_216 A6
D9 MGTPRX3_216 MGTPXP3_216 D7
C9 MGTPRXN3_216 MGTPTXN3_216 C7

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U3B

BANK 14

IO_0_14 P20 B14_IO0 B14_IO0
IO_L1P_T0_D00_MOSI_14 P22 QSPI_D0
IO_L1N_T0_D01_DIN_14 R22 QSPI_D1
IO_L2P_T0_D02_14 P21 QSPI_D2
IO_L2N_T0_D03_14 R21 QSPI_D3
IO_L3P_T0_DQS_PUDC_B_14 U22 B14_L3_P PHY_MDC
IO_L3N_T0_DQS_EMCCCLK_14 V22 B14_L3_N RGMII0_RXD3
IO_L4P_T0_D04_14 T21 B14_L4_P PHY_RST_N
IO_L4N_T0_D05_14 U21 B14_L4_N PHY_MDIO
IO_L5P_T0_D06_14 P19 B14_L5_P UART2_RX
IO_L5N_T0_D07_14 R19 B14_L5_N UART2_TX
IO_L6P_T0_FCS_B_14 T19 QSPI_CS
IO_L6N_T0_D08_VREF_14 T20 B14_L6_N GBC_KEY
IO_L7P_T1_D09_14 W21 B14_L7_P RGMII0_RXD1
IO_L7N_T1_D10_14 W22 B14_L7_N RGMII0_RXD2
IO_L8P_T1_D11_14 AA20 B14_L8_P RGMII0_TXD1
IO_L8N_T1_D12_14 AA21 B14_L8_N RGMII0_TXC
IO_L9P_T1_DQS_14 Y21 B14_L9_P RGMII0_RXCTL
IO_L9N_T1_DQS_D13_14 Y22 B14_L9_N RGMII0_RXD0
IO_L10P_T1_D14_14 AB21 B14_L10_P RGMII0_TXD0
IO_L10N_T1_D15_14 AB22 B14_L10_N RGMII0_TXD1
IO_L11P_T1_SRCC_14 U20 B14_L11_P GBC_LED
IO_L11N_T1_SRCC_14 V20 B14_L11_N LCD_G4
IO_L12P_T1_MRCC_14 W19 B14_L12_P RGMII0_RXC
IO_L12N_T1_MRCC_14 W20 B14_L12_N LCD_R4
IO_L13P_T2_MRCC_14 Y18 B14_L13_P LCD_R3
IO_L13N_T2_MRCC_14 Y19 B14_L13_N LCD_R2
IO_L14P_T2_SRCC_14 V18 B14_L14_P LCD_R6
IO_L14N_T2_SRCC_14 V19 B14_L14_N LCD_G2
IO_L15P_T2_DQS_RDWR_B_14 AA19 B14_L15_P RGMII0_TXD3
IO_L15N_T2_DQS_DOUT_CSO_B_14 AB20 B14_L15_N RGMII0_TXD2
IO_L16P_T2_CSL_B_14 V17 B14_L16_P LCD_R7
IO_L16N_T2_A15_D31_14 W17 B14_L16_N LCD_R5
IO_L17P_T2_A14_D30_14 AA18 B14_L17_P LCD_R1
IO_L17N_T2_A13_D29_14 AB18 B14_L17_N LCD_R0
IO_L18P_T2_A12_D28_14 U17 B14_L18_P LCD_G1
IO_L18N_T2_A11_D27_14 U18 B14_L18_N LCD_G0
IO_L19P_T3_A10_D26_14 P14 B14_L19_P LCD_B3
IO_L19N_T3_A09_D25_VREF_14 R14 B14_L19_N LCD_B2
IO_L20P_T3_A08_D24_14 R18 B14_L20_P LCD_G5
IO_L20N_T3_A07_D23_14 T18 B14_L20_N LCD_G3
IO_L21P_T3_DQS_14 N17 B14_L21_P LCD_G6
IO_L21N_T3_DQS_A06_D22_14 P17 B14_L21_N LCD_G7
IO_L22P_T3_A05_D21_14 P15 B14_L22_P LCD_B1
IO_L22N_T3_A04_D20_14 R16 B14_L22_N LCD_B0
IO_L23P_T3_A03_D19_14 N13 B14_L23_P LCD_B5
IO_L23N_T3_A02_D18_14 N14 B14_L23_N LCD_B4
IO_L24P_T3_A01_D17_14 P16 B14_L24_P BEEP
IO_L24N_T3_A00_D16_14 R17 B14_L24_N B14_L16_N
IO_25_14 N15 B14_IO25 B14_IO25

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U3C

BANK 15

IO_0_15	J16	B15	IO0	B15	IO0
IO_L1P_T0_AD0P_15	H13	B15	L1_P	LEDSEG	CH4
IO_L1N_T0_AD0N_15	G13	B15	L1_N	LEDSEG	F
IO_L2P_T0_AD8P_15	G15	B15	L2_P	LEDSEG	D
IO_L2N_T0_AD8N_15	G16	B15	L2_N	LEDSEG	B
IO_L3P_T0_DQS_AD1P_15	J14	B15	L3_P	LEDSEG	DOT
IO_L3N_T0_DQS_AD1N_15	H14	B15	L3_N	LEDSEG	G
IO_L4P_T0_15	G17	B15	L4_P	LEDSEG	CH3
IO_L4N_T0_15	G18	B15	L4_N	LEDSEG	CH1
IO_L5P_T0_AD9P_15	J15	B15	L5_P	LEDSEG	CH6
IO_L5N_T0_AD9N_15	H15	B15	L5_N	LEDSEG	A
IO_L6P_T0_15	H17	B15	L6_P	LEDSEG	CH5
IO_L6N_T0_VREF_15	H18	B15	L6_N	LEDSEG	CH2
IO_L7P_T1_AD2P_15	J22	B15	L7_P	B15	L7_P
IO_L7N_T1_AD2N_15	H22	B15	L7_N	B15	L7_N
IO_L8P_T1_AD10P_15	H20	B15	L8_P	B15	L8_P
IO_L8N_T1_AD10N_15	G20	B15	L8_N	B15	L8_N
IO_L9P_T1_DQS_AD3P_15	K21	B15	L9_P	B15	L9_P
IO_L9N_T1_DQS_AD3N_15	K22	B15	L9_N	B15	L9_N
IO_L10P_T1_AD11P_15	M21	B15	L10_P	B15	L10_P
IO_L10N_T1_AD11N_15	J21	B15	L10_N	B15	L10_N
IO_L11P_T1_SRCC_15	J20	B15	L11_P	B15	L11_P
IO_L11N_T1_SRCC_15	J21	B15	L11_N	B15	L11_N
IO_L12P_T1_MRCC_15	J19	B15	L12_P	B15	L12_P
IO_L12N_T1_MRCC_15	H19	B15	L12_N	B15	L12_N
IO_L13P_T2_MRCC_15	K18	B15	L13_P	B15	L13_P
IO_L13N_T2_MRCC_15	K19	B15	L13_N	B15	L13_N
IO_L14P_T2_SRCC_15	L19	B15	L14_P	B15	L14_P
IO_L14N_T2_SRCC_15	J20	B15	L14_N	B15	L14_N
IO_L15P_T2_DQS_15	N22	B15	L15_P	B15	L15_P
IO_L15N_T2_DQS_ADV_B_15	M22	B15	L15_N	B15	L15_N
IO_L16P_T2_A28_15	M18	B15	L16_P	B15	L16_P
IO_L16N_T2_A27_15	L18	B15	L16_N	B15	L16_N
IO_L17P_T2_A26_15	N18	B15	L17_P	B15	L17_P
IO_L17N_T2_A25_15	N19	B15	L17_N	B15	L17_N
IO_L18P_T2_A24_15	N20	B15	L18_P	B15	L18_P
IO_L18N_T2_A23_15	M20	B15	L18_N	B15	L18_N
IO_L19P_T3_A22_15	K13	B15	L19_P	LEDSEG	E
IO_L19N_T3_A21_VREF_15	K14	B15	L19_N	B15	L19_N
IO_L20P_T3_A20_15	M13	B15	L20_P	B15	L20_P
IO_L20N_T3_A19_15	J13	B15	L20_N	LEDSEG	C
IO_L21P_T3_DQS_15	K17	B15	L21_P	B15	L21_P
IO_L21N_T3_DQS_A18_15	J17	B15	L21_N	B15	L21_N
IO_L22P_T3_A17_15	L14	B15	L22_P	B15	L22_P
IO_L22N_T3_A16_15	L15	B15	L22_N	B15	L22_N
IO_L23P_T3_FOE_B_15	L16	B15	L23_P	B15	L23_P
IO_L23N_T3_FWE_B_15	K16	B15	L23_N	B15	L23_N
IO_L24P_T3_RS0_15	M15	B15	L24_P	B15	L24_P
IO_L24N_T3_RS0_15	M16	B15	L24_N	B15	L24_N
IO_25_15	M17	B15	IO25	B15	IO25

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U3D

BANK 16

IO_0_16	F15	B16	IO0	CMOS	XCLK
IO_L1P_T0_16	F13			CMOS	D7
IO_L1N_T0_16	F14			CMOS	PCLK
IO_L2P_T0_16	F16			CMOS	PWDN
IO_L2N_T0_16	E17			B16	L2_N
IO_L3P_T0_DQS_16	C14			CMOS	D0
IO_L3N_T0_DQS_16	C15			CMOS	RESET
IO_L4P_T0_16	E13			CMOS	D6
IO_L4N_T0_16	E14	B16	L4_N	CMOS	D5
IO_L5P_T0_16	E16	B16	L5_P	CMOS	VSYN
IO_L5N_T0_16	D16	B16	L5_N	CMOS	SCL
IO_L6P_T0_16	D14	B16	L6_P	CMOS	D4
IO_L6N_T0_VREF_16	D15	B16	L6_N	CMOS	D3
IO_L7P_T1_16	B15			CMOS	HREF
IO_L7N_T1_16	B16			CMOS	SDA
IO_L8P_T1_16	C13			CMOS	D2
IO_L8N_T1_16	B13			CMOS	D1
IO_L9P_T1_DQS_16	A15	B16	L9_P	HDMIB	D1_P
IO_L9N_T1_DQS_16	A16	B16	L9_N	HDMIB	D1_N
IO_L10P_T1_16	A13			HDMIB	D2_P
IO_L10N_T1_16	A14			HDMIB	D2_N
IO_L11P_T1_SRCC_16	B17	B16	L11_P	HDMIB	CLK_P
IO_L11N_T1_SRCC_16	B18	B16	L11_N	HDMIB	CLK_N
IO_L12P_T1_MRCC_16	D17			B16	L12_P
IO_L12N_T1_MRCC_16	C17			HDMIB	SCL
IO_L13P_T2_MRCC_16	C18	B16	L13_P	HDMIA	CLK_P
IO_L13N_T2_MRCC_16	C19	B16	L13_N	HDMIA	CLK_N
IO_L14P_T2_SRCC_16	E19	B16	L14_P	B16	L14_P
IO_L14N_T2_SRCC_16	D19	B16	L14_N	B16	L14_N
IO_L15P_T2_DQS_16	F18	B16	L15_P	B16	L15_P
IO_L15N_T2_DQS_16	E18	B16	L15_N	HDMIB	HPD
IO_L16P_T2_16	B20	B16	L16_P	HDMIA	D2_P
IO_L16N_T2_16	A20	B16	L16_N	HDMIA	D2_N
IO_L17P_T2_16	A18	B16	L17_P	HDMIB	D0_P
IO_L17N_T2_16	A19	B16	L17_N	HDMIB	D0_N
IO_L18P_T2_16	F19	B16	L18_P	B16	L18_P
IO_L18N_T2_16	F20	B16	L18_N	B16	L18_N
IO_L19P_T3_16	D20	B16	L19_P	HDMIB	SDA
IO_L19N_T3_VREF_16	C20	B16	L19_N	HDMIB	OUT_EN
IO_L20P_T3_16	C22	B16	L20_P	HDMIA	D0_P
IO_L20N_T3_16	B22	B16	L20_N	HDMIA	D0_N
IO_L21P_T3_DQS_16	B21	B16	L21_P	HDMIA	D1_P
IO_L21N_T3_DQS_16	A21	B16	L21_N	HDMIA	D1_N
IO_L22P_T3_16	E22	B16	L22_P	HDMIA	HPD
IO_L22N_T3_16	D22	B16	L22_N	HDMIA	SCL
IO_L23P_T3_16	E21	B16	L23_P	HDMIA	OUT_EN
IO_L23N_T3_16	D21	B16	L23_N	HDMIA	SDA
IO_L24P_T3_16	G21	B16	L24_P	B16	L24_P
IO_L24N_T3_16	G22	B16	L24_N	B16	L24_N
IO_25_16	F21	B16	IO25	B16	IO25

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U3F

BANK 35

IO_0_35	F4	DDR3 RESET_N	
IO_L1P_T0_AD4P_35	B1	DDR3 DQ12	
IO_L1N_T0_AD4N_35	A1	DDR3 DQ10	
IO_L2P_T0_AD12P_35	C2	DDR3 DQ14	
IO_L2N_T0_AD12N_35	B2	DDR3 DQ8	
IO_L3P_T0_DQS_AD5P_35	E1	DDR3 DQS_P1	
IO_L3N_T0_DQS_AD5N_35	D1	DDR3 DQS_N1	
IO_L4P_T0_35	E2	DDR3 UDM	
IO_L4N_T0_35	D2	DDR3 DQ11	
IO_L5P_T0_AD13P_35	G1	DDR3 DQ13	
IO_L5N_T0_AD13N_35	F1	DDR3 DQ9	
IO_L6P_T0_35	F3	DDR3 DQ15	
IO_L6N_T0_VREF_35	E3		
IO_L7P_T1_AD6P_35	K1	DDR3 DQ3	✗
IO_L7N_T1_AD6N_35	J1	DDR3 LDM	
IO_L8P_T1_AD14P_35	H2	DDR3 DQ2	
IO_L8N_T1_AD14N_35	G2	DDR3 DQ1	
IO_L9P_T1_DQS_AD7P_35	K2	DDR3 DQS_P0	
IO_L9N_T1_DQS_AD7N_35	J2	DDR3 DQS_N0	
IO_L10P_T1_AD15P_35	H5	DDR3 DQ4	
IO_L10N_T1_AD15N_35	H3	DDR3 DQ6	
IO_L11P_T1_SRCC_35	G3	DDR3 DQ5	
IO_L11N_T1_SRCC_35	G4	DDR3 DQ7	
IO_L12P_T1_MRCC_35	K4	DDR3 DQ0	
IO_L12N_T1_MRCC_35	J4		
IO_L13P_T2_MRCC_35	K4	DDR3_CKE	✗
IO_L13N_T2_MRCC_35	J4	DDR3_RAS_N	
IO_L14P_T2_SRCC_35	L3	DDR3_A8	
IO_L14N_T2_SRCC_35	K3	DDR3_A1	
IO_L15P_T2_DQS_35	M1	DDR3_WE_N	
IO_L15N_T2_DQS_35	L1	DDR3_BA1	
IO_L16P_T2_35	M3	DDR3_A6	
IO_L16N_T2_35	M2	DDR3_A4	
IO_L17P_T2_35	K6	DDR3_A13	
IO_L17N_T2_35	J6	DDR3_A11	
IO_L18P_T2_35	L5	DDR3_A12	
IO_L18N_T2_35	L4	DDR3_A10	
IO_L19P_T3_35	N4	DDR3_A7	
IO_L19N_T3_VREF_35	N3	DDR3_A5	
IO_L20P_T3_35	R1	DDR3_CAS_N	
IO_L20N_T3_35	P1	DDR3_ODT	
IO_L21P_T3_DQS_35	P5	DDR3_CLK_P	
IO_L21N_T3_DQS_35	P4	DDR3_CLK_N	
IO_L22P_T3_35	P2	DDR3_A0	
IO_L22N_T3_35	N2	DDR3_BA0	
IO_L23P_T3_35	M6	DDR3_A3	
IO_L23N_T3_35	M5	DDR3_BA2	
IO_L24P_T3_35	P6	DDR3_A9	
IO_L24N_T3_35	N5	DDR3_A2	
IO_25_35	L6	DDR3_CS_N	

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U3E

BANK 34

IO_0_34	T3	KEY3	
IO_L1P_T0_34	T1	KEY0	
IO_L1N_T0_34	J1	KEY1	
IO_L2P_T0_34	J2	RESET	
IO_L2N_T0_34	V2	LED2	
IO_L3P_T0_DQS_34	R3	LED1	
IO_L3N_T0_DQS_34	R2	LED0	
IO_L4P_T0_34	W2	KEY2	
IO_L4N_T0_34	V2	LED3	
IO_L5P_T0_34	W1	USBSS_RXF	
IO_L5N_T0_34	Y1	USBSS_D7	
IO_L6P_T0_34	U3	USBSS_OE	
IO_L6N_T0_VREF_34	V3	USBSS_SIWUA	
IO_L7P_T1_34	AA1	USBSS_D6	
IO_L7N_T1_34	AB1	USBSS_D5	
IO_L8P_T1_34	AB3	USBSS_D2	
IO_L8N_T1_34	AB2	USBSS_D4	
IO_L9P_T1_DQS_34	V3	USBSS_WR	
IO_L9N_T1_DQS_34	AA3	USBSS_D3	
IO_L10P_T1_34	AA5	USBSS_TXF	
IO_L10N_T1_34	AB5	USBSS_D0	
IO_L11P_T1_SRCC_34	Y4	USBSS_CLKOUT	
IO_L11N_T1_SRCC_34	AA4	USBSS_D1	
IO_L12P_T1_MRCC_34	V4	USBSS_RD	
IO_L12N_T1_MRCC_34	W4	TF_DAT3	
IO_L13P_T2_MRCC_34	R4	FPGA_GCLK1	
IO_L13N_T2_MRCC_34	T4	IIC1_SDA	
IO_L14P_T2_SRCC_34	T5	TPAD	
IO_L14N_T2_SRCC_34	U5	UART1_RXD	
IO_L15P_T2_DQS_34	W6	TF_DAT0	
IO_L15N_T2_DQS_34	W5	TF_CMD	
IO_L16P_T2_34	U6	TF_DAT2	
IO_L16N_T2_34	V5	TF_CLK	
IO_L17P_T2_34	R6	IIC1_SCL	
IO_L17N_T2_34	T6	UART1_TXD	
IO_L18P_T2_34	Y6	TF_DAT1	
IO_L18N_T2_34	AA6	TF_CD	
IO_L19P_T3_34	V7	LCD_BL	
IO_L19N_T3_VREF_34	W7	LCD_RST	
IO_L20P_T3_34	AB7	LCD_DE	
IO_L20N_T3_34	AB6	T_CS	
IO_L21P_T3_DQS_34	V9	LCD_B6	
IO_L21N_T3_DQS_34	V8	LCD_HSYNC	
IO_L22P_T3_34	AA8	T_MISO	
IO_L22N_T3_34	AB8	T_MOSI	
IO_L23P_T3_34	Y8	T_SCK	
IO_L23N_T3_34	V7	T_PEN	
IO_L24P_T3_34	W9	LCD_B7	
IO_L24N_T3_34	Y9	LCD_CLK	
IO_25_34	U7	LCD_VSYNC	

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Title		
Size	Number	Revision
A4		
Date:	3/13/2023	Sheet of
File:	H:\11111\...\FPGA_Bank34_35.SchDoc	Drawn By:

1

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A

B

C

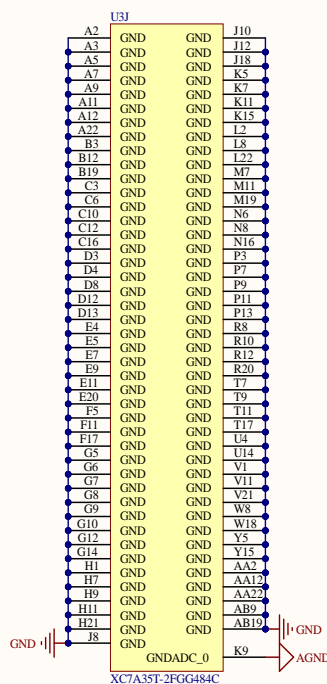
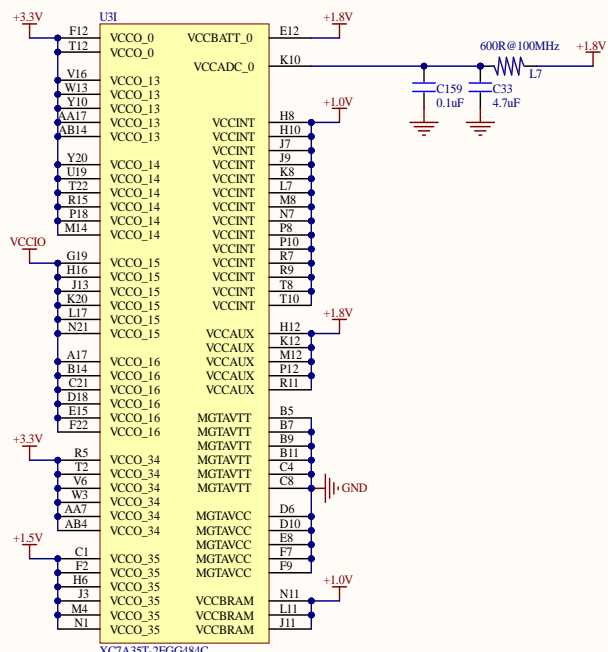
D

A

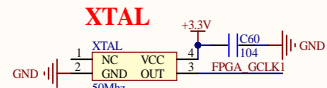
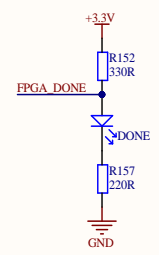
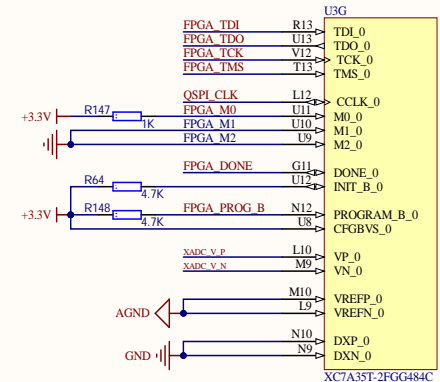
B

C

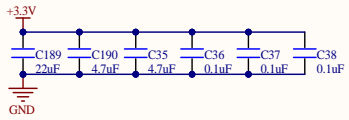
D



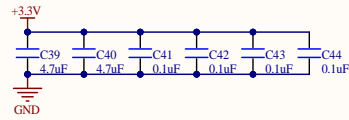
MASTER SPI x4 M[2:0] = 001



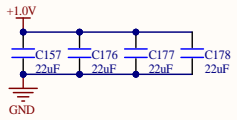
For VCCO_13



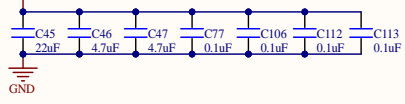
For VCCO_34



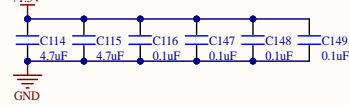
For VCCINT



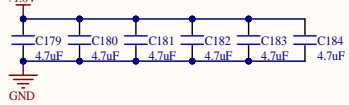
For VCCO_14



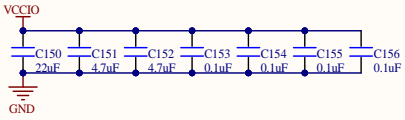
For VCCO_35



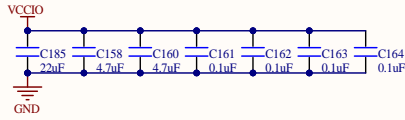
For VCCINT



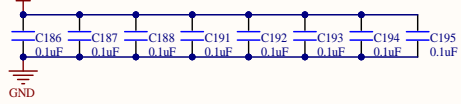
For VCCO_15



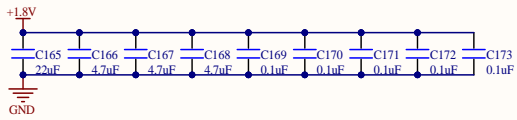
For VCCO_16



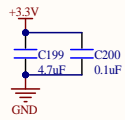
For VCCINT



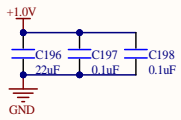
For VCCAUX



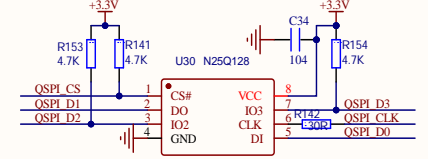
For VCCO_0



For VCCBRAM

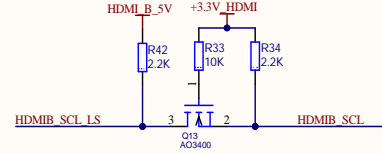
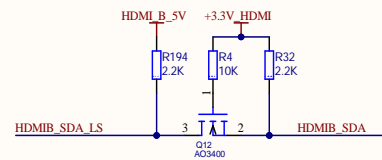
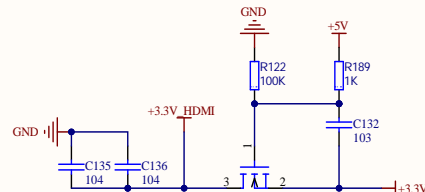
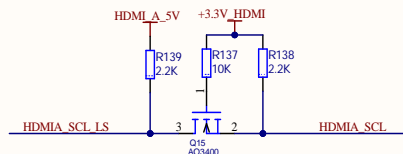
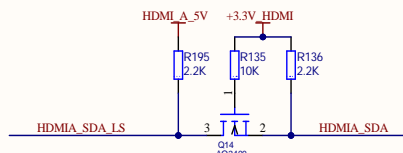
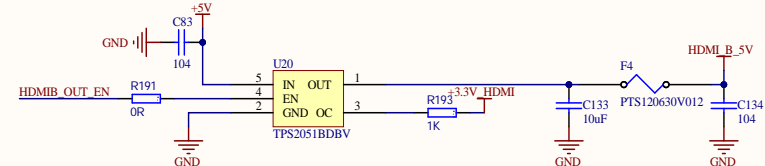
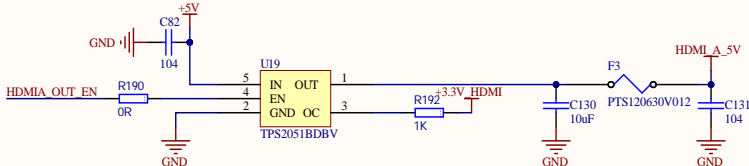
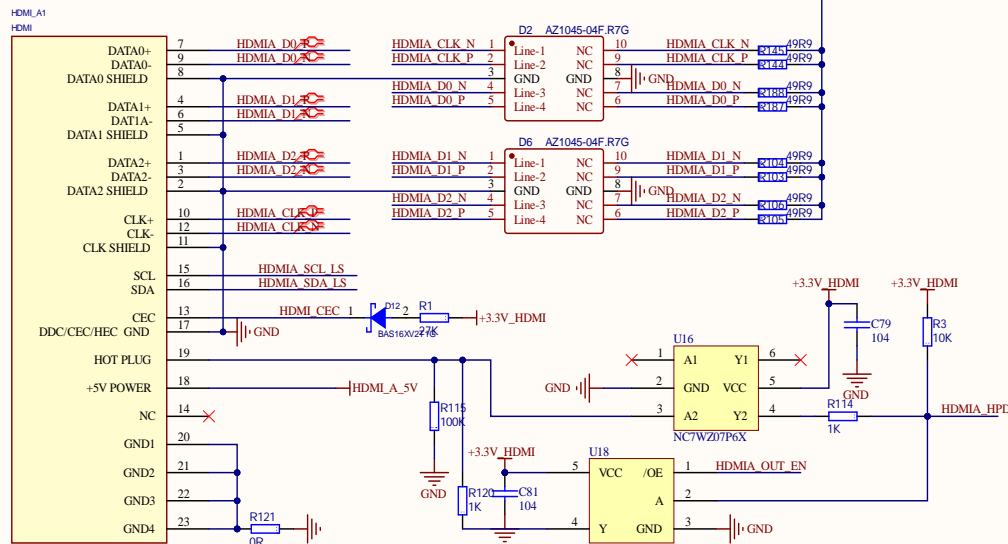


QSPI FLASH

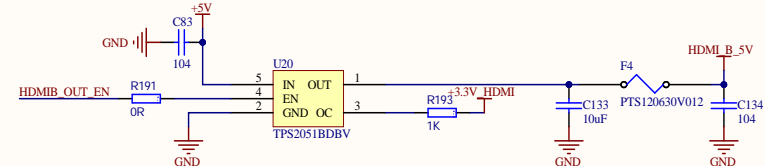
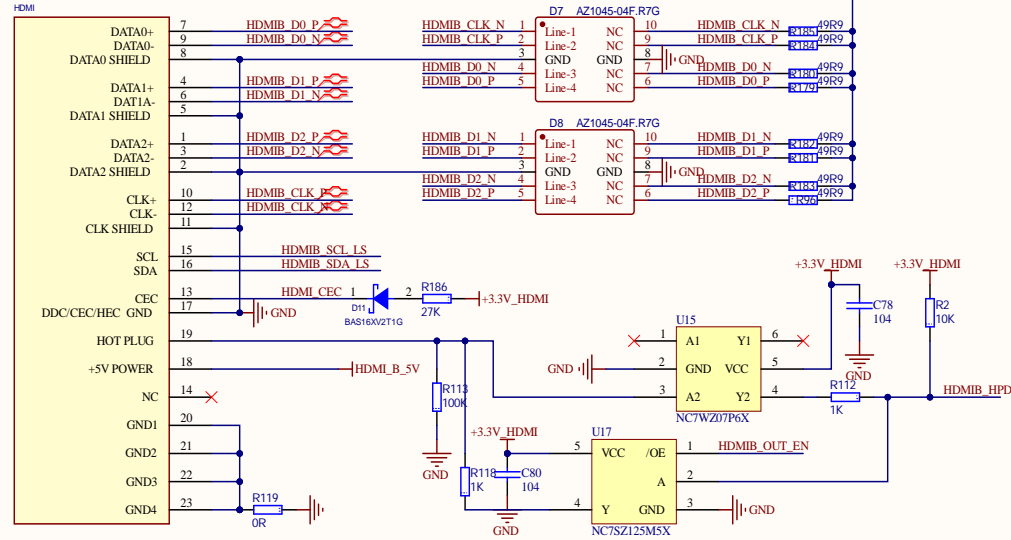


Title			
Size	Number	Revision	
A3			
Date:	3/13/2023	Sheet of	
File:	H:\11111\FPGA_Power_Config.SchDoc	Drawn By:	

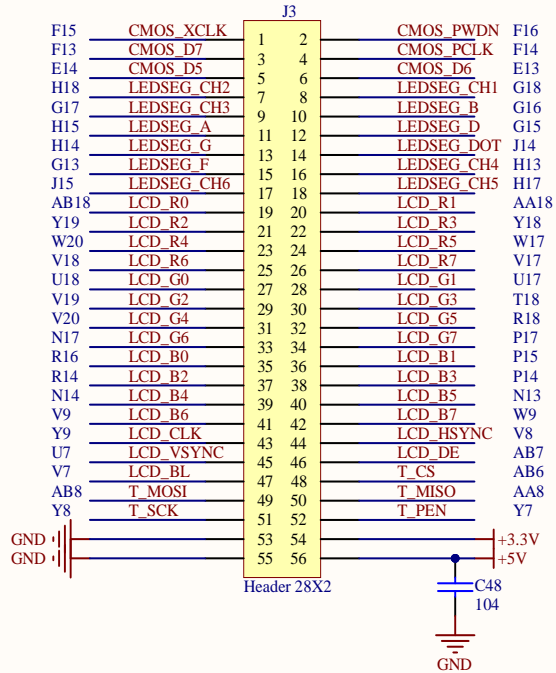
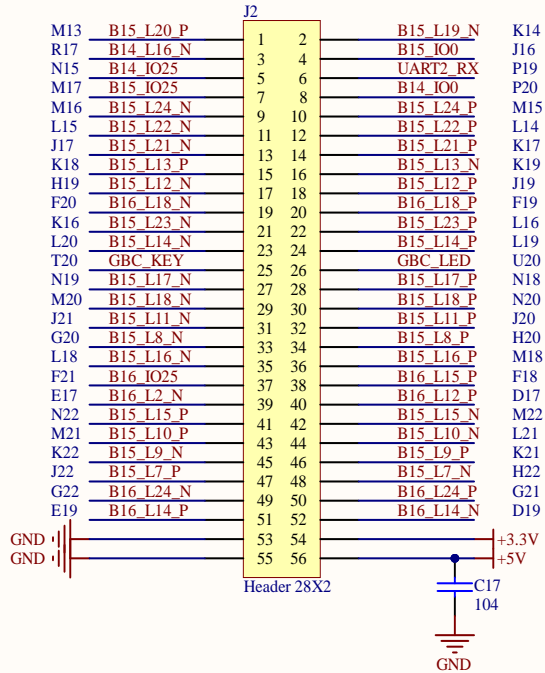
HDMI A



HDMI B

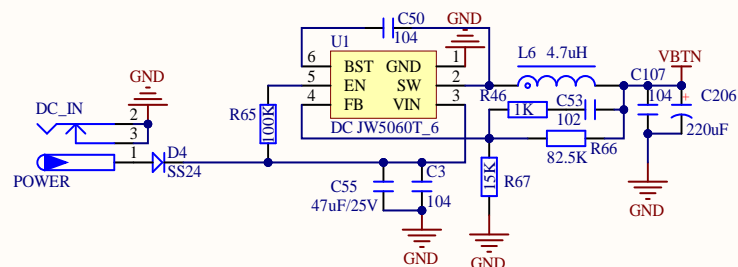


Title		
Size	Number	Revision
A3		
Date:	3/13/2023	Sheet of
File:	H:\11111\HDMI.SchDoc	Drawn By:

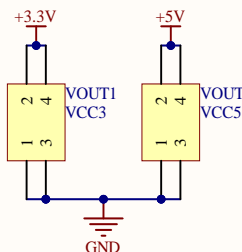


Title		
Size	Number	Revision
A4		
Date:	3/13/2023	Sheet of
File:	H:\11111\0309\ATK-DF7A35 V2.1\IO.SchDocDrawn By:	

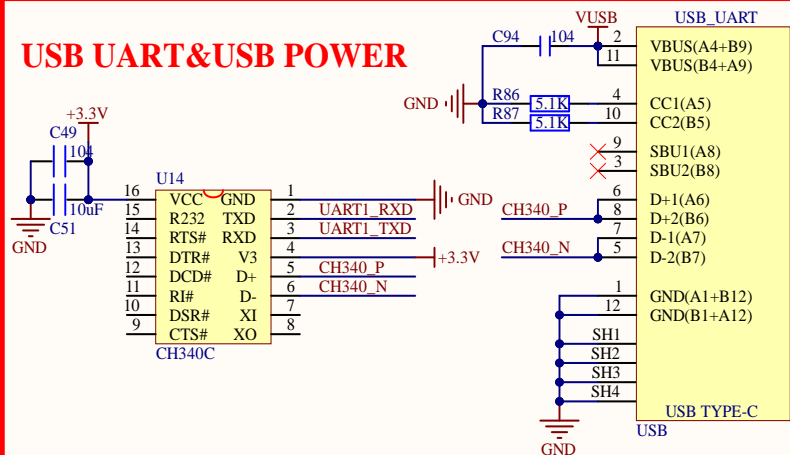
DC-DC POWER IN



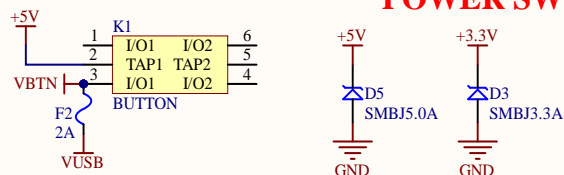
ON BOARD POWER SOURCE



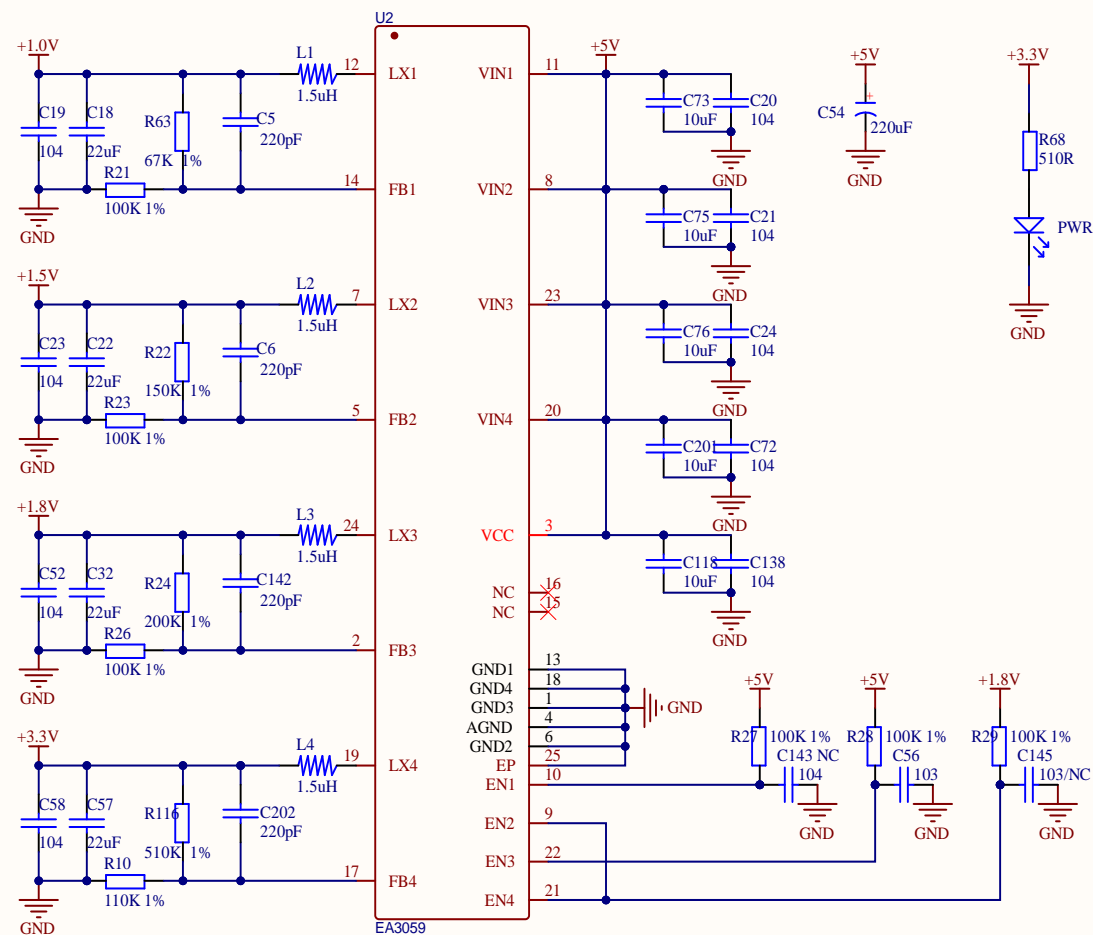
USB UART&USB POWER



POWER SWITCH



VCCINT(1.0V)->VCCBRAM(1.0V)->VCCAUX-(1.8V)>VCCO(1.5V and 3.3V)



Title		
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A4		
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File:	H:\11111\POWER&UART.SchDoc	Drawn By:

