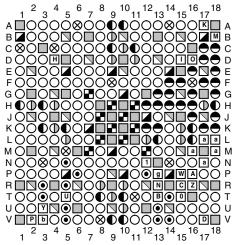


CSG324 Package—LX16



User I/O Pins	Multi-Function Pins		Dedicated Pins	Other Pins
O IO_LXXY_#	 ◇ VREF ① P_GCLK ① N_GCLK ② D0 - D15 ③ A0 - A25 ② FCS / FWE / FOE / HDC / LDC ☑ RDWR_B_VREF [C CCLK C CSI CSO	P PROGRAM_B_2 TCK TDI TDO TDO TM TMS D DONE_2 Z SUSPEND G CMPCS_B_2	GND VCCAUX VCCINT VCCO NC

UG385_c3_13_111909

Figure 3-13: CSG324 Package—LX16 Pinout Diagram

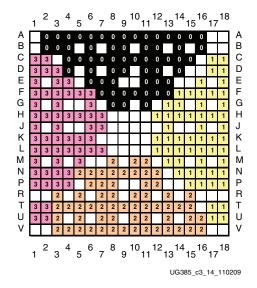


Figure 3-14: CSG324 Package—LX16 I/O Bank Diagram