ECE 100 – Linear Electronic Systems Lab

Lab 2 Report – Active Sallen-Key

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Abstract

In this Lab we explore circuit design with a Butterworth filter. The report includes theoretical circuit analysis, circuit simulation as well building a circuit to model the filter. Theoretical circuit analysis started with a low pass filter, then adjust it to ripple from -1 dB to 1 dB, then back to -1 dB at our f_0 . We then increase f_0 so that our transfer function clears the passband edge and the stopband edge by the same factor. We plotted our theoretical results on bode plots and compared it with our simulation as well as readings from physical circuit using an oscilloscope.

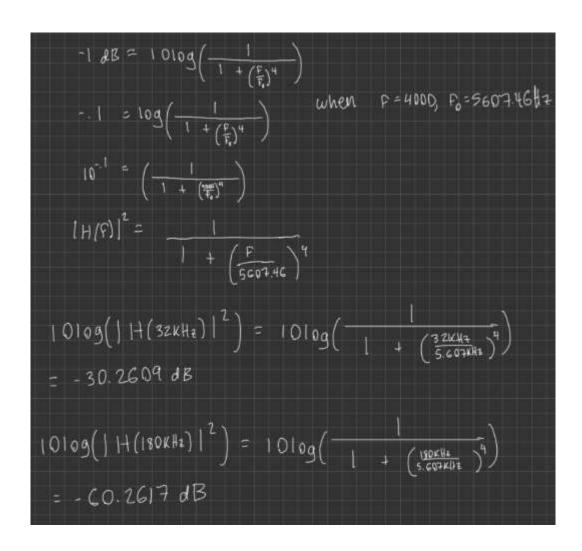
Procedure

- Analytically designed a circuit to meet certain requirements including a gain of -40 dB/decade, and break frequency of 4kHz.
- Once we analytically solved for the optimal theoretical values of the circuit components, we picked out components with values as close to our desired specs as possible while also being cost effective.
- Once we built the circuit, we put probes on the input and output of our circuit and measured the
 output voltage peak-to-peak value given various input voltage peak-to-peak values and different
 frequencies.
- We then tested our circuit for extremely high frequency values, going to the high end of the function generators frequency limit.

Experimental Procedures and Results

Problem 1: Approximation

a)



b)

$$\begin{aligned} &H(y\omega) = \frac{\sqrt{.7943}}{1 + \frac{25}{\omega_0^2}(y\omega) + \frac{(3\omega)^2}{\omega_0^2}} \\ &|H(y\omega)| = \frac{\sqrt{.7443}}{\sqrt{\left(1 - \frac{\omega^2}{\omega_0^2}\right)^2 + \left(\frac{15}{\omega_0^2}y\omega\right)^2}} = \frac{\sqrt{.7443}}{\sqrt{.7443}} \\ &= \frac{\sqrt{.7443}}{\sqrt{.7443}} \\ &= \frac{\sqrt{.7443}}{\sqrt{1 - \omega^2}\left(\frac{Z}{\omega_0^2} - \frac{45^2}{\omega_0^2}\right) + \frac{\omega^4}{\omega_0^4}} \\ &\frac{d|H(y\omega)|}{d\omega} = -\frac{1}{2}\sqrt{.7443}\left(1 - \omega^2\left(\frac{Z}{\omega_0^2} - \frac{45^2}{\omega_0^2}\right) + \frac{\omega^4}{\omega_0^4}\right)^{-32}\left(-2\omega\left(\frac{Z}{\omega_0^2} - \frac{45^2}{\omega_0^2}\right) + \frac{4\omega^3}{\omega_0^4}\right)} \\ &\frac{d|H(y\omega)|}{d\omega} = 0 = -\frac{1}{2}\sqrt{.7443}\left(-2\omega\left(\frac{Z}{\omega_0^2} - \frac{45^2}{\omega_0^2}\right) + \frac{4\omega^3}{\omega_0^4}\right)} \\ &0 = -\sqrt{.7443}\left(-\omega\left(\frac{Z}{\omega_0^2} - \frac{45^2}{\omega_0^2}\right) + \frac{2\omega^3}{\omega_0^4}\right)\right) \\ &0 = 2\omega\sqrt{.7443}\left(1 - 25^2 - \frac{\omega^2}{\omega_0^2}\right) \\ &\omega = \omega_0\sqrt{1 - 25^2} \\ &Z\pi f_p = 2\pi f_0\sqrt{1 - 25^2} \\ &Z\pi f_p = F_0\sqrt{1 - 25^2} \end{aligned}$$

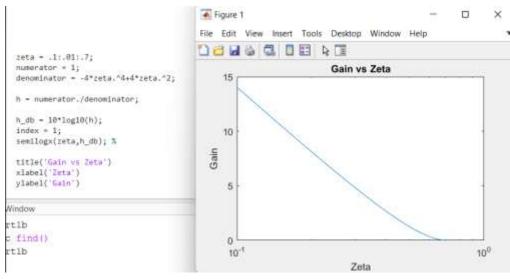
$$\begin{aligned} |H(yu)| &= \frac{(7943)}{\sqrt{(1-\frac{\omega^{2}}{\omega_{h}^{2}})^{2}} \cdot (\frac{25}{\omega_{h}} \omega)^{2}} & f_{p} &= F_{o} \sqrt{1-25} ^{2} \\ |H(yu)|^{2} &= \frac{7943}{(1-\frac{\omega^{2}}{\omega^{2}})^{2} + (\frac{25}{\omega_{h}} \omega)^{2}} \\ &= \frac{.7943}{(1-\frac{f_{p}^{2}}{f_{o}^{2}})^{2} + (\frac{25}{f_{o}} f_{p})^{2}} \\ &= \frac{.7943}{(1-\frac{f_{p}^{2}}{f_{o}^{2}})^{2} + (\frac{25}{f_{o}} f_{o} \sqrt{1-25})^{2}} \\ |H(yu)|^{2} &= \frac{.7943}{(1-\frac{f_{p}^{2}}{f_{o}^{2}})^{2} + (\frac{25}{f_{o}^{2}} f_{o} \sqrt{1-25})^{2}} \\ &= \frac{.7943}{(1-\frac{f_{o}^{2}}{f_{o}^{2}})^{2} + (\frac{25}{f_{o}^$$

$$|dB = 10109 \left(\frac{1}{-45^{4} + 45^{2}} \right)$$

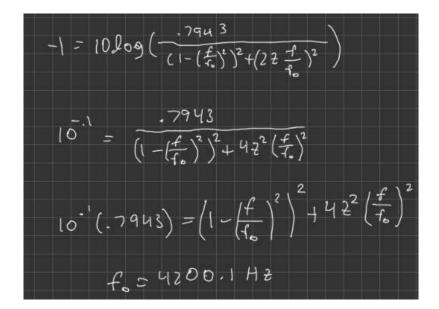
$$10^{1} = \frac{1}{-45^{4} + 45^{2}}$$

$$5 = .5227, .85245$$

$$.85245 > .7, .50 5 = .5227$$



c)

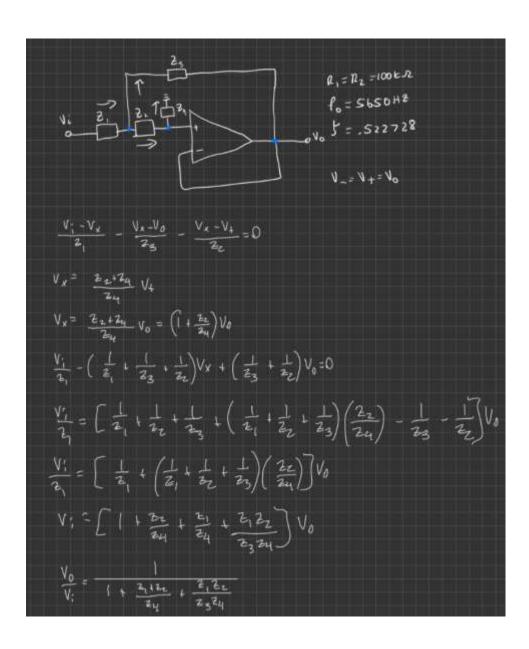


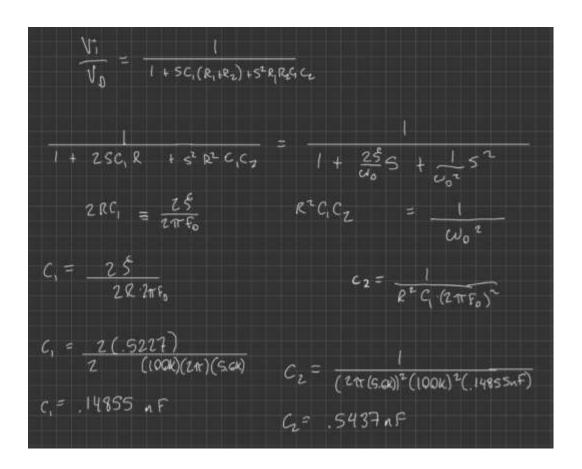
d)

Fo = 5650 Hz

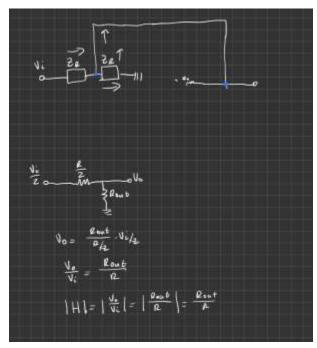
Problem 2: Realization/Simulation

a)

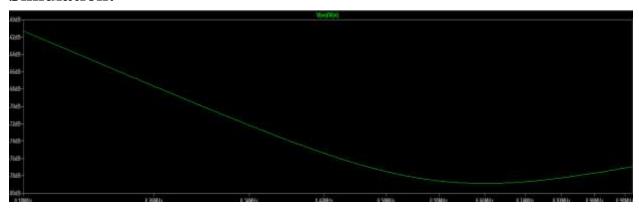




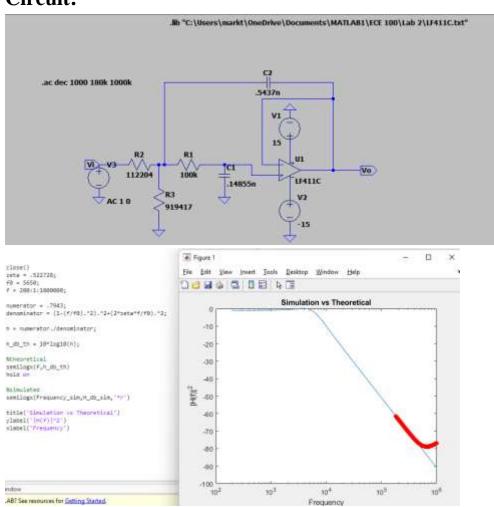
Part B:



Part C: Simulation:

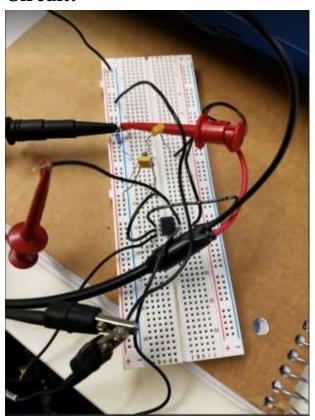


Circuit:



Problem 3: Experiment

Circuit:



Components: 1 MOhm resistor, 490 pF capacitor, 120 pF cap, 120 kOhm Resistor, LF411 Op-amp

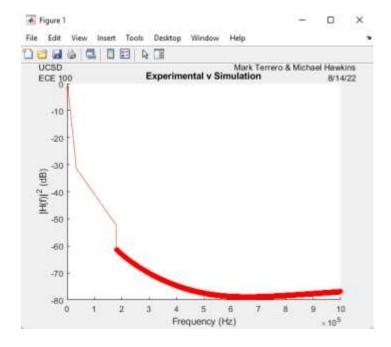
Hz	Input (Vpp)	Output (Vpp)	Output (Vpp)	H(f) ^2 dB
10	5	4.56		-0.800103233
10	10	9.04		-0.87663139
10	20	15.2	18.4	-2.383728154
5600	5	4.4		-1.110346557
5600	10	8		-1.93820026
5600	20	15.4	15.2	-2.270185497
4000	5	4.56		-0.800103233
4000	10	9		-0.915149811
4000	20	15.2	18,4	-2.383728154
32000	5	0.18		-28.87394998
32000	-10	0.308		-30.22898567
32000	20	0.56	560mV	-31.05683937
180000	5	0.012		-52.39577517
180000	10	0.015		-55.91760035
180000	20	0.0202	80mV	-59.91357252
1000000	5			
1000000	10			
1000000	20			

*Oscilloscope was not reading Vout data anywhere from the range of $180 \mathrm{kHz} < \mathrm{f} <= 1 \mathrm{MHz}$. Second Output column is to double check oscilloscope readings.

Part B



f = 5MHz Vout is still a sine wave.



Conclusion

This lab went through active circuit design of a Butterworth filter. We first do circuit analysis to find f_0 analytically and find the value of the transfer function at 32 kHz and 180 kHz and find that the filter does meet the specification but is very tight. Using the derivative of the transfer function we can find the peak frequency as a function of f_0 . To add the ripple from -1 dB to 1 dB, then back to 0 dB at 4kHz, we use this peak frequency in the equation $\frac{|H(jw)|^2}{|H(0)|^2} = 1$ dB to find our zeta value. In our case we found our zeta ≈ 0.5227 , and $f_0 \approx 4200.1$ Hz. Adjusting our $f_0 = 5650$ Hz we can clear the passband edge and the stopband edge by the same factor and check the distance in gain to be < .1 dB. We now have a transfer function to model our Butterworth filter. This filter will have a gain of -1 dB in the passband region, and near f_0 will rise to 1dB then back to -1dB at f_0 . It will then have a lose of -30 dB per decade.

We then create a circuit simulation based on a Sallen-Key circuit with some modification. We use a Thevenin equivalent to the resistor at our input voltage to reduce our input voltage by -1 dB, or $\sqrt{.7943}$ in magnitude. We then made the general form of the transfer function equivalent to the transfer function found with circuit analysis and found the capacitor values.

We finally build our circuit on a breadboard using $\pm 15~V$ on the op amp rails, with varying input signals to obtain output signals and compare them with the theoretical values that should be attained by our transfer function. After 180 kHz our oscilloscope was not reading an output signal. Overall, our theoretical equation, circuit simulation, and values from our built circuit were consistent and we got good results in this lab besides the output voltage after 180 kHz.