

Projects x Files Classes

- led
 - Header Files
 - Important Files
 - Linker Files
 - Source Files
 - led.c
 - Libraries
 - Loadables

led - Dashboard main() - Navigator x

- _XTAL_FREQ
- main()

Start Page x MPLAB X Store x led.c x

Source History

```

12 #pragma config WRT = OFF           // Flash Program Memory Write Enable bits (Write protection off; all program memory may be
13 #pragma config CP = OFF            // Flash Program Memory Code Protection bit (Code protection off)
14
15 // #pragma config statements should precede project file includes.
16 // Use project enums instead of #define for ON and OFF.
17
18 #include <xc.h>
19
20 #define _XTAL_FREQ 6000000
21
22 void main(void) {
23
24     TRISD = 0xFB; // 1111 1011 set RD2 pin as output
25     while(1)
26     {
27         PORTD = 0x04; // 0000 0100 set RD2 high
28         _delay_ms(1000); // delay 1sec
29         PORTD = 0x00; // 0000 0000 set RD2 low
30         _delay_ms(1000); // delay again
31     }
32 }

```

main while(1)

Output x Configuration Bits

Project Loading Warning x Config Bits Source x led (Clean, Build, ...) x

ID	Location	space	used	0h (0)	of	4h bytes	(0.0%
make[2]: Leaving directory 'C:/Users/hp/embedded firware/class work/led.X'									
BUILD SUCCESSFUL (total time: 1s)									
Loading code from C:/Users/hp/embedded firware/class work/led.X/dist/default/production/led.X.production.hex...									
Program loaded with pack, PIC16Fxxx_DFP, 1.6.186, Microchip									
Loading completed									