

A List of Scheduling Primitives

A.1 Loop Transformations

Scheduling operation	Code transformation	Safety conditions
reorder_loops(p, loops)	<pre> for i: for j: for j: ~ for i: s s </pre>	The j loop's bounds cannot depend on i . The loop body s commutes for different (i, j) and (i', j') pairs.
divide_loop(p, loop, c, [i ₀ , i ₁], tail_strategy)	<pre> # tail_strategy=perfect for i < I: for io < I/c: s ~ for ii < c: s[i ↦ c*io+ii] # tail_strategy=guard for i < I: for io < I/c: s ~ if c*io+ii < I: s[i ↦ c*io+ii] # tail_strategy=cut for i < I: for io < I/c: s ~ for ii < c: s[i ↦ c*io+ii] for i < I: for ii < I/c: s ~ for ii < I/c: s[i ↦ c*I/c+ii] # tail_strategy=cut_and_guard for io < I/c: for ii < c: for i < I: s ~ s[i ↦ c*io+ii] ~ if I%c > 0: for ii < I%c: s[i ↦ c*I/c+ii] </pre>	For perfect tail_strategy, the loop bound is perfectly divisible by c .
divide_with_recompute(p, loop, N, c, [i ₀ , i ₁])	<pre> for i < I: for io < N: s ~ for ii < c+I-N*c: s[i ↦ c*io+ii] </pre>	s is idempotent and $N*c \leq I$.
mult_loops(p, loops, k)	<pre> for i < I: for k < I*c: for j < c: ~ s[i ↦ k/c, j ↦ k%c] s </pre>	The j loop is the only statement in the i loop's body. Also, c is constant.
cut_loop(p, loop, e)	<pre> for i in l, h: s for i in e, h: s </pre>	The cutoff e lies between the loop bounds l and h
join_loops(p, loop, loop2)	<pre> for i in l1, h1: s for i2 in l2, h2: ~ for i in l1, h2: s s </pre>	The loops are adjacent, have identical bodies, and $h_1 = l_2$.
shift_loop(p, loop, e)	<pre> for i in l, h: ~ for i in e, h+e-l: s s </pre>	The new lower bound $e \geq 0$.
fission(p, gap)	<pre> for i: for i: s1 s1 ~~~~~~ s2 for i: s2 </pre>	s_2 cannot depend on allocations in s_1 , and effects of s_1 and s_2 commute
remove_loop(p, loop)	<pre> for i: ~ s s </pre>	s is idempotent and cannot depend on i . Loop executes at least once.
add_loop(p, s, i, hi, guard)	<pre> # guard = False s ~ for i < hi: s # guard = True s ~ for i < hi: if i == 0: s </pre>	s is idempotent and hi is positive.
unroll_loop(p, loop)	<pre> for i in lo, hi: ~ s[i ↦ lo] s ~... s[i ↦ hi - 1] </pre>	hi and lo are constants, $hi - lo > 0$

A.2 Code Rearrangement

Scheduling operation	Code transformation	Safety conditions
reorder_stmts(p, s1, s2)	$s1 \rightsquigarrow s2$ $s2 \rightsquigarrow s1$	s1 and s2 commute.
commute_expr(p, e)	$x+y \rightsquigarrow y+x$ $x*y \rightsquigarrow y*x$	N/A

A.3 Scope Transformations

Scheduling operation	Code transformation	Safety conditions
specialize(p, s, conds)	<pre> if conds[0]: s else: s \rightsquigarrow if conds[1]: s else: ... </pre>	conds only contains valid boolean expressions.
fuse(p, scope, scope2)	<pre> for i<I: s for i<I: s2 </pre> \rightsquigarrow <pre> for i<I: for i<I: s s2 </pre>	Equivalent upper loop bound I in its context. Iterations of s commute with any iteration of s2.
	<pre> if e: s if e: s2 </pre> \rightsquigarrow <pre> if e: if e: s s2 </pre>	Equivalent if conditional expression e in its context.
		For all cases, scope is the only statement in its parent's body.
lift_scope(p, scope)	<pre> if e: if e2: # scope s else: s2 else: s3 </pre> \rightsquigarrow <pre> if e2: if e: s else: s3 else: if e: s2 else: s3 </pre>	N/A
	<pre> if e: # scope for i: if e: s else: s2 </pre> \rightsquigarrow <pre> for i: if e: # scope s else: s2 </pre>	if statement cannot have an else clause.
	<pre> for i: if e: # scope s else: s2 </pre> \rightsquigarrow <pre> for i: if e: for i: s else: for i: s2 </pre>	e cannot depend on i.

A.4 Multiple procedures

Scheduling operation	Code transformation	Safety conditions
inline(p, foo)	Inline a callsite of foo	N/A
replace(p, s, instr)	Replace s with a call to an instruction instr	s and the instr function are unifiable.
call_eqv(p, foo, bar)	Replace foo with a call to an equivalent procedure bar	The two procedures foo and bar are equivalent, e.g. scheduled from the same procedure
extract_subproc(p, s, foo)	Isolates s into a function named foo, and replaces s with a call to foo.	N/A

A.5 Buffer Transformations

Scheduling operation	Code transformation	Safety conditions
lift_alloc(p, a)	<pre> for i: a: T[sz] a: T[sz] ~~~ for i: s s </pre>	Dimensions SZ don't depend on i. No loop-carry dependencies.
sink_alloc(p, a)	<pre> a: T for i: for i: ~~~ a: T s s </pre>	a is only accessed in the i loop. No loop-carried dependencies.
delete_buffer(p, a)	<pre> a: T ~~~ s s </pre>	a should be dead.
reuse_buffer(p, a, b)	<pre> a : T[sz] a : T[sz] ... b : T[sz] ~~~... s s[b ↦ a] </pre>	a and b have the same type and size. a is dead after b's allocation.
resize_dim(p, a, dim, sz, off, fold)	<pre> ## fold = False a: T[_] ~~~ a: T[_] s s[a[e] ↦ a[e-off]] ## fold = True a: T[sz] ~~~ a: T[sz] s s[a[e] ↦ a[(e-off)%sz]] </pre>	<p>1 < h, a only accessed in dim-th dimension between l and h.</p> <p>1 < h, a only accessed in dim-th dimension between l and h. Also, accesses to a never access more than SZ earlier than the largest access so far.</p>
expand_dim(p, a, sz, e)	<pre> a: T[_] ~~~ a: T[_] s s[a[_] ↦ a[e, _]] </pre>	SZ is positive, e only uses existing variables, and e < SZ in all contexts.
rearrange_dim(p, a, p_vec)	<pre> # p_vec = [2, 0, 1] a: T[N, M, K] ~~~ a: T[K, N, M] </pre>	a cannot be windowed or passed in function calls.
divide_dim(p, a, dim, c)	<pre> # dim = 0, c = 4 a: R[12, _] ~~~ a: R[3, 4, _] s s[a[i, _] ↦ a[i/4, i%4, _]] </pre>	a's dim-th dimension size is constant and divisible by c
mult_dim(p, a, dim, dim2)	<pre> # dim = 0, dim2 = 2 a: R[n, _, 4] ~~~ a: R[4*n, _] s s[a[i, _, j] ↦ a[4*i+j, _]] </pre>	a cannot be windowed or passed in function call. One of the dimensions is constant size.
unroll_buffer(p, a, dim)	<pre> a1: T a: T[c] ~~~... ac: T </pre>	For this dimension, a has constant size and index accesses. a cannot be windowed along this dimension.
bind_expr(p, e, a, cse)	<pre> a: T s ~~~ a = e s[e ↦ a] </pre>	N/A
stage_mem(p, s, a, w, tmp)	<pre> tmp: T[n, 2] for k0 < n: for k1 < 2: tmp[k0, k1] = a[k0, j-1+k1] # w = a[0:n, j-1:j] for i < n-1: # s ~~~ for i < n-1: a[i, j] = a[i+1, j-1] tmp[i, 1] = tmp[i+1, 0] for k0 < n: for k1 < 2: a[k0, j-1+k1] = tmp[k0, k1] </pre>	The code block s does not access buffer a outside of the given window.

A.6 Simplification

Scheduling operation	Code transformation	Safety conditions
simplify(p)	does arithmetic simplifications and trivial branch elimination on the entire procedure	N/A
eliminate_dead_code(p, scope)	<pre> for i in l, h: pass if e: s else s2 </pre>	The loop runs 0 times, e.g. $l \geq h$. e is equivalent to True or False in its context.
rewrite_expr(p, e, e')	$e \rightsquigarrow e'$	e and e' are equivalent in the context in which e appears.
merge_writes(p, s1, s2)	<pre> x = e1 x = e2 \rightsquigarrow x = e2 x += e1 x = e2 \rightsquigarrow x = e2 x = e1 x += e2 \rightsquigarrow x = e1 + e2 x += e1 x += e2 \rightsquigarrow x += e1 + e2 </pre>	The two statements write to the same destination x. e2 cannot read from x.
inline_window(p, w)	<pre> w = a[_] \rightsquigarrow s[w \mapsto a[_]] s </pre>	N/A
inline_assign(p, x = e)	<pre> x = e \rightsquigarrow s[x \mapsto e] s </pre>	x cannot be written to in the block s after s.

A.7 Backend-Checked Annotations

All the scheduling primitives that we have discussed so far are safety-checked within their rewrite process. By contrast, consistency of precision types, memory annotations, and window annotations are performed as back-end checks after all scheduling is complete; immediately prior to code generation.

Scheduling operation	Code transformation	Compilation safety check
set_memory(p, a, MEM')	$a @ \text{MEM} \rightsquigarrow a @ \text{MEM}'$	Ensures that the accesses to the buffer obeys the custom memory read/write/window definition, and the caller and callee have the same memory types
set_precision(p, a, T')	$a: T \rightsquigarrow a: T'$	Checks the caller, callee, and the both sides of binary operation have the same precision types
parallelize_loop(p, loop)	Annotate loop as parallel	Ensures that the loop iterations do not have RAW or WAW dependencies.
set_window(p, a)	$a: T[_] \rightsquigarrow a: [T][_]$	Checks the caller and callee have the same window shapes

A.8 Configuration State

Scheduling operation	Code transformation	Safety conditions
bind_config(p, e, cfg, field)	<pre> s \rightsquigarrow cfg.field = e s[e \mapsto cfg.field] </pre>	cfg.field is not read by code which executes afterwards.
delete_config(p, cfg, field = _)	<pre> s cfg.field = _ \rightsquigarrow s s2 </pre>	cfg.field is not read by code which executes afterwards.
write_config(p, gap, cfg, field, e)	<pre> s s2 \rightsquigarrow s cfg.field = e s2 </pre>	cfg.field is not read by code which executes afterwards.