Theo Hatzis

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Hardware design/development, Semiconductor Validation, Verification & Test Automation

- Electronics design/development (30y++), Semiconductor Validation/Verification (15y)
- Availability Q3/2021 on a freelance basis
- VAT/USt-IdNr: DE259921718

Experience Domains

- 2G - 4G Modems verification. RF measurements
- Semiconductor post silicon bench, PMU, PMICS and DCDC.
- Bench Automation with Python, C# and Teststand
- Hardware design and development on Medical, Scientific, Transportation Systems (Highways) and Wireless Terminals
- Design to requirements, design for conformance, safety, emissions, and environmental performance

Interests

- Hardware design and development and Test
- Semiconductor power devices (PMU/PMIC/DCDC)
- Characterization, verification and validation
- Automation measurements in Python, C# and Teststand
- Sensors and data and Interfaces. Testing
- Battery test systems

Online CV

- [Theo Hatzis MSc](#) (html)

CV Download

- [Longer](#) (PDF)
- [Short](#) (PDF)