

Tampering Protection

Link

<https://www.youtube.com/watch?v=qCuVBD2dmTA&list=PLnMKNibPkDnFzux3PHKUEi14ftDn9Cbm7&index=27>


Description

In this paperwork, we will learn how to use asymmetric encryption on our board.

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Prerequisites




Security Features by STM32 Series

7

STM32 Series	Security Features																	Arm Cortex®
	96-Bit Unique ID	FLASH WRP	FLASH PCROP	FLASH RDP	Unique entry point	Secure mem/HDP	MPU	Firewall	Trustzone	OTFDEC	Tamper	TRNG	CRYPT AES	HASH	PKA	Cryptolib		
STM32 F0	Available on all devices	Available on all devices		Available on all devices							Available on all devices					Available on all devices	M0	
STM32 F1	Available on all devices	Available on all devices		Available on all devices			Available on all devices				Available on all devices					Available on all devices	M3	
STM32 F2	Available on all devices	Available on all devices		Available on all devices			Available on all devices				Available on all devices	Depends on device part number	Depends on device part number	Depends on device part number		Available on all devices	M3	
STM32 F3	Available on all devices	Available on all devices		Available on all devices			Available on all devices				Available on all devices		Depends on device part number	Depends on device part number		Available on all devices	M4	
STM32 F4	Available on all devices	Available on all devices	Depends on device part number	Available on all devices			Available on all devices				Available on all devices	Depends on device part number	Depends on device part number	Depends on device part number		Available on all devices	M4	
STM32 F7	Available on all devices	Available on all devices	Depends on device part number	Available on all devices			Available on all devices				Available on all devices	Depends on device part number	Depends on device part number	Depends on device part number		Available on all devices	M7	
STM32 L0	Available on all devices	Available on all devices		Available on all devices			Available on all devices	Available on all devices			Available on all devices		Depends on device part number	Depends on device part number		Available on all devices	M0+	
STM32 L1	Available on all devices	Available on all devices		Available on all devices			Available on all devices				Available on all devices		Depends on device part number	Depends on device part number		Available on all devices	M3	
STM32 L4	Available on all devices	Available on all devices		Available on all devices			Available on all devices	Available on all devices			Available on all devices	Depends on device part number	Depends on device part number	Depends on device part number		Available on all devices	M4	
STM32 L5	Available on all devices	Available on all devices		Available on all devices	Depends on device part number	Depends on device part number	Available on all devices		Available on all devices	Available on all devices	Available on all devices	Depends on device part number	Depends on device part number	Depends on device part number	Available on all devices	Available on all devices	M33	
STM32 H7	Available on all devices	Available on all devices	Depends on device part number	Available on all devices	Depends on device part number	Depends on device part number	Available on all devices				Available on all devices	Depends on device part number	Depends on device part number	Depends on device part number		Available on all devices	M7/M4	
STM32 G0	Available on all devices	Available on all devices		Available on all devices	Depends on device part number	Depends on device part number	Available on all devices				Available on all devices	Depends on device part number	Depends on device part number	Depends on device part number		Available on all devices	M0+	
STM32 G4	Available on all devices	Available on all devices		Available on all devices	Depends on device part number	Depends on device part number	Available on all devices				Available on all devices	Depends on device part number	Depends on device part number	Depends on device part number		Available on all devices	M4	
STM32 WB	Available on all devices	Available on all devices		Available on all devices			Available on all devices				Available on all devices	Depends on device part number	Depends on device part number		Available on all devices	Available on all devices	M4/M0+	

Available on all devices

Depends on device part number


life.augmented

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STM32 Board

ST-Link cable

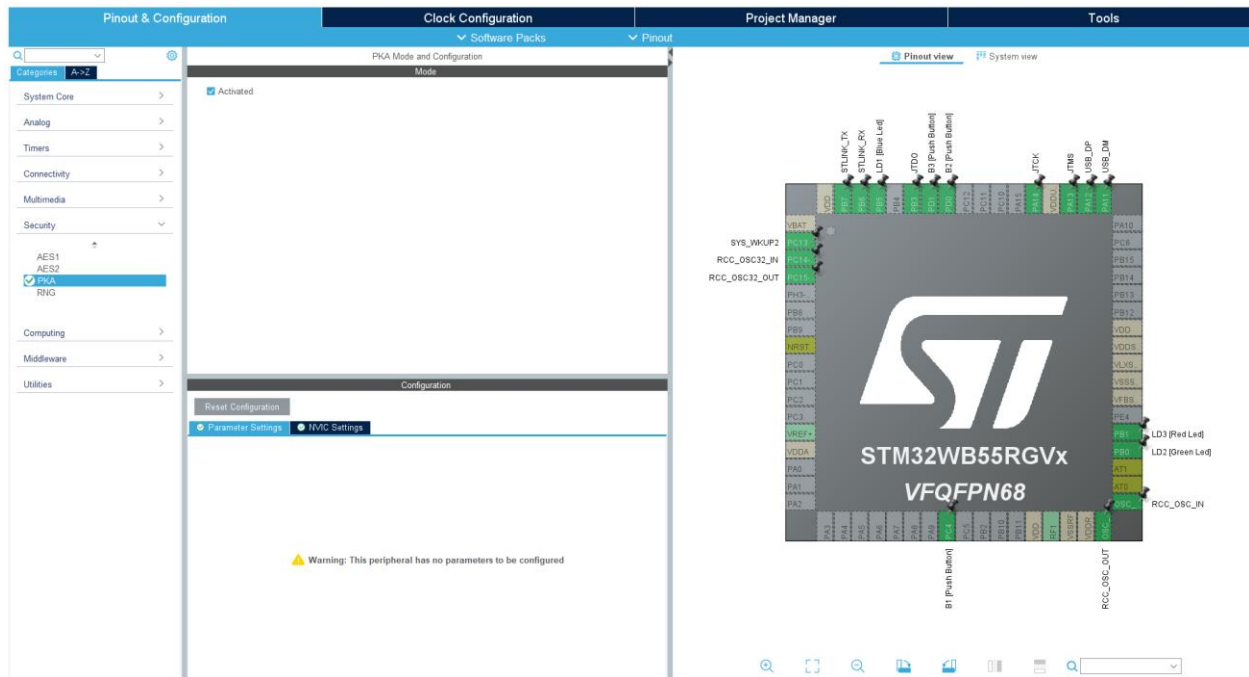
STM32CubeMX

STM32CubeIDE

Walkthrough

Step 1 : Launch STM32CubeMX and generate the code

Launch STM32CubeMX and select the right board depending on the one you are using. In my case I use the WB55 Nucleo board. Then you can generate the code of your project. Then you have to activate PKA.



Step 2 : Generate the private and public key.

To generate your keys, you have to install openssl first. Once it is installed, you can generate the following commands using the application to generate them.

Generate private key :

```
(user@kali)-[~]
$ openssl rsa -in MyPrivKey.pem
writing RSA key
-----BEGIN RSA PRIVATE KEY-----
MIICXAIBAAKBgQC1tZiF2UJFwA3ds4mspvqmxjBIyf5Terq7Xzwh2Q0jUCzW1aD
3r00QqK/JhiY4NcluzkSnXSasRrY5qxbSsjGELC8VgsW61BsdB3Nh+UCfib09czb
Y7DIX7pdaIQMiSyUadhasKzTe02c620VeABAjgXizWqbGpJwAeJa6ziHmQIDAQAB
AoGABS3j5wzGQs6ylsJlXQ8+Lv1bF21jh0VdvnD8Raa139XNMWJtcCHivyDPweK
8/CUsVKg0dMDG9Woej4483EyP8atCdPT9hJQOQX1JNqx8gfQBUrwDr+1ffwFxUyd
hEoomWqWn+bmUcJE9Dpt4JLHfnLV6BS7tN+m0ZunQ4ZQHcECQQDyPvxRwdWSRxpQ
sjBee+M2fzcynDpZNI6hLrZ9ouRR88TVCW3WVG4sTF9Hou8rN8tG0XgUJnHpPMmZ
obJdK/z9AkEAWaA3r0Fjkkfnfh8IFDI0awFXxKZBsIz8sFO0eHjg/y+M0I3mENve
YCzAvUP7c7HPnTeDB69iSrMjaK5oM6YFzQJAWFtg7PEmVRRaJNTZj5zgYyBDodIZ
9i+VvNUTWv/vB3VCdFhafjKNfNreZeKoGbtgCZSdl7vuEIR7g+3Wg0VqgQJAawjT
hMe1KqptvH0rkaZSVXrQI0rQvso3Z0mL1lb6gwNMKEuP+8GyeEU5wcWM+XYZVXbF
0JjP3vdv00BL0M4v2QJBAI8kcn+nlpVXE0SFdY7njnnzh4LZd9wCDfLWYWNsTWuq
2x5Hf/ZxEzPJk60A90Q9CDfvePtVKek9T1VVUQEnRg=
-----END RSA PRIVATE KEY-----
```

Read private key components :

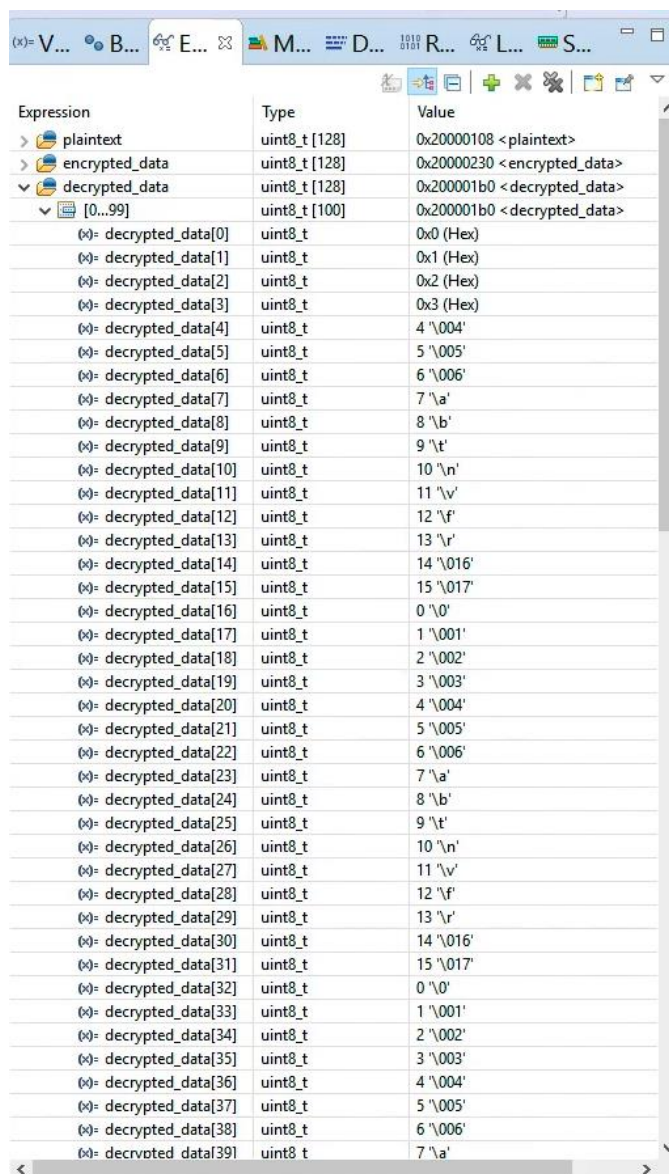
```
(user@kali)-[~]
$ openssl rsa -in MyPrivKey.pem -text
RSA Private-Key: (1024 bit, 2 primes)
modulus:
00:b5:b5:98:85:d9:42:45:c0:0d:dd:b3:89:ac:a6:
fa:a6:c6:30:48:c9:fe:53:7a:ba:bb:5f:3c:24:87:
64:0e:8d:40:b3:5b:56:83:de:bd:0e:42:a2:bf:26:
18:98:e0:d7:25:bb:39:12:9d:74:9a:b1:1a:d8:e6:
ac:5b:4a:c8:c6:12:50:bc:56:0b:16:eb:50:6c:74:
1d:cd:87:e5:02:7e:26:ce:f5:cc:db:63:b0:c8:5f:
ba:5d:68:84:0c:89:2c:94:69:d8:5a:b0:ac:d3:7b:
4d:9c:eb:6d:15:78:00:40:8e:05:e2:cd:6a:9b:1a:
98:f0:01:e2:5a:eb:38:87:99
publicExponent: 65537 (0x10001)
privateExponent:
05:2d:e3:e7:0c:c6:42:ce:b2:96:7b:23:95:74:3c:
f8:bb:f5:6c:5d:b5:8e:13:95:76:f9:c3:f1:16:9a:
d7:7f:57:34:c5:89:b5:c0:87:8a:fc:83:3f:07:8a:
f3:f0:94:b1:52:a0:d1:d3:03:1b:d5:a8:7a:3e:38:
f3:71:32:3f:c6:ad:09:d3:d3:f6:12:50:39:05:f5:
24:da:b1:f2:07:d0:05:4a:f0:0e:bf:b5:7d:fc:05:
c5:4c:9d:84:4a:28:99:6a:96:9f:e6:e6:51:c2:44:
f4:3a:6d:e0:92:c7:7e:79:55:e8:14:bb:b4:df:a6:
39:9b:a7:43:86:50:1d:c1
```

Generate public key :

```
(user@kali)-[~]
$ openssl rsa -in MyPrivKey.pem -pubout -out MyPubKey.pem
writing RSA key
```


Step 4 : Check the encryption

Finally we just have to put our variables in the debug expressions and check them after the program has been launched.



Expression	Type	Value
plaintext	uint8_t [128]	0x20000108 <plaintext>
encrypted_data	uint8_t [128]	0x20000230 <encrypted_data>
decrypted_data	uint8_t [128]	0x200001b0 <decrypted_data>
decrypted_data [0...99]	uint8_t [100]	0x200001b0 <decrypted_data>
(x)= decrypted_data[0]	uint8_t	0x0 (Hex)
(x)= decrypted_data[1]	uint8_t	0x1 (Hex)
(x)= decrypted_data[2]	uint8_t	0x2 (Hex)
(x)= decrypted_data[3]	uint8_t	0x3 (Hex)
(x)= decrypted_data[4]	uint8_t	4 '\004'
(x)= decrypted_data[5]	uint8_t	5 '\005'
(x)= decrypted_data[6]	uint8_t	6 '\006'
(x)= decrypted_data[7]	uint8_t	7 '\a'
(x)= decrypted_data[8]	uint8_t	8 '\b'
(x)= decrypted_data[9]	uint8_t	9 '\t'
(x)= decrypted_data[10]	uint8_t	10 '\n'
(x)= decrypted_data[11]	uint8_t	11 '\v'
(x)= decrypted_data[12]	uint8_t	12 '\f'
(x)= decrypted_data[13]	uint8_t	13 '\r'
(x)= decrypted_data[14]	uint8_t	14 '\016'
(x)= decrypted_data[15]	uint8_t	15 '\017'
(x)= decrypted_data[16]	uint8_t	0 '\0'
(x)= decrypted_data[17]	uint8_t	1 '\001'
(x)= decrypted_data[18]	uint8_t	2 '\002'
(x)= decrypted_data[19]	uint8_t	3 '\003'
(x)= decrypted_data[20]	uint8_t	4 '\004'
(x)= decrypted_data[21]	uint8_t	5 '\005'
(x)= decrypted_data[22]	uint8_t	6 '\006'
(x)= decrypted_data[23]	uint8_t	7 '\a'
(x)= decrypted_data[24]	uint8_t	8 '\b'
(x)= decrypted_data[25]	uint8_t	9 '\t'
(x)= decrypted_data[26]	uint8_t	10 '\n'
(x)= decrypted_data[27]	uint8_t	11 '\v'
(x)= decrypted_data[28]	uint8_t	12 '\f'
(x)= decrypted_data[29]	uint8_t	13 '\r'
(x)= decrypted_data[30]	uint8_t	14 '\016'
(x)= decrypted_data[31]	uint8_t	15 '\017'
(x)= decrypted_data[32]	uint8_t	0 '\0'
(x)= decrypted_data[33]	uint8_t	1 '\001'
(x)= decrypted_data[34]	uint8_t	2 '\002'
(x)= decrypted_data[35]	uint8_t	3 '\003'
(x)= decrypted_data[36]	uint8_t	4 '\004'
(x)= decrypted_data[37]	uint8_t	5 '\005'
(x)= decrypted_data[38]	uint8_t	6 '\006'
(x)= decrypted_data[39]	uint8_t	7 '\a'

You could normally see that the plaintext is equal to the decrypted data.