第二次实例分析

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1. xv6中自旋锁 spinlock 的数据结构以及重要字段的作用

Locked:锁是否被持有?

1:被持有

0: 不被持有

2. acquire 函数获取锁的时候,为什么首先进行要执行 pushcli "关中断"操作?

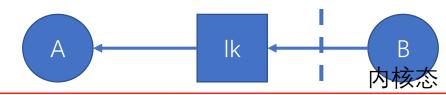
```
// Acquire the lock.
21
     // Loops (spins) until the lock is acquired.
22
     // Holding a lock for a long time may cause
23
     // other CPUs to waste time spinning to acquire it.
24
     void
25
     acquire(struct spinlock *lk)
26 ⊟{
27
       pushcli(); // disable interrupts to avoid deadlock.
28
       if (holding(lk))
29
         panic("acquire");
30
31
       // The xchq is atomic.
32
       while (xchq(\&lk->locked, 1) != 0)
33
34
35
       // Tell the C compiler and the processor to not move loa
36
       // past this point, to ensure that the critical section'
37
       // references happen after the lock is acquired.
38
        sync synchronize();
39
40
       // Record info about lock acquisition for debugging.
41
       1k - cpu = mycpu();
42
       getcallerpcs(&lk, lk->pcs);
43
       getcallerpcs(&lk, lk->pcs);
```

IK->cpu = mycpu();

理论上,如果有中断程序可能获得锁lk,则需要在acquire()开始处关中断。否则可能造成死锁。

实例:

- a. 应用程序A持有锁lk
- b. 应用程序A执行的过程中, 出现了中断
- c. 系统陷入内核, 执行中断处理程序B
- d. 中断处理程序B执行的过程中,申请锁lk
- e. 但此时锁被应用程序A持有,造成死锁



3. 内联汇编函数 xchg 返回 lk->locked 在 xchg 执行之前的值,考虑两个core上的分别有一个进程先后执行 xchg , 返回值分别为0和1时,这两个进程分别发生了什么?

结合这个例子,说明xchg指令是原子指令的重要性

```
// Acquire the lock.
     // Loops (spins) until the lock is acquired.
22
     // Holding a lock for a long time may cause
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24
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       pushcli(); // disable interrupts to avoid deadlock.
28
       if(holding(lk))
29
         panic("acquire");
30
31
       // The xchq is atomic.
32
       while (xchq(\&lk->locked, 1) != 0)
33
34
35
       // Tell the C compiler and the processor to not move load
36
       // past this point, to ensure that the critical section's
37
       // references happen after the lock is acquired.
38
       sync synchronize();
39
       // Record info about lock acquisition for debugging.
40
41
       1k - cpu = mycpu();
42
       getcallerpcs(&lk, lk->pcs);
43
       getcallerpcs(&lk, lk->pcs);
       TK->cpu = mycpu();
```

 Xchg指令:交换arg1和arg2中的内容,
 返回交换前arg1的值。

 (1)锁被占用: lk->locked == 1, 返回值为1,

 1写入lk->locked,继续while循环内申请锁;

 (2)锁未被占用: lk->locked == 0, 返回值为0, 1写入lk->locked, 跳出while循环。

如果xchg为原子指令:

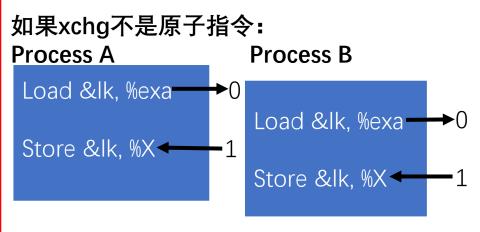
- (1)A进程申请锁lk, lk->locked==0, 将lk->locked改写为1, 返回0;
- (2)B进程申请锁lk, lk->locked==1, 返回1;
 - (3)进程A获得锁,进程B继续申请。

3. 内联汇编函数 xchg 返回 lk->locked 在 xchg 执行之前的值,考虑两个core上的分别有一个进程先后执行 xchg , 返回值分别为0和1时,这两个进程分别发生了什么?

结合这个例子,说明xchg指令是原子指令的重要性

```
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     // Loops (spins) until the lock is acquired.
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       pushcli(); // disable interrupts to avoid deadlock.
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29
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30
31
       // The xchq is atomic.
32
       while (xchq(\&lk->locked, 1) != 0)
33
34
35
       // Tell the C compiler and the processor to not move load
36
       // past this point, to ensure that the critical section's
37
       // references happen after the lock is acquired.
38
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       1k - cpu = mycpu();
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       getcallerpcs(&lk, lk->pcs);
       TK->cpu = mycpu();
```

Xchg指令: 交换arg1 和 arg2 中的内容, 返回交换前arg1的值。 (1)锁被占用: lk->locked == 1, 返回值为1, 1写入lk->locked, 继续while循环内申请锁; (2)锁未被占用: lk->locked == 0, 返回值 为0, 1写入lk->locked, 跳出while循环。



两个进程都返回1,两个进程都获得锁。

4. 在编译后的xv6源码目录下执行 objdump -d spinlock.o , 观察 acquire 函数中, xchg 函数 和它周围的while循环被编译成了什么汇编代码? 138 00000170 <acquire>: 试将该汇编代码和C代码对应起来。 170: 139 55 push %ebp 140 171: 89 e5 mov %esp, %ebp 141 173: 53 %ebx push 142 174: \$0x14, %esp 83 ec 14 sub // Acquire the lock. 177: e8 fc ff ff ff call 178 <acquire+0x8> // Loops (spins) until the lock is acquired. 17c: 8b 45 08 0x8 (%ebp), %eax mov // Holding a lock for a long time may cause 17f: 89 04 24 %eax, (%esp) mov // other CPUs to waste time spinning to acquire it. 182: e8 fc ff ff ff call 183 <acquire+0x13> void 187: 85 c0 %eax, %eax test acquire(struct spinlock *lk) 189: 75 3c 1c7 < acquire + 0x57 >jne b9 01 00 00 00 \$0x1.%ecx 18b: pushcli(); // disable interrupts to avoid deadlock. WOW 8b 55 08 if(holding(lk)) 190: 0x8 (%ebp), %edx mov panic("acquire"); 193: 89 c8 %ecx, %eax mov 195: f0 87 02 lock xchq %eax, (%edx) // The xchq is atomic. 198: 85 c0 %eax, %eax test while (xchq(&lk->locked, 1) != 0) 75 f4 19a: jne 190 <acquire+0x20> lock orl \$0x0,(%esp) 19c: 10 83 Oc 24 OO 1a1: 8b 5d 08 0x8(%ebp), %ebx mov // Tell the C compiler and the processor to not move 1 1a4: e8 fc ff ff ff call 1a5 <acquire+0x35> // past this point, to ensure that the critical section 1a9: 89 43 08 %eax, 0x8 (%ebx) mov // references happen after the lock is acquired. 1ac: 8b 45 08 0x8(%ebp), %eax mov sync synchronize(); laf: 83 c0 0c add \$0xc, %eax 1b2: 89 44 24 04 %eax, 0x4 (%esp) mov // Record info about lock acquisition for debugging. 8d 45 08 1b6: 0x8 (%ebp), %eax lea $1k \rightarrow cpu = mycpu();$ 1b9: 89 04 24 %eax, (%esp) mov getcallerpcs(&lk, lk->pcs); 1bc: e8 fc ff ff ff 1bd <acquire+0x4d> call 1c1: 83 c4 14 \$0x14, %esp 165 add 166 1c4: 5b %ebx qoq getcallerpcs(&lk, lk->pcs); 167 1c5: 5d %ebp qoq TK->cbu = mycpu();

168

1c6:

с3

ret

```
138
                                                            00000170 <acquire>:
                                                             170:
                                                       139
                                                                     55
                                                                                                      %ebp
                                                                                              push
                                                             171:
                                                                     89 e5
                                                       140
                                                                                              mov
                                                                                                      %esp, %ebp
                                                       141
                                                             173:
                                                                     53
                                                                                              push
                                                                                                      %ebx
                                                       142
                                                             174:
                                                                    83 ec 14
                                                                                                      $0x14, %esp
                                                                                              sub
                                                             177:
                                                                     e8 fc ff ff ff
                                                                                              call
                                                                                                      178 <acquire+0x8>
                                                       143
内联汇编
                                                       144
                                                             17c:
                                                                     8b 45 08
                                                                                                      0x8 (%ebp), %eax
                                                                                              mov
                                                       145
                                                             17f:
                                                                     89 04 24
                                                                                                      %eax, (%esp)
                                                                                              mov
                                                       146
                                                             182:
                                                                     e8 fc ff ff ff
                                                                                              call
                                                                                                      183 <acquire+0x13>
static inline uint
                                                             187:
                                                       147
                                                                     85 c0
                                                                                                      %eax, %eax
                                                                                              test
xchg(volatile uint *addr, uint newval)
                                                             189:
                                                                     75 3c
                                                       148
                                                                                                      1c7 < acquire + 0x57 >
                                                                                              jne
                 防止GCC优化
                                                       149
                                                             18b:
                                                                    b9 01 00 00 00
                                                                                                      $0x1.%ecx
                                                                                              WOW
  uint result;
                                                       150
                                                             190:
                                                                     8b 55 08
                                                                                                      0x8 (%ebp), %edx
                                                                                              mov
                                                       151
                                                             193:
                                                                     89 c8
                                                                                                      %ecx, %eax
                                                                                              mov
           in "+m" denotes a read-modify-write 0 152
  // The +
                                                             195:
                                                                     f0 87 02
                                                                                              lock xchq %eax, (%edx)
  asm volatile ("lock; xchgl %0, %1":
                                                             198:
                                                                     85 c0
                                                                                                      %eax, %eax
                                                                                              test
                 "+m" (*addr), "=a" (result) :
                                                       154
                                                                     75 f4
                                                             19a:
                                                                                              jne
                                                                                                      190 <acquire+0x20>
                     (newval) :
                                                       155
                                                                                              lock orl $0x0,(%esp)
                                                             19C:
                                                                     10 83 Oc 24 OO
                 "cc");
                                                       156
                                                             1a1:
                                                                     8b 5d 08
                                                                                                      0x8(%ebp), %ebx
                                                                                              mov
                                                             1a4:
                                                       157
                                                                     e8 fc ff ff ff
                                                                                                      1a5 <acquire+0x35>
                                                                                              call
  return result;
                                                             1a9:
                                                                     89 43 08
                                                       158
                                                                                              mov
                                                                                                      %eax, 0x8 (%ebx)
                                                                     8b 45 08
                                                       159
                                                             lac:
                                                                                                      0x8 (%ebp), %eax
                                                                                              mov
                                                       160
                                                             laf:
                                                                     83 c0 0c
                                                                                              add
                                                                                                      $0xc, %eax
  return result;
                                                             1b2:
                                                       161
                                                                     89 44 24 04
                                                                                                      %eax, 0x4 (%esp)
                                                                                              mov
                                                                     8d 45 08
                                                       162
                                                             1b6:
                                                                                                      0x8 (%ebp), %eax
                                                                                              lea
                                                       163
                                                             1b9:
                                                                     89 04 24
                                                                                                      %eax, (%esp)
                                                                                              mov
                                                       164
                                                             1bc:
                                                                     e8 fc ff ff ff
                                                                                              call
                                                                                                      1bd <acquire+0x4d>
                                                       165
                                                             1c1:
                                                                     83 c4 14
                                                                                              add
                                                                                                      $0x14, %esp
                                                             1c4:
                                                                                                      %ebx
                                                       166
                                                                     5b
                                                                                              qoq
                                                       167
                                                             1c5:
                                                                     5d
                                                                                                      %ebp
                                                                                              qoq
                                                       168
                                                             1c6:
                                                                     с3
                                                                                              ret
```

138

```
00000170 <acquire>:
                                                             170:
                                                       139
                                                                     55
                                                                                                       %ebp
                                                                                               push
                                                       140
                                                              171:
                                                                     89 e5
                                                                                               mov
                                                                                                       %esp, %ebp
                                                       141
                                                             173:
                                                                     53
                                                                                               push
                                                                                                       %ebx
                                                       142
                                                             174:
                                                                     83 ec 14
                                                                                                       $0x14, %esp
                                                                                               sub
                                                             177:
                                                                     e8 fc ff ff ff
                                                                                               call
                                                       143
                                                                                                      178 <acquire+0x8>
内联汇编
                                                       144
                                                              17c:
                                                                     8b 45 08
                                                                                                       0x8 (%ebp), %eax
                                                                                               mov
                                                       145
                                                              17f:
                                                                     89 04 24
                                                                                                       %eax, (%esp)
                                                                                               mov
                                                       146
                                                              182:
                                                                     e8 fc ff ff ff
                                                                                               call
                                                                                                       183 <acquire+0x13>
static inline uint
                                                              187:
                                                       147
                                                                     85 c0
                                                                                                       %eax, %eax
                                                                                               test
xchq(volatile uint *addr, uint newval)
                                                                     75 3c
                                                       148
                                                              189:
                                                                                                       1c7 < acquire + 0x57 >
                                                                                               jne
                                                       149
                                                             18b:
                                                                     b9 01 00 00 00
                                                                                                       $0x1.%ecx
                                                                                               WOW
  uint result;
                                                       150
                                                             190:
                                                                     8b 55 08
                                                                                                       0x8 (%ebp), %edx
                                                                                               mov
                                                       151
                                                              193:
                                                                     89 c8
                                                                                                       %ecx, %eax
                                                                                               mov
  // The <u>+ in "+m" denotes a read-modify-wri</u>te o 152_
                                                              195:
                                                                     f0 87 02
                                                                                               lock xchq %eax, (%edx)
  asm volatile("lock; xchgl %0, %1":
                                                              198:
                                                                     85 c0
                                                                                                       %eax, %eax
                                                                                               test
                 "+m" (*addr), "=a" (result) :
                                                       154
                                                                     75 f4
                                                             19a:
                                                                                               jne
                                                                                                       190 <acquire+0x20>
                                                        155
                      (newval) :
                                                                                               lock orl $0x0,(%esp)
                                                              19C:
                                                                     10 83 Oc 24 OO
                 "cc");
                                                       156
                                                              1a1:
                                                                     8b 5d 08
                                                                                                       0x8(%ebp), %ebx
                                                                                               mov
                                                       157
                                                              1a4:
                                                                     e8 fc ff ff ff
                                                                                               call
                                                                                                      1a5 <acquire+0x35>
  return result;
                                                              1a9:
                                                                     89 43 08
                                                       158
                                                                                               mov
                                                                                                       %eax, 0x8 (%ebx)
                                                                     8b 45 08
                                                       159
                                                              lac:
                                                                                                       0x8(%ebp), %eax
                                                                                               mov
                                                       160
                                                              laf:
                                                                     83 c0 0c
                                                                                               add
                                                                                                       $0xc, %eax
  return result;
                                                              1b2:
                                                       161
                                                                     89 44 24 04
                                                                                                       %eax, 0x4 (%esp)
                                                                                               mov
                                                                     8d 45 08
                                                       162
                                                              1b6:
                                                                                                       0x8 (%ebp), %eax
                                                                                               lea
                                                       163
                                                              1b9:
                                                                     89 04 24
                                                                                                       %eax, (%esp)
                                                                                               mov
                                                       164
                                                              1bc:
                                                                     e8 fc ff ff ff
                                                                                               call
                                                                                                       1bd <acquire+0x4d>
                                                                     83 c4 14
                                                       165
                                                              1c1:
                                                                                               add
                                                                                                       $0x14, %esp
                                                              1c4:
                                                                                                       %ebx
                                                       166
                                                                     5b
                                                                                               qoq
                                                       167
                                                              1c5:
                                                                     5d
                                                                                                       %ebp
                                                                                               qoq
                                                       168
                                                             1c6:
                                                                     с3
                                                                                               ret
```

```
00000170 <acquire>:
                                                      138
                                                             170:
                                                       139
                                                                    55
                                                                                                     %ebp
                                                                                              push
                                                       140
                                                             171:
                                                                    89 e5
                                                                                              mov
                                                                                                     %esp, %ebp
                                                       141
                                                             173:
                                                                    53
                                                                                              push
                                                                                                     %ebx
                                                      142
                                                             174:
                                                                    83 ec 14
                                                                                                     $0x14, %esp
                                                                                              sub
                                                             177:
                                                                    e8 fc ff ff ff
                                                                                              call
                                                      143
                                                                                                     178 <acquire+0x8>
内联汇编
                                                       144
                                                             17c:
                                                                    8b 45 08
                                                                                                     0x8 (%ebp), %eax
                                                                                              mov
                                                       145
                                                             17f:
                                                                    89 04 24
                                                                                                     %eax, (%esp)
                                                                                              mov
                                                       146
                                                             182:
                                                                    e8 fc ff ff ff
                                                                                              call
                                                                                                     183 <acquire+0x13>
static inline uint
                                                             187:
                                                       147
                                                                    85 c0
                                                                                                     %eax, %eax
                                                                                              test
xchq(volatile uint *addr, uint newval)
                                                                    75 3c
                                                       148
                                                             189:
                                                                                                     1c7 < acquire + 0x57 >
                                                                                              jne
                                                       149
                                                             18b:
                                                                    b9 01 00 00 00
                                                                                                     $0x1.%ecx
                                                                                              WOW
  uint result;
                                                       150
                                                             190:
                                                                    8b 55 08
                                                                                                     0x8 (%ebp), %edx
                                                                                              mov
                                                       151
                                                             193:
                                                                    89 c8
                                                                                                     %ecx, %eax
                                                                                              mov
  // The <u>+ in "+m" denotes a read-modify-wri</u>te o 152_
                                                             195:
                                                                    f0 87 02
                                                                                              lock xchq %eax, (%edx)
  asm volatile ("lock; xchgl %0, %1":
                                                             198:
                                                                    85 c0
                                                                                                     %eax, %eax
                                                                                              test
                      (*addr), "=a" (result) :
                                                       154
                                                                    75 f4
                                                             19a:
                                                                                              jne
                                                                                                     190 <acquire+0x20>
                                                       155
                     (newval)
                                                                                              lock orl $0x0,(%esp)
                                                             19C:
                                                                    10 83 Oc 24 OO
                                                       156
                                                             1a1:
                                                                    8b 5d 08
                 "cc");
                                                                                                     0x8(%ebp), %ebx
                                                                                              mov
                                                       157
                                                             1a4:
                                                                    e8 fc ff ff ff
                                                                                              call
                                                                                                     1a5 <acquire+0x35>
  return result;
                                                             1a9:
                                                                    89 43 08
                                                       158
                                                                                              mov
                                                                                                     %eax, 0x8 (%ebx)
                                                       159
                                                             lac:
                                                                    8b 45 08
                                                                                                     0x8(%ebp), %eax
                                                                                              mov
                                                       160
                                                             laf:
                                                                    83 c0 0c
                                                                                              add
                                                                                                     $0xc, %eax
  return result;
                                                       161
                                                             1b2:
                                                                    89 44 24 04
                                                                                                     %eax, 0x4 (%esp)
                                                                                              mov
                          两个输出值
                                                                    8d 45 08
                                                       162
                                                             1b6:
                                                                                                     0x8 (%ebp), %eax
                                                                                              lea
                                                       163
                                                             1b9:
                                                                    89 04 24
                                                                                                     %eax, (%esp)
                                                                                              mov
                           '+'可读可输出,'='输出
                                                       164
                                                             1bc:
                                                                    e8 fc ff ff ff
                                                                                              call
                                                                                                     1bd <acquire+0x4d>
                           'm'内存,'a'寄存器%exa
                                                                    83 c4 14
                                                       165
                                                             1c1:
                                                                                              add
                                                                                                     $0x14, %esp
                                                       166
                                                             1c4:
                                                                    5b
                                                                                                     %ebx
                                                                                              qoq
                                                      167
                                                             1c5:
                                                                    5d
                                                                                                     %ebp
                                                                                              qoq
                                                      168
                                                            1c6:
                                                                    с3
                                                                                              ret
```

```
00000170 <acquire>:
                                                     138
                                                     139
                                                           170:
                                                                  55
                                                                                          push
                                                                                                  %ebp
                                                     140
                                                           171:
                                                                  89 e5
                                                                                          mov
                                                                                                  %esp, %ebp
                                                     141
                                                           173:
                                                                  53
                                                                                          push
                                                                                                  %ebx
                                                     142
                                                           174:
                                                                                                  $0x14, %esp
                                                                  83 ec 14
                                                                                           sub
                                                                  e8 fc ff ff ff
                                                           177:
                                                                                                  178 <acquire+0x8>
                                                     143
                                                                                          call
内联汇编
                                                     144
                                                           17c:
                                                                  8b 45 08
                                                                                                  0x8 (%ebp), %eax
                                                                                          mov
                                                           17f:
                                                                  89 04 24
                                                     145
                                                                                                  %eax, (%esp)
                                                                                          mov
                                                     146
                                                           182:
                                                                  e8 fc ff ff ff
                                                                                          call
                                                                                                  183 <acquire+0x13>
static inline uint
                                                           187:
                                                     147
                                                                  85 c0
                                                                                                  %eax, %eax
                                                                                          test
xchq(volatile uint *addr, uint newval)
                                                                  75 3c
                                                     148
                                                           189:
                                                                                                  1c7 < acquire + 0x57 >
                                                                                          jne
                                                           18b:
                                                                  b9 01 00 00 00
                                                                                                  $0x1.%ecx
                                                     149
                                                                                          WOW
  uint result;
                                                     150
                                                           190:
                                                                  8b 55 08
                                                                                                  0x8 (%ebp), %edx
                                                                                          mov
                                                     151
                                                           193:
                                                                  89 c8
                                                                                                  %ecx, %eax
                                                                                          mov
  // The + in "+m" denotes a read-modify-write o 152_
                                                           195:
                                                                  f0 87 02
                                                                                          lock xchq %eax, (%edx)
  asm volatile ("lock; xchgl %0, %1":
                                                           198:
                                                                  85 c0
                                                                                                  %eax, %eax
                                                                                          test
                                     (result) :
                      (*adar),
                                                     154
                                                                  75 f4
                                                           19a:
                                                                                          jne
                                                                                                  190 <acquire+0x20>
                     (newval)
                                                     155
                                                                                           lock orl $0x0,(%esp)
                                                           19C:
                                                                  10 83 Oc 24 OO
                                                     156
                                                           1a1:
                                                                  8b 5d 08
                "cc")
                                                                                                  0x8(%ebp), %ebx
                                                                                          mov
                                                     157
                                                           1a4:
                                                                  e8 fc ff ff ff
                                                                                                  1a5 <acquire+0x35>
                                                                                          call
  return result:
                                                           1a9:
                                                                  89 43 08
                                                                                                  %eax, 0x8 (%ebx)
                                                     158
                                                                                          wow
                                                     159
                                                           lac:
                                                                  8b 45 08
                                                                                                  0x8(%ebp), %eax
                                                                                          mov
                                                     160
                                                           laf:
                                                                  83 c0 0c
                                                                                           add
                                                                                                  $0xc, %eax
  return resu
                                                     161
                                                           1b2:
                                                                  89 44 24 04
                                                                                                  %eax, 0x4 (%esp)
                                                                                          mov
       "Lock"指令前缀
                                                                  8d 45 08
                                                     162
                                                           1b6:
                                                                                                  0x8 (%ebp), %eax
                                                                                          lea
       保证了这条指令对总线和缓存的独占权,
                                                     163
                                                           1b9:
                                                                  89 04 24
                                                                                                  %eax, (%esp)
                                                                                          mov
                                                     164
                                                           1bc:
                                                                  e8 fc ff ff ff
                                                                                          call
                                                                                                  1bd <acquire+0x4d>
       保证了这条指令的执行过程中不会有其他CPU
                                                     165
                                                           1c1:
                                                                  83 c4 14
                                                                                                  $0x14, %esp
                                                                                           add
       或同CPU内的指令访问缓存和内存
                                                     166
                                                           1c4:
                                                                                                  %ebx
                                                                  5b
                                                                                          qoq
                                                     167
                                                           1c5:
                                                                  5d
                                                                                                  %ebp
                                                                                           qoq
                                                     168
                                                           1c6:
                                                                  с3
                                                                                          ret
```

6. acquire 和 release函数中的__sync_synchronize() 语句的作用是什么? 在汇编程序中是如何体现的?

- 设置一个内存屏障(memory barrier), 防止编译器reorder的时候把临界区的访存指令移到锁操作之前
- 该语句被编译为lock orl \$0x0, (%esp)指令, lock指令前缀…

```
// Release the lock.
                                                                                     00000210 <release>:
     void
                                                                                196
                                                                                      210:
                                                                                                                        push
                                                                                                                                %ebp
     release(struct spinlock *lk)
                                                                                197
                                                                                      211:
                                                                                              89 e5
                                                                                                                                %esp, %ebp
                                                                                                                        mov
48
                                                                                198
                                                                                      213:
                                                                                              53
                                                                                                                        push
                                                                                                                                %ebx
49
       if (!holding(lk))
                                                                                199
                                                                                      214:
                                                                                              83 ec 10
                                                                                                                                $0x10,%esp
                                                                                                                        sub
         panic("release");
50
                                                                                200
                                                                                      217:
                                                                                              8b 5d 08
                                                                                                                                0x8(%ebp), %ebx
                                                                                                                        mov
51
                                                                                201
                                                                                      21a:
                                                                                              53
                                                                                                                                %ebx
                                                                                                                        push
52
       1k - pcs[0] = 0;
                                                                                202
                                                                                              e8 fc ff ff ff
                                                                                                                                21c <release+0xc>
                                                                                      21b:
                                                                                                                        call
53
       1k \rightarrow cpu = 0;
                                                                                203
                                                                                      220:
                                                                                              83 c4 10
                                                                                                                        add
                                                                                                                                $0x10,%esp
54
                                                                                204
                                                                                      223:
                                                                                              85 c0
55
       // Tell the C compiler and the processor to not move loads or stores
                                                                                                                        test
                                                                                                                                %eax,%eax
       // past this point, to ensure that all the stores in the critical
                                                                                205
                                                                                              74 22
56
                                                                                      225:
                                                                                                                                249 <release+0x39>
57
       // section are visible to other cores before the lock is released.
                                                                                206
                                                                                                                                $0x0,0xc(%ebx)
                                                                                      227:
                                                                                              c7 43 0c 00 00 00 00
                                                                                                                        movl
58
       // Both the C compiler and the hardware may re-order loads and
                                                                                207
                                                                                              c7 43 08 00 00 00 00
                                                                                      22e:
                                                                                                                                $0x0,0x8(%ebx)
                                                                                                                        movl
59
       // stores; sync synchronize() tells them both not to.
                                                                                208
                                                                                      235:
                                                                                              f0 83 0c 24 00
                                                                                                                        lock orl $0x0, (%esp)
        sync synchronize();
60
                                                                                      23a:
                                                                                              c7 03 00 00 00 00
                                                                                                                        movl
                                                                                                                                $0x0, (%ebx)
61
                                                                                210
                                                                                      240:
                                                                                              8b 5d fc
                                                                                                                                -0x4 (%ebp), %ebx
                                                                                                                        mov
62
       // Release the lock, equivalent to lk->locked = 0.
                                                                                      243:
                                                                                211
                                                                                                                        leave
63
       // This code can't use a C assignment, since it might
                                                                                              e9 77 fe ff ff
                                                                                212
                                                                                      244:
                                                                                                                                c0 <popcli>
                                                                                                                        qmp
64
       // not be atomic. A real OS would use C atomics here.
                                                                                213
                                                                                      249:
                                                                                              83 ec 0c
                                                                                                                        sub
                                                                                                                                $0xc, %esp
65
       asm volatile("movl $0, %0" : "+m" (lk->locked) : );
                                                                                214
66
                                                                                      24c:
                                                                                              68 26 00 00 00
                                                                                                                        push
                                                                                                                                $0x26
                                                                                215
                                                                                      251:
                                                                                              e8 fc ff ff ff
                                                                                                                        call
                                                                                                                                252 < release + 0x42 >
       popcli();
                                                                                216
```

7. 查阅i386手册,了解pushcli函数中的EFLAGS和FL_IF分别是什么? sti和cli内联汇编函数是如何影响前者的?

- EFLAGS: 32位的标志位寄存器
- FL_IF: xv6自定义的宏,与EFLAGS相与可以得到中断位IF

```
Figure 2-8. EFLAGS Register
       // This file contains definitions for the
       // x86 memory management unit (MMU).
                                                                                                                      16-BIT FLAGS REGISTER
       // Eflags register
                                                                                                                  15
                                                                                31
       #define FL IF
                                   0x00000200
                                                      // Interrupt Enable
                                                                                 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
104
       void
       pushcli (void)
                                                                                     VIRTUAL 8086 MODE——X
106
     ⊟{
                                                                                     NESTED TASK FLAG-X
107
         int eflags;
                                                                                   I/O PRIVILEGE LEVEL-
108
                                                                                       DIRECTION FLAG-
109
         eflags = readeflags();
                                                                                      INTERRUPT ENABLE --- X
                                                                                            TRAP FLAG-S-
110
        cli();
111
         if(mycpu()->ncli == 0)
112
            mycpu()->intena = eflags & FL IF;
                                                                                          PARITY FLAG-
         mycpu() ->ncli += 1;
                                                                                           CARRY FLAG-S-
                                                                                       S = STATUS FLAG, C = CONTROL FLAG, X = SYSTEM FLAG
```

NOTE: 0 OR 1 INDICATES INTEL RESERVED. DO NOT DEFINE

7. 查阅i386手册,了解pushcli函数中的EFLAGS和FL_IF分别是什么? sti和cli内联汇编函数是如何影响前者的?

- STI: 设置 EFLAGS 寄存器中的中断标志 (IF)。设置 IF 标志之后,处理器在执 行下一个指令之后,开始响应可屏蔽的外部中断。(例如STI+RET)
- CLI: 如果CPL小于等于IOPL,清除EFLAGS寄存器中的IF标志,否则不清除, EFLAGS的其他标志位不会受到影响。

8. mycpu()->intena变量的作用是什么?

• 表示在调用pushcli函数之前 是否允许中断

• pushcli和popcli的调用过程… "

```
struct cpu {
  uchar apicid;
                               // Local APIC ID
  struct context *scheduler;
                               // swtch() here to enter scheduler
  struct taskstate ts;
                               // Used by x86 to find stack for interrupt
                               // x86 global descriptor table
  struct segdesc gdt[NSEGS];
  volatile uint started;
                               // Has the CPU started?
  int ncli;
                               // Depth of pushcli nesting.
                               // Were interrupts enabled before pushcli?
  int intena;
  struct proc *proc;
                               // The process running on this cpu or null
```

```
void
pushcli(void)
{
  int eflags;

  eflags = readeflags();
  cli();
  if(mycpu()->ncli == 0)
    mycpu()->intena = eflags & FL_IF;
  mycpu()->ncli += 1;
}
```

```
void
popcli(void)
{
   if(readeflags()&FL_IF)
     panic("popcli - interruptible");
   if(--mycpu()->ncli < 0)
     panic("popcli");
   if(mycpu()->ncli == 0 && mycpu()->intena)
     sti();
}
```

// Per-CPU state

Linux 锁相关代码分析(x86版本)

- 1. x86实现原子指令
- 方法: x86提供了附加的lock前缀,使带lock前缀的读修改写指令能原子性执行。
- 实现原理: 带lock前缀的指令在操作时会**锁住总线**, 使自身的执行即使在多处理器间也是原子性执行的。
- API使用: 原子性操作是线程间同步的基础,linux专门定义了一种只进行原子操作的类型atomic_t,并提供相关的原子读写调用API。

```
atomic_t数据结构与API
typedef struct {
   volatile int counter;
} atomic_t;
• 原子类型其实是int类型,只是禁止寄存器对其暂存

    static inline int atomic_read(const atomic_t *v)

   return v->counter;
static inline void atomic_set(atomic_t *v, int i)
   v->counter = i;
```

• 单独读或者写, 在x86下是原子性的

原子加,原子减

```
    static inline void atomic_add(int i, atomic_t *v)
```

```
• {
```

asm volatile(LOCK_PREFIX "addl %1,%0"

```
• : "+m" (v->counter)
```

```
• : "ir" (i));
```

• }

•

static inline void atomic_sub(int i, atomic_t *v)

```
• {
```

asm volatile(LOCK_PREFIX "subl %1,%0"

```
• : "+m" (v->counter)
```

```
• : "ir" (i));
```

• }

LOCK_PREFIX 原子指令前缀

counter值加上i 再存回 counter

linux FIFO ticket-based spinlock

- 1. 普通自旋锁的缺陷: 由于无序竞争的本质特点,内核执行线程 无法保证何时可以取到锁,某些执行线程可能需要等待很长时间, 导致"不公平"问题的产生。
- 2. 改进方法: 先申请锁的排一个号码, 当锁释放时候,然后按照号码来调度号码小的任务(类似去银行办理服务, 假设只有一个窗口,每个人有排队号码)
- 3. 数据结构: next 与 owner 域, 均初始化为0
- next: 等待服务的任务数目,
- owner: 已经服务完成的任务数目

```
next: 等待服务的任务数目,
owner: 已经服务完成的任务数目
                                   next++,
                                   返回原来值
                                   新来了一个服务
acquire{
  tmp = fetch_and_add(next)
  while( tmp != owner )
                                  当前完成任务不等于等待服
                                  务任务数, 忙等待(直到排队
                                  号到了)
release{
 atomic_add(owner);
                                    服务完成任务数++
```