

Cyber-physical WebAssembly: Secure Hardware Interfaces and Pluggable Drivers

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Abstract—The rapid expansion of Internet of Things (IoT), edge, and embedded devices in the past decade has introduced numerous challenges in terms of security and configuration management. Simultaneously, advances in cloud-native development practices have greatly enhanced the development experience and facilitated quicker updates, thereby enhancing application security. However, applying these advances to IoT, edge, and embedded devices remains a complex task, primarily due to the heterogeneous environments and the need to support devices with extended lifespans. WebAssembly and the WebAssembly System Interface (WASI) has emerged as a promising technology to bridge this gap. As WebAssembly becomes more popular on IoT, edge, and embedded devices, there is a growing demand for hardware interface support in WebAssembly programs. This work presents WASI proposals and proof-of-concept implementations to enable hardware interaction with I2C and USB, which are two commonly used protocols in IoT, directly from WebAssembly applications. This is achieved by running the device drivers within WebAssembly as well. A thorough evaluation of the proof of concepts shows that WASI-USB introduces a minimal overhead of at most 8% compared to native operating system USB APIs. However, the results show that runtime initialization overhead can be significant in low-latency applications.

Index Terms—Cyber-physical, edge, embedded, hardware abstraction layer, Internet of Things (IoT), WebAssembly, WebAssembly System Interface (WASI)

I. INTRODUCTION

The Internet of Things (IoT) refers to the network of devices and electronics embedded with various sensors, software, and other technologies to gather and exchange information with other devices and systems over the internet [1]. During the past decade, IoT has experienced remarkable growth in a variety of industries and has become an integral part of our daily lives with more than 17 billion devices connected in 2024 [2].

This work has been partially funded by the European Union (Horizon Europe, Smart Networks and Services Joint Undertaking, ELASTIC project, Grant Agreement number 101139067, <https://elasticproject.eu/>).

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A large amount of IoT devices are integral to critical infrastructure [3] and operate on the edge of the internet, where the management of their software and firmware poses significant challenges due to their inherent complexity [4]. In accordance with the Cyber Resilience Act [5], manufacturers must ensure effective vulnerability management and provide security updates for at least the time the product is expected to be in use. Moreover, the automotive industry is increasingly adopting the practice of wirelessly distributing software updates to vehicles, known as over-the-air updates, where security is of critical importance [6], [7]. In parallel, Industrial Internet of Things systems often feature devices with operational lifespans of more than 30 years [8], [9]. To ensure forward compatibility, these systems must be able to integrate with newer hardware components, such as updated sensors and displays, since the original hardware usually reaches end of life when they need to be replaced. This necessitates modularity at the driver level. Additionally, these systems are often tightly integrated with devices that have limited hardware capabilities, increasing the need for minimal overhead.

Although cloud-native development practices have introduced benefits such as isolation, componentization, containerization, and orchestration platforms, these advances are difficult to apply to edge environments and even more so for IoT devices, embedded systems, and microcontrollers [10]. Ensuring drivers are secure and up-to-date is crucial for supply chain security, as drivers often have elevated system privileges or direct access to hardware. W3C [11] WebAssembly (Wasm) [12] and the WebAssembly System Interface (WASI) [13] emerges as a promising solution to shrink the trusted computing base and enhance embedded development and maintainability through a trusted runtime, modern development methodologies, toolchains and software development kits. Wasm was originally created to execute binary code within the browser to improve the performance of web applications [14], and is now actively used outside the browser via WASI [15]. WASI facilitates operating system communication within Wasm by providing a set of portable Application Programming Interfaces (APIs). However, embedded and IoT applications require

access to hardware interfaces to enable communication with the system in which they operate. Wasm does not currently meet the demands of cyber-physical systems, particularly in accessing connected hardware. Standardized WASI interfaces are needed in Wasm runtimes to ensure portability and enable communication with hardware across diverse devices.

In this work, we propose standardized WASI interfaces and proof-of-concept implementations for connecting hardware using I2C and USB, along with security mediation and isolation, on low-powered embedded and IoT devices running a Wasm runtime with pluggable drivers. Our solution aims to enhance supply chain security by reducing the number of components that need to be trusted, addressing the inherent insecurity of native drivers. Furthermore, we emphasize the importance of forward compatibility and modularity at the driver level to ensure adaptability and security in evolving technological landscapes. Specifically, this paper aims to answer the following **RESEARCH QUESTIONS**:

RQ 1: How to enable WebAssembly applications outside the browser to interact with hardware interfaces such as I2C and USB?

RQ 2: How to apply access control security measures in WebAssembly to ensure authorized access to hardware interfaces?

RQ 3: How does our solution to interact with hardware interfaces through WebAssembly impact performance compared to native solutions?

We begin by positioning our work within the existing literature in Section II. Then, we provide a threefold contribution to the state of the art. Specifically, our **CONTRIBUTIONS** are:

Contribution 1: Develop a novel solution to enable WebAssembly-based workloads on IoT and edge devices to interact with (I2C and USB) hardware interfaces (Section III). Specifically, our solution tackles the challenge of giving sandboxed WebAssembly applications mediated access to device resources.

Contribution 2: Demonstrate the feasibility of our solution through the development of multiple proof-of-concept implementations (Section IV).

Contribution 3: Evaluate our solution by conducting benchmarks and measuring performance overhead compared to native hardware access (Section V).

II. RELATED WORK

WebAssembly has emerged as a promising technology for enabling high-performance, cross-platform applications, particularly in the context of embedded systems and IoT devices. Several studies have explored the integration of Wasm with various hardware and software environments to enhance the capabilities and security of these systems, which are discussed below.

Singh et al. [16] developed the WARDUINO virtual machine (VM), which exposes common IoT Arduino APIs to the Wasm runtime. WARDUINO only facilitates the use of Wasm in, as its name suggests, Arduino-based projects. This poses notable limitations in terms of portability. This also means that the

hosting device must provide all the necessary APIs and that there is no hardware support beyond the Arduino ecosystem.

Aerogel [17] is a lightweight access control framework that addresses security gaps between the bare-metal IoT devices and the Wasm execution environment concerning access control for sensors, actuators, processor energy usage, and memory usage. Aerogel enhances the security and provides resource management in multi-tenant Wasm runtime environments, where each Wasm application is considered a tenant. A user-defined access control specification sheet allows one to set restrictions on specific inputs/outputs (I/Os), limitations on the number of accesses within a specified time frame, an upper bound on memory and CPU usage, and the maximum number of applications having shared access to a specific hardware component. Access to the requested resource will only be granted if all access control conditions are met.

WiProg [18] is a framework that simplifies integrated IoT programming and application development for sensor-to-edge systems using Wasm, focusing on application offloading. It allows for a monolithic application development approach, enabling offloading based on compiler annotations and policies. WiProg provides hardware-agnostic APIs for analog I/O, digital I/O, and Universal Asynchronous Receiver-Transmitter (UART), but lacks support for important protocols like I2C.

Li et al. [19] proposed WAIT, a lightweight Wasm runtime for IoT devices in device-cloud integrated applications. It enables Wasm ahead-of-time (AOT) compilation on resource-constrained devices. Upon receiving a Wasm module binary, WAIT performs on-device AOT compilation, checks sand-boxing guarantees, and executes the module. Native APIs for peripherals and I/O are exposed via the Wasm import mechanism. The AOT compiler then uses binary rewriting to replace imported function calls with direct assembly code, reducing external function call overhead.

Finally, Wasmachine [20] is a bare-metal operating system for embedded devices, running everything in kernel mode for zero-cost system calls. It exports system calls in WASI prototypes and runs each Wasm application as a kernel thread, allowing the application to invoke system calls as normal function calls without additional context switching costs. Hardware access is provided using WASI with per-application permissions. Despite its innovative approach, it has certain limitations, including limited CPU architecture support and the reliance on a Unix-like monolithic kernel architecture, which may affect its feasibility on some IoT or edge devices.

III. ARCHITECTURE

The proof-of-concept architecture designed by the authors to enable mediated (I2C and USB) hardware interface support in Wasm applications is shown in Fig. 1. Both the application and the peripheral device drivers run as **WebAssembly components**. These connect to the runtime via standardized generic I2C and USB **WebAssembly System Interfaces**. The runtime itself has a number of **host components**, one for each interface, that implement the interfaces and provide access-control list (**ACL**) and **capability-based security**. The host

component itself communicates with the host operating system (OS) and uses **OS-specific APIs** for hardware access. The remainder of this section examines important concepts of our novel solution in more detail.

A. Interface Worlds

An interface world describes the system calls that are supported by the runtime using the Component Model’s Interface Description Language (IDL) called Wasm Interface Type (WIT). For example, the USB world contains a WIT description of all the system calls that Wasm components can use to communicate with USB devices.

This world is a high-level interface contract between the runtime and the guest component and can be used by tools such as `wit-bindgen` [21] to automatically generate bindings for calling these APIs from various programming languages. The source code of the guest components uses these bindings to implement the world and to call the imported APIs, allowing them to interact with hardware devices supported by the interface. The runtime host component can use these bindings to implement and export the functions that make up the API.

B. Host Component

The host component is the host-side implementation of the system call API. It is embedded in the runtime and runs as native code directly on the underlying OS, as it requires access to native APIs of the host OS to communicate with hardware devices. As such, the host component is highly dependent on the specific host OS interfaces. Because it is embedded in the runtime, the host component also depends on the specific implementation details of the runtime. Therefore, the implementation of the host component is unique for every combination of runtime and underlying (operating) system. Any new system or runtime that wishes to support certain worlds, will require a modified implementation of the host component. However, this burden can be lessened by using higher-level cross-platform libraries to implement the host component, rather than depending directly on the OS through system calls. This way, one implementation of the host component can be compiled for multiple operating systems. Unfortunately, using a higher-level library is not always feasible, especially on certain low-powered embedded devices due to device-specific limitations, requiring a custom host component implementation for those devices.

C. Guest Components

Guest components contain the application logic and device drivers or a hardware abstraction layer (HAL). These can be made up of multiple separate components, or the application logic can embed the driver and HAL logic inside of it. A guest component specifies which interfaces it requires through imports and the interfaces it provides through exports using a WIT world description. This informs the runtime about which host component interfaces to connect to the guest component, allowing the guest component to interact

with hardware devices through host component function calls. Guest components are contained within a sandbox by the runtime, ensuring they remain isolated and cannot access system resources without authorization.

D. Runtime

The Wasm runtime resolves the imports and exports for all components and links them together. To successfully resolve imports and exports, the runtime must support the WIT world that a component implements. This can be achieved by a host component providing the necessary implementations for the guest component’s imported functions. After linking, the guest component has access to the imported functions from the world it implements. The runtime, in turn, has access to the functions that the world exports. To begin execution, the runtime must call one of those exported functions. Since interface worlds are not concerned with how a component starts execution, this is delegated to the `wasi-cli` [22] world to specify the entry-point. Popular Wasm runtimes include Wasmtime [23] and WebAssembly Micro Runtime (WAMR) [24], which are both being developed by the Bytecode Alliance [25].

E. Capability-Based Security

One of the strengths of WASI is its capability-based security mechanism. This mechanism implicitly allows granular control over what resources an application can access based on the API definition. If a resource is not provided, the module cannot interact with it, implicitly revoking the capability for that resource.

Although WASI enables the implementation of capability-based security for system interfaces, it does not stipulate the level of granularity at which these security controls should be constructed. To illustrate, a USB API may employ capability-based access control at multiple levels:

- Device level: Allow only devices with specific vendor and product ID pairs.
- Configuration level: Allow only devices with specific configuration types, e.g., self-powered devices.
- Interface level: Allow only interfaces with specific property types, e.g., those with the mass storage class code.
- Endpoint level: Allow only endpoints with specific property types, e.g., those with an IN direction that restricts the guest to receiving data only.

The capability-based security model is inherently defined by the API, which also determines the granularity of the security measures. This implies that access control must define the resources to be protected, which is impractical for fine granularity and challenging when control layers are orthogonal or complementary. Consequently, we propose an extension of capability-based security with explicit access control lists.

F. ACL-Based Security

Access control lists add a layer of granular and orthogonal access control, complementing capability-based access control. Unlike capability-based access control, which requires changes

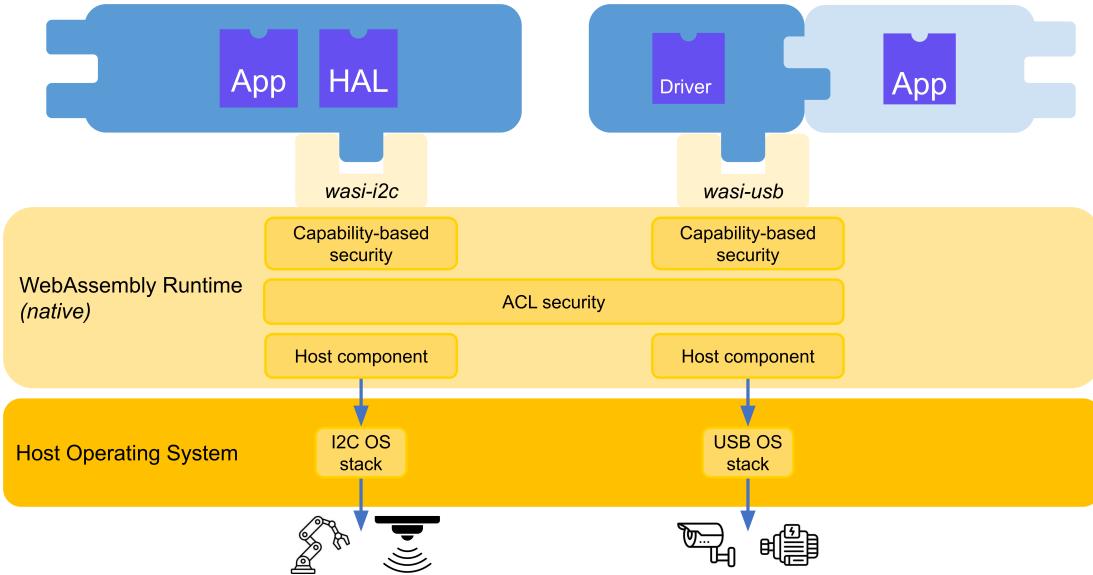


Fig. 1. Schematic representation of the proof-of-concept architecture that enables I2C and USB hardware interface support in Wasm applications. The application and device drivers run as Wasm guest components and connect to the host components via WASI. The host components provide ACL and capability-based security and use underlying OS-specific APIs to implement the interfaces. This figure contains resources from Flaticon.com.

to the API, ACLs can be implemented without affecting the existing API. For example, an ACL rule might allow an application to read from a device but not write to it. Furthermore, ACLs offer greater flexibility and can be fine-tuned to the environment and runtime as needed. Based on these advantages, we propose that standardized interfaces use capability-based security for high-level concepts and use an additional ACL to further restrict permissions.

Our implementation of ACL-based access control necessitates the incorporation of logic specific to the runtime, environment, and peripheral. Whenever the runtime creates a new module instance, an I/O context is created and linked to the corresponding execution environment. Whenever a device interaction is performed, the corresponding I/O context is retrieved from the execution environment, and depending on the type of device interaction and the I/O context's properties, the action is permitted or denied.

The I/O context's properties may refer to permitted interaction types, such as read or write. Additionally, specific values may be blocked, which is necessary for device interaction with arbitrary data, but, for example, prevents changes in the configuration. Furthermore, the context may specify how connections should be established with end devices, preventing communication with other peripherals sharing the same bus.

IV. IMPLEMENTATION DETAILS

To validate our proposed cyber-physical WebAssembly architecture, we developed three proof-of-concept implementations, each addressing different aspects of the solution. Our primary objectives were to confirm the feasibility of the approach and identify potential opportunities or shortcomings in the underlying architecture and standardization methodologies.

- **I2C in Wasmtime:** Demonstrates driving an I2C seven-segment display from Wasm on a Linux host and a Raspberry Pi Pico, and reading temperature and humidity data from an HTS221 sensor on a Raspberry Pi 3B.
- **I2C in WAMR:** Demonstrates reading data from an HTS221 sensor. Due to WAMR's current limitations, including the lack of WASI Preview 2 support, this solution uses an ACL-based security system instead of the component model or capability-based security.
- **USB in Wasmtime:** Demonstrates Wasm applications communicating with USB devices, including a flash drive and an Xbox controller using Wasm-based drivers.

A. I2C in Wasmtime

The I2C in Wasmtime proof-of-concept implementation [26] is made up of several components: the host implementation, two WIT worlds and two guest implementations.

The host implementation handles the I2C connection with the device, whereas the guests are responsible for device-specific logic, such as reading temperature or displaying time. The host and guest components interact through two WIT worlds, one for the display and the other for the HTS221 sensor. These WIT worlds import the necessary interfaces from the WASI-I2C [27] proposal that are implemented by the host, allowing the guest to communicate with the I2C device. Additionally, the WIT worlds export device-specific functions implemented by the guest. Although these worlds were written for our specific proof of concepts, they aim to be as generic as possible to allow future support for other displays and sensors.

The guest implementations do not use Rust's `std` library, only importing necessary types individually, which helps minimize the size of the compiled Wasm binaries. The guest component for the seven-segment display di-

rectly writes bytes to control it, while the HTS221 sensor component leverages the `embedded-hal` [28] compatible `hts221` [29] Rust crate for simplicity. To adapt the `embedded-hal` API to the slightly different generated bindings, the `wasi-embedded-hal` [30] crate was developed.

B. I2C in WAMR

WAMR currently does not support WASI Preview 2. Consequently, the component model and WIT are not available, which means that a standardized I2C connection cannot be passed to the guest. As a workaround, the I2C connection remains global on the host side. For the host to bind with the guest, it has to register the function that the guest imports and find the functions that the guest exports.

C. USB in Wasmtime

The proof-of-concept implementation [31], [32] of USB in Wasmtime includes a host implementation along with several guest implementations. The main concept of the USB implementation in Wasmtime mirrors that of the I2C implementation in Wasmtime: the host component is platform dependent and interacts with the underlying system to access the USB resource, while the guest components handle device-specific logic, such as reading the inputs of an Xbox controller.

To facilitate the development of the host component and enhance its platform independence, `rusb` [33], a Rust wrapper around `libusb` [34], is used. The USB flash drive guest component supports various USB Attached SCSI device commands, including basic file system operations like listing directory contents and reading files. The file system support is fully integrated within the guest component itself using the `fatfs` [35] Rust crate, without relying on the host operating system's file system capabilities. The Xbox guest component continuously monitors the current state of the controller through its USB interrupt pipe and writes button presses and joystick positions to standard output.

V. EVALUATION RESULTS

This section assesses the practicality of our proposals in real-world scenarios. Initially, each proof of concept is evaluated to ensure it meets the functional requirements, typically by manually verifying device functionality. Subsequently, various benchmarks are performed to identify any performance implications.

A. I2C Performance Evaluation

The I2C proof of concept for the seven-segment display is evaluated on a Raspberry Pi 4B, whereas the HTS221 sensor proof of concept is evaluated on a Raspberry Pi 3B. The display evaluation will compare the native, Wasmtime, and WAMR implementations, while the HTS221 sensor evaluation will compare the native and Wasmtime implementations. All benchmark results are available [26] on GitHub.

To assess performance overhead, we conduct 100 iterations of writing a four-digit number to the display and also reading the temperature from the HTS221 sensor. To highlight the

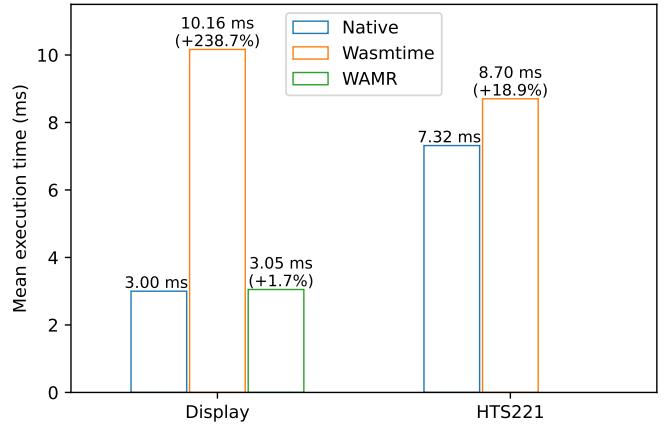


Fig. 2. Mean cold start execution times for writing a four-digit number to the display and reading the temperature from the HTS221 sensor, comparing native, Wasmtime, and WAMR implementations.

worst-case overhead, cold start execution times are measured, where each measurement includes the time required to instantiate the Wasm binary within the runtime. The mean cold start execution times are then compared across the native, Wasmtime, and WAMR implementations. Fig. 2 illustrates the mean cold start execution times for the two proof of concepts. Wasmtime shows a notable overhead relative to the native implementation, with the mean cold start execution times being 7.16 ms (+238.7%) longer for the display and 1.38 ms (+18.9%) longer for the HTS221 proof of concept. In contrast, the WAMR implementation incurs only a minimal overhead of 50 μ s (+1.7%). Most of the additional time consumed by Wasmtime can be attributed to functions associated with Cranelift [36], a compiler backend utilized by Wasmtime for both just-in-time and ahead-of-time compilation.

B. USB Performance Evaluation

The USB proof of concept is evaluated on three systems: one with an AMD Ryzen 7 5800H and USB3.0 running x86 Linux 6.8.9, a Raspberry Pi 3B+ with USB2.0 running AArch64 Linux 6.8.0, and a third with an AMD Ryzen 7 5800X3D and USB3.0 running x86 Windows 10 22H2. All benchmark results are available [37] on GitHub.

To assess the runtime size and memory usage overhead introduced by the USB host component, we compare the Wasmtime runtime both with and without the USB host component. Our measurements show that the runtime size overhead is minimal, adding only 160 KiB (+1.1%) for the x86 Linux runtime, 220 KiB (+1.7%) for the AArch64 Linux runtime, and 275 KiB (+1.9%) for the Windows runtime. Memory usage overhead is also minimal for the Linux runtimes, comprising only 459 KiB (+1.7%) for the x86 Linux runtime and 295 KiB (+1.5%) for the AArch64 Linux runtime. The Windows runtime incurs a more severe memory overhead of 1427 KiB (+11.0%), which can be attributed to an entirely different backend being used by `libusb` on Windows.

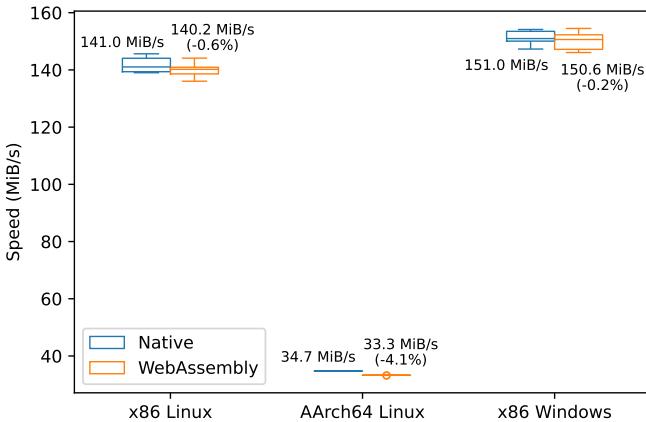


Fig. 3. USB flash drive sequential read speeds across various platforms for both native and Wasm guest applications show minimal overhead on high-powered machines, while lower-powered devices experience a larger overhead. The reported speeds represent the median throughput.

The impact on throughput is assessed using a USB flash drive to read or write as many data blocks as quickly as possible. Fig. 3 depicts the results for a sequential read benchmark. On x86 Linux and Windows, the overhead is minimal, respectively resulting in only a 0.8 MiB/s (-0.6%) and 0.4 MiB/s (-0.2%) reduction in median throughput. The overall throughput on AArch64 Linux is notably lower, along with a 1.4 MiB/s (-4.1%) decrease in median throughput in Wasm. This can be attributed to the fact that the Raspberry Pi 3B+ only supports USB2.0, and this also implies that the overhead is larger on less powerful hardware. Despite this, the overhead remains relatively modest. Benchmarks for sequential write, random read, and random write show similar relative overheads.

The latency overhead is evaluated using an Arduino Nano 33 IoT device with custom firmware that receives a message from the host device and echoes it back to the host without modifications. This allows the host to measure the round-trip time (RTT) of this message. Fig. 4 shows the observed RTTs for a bulk transfer of a 32-byte message. Due to the low priority of bulk transfers among USB transfer modes, numerous outliers are present. Some outliers have been excluded to facilitate the comparison of the median RTTs. The overhead introduced by Wasm is relatively modest. On x86 Linux, it results in a median RTT that is only 7 μ s (+3.5%) longer than the native implementation. On x86 Windows, an even lower overhead of 2 μ s (+0.8%) is observed. However, on AArch64, the overhead is more pronounced, with an increase of 45 μ s (+8.0%).

VI. CONCLUSION

With the growing popularity of WebAssembly on embedded and IoT devices, the need for standardized hardware interface support in WebAssembly arose. This paper introduces two WebAssembly System Interface proposals, WASI-I2C and WASI-USB, enabling the use of I2C and USB within WebAssembly. Our cyber-physical WebAssembly proof-of-concept

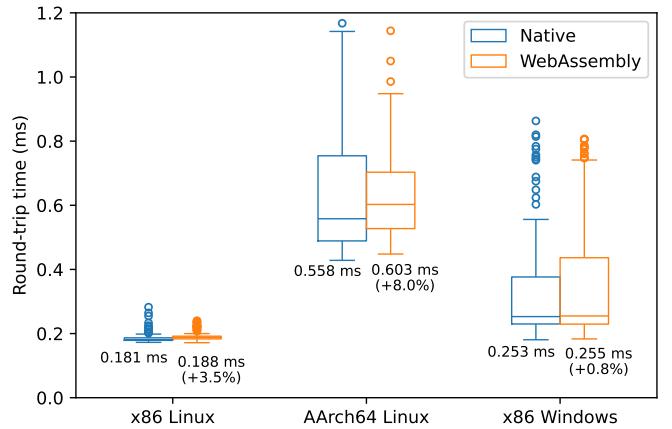


Fig. 4. Round-trip time for a 32-byte USB bulk transfer across different platforms, comparing native and Wasm guest applications. x86 Linux shows minimal outliers and reasonable overhead, whereas AArch64 displays greater overhead. The reported times represent the median round-trip time.

architecture is described in depth, including a solution for authorized hardware interface access. Hardware interface access is managed through the capability-based security mechanism of WASI, and is extended with explicit ACL-based security for more granular access control. Proof-of-concept implementations showcase the feasibility of the proposed architecture and are evaluated to confirm that they meet functional requirements and to identify any performance impacts. Our evaluation results show that the overhead introduced by WASI-USB is minimal, resulting in a maximum throughput reduction of 1.4 MiB/s (-4.1%) and an increase in round-trip time of up to 45 μ s (+8.0%). Our test results for WASI-I2C include the initialization of the Wasm binary within the runtime in each measurement. This reveals significantly worse overhead in Wasmtime, with the mean cold start execution time increasing by up to 7.16 ms (+238.7%) at worst and 1.38 ms (+18.9%) at best. This emphasizes that the overhead of runtime and binary initialization is significant, indicating that low-latency applications require a warm-up period.

This work has established a foundation for standardizing the I2C and USB WASI interfaces, and demonstrates the feasibility of a cyber-physical WebAssembly future on IoT and embedded devices. However, future work will be necessary to stabilize the standardization proposals, which will involve extensive testing, implementation across multiple runtimes, and gathering community feedback. Additionally, these proposals lack asynchronous support, which limits their functionality in certain environments.

ACKNOWLEDGMENT

We extend our heartfelt gratitude to everyone who contributed to these standardization proposals: Valentin Olpp, Dan Gohman, Emiel Van Severen and the Bytecode Alliance & WASI subgroup of the W3C WebAssembly Community Group. This work received funding from the Smart Networks and Services Joint Undertaking (SNS JU) under the

Horizon Europe research and innovation program of the European Union under Grant Agreement number 101139067 (<https://elasticproject.eu/>). Expressed views and opinions are however those of the authors only and do not necessarily reflect those of the European Union. Neither the European Union nor the granting authority can be held responsible for them.

REFERENCES

- [1] IEC Market Strategy Board, “IoT 2020: Smart and secure IoT platform,” IEC, Tech. Rep., Oct. 2016.
- [2] L. S. Vailshery, “Number of Internet of Things (IoT) connected devices worldwide from 2019 to 2030,” Jul. 2023. [Online]. Available: <https://www.statista.com/statistics/1194682/iot-connected-devices-vertically/>
- [3] E. A. Lee, “Cyber Physical Systems: Design Challenges,” in *2008 11th IEEE International Symposium on Object and Component-Oriented Real-Time Distributed Computing (ISORC)*, May 2008, pp. 363–369.
- [4] D. Gustin and J. Jasperneite, “IoT Device Management Based on Open Source Platforms - Requirements Analysis and Evaluation,” in *NOMS 2022-2022 IEEE/IFIP Network Operations and Management Symposium*, Apr. 2022, pp. 1–4, iSSN: 2374-9709.
- [5] European Commission, “EU Cyber Resilience Act,” Jul. 2024.
- [6] A. Ghosal, S. Halder, and M. Conti, “Secure over-the-air software update for connected vehicles,” *Computer Networks*, vol. 218, Dec. 2022.
- [7] J. Oliveira, P. Almeida, P. Rito, D. Raposo, and S. Sargento, “Over-The-Air updates for Software Defined Vehicle services with IPFS,” in *NOMS 2024-2024 IEEE Network Operations and Management Symposium*, May 2024, pp. 1–9, iSSN: 2374-9709.
- [8] H. P. Breivold and K. Sandström, “Internet of Things for Industrial Automation - Challenges and Technical Solutions,” in *2015 IEEE International Conference on Data Science and Data Intensive Systems*, Dec. 2015, pp. 532–539.
- [9] K. Stouffer, M. Pease, C. Tang, T. Zimmerman, V. Pillitteri, S. Lightman, A. Hahn, S. Saravia, A. Sherule, and M. Thompson, “Guide to Operational Technology (OT) security,” National Institute of Standards and Technology (U.S.), Gaithersburg, MD, Tech. Rep. NIST SP 800-82r3, Sep. 2023.
- [10] L. E. Lwakatare, T. Karvonen, T. Sauvola, P. Kuvaja, H. H. Olsson, J. Bosch, and M. Oivo, “Towards DevOps in the Embedded Systems Domain: Why is It So Hard?” in *2016 49th Hawaii International Conference on System Sciences (HICSS)*, Jan. 2016, pp. 5437–5446.
- [11] “W3C.” [Online]. Available: <https://www.w3.org/>
- [12] “WebAssembly.” [Online]. Available: <https://webassembly.org/>
- [13] “WASI.” [Online]. Available: <https://wasi.dev/>
- [14] A. Haas, A. Rossberg, D. L. Schuff, B. L. Titzer, M. Holman, D. Gohman, L. Wagner, A. Zakai, and J. Bastien, “Bringing the web up to speed with WebAssembly,” in *Proceedings of the 38th ACM SIGPLAN Conference on Programming Language Design and Implementation*. Barcelona Spain: ACM, Jun. 2017, pp. 185–200.
- [15] B. Spies and M. Mock, “An Evaluation of WebAssembly in Non-Web Environments,” in *2021 XLVII Latin American Computing Conference (CLEI)*, Oct. 2021, pp. 1–10.
- [16] R. G. Singh and C. Scholliers, “WARDuino: A dynamic WebAssembly virtual machine for programming microcontrollers,” *Proceedings of the 16th ACM SIGPLAN International Conference on Managed Programming Languages and Runtimes*, 2019.
- [17] R. Liu, L. Garcia, and M. Srivastava, “Aerogel: Lightweight Access Control Framework for WebAssembly-Based Bare-Metal IoT Devices,” in *2021 IEEE/ACM Symposium on Edge Computing (SEC)*, Dec. 2021, pp. 94–105.
- [18] B. Li, W. Dong, and Y. Gao, “WiProg: A WebAssembly-based approach to integrated IoT programming,” in *IEEE INFOCOM 2021 - IEEE Conference on Computer Communications*, 2021, pp. 1–10.
- [19] B. Li, H. Fan, Y. Gao, and W. Dong, “Bringing webassembly to resource-constrained iot devices for seamless device-cloud integration,” *Proceedings of the 20th Annual International Conference on Mobile Systems, Applications and Services*, 2022.
- [20] E. Wen and G. Weber, “Wasmachine: Bring the edge up to speed with A WebAssembly OS,” in *2020 IEEE 13th International Conference on Cloud Computing (CLOUD)*, 2020, pp. 353–360.
- [21] “bytecodealliance/wit-bindgen,” Sep. 2024, original-date: 2021-02-23T22:25:51Z. [Online]. Available: <https://github.com/bytecodealliance/wit-bindgen>
- [22] “WebAssembly/wasi-cli,” Sep. 2024, original-date: 2023-02-24T19:16:21Z. [Online]. Available: <https://github.com/WebAssembly/wasi-cli>
- [23] “Wasmtime.” [Online]. Available: <https://wasmtime.dev/>
- [24] “WebAssembly Micro Runtime.” [Online]. Available: <https://bytecodealliance.github.io/wamr.dev/>
- [25] “Bytecode Alliance.” [Online]. Available: <https://bytecodealliance.org/>
- [26] “idlabs-discover/i2c-wasm-components,” Sep. 2024, original-date: 2023-12-19T08:25:38Z. [Online]. Available: <https://github.com/idlabs-discover/i2c-wasm-components>
- [27] “WebAssembly/wasi-i2c,” Sep. 2024, original-date: 2023-03-31T22:38:38Z. [Online]. Available: <https://github.com/WebAssembly/wasi-i2c>
- [28] “rust-embedded/embedded-hal,” Sep. 2024, original-date: 2017-06-09T20:57:29Z. [Online]. Available: <https://github.com/rust-embedded/embedded-hal>
- [29] “Zelzahn/hts221,” Mar. 2024, original-date: 2024-03-09T19:42:30Z. [Online]. Available: <https://github.com/Zelzahn/hts221>
- [30] “idlabs-discover/wasi-embedded-hal,” Sep. 2024, original-date: 2024-03-15T17:09:02Z. [Online]. Available: <https://github.com/idlabs-discover/wasi-embedded-hal>
- [31] “idlabs-discover/usb-wasm,” Sep. 2024, original-date: 2024-03-03T16:29:03Z. [Online]. Available: <https://github.com/idlabs-discover/usb-wasm>
- [32] “WebAssembly/wasi-usb,” Aug. 2024, original-date: 2024-03-05T12:44:13Z. [Online]. Available: <https://github.com/WebAssembly/wasi-usb>
- [33] I. Averyanov, “alien/rusb,” Oct. 2024, original-date: 2019-07-28T13:25:48Z. [Online]. Available: <https://github.com/alien/rusb>
- [34] libusb, “libusb/libusb,” Sep. 2024, original-date: 2013-12-11T22:19:17Z. [Online]. Available: <https://github.com/libusb/libusb>
- [35] R. Harabieñ, “rafallh/rust-fatfs,” Sep. 2024, original-date: 2017-09-24T01:14:45Z. [Online]. Available: <https://github.com/rafallh/rust-fatfs>
- [36] “Cranelift.” [Online]. Available: <https://cranelift.dev/>
- [37] “idlabs-discover/wasi-usb-benchmarks,” Oct. 2024, original-date: 2024-10-09T21:28:38Z. [Online]. Available: <https://github.com/idlabs-discover/wasi-usb-benchmarks>