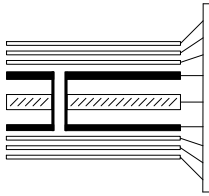
USB IO Extender Fabrication Document

Top Fabrication (Scale 1:1)

Layer Stack Legend



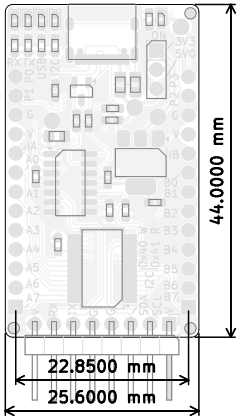
Material	Layer	Thickness	Dielectric Material	Type	Gerber
	F.Paste			Paste Mask	GBR
	F.Silkscreen			Legend	GBR
	F.Mask		Solder Resist	Solder Mask	GBR
Copper	**L1 (Sig, PWR)**	**0.07mm (2oz)**		**Signal**	**GBR**
Core		1.48mm	FR-4	Dielectric	
Copper	**L2 (GND)**	**0.07mm (2oz)**		**Signal**	**GBR**
	B.Mask		Solder Resist	Solder Mask	GBR
	B.Silkscreen			Legend	GBR
	B.Paste			Paste Mask	GBR

Total thickness: 1.62mm

Note: external layer thicknesses are specified after plating.

BOARD CHARACTERISTICS

Copper Layer Count:	2	Board Thickness:	1.6600 mm
Board overall dimensions:	25.6000 mm x 44.0000 mm		
Min track/spacing:	0.2000 mm / 0.1500 mm	Min hole diameter:	0.2500 mm
Copper Finish:	Immersion gold	Impedance Control:	Yes
Castellated pads:	No	Plated Board Edge:	No
Edge card connectors:	No		



FABRICATION NOTES (UNLESS OTHERWISE SPECIFIED)

1) OUTLINE DEFINED IN SEPARATE GERBER FILE WITH "Edge_Cuts.GBR" SUFFIX. DIMENSIONS OF CIRCUMSIZED RECTANGLE SHOWN ON THIS DWG FOR REF ONLY.

2) SEE SEPARATE DRILL FILES WITH ".DRL" SUFFIX FOR HOLE LOCATIONS. SELECTED HOLE LOCATIONS SHOWN ON THIS DWG FOR REF ONLY.

3) IMPEDANCE CONTROL REQUIRED.
Microstrip 100-Ohm Differential (L1 ref. L2)
0.2032mm width, 0.28mm spacing

4) CONFIRM TRACE WIDTHS AND SPACINGS.

5) DESIGN GEOMETRY MINIMUM FEATURE SIZES:

TRACE WIDTH	0.2 mm
TRACE TO TRACE	0.2 mm
MIN. HOLE (PTH)	0.25 mm
MIN. HOLE (NPTH)	0.5 mm
ANNULAR RING	0.15 mm
COPPER TO HOLE	0.254 mm
COPPER TO EDGE	0.3 mm
HOLE TO HOLE	0.5 mm

All dimensions are in millimeters unless otherwise specified.

Comments:	Company: thiago.palmieri@adastra.aleeas.com		Variant: No variant	
Board2Pdf: FABRICATION_TOP - Page 1	Board Name: USB IO Extender		Project Name: USB IO Extender	
Sheet Title: Fabrication Document	File Name: IOExtender.kicad_pcb	Designer: Thiago Palmieri	Date: 2024-04-13	Revision: 1.1
Sheet Path: FABRICATION_TOP		Reviewer: Thiago Palmieri	Size: A4	Sheet: of 1

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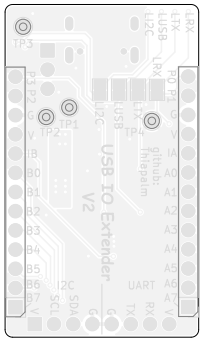
Bottom Fabrication (Scale 1:1)



All dimensions are in millimeters unless otherwise specified.

	Comments: Board2Pdf: FABRICATION_BOT - Page 1/1	Company: thiago.palmieri@adastra.aleeas.com		Variant: No variant
		Board Name: **USB IO Extender**		Project Name: **USB IO Extender**
	Sheet Title: Fabrication Document	File Name: IOExtender.kicad_pcb	Designer: Thiago Palmieri	Date: 2024-04-13 / Revision: 1.1
	Sheet Path: FABRICATION_BOT		Reviewer: Thiago Palmieri	Size: **A4** / Sheet: of **1**

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Drill Drawing (Top View) (Scale 1:1)

Symbol	Count	Hole Size	Plated
▪	???	???mm	Plated
▪	???	???mm	Plated
▪	???	???mm	Plated
▫	???	???mm	Plated
◇	??? (slot)	???mm	Plated
⊠	???	???mm	Plated
✳	???	???mm	Plated
⊠	???	???mm	Plated
⊠	???	???mm	Plated
⊕	???	???mm	Unplated

	Comments: Board2Pdf: DRILL_DRAWING - Page 3/	Company: thiago.palmieri@adastra.aleeas.com		Variant: No variant	
		Board Name: **USB IO Extender**		Project Name: **USB IO Extender**	
	Sheet Title: Fabrication Document	File Name: IOExtender.kicad_pcb	Designer: Thiago Palmieri	Date: 2024-04-13	Revision: 1.1
	Sheet Path: DRILL_DRAWING		Reviewer: Thiago Palmieri	Size: **A4**	Sheet: of **1**

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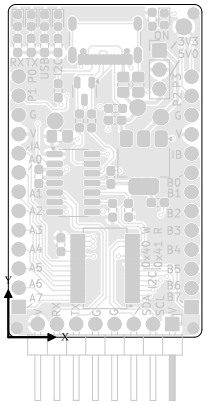
Top Test Points (Scale 1:1)

Ref.	Net	X [mm]	Y [mm]
TP?	???	???	???
TP?	???	???	???
TP?	???	???	???



All dimensions are in millimeters unless otherwise specified.

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	Sheet Title: Fabrication Document	File Name: IOExtender.kicad_pcb	Designer: Thiago Palmieri	Date: 2024-04-13 / Revision: 1.1
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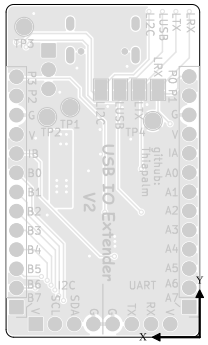
Bottom Test Points (Scale 1:1)

Ref.	Net	X [mm]	Y [mm]
TP1	+5V	130.618	94.618
TP2	+3V3	133.666	95.888
TP3	VDD	136.714	83.95
TP4	GND	119.696	96.396



All dimensions are in millimeters unless otherwise specified.

	Comments: Board2Pdf: TESTPOINTS_BOT - Page 5	Company: thiago.palmieri@adastra.aleeas.com		Variant: No variant
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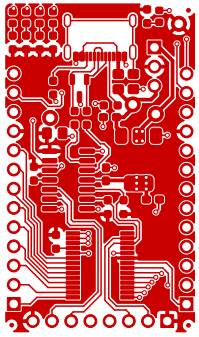
	Sheet Title: Fabrication Document	File Name: IOExtender.kicad_pcb	Designer: Thiago Palmieri	Date: 2024-04-13 / Revision: 1.1
	Sheet Path: TESTPOINTS_BOT		Reviewer: Thiago Palmieri	Size: **A4** / Sheet: of **1**

USB IO Extender Fabrication Document



	Comments: Board2Pdf: L1 - Page 6/11	Company: thiago.palmieri@adastra.aleeas.com		Variant: No variant	
		Board Name: **USB IO Extender**		Project Name: **USB IO Extender**	
	Sheet Title: Fabrication Document	File Name: IOExtender.kicad_pcb	Designer: Thiago Palmieri	Date: 2024-04-13	Revision: 1.1
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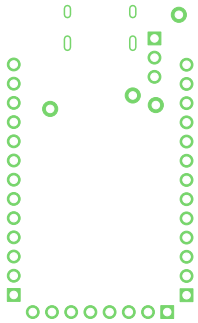
USB IO Extender Fabrication Document



	Comments: Board2Pdf: L2 - Page 7/11	Company: thiago.palmieri@adastra.aleeas.com		Variant: No variant
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	Sheet Title: Fabrication Document	File Name: IOExtender.kicad_pcb	Designer: Thiago Palmieri	Date: 2024-04-13 / Revision: 1.1
	Sheet Path: L2		Reviewer: Thiago Palmieri	Size: **A4** / Sheet: of **1**

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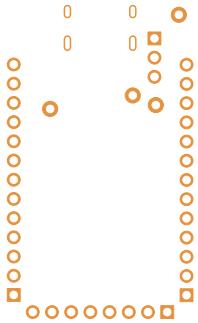

	Comments:	Company: thiago.palmieri@adastra.aleeas.com		Variant: No variant
	Board2Pdf: L3 - Page 8/11	Board Name: **USB IO Extender**		Project Name: **USB IO Extender**
	Sheet Title: Fabrication Document	File Name: IOExtender.kicad_pcb	Designer: Thiago Palmieri	Date: 2024-04-13 / Revision: 1.1
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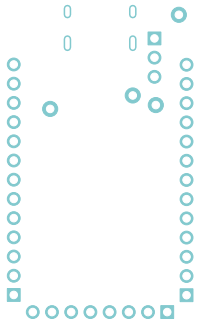
USB IO Extender Fabrication Document



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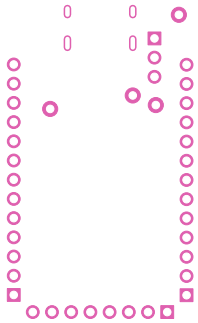
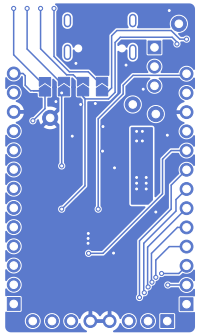
	Sheet Title: Fabrication Document	File Name: IOExtender.kicad_pcb	Designer: Thiago Palmieri	Date: 2024-04-13	Revision: 1.1
	Sheet Path: L4		Reviewer: Thiago Palmieri	Size: **A4**	Sheet: of **1**

USB IO Extender Fabrication Document



	Comments:	Company: thiago.palmieri@adastra.aleeas.com		Variant: No variant
	Board2Pdf: L5 - Page 10/11	Board Name: **USB IO Extender**		Project Name: **USB IO Extender**
	Sheet Title: Fabrication Document	File Name: IOExtender.kicad_pcb	Designer: Thiago Palmieri	Date: 2024-04-13 / Revision: 1.1
	Sheet Path: L5		Reviewer: Thiago Palmieri	Size: **A4** / Sheet: of **1**

USB IO Extender Fabrication Document



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		Board Name: **USB IO Extender**		Project Name: **USB IO Extender**	
	Sheet Title: Fabrication Document	File Name: IOExtender.kicad_pcb	Designer: Thiago Palmieri	Date: 2024-04-13	Revision: 1.1
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