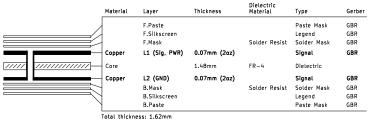
### **USB IO Extender Fabrication Document**

## **Top Fabrication (Scale 1:1)**

### **Layer Stack Legend**



Note: external layer thicknesses are specified after plating.

#### FABRICATION NOTES (UNLESS OTHERWISE SPECIFIED)

- OUTLINE DEFINED IN SEPARATE GERBER FILE WITH "Edge\_Cuts.GBR" SUFFIX.
   DIMENSIONS OF CIRCUMSIZED RECTANGLE SHOWN ON THIS DWG FOR REF ONLY.
- 2) SEE SEPARATE DRILL FILES WITH ".DRL" SUFFIX FOR HOLE LOCATIONS. SELECTED HOLE LOCATIONS SHOWN ON THIS DWG FOR REF ONLY.
- CONFIRM TRACE WIDTHS AND SPACINGS.
- DESIGN GEOMETRY MINIMUM FEATURE SIZES: TRACE WIDTH 0.2 mm TRACE TO TRACE 0.2 mm MIN. HOLE (PTH) MIN. HOLE (NPTH) 0.25 mm 0.5 mm ANNULAR RING 0.15 mm COPPER TO HOLE 0.254 mm COPPER TO EDGE 0.3 mm HOLE TO HOLE 0.5 mm

#### **BOARD CHARACTERISTICS**

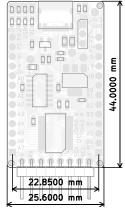
Copper Layer Count: 2 Board Thickness: 1.6600 mm

Board overall dimensions: 25.6000 mm x 44.0000 mm

Min track/spacing: 0.2000 mm / 0.1500 mm Min hole diameter: 0.2500 mm

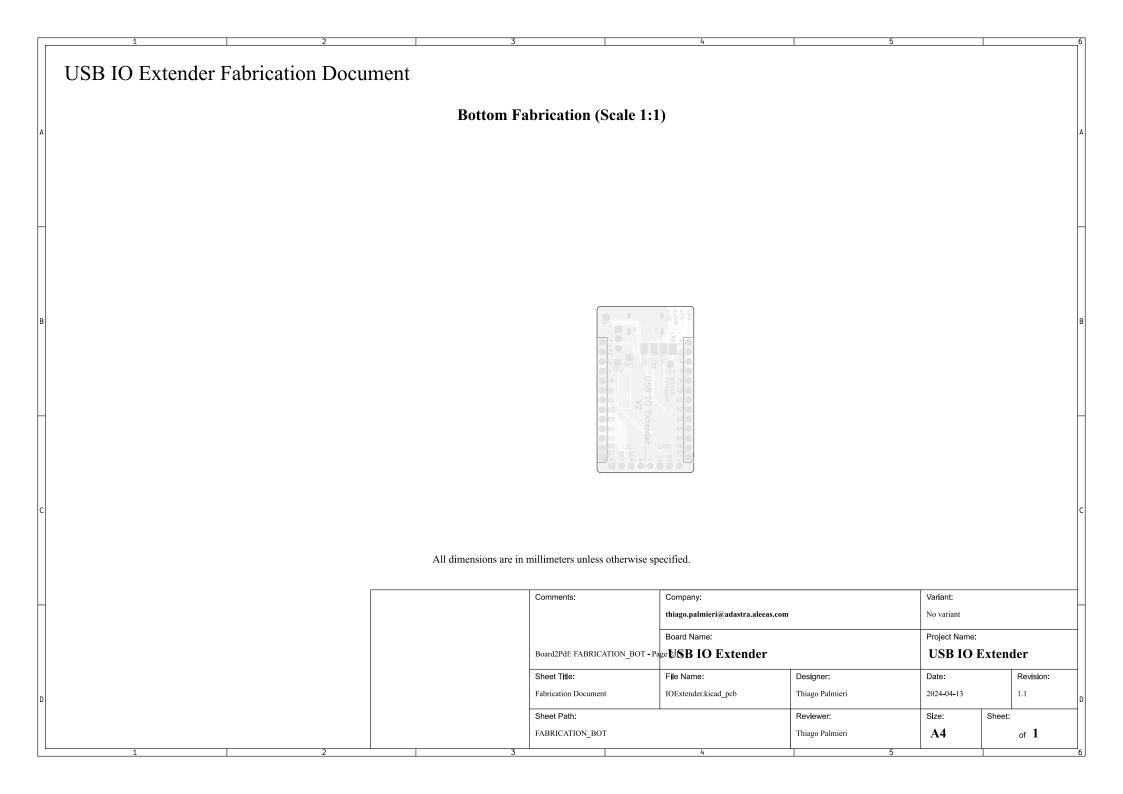
Copper Finish: Immersion gold Impedance Control: Yes
Castellated pads: No Plated Board Edge: No

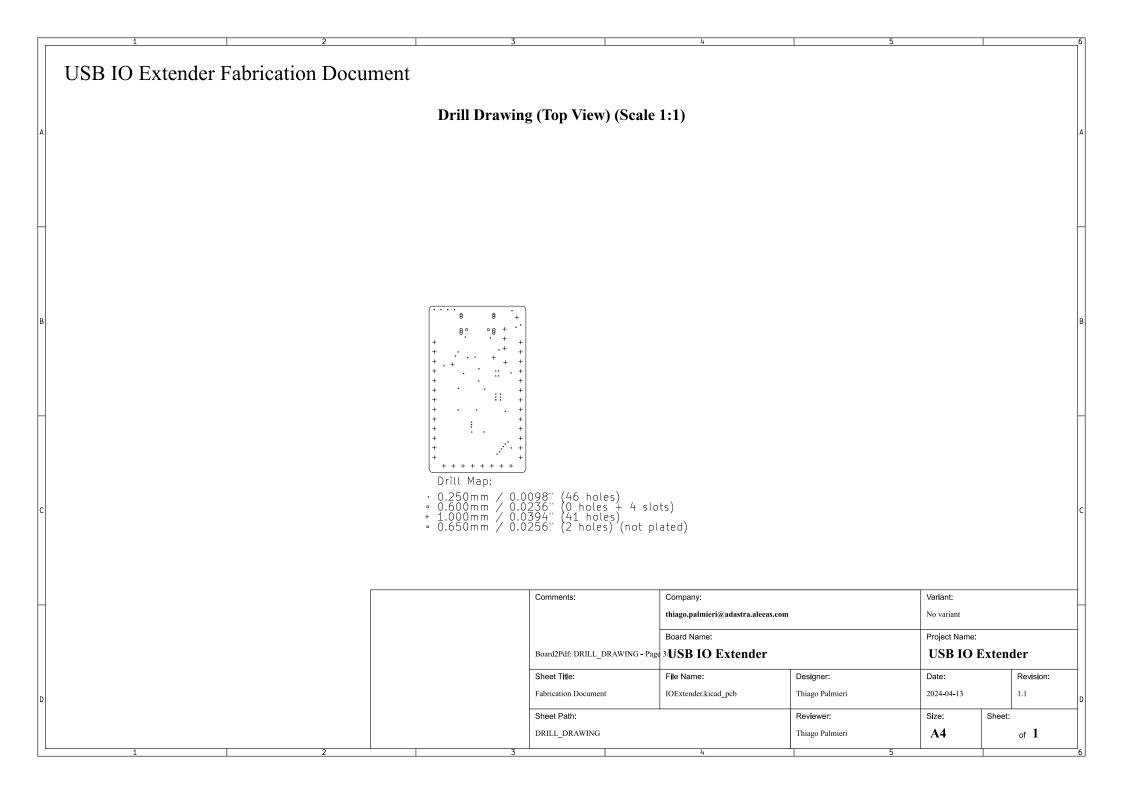
Edge card connectors: No



All dimensions are in millimeters unless otherwise specified.

	Comments:	Company:	Company:			Variant:		
	thiago.palmieri@adastra.aleeas.coi			No variant				
		Board Name:	Board Name:		Project Name:	Project Name:		
	Board2Pdf: FABRICATION	_TOP - Page <b>USB IO I</b>	SB IO Extender USB IO Ex		Extender			
	Sheet Title:	File Name:		Designer:	Date:	Revision:		
	Fabrication Document	IOExtender.kicad	d_pcb	Thiago Palmieri	2024-04-13	1.1	D	
	Sheet Path: FABRICATION_TOP			Reviewer:	Size:	Sheet:		
				Thiago Palmieri	A4	of $1$		
3		4	•	5	•		<u></u> 6	



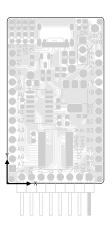


1 2 3 4 5

# USB IO Extender Fabrication Document

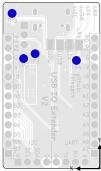
# **Top Test Points (Scale 1:1)**

Ref.	Net	X [mm]	Y [mm
TP?	???	???	???
TP?	???	???	???
TP?	???	???	???



All dimensions are in millimeters unless otherwise specified.

		Comments: Company:			Variant:				
		thiago.palmieri@adastra.aleeas.com		No variant	No variant				
				Board Name:		Project Name:			11
Board2Pdf: TESTPOINTS_TOP - Page		age 4USB IO Extender USB IO Ex		Exten	Extender				
		Sheet Title:		File Name:	Designer:	Date:		Revision:	1
		Fabrication Document		IOExtender.kicad_pcb	Thiago Palmieri	2024-04-13		1.1	D
		Sheet Path:			Reviewer:	Size:	Sheet		11
		TESTPOINTS_TOP			Thiago Palmieri	A4		of $1$	
	3			4	5	<u>'</u>			<b>-</b> 6



All dimensions are in millimeters unless otherwise specified.

	Comments:	Company:		Variant:		
		thiago.palmieri@adastra.aleeas.com		No variant		
		Board Name:		Project Name:		
	Board2Pdf: TESTPOINTS_BOT - Pag	<b>5USB IO Extender</b>		<b>USB IO Extender</b>		
	Sheet Title:	File Name:	Designer:	Date:	Revision:	
Fabrication Document		IOExtender.kicad_pcb	Thiago Palmieri	2024-04-13		
Sheet Path:			Reviewer:	Size:	Sheet:	
	TESTPOINTS_BOT		Thiago Palmieri	A4	of $1$	
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