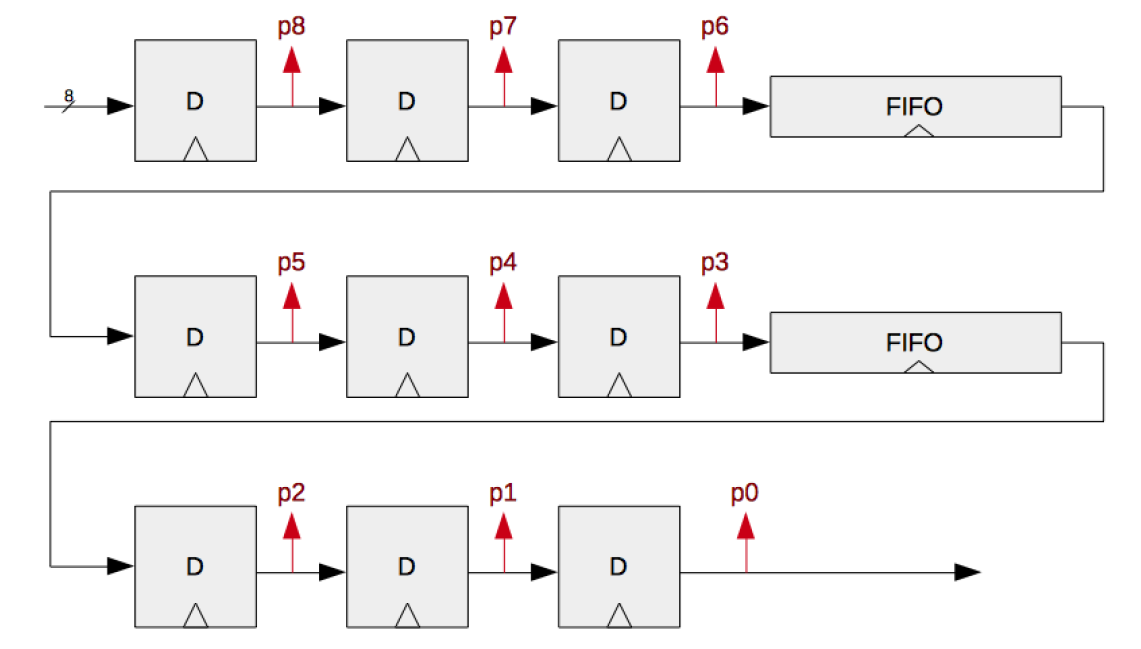
**Project : 2D filter implementation**

The goal is to process the input data flow (corresponding to lena image) using a 2D filter. Two main tasks are expected:

* The design and the validation of a customizable 2D filter (filter IP)
* The implementation on a Nexys4 evaluation board of the 2D filter. The filter IP implementation should be included in a reference design (furnished by teacher) to ease the integration.

The filter IP could be split into two main parts: the memory cache which aims to be temporarily stored the data flow before filtering and the processing part.

The cache memory designed for simultaneous pixel accesses enables a 3x3 pixel neighbourhood to be accessible in one clock cycle. The structure is based on flip-flop registers and First-In-First-Out (FIFO) memory. The structure is represented in the following:

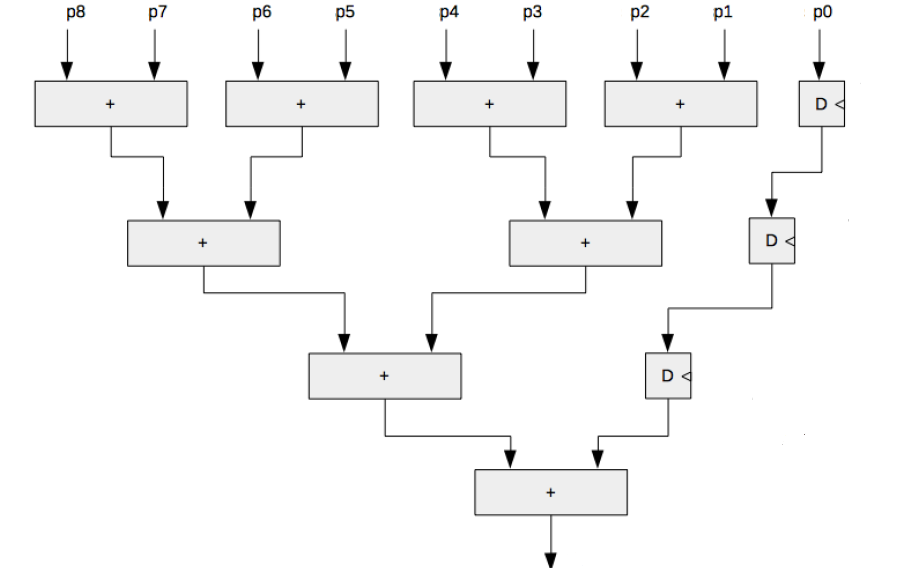


Camera output

Advices :

1. Develop and validate a generic flip-flop
2. Generate a FIFO memory using Xilinx COREGENERATOR (ref to teacher), validate
3. Turn the FIFO in delay line (the image spatial resolution is 128x128), validate
4. Associate the different components and design a control state machine to control the data flow synchronisation. Please note that the camera provides one pixel an each cycle

The processing part is in charge of filtering to be performed. The nine pixels extracted at each cycle in the memory cache are used in a pipelined architecture (to be designed). An example of a simple average filter based on pipelined structure is represented in the following figure:



A stage of multipliers has to be included to perform more complex filtering. The filter coefficients must be tuneable (configuration mode to be done using evaluation board switches). The student should proposed different filter:

1. Average (blur effect)using the following coefficients [1 1 1][1 0 1][1 1 1] and then [1 1 1][1 1 1][1 1 1]
2. A sobel filter (horizontal and vertical filter to be tested)
3. Gaussian filter

The coefficient dynamic and output normalization are going to be discussed with the teacher.