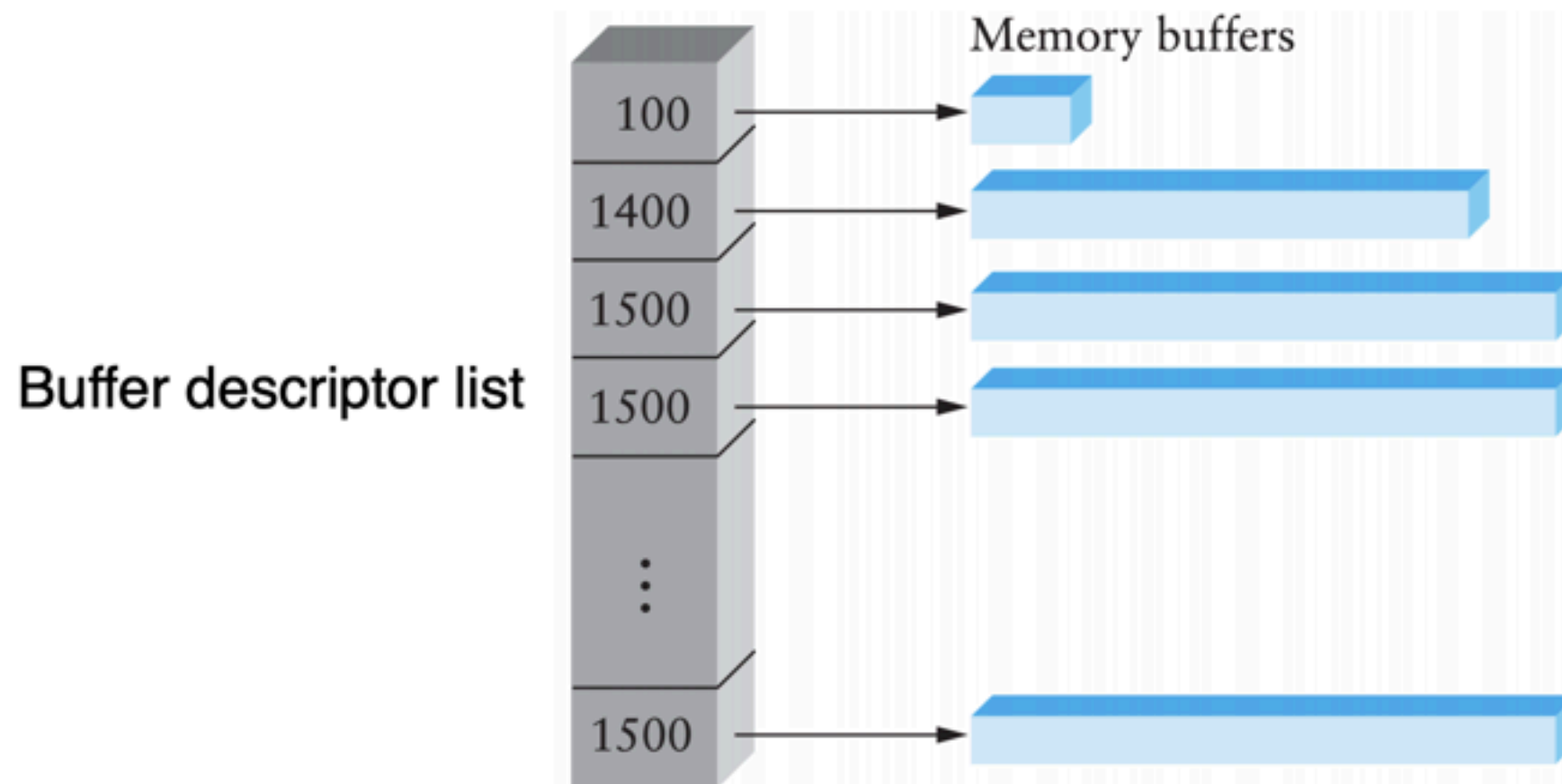


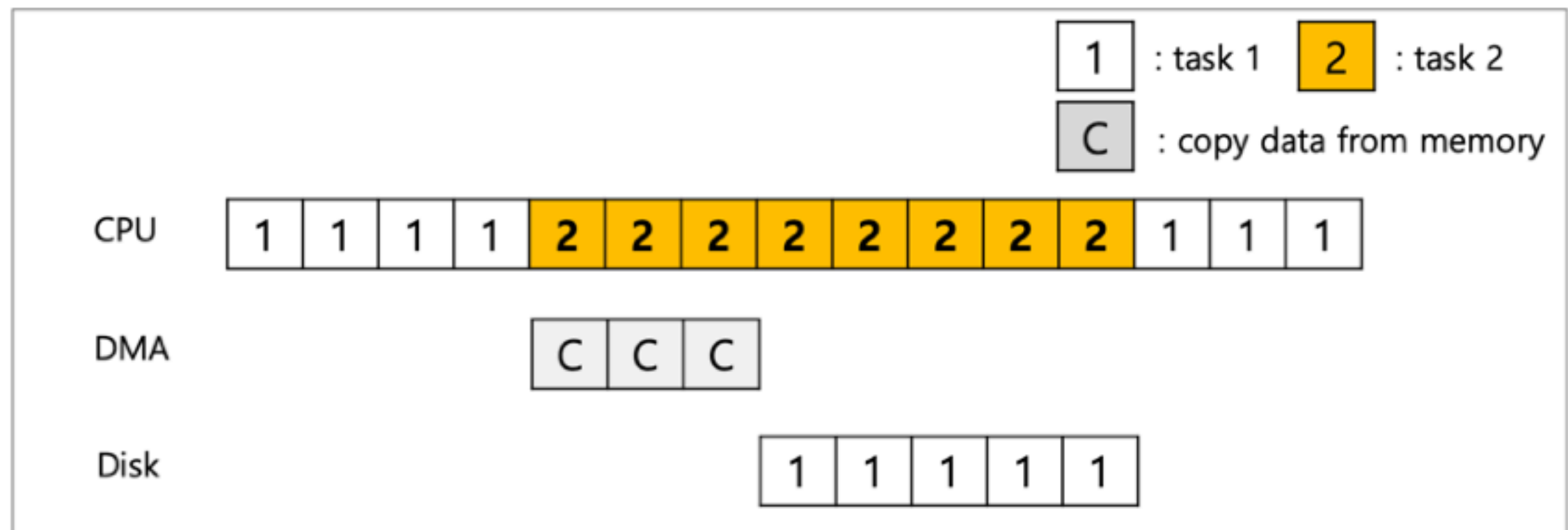
# DMA (Direct Memory Access)

- ➔ Only use CPU to transfer control requests, not data, by passing buffer locations in memory
  - Device reads list and accesses buffers through DMA
  - Descriptions sometimes allow for scatter/gather I/O



# DMA (Direct Memory Access)

1. OS writes DMA command block into memory
2. DMA bypasses CPU to transfer data directly between I/O device and memory
3. When completed, DMA raises an interrupt



**Diagram of CPU utilization by DMA**