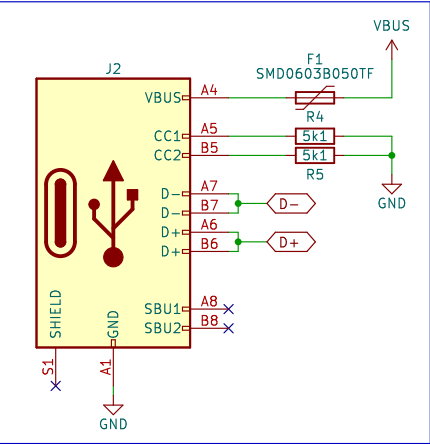
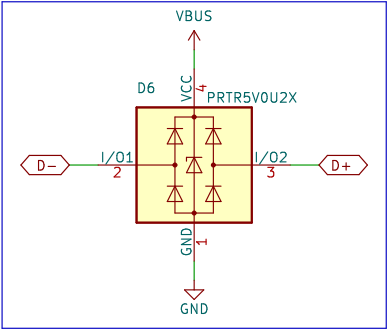


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USB-C

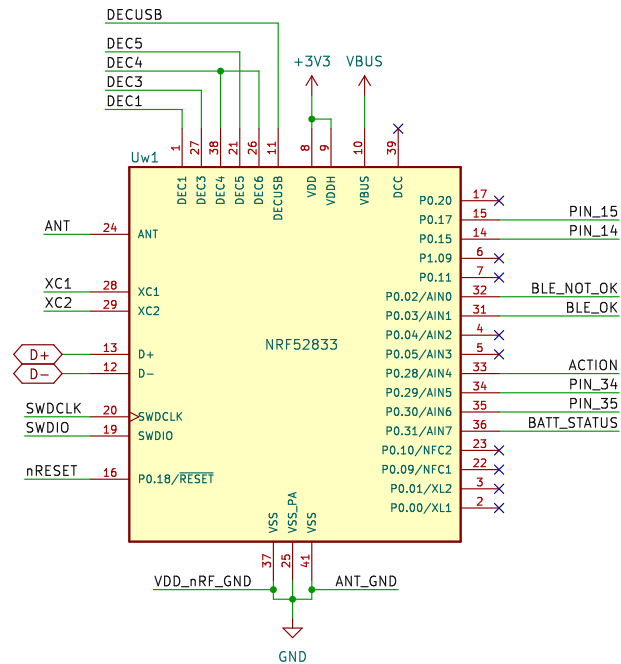


ESD



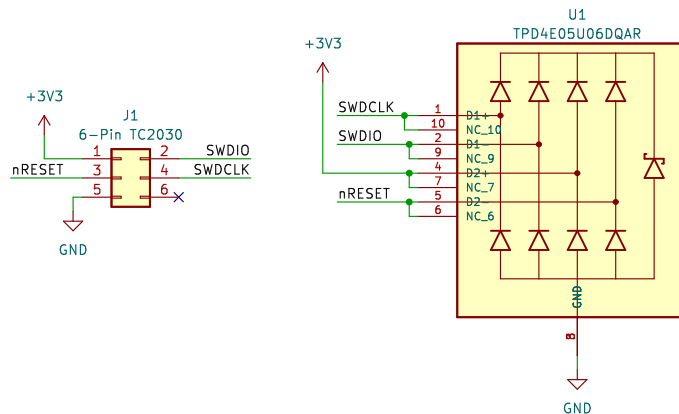
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MCU

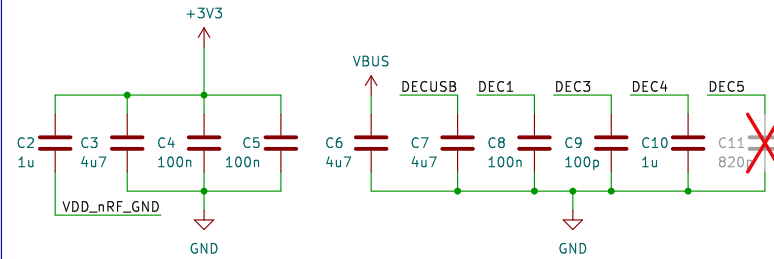


Schematic taken from "7.3.9 Circuit configuration no. 3 for QDAA QFN40" in datasheet V1.5

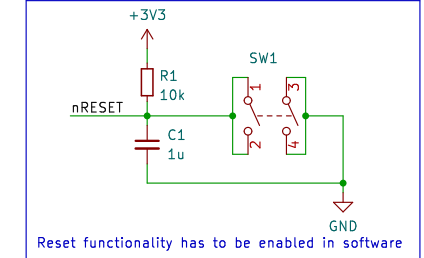
SWD + ESD



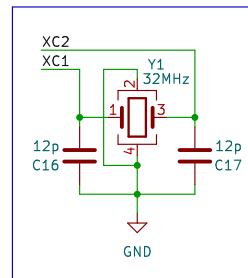
Decoupling



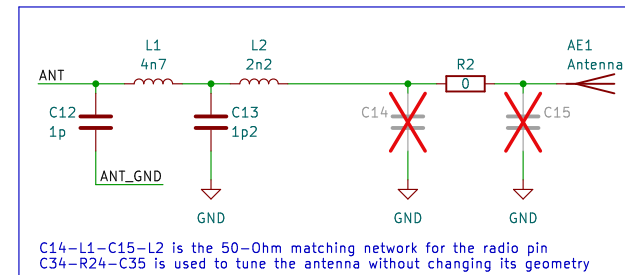
Reset button



Crystal



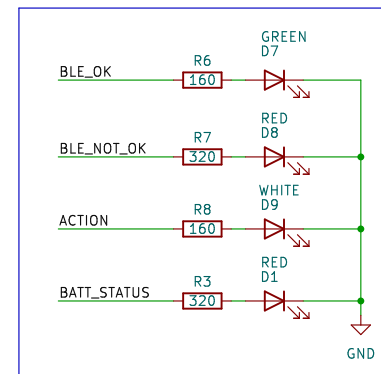
Antenna



Test Points

PIN_35	TP4
PIN_34	TP5
PIN_15	TP6
PIN_14	TP7

LEDs



Sheet: /MCU/
File: mcu.kicad_sch

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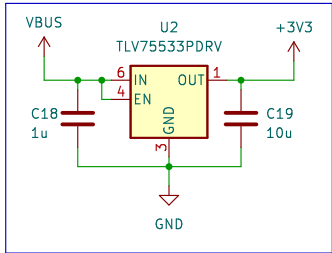
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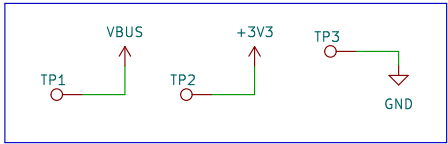
Rev:

Id: 3/4

LDO



Test Points



Sheet: /Power/ File: power.kicad_sch		
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Size: A4	Date:	Rev:
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