

# Production Line Task Scheduling

Stefano Nicolis  
A.A. 2021-2022

1 marzo 2022



Embedded and IoT Systems Design

# Indice

1	Introduction	2
2	Assumptions	3
3	Final remarks	4

# **1 Introduction**

The scope of this assignment is to implement the Verilog module to multiply two 3x3 matrices.

## 2 Assumptions

-The processingTime for an operation is assumed to be greater or equal to 1

-Although the input file has add-materials operations, there is no information in the Recipes.json file about how much material a certain operation uses, so even though the algorithm checks for the material availability, it's a futile check because it will always return a positive result

-The algorithm can decide which tools to use for each operation when more than one are available, this is not decided at each start of new operation, so theoretically one could change the requirement while the recipe is running, and optimize the first part of it for speed and the other for

-

### 3 Final remarks