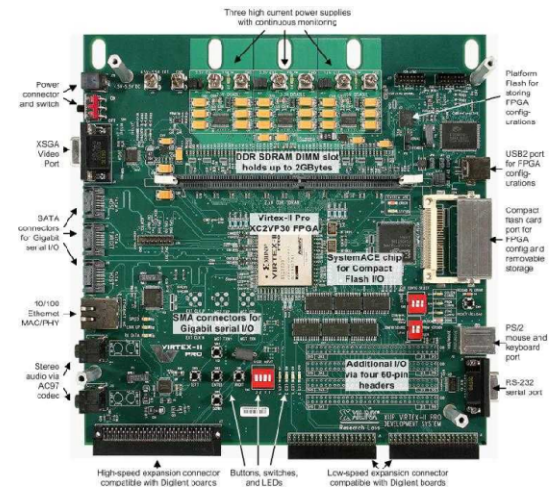
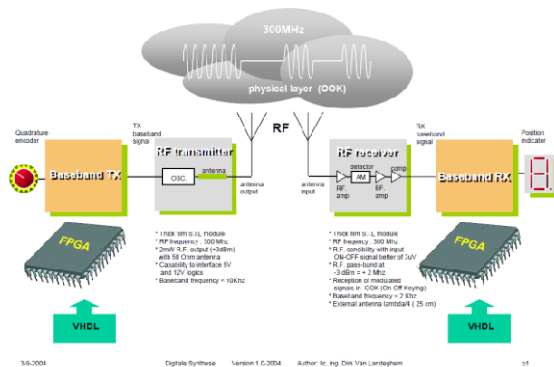


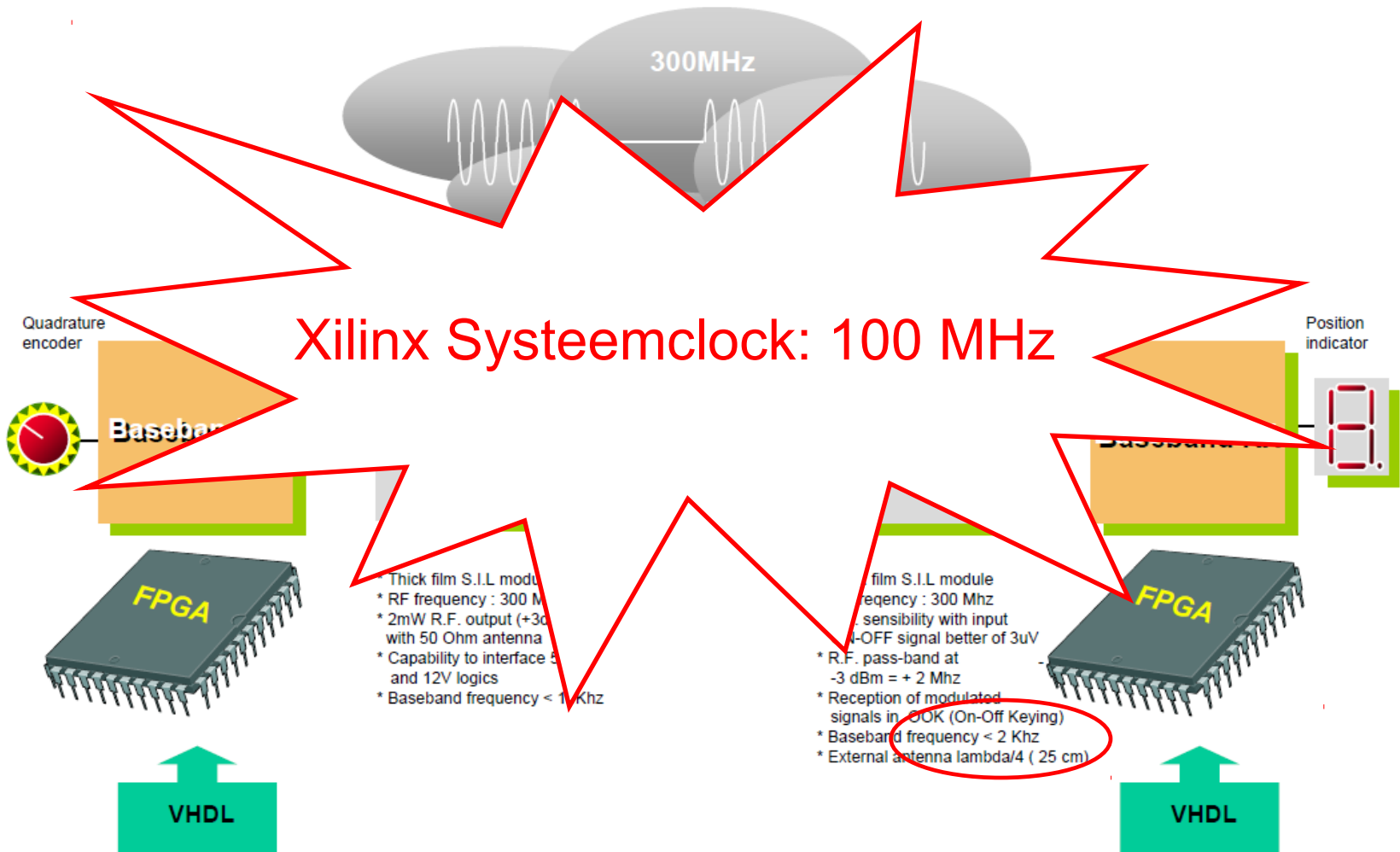
# DSSS Implementation

## Spread Spectrum Wireless Communication



# DSSS Implementation

## Spread Spectrum Wireless Communication



# DSSS Implementation

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- Chipsnelheid = clocksnelheid zender  
    ➔ Clocksnelheid zender = max 10 KHz
- XUP Systeemclock: 100 MHz ➔ Clockdivider nodig
- Factor  $100\text{MHz}/10\text{KHz} = 10000$
- $10000 \approx 16384 \rightarrow 2^{14}$

Gebruik 14-bit teller, genereer CE signaal (bv. if teller = 0)

➔  $100\text{MHz}/(2^{14}) = 6.104 \text{ KHz}$

Ontvanger draait 16 keer sneller ( $2^4 = 16$ ), genereer CE signaal (bv. if teller = 0)

➔ Gebruik 10-bit teller

# DSSS Implementation

