

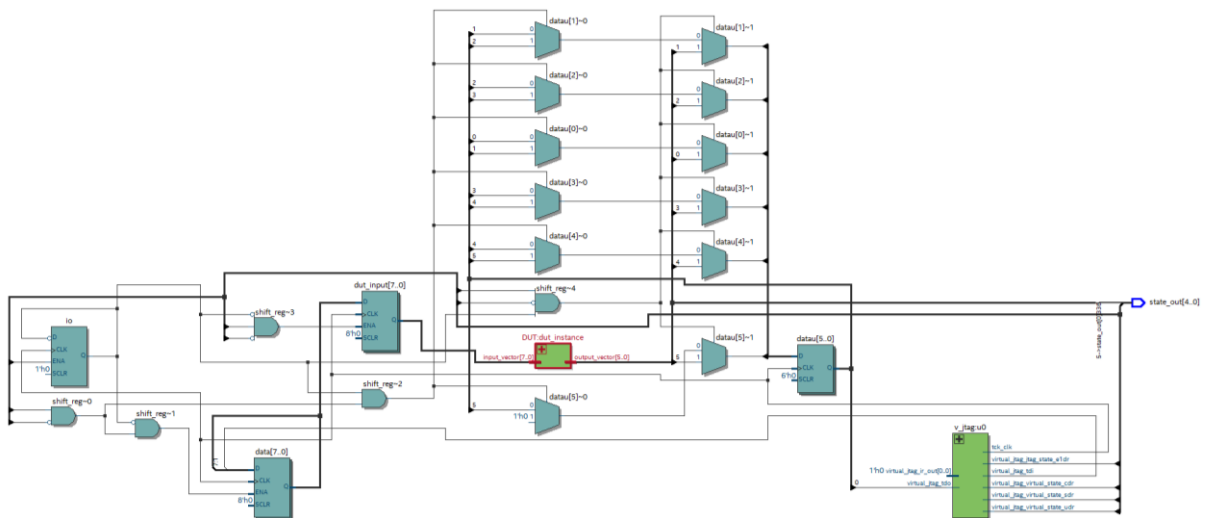
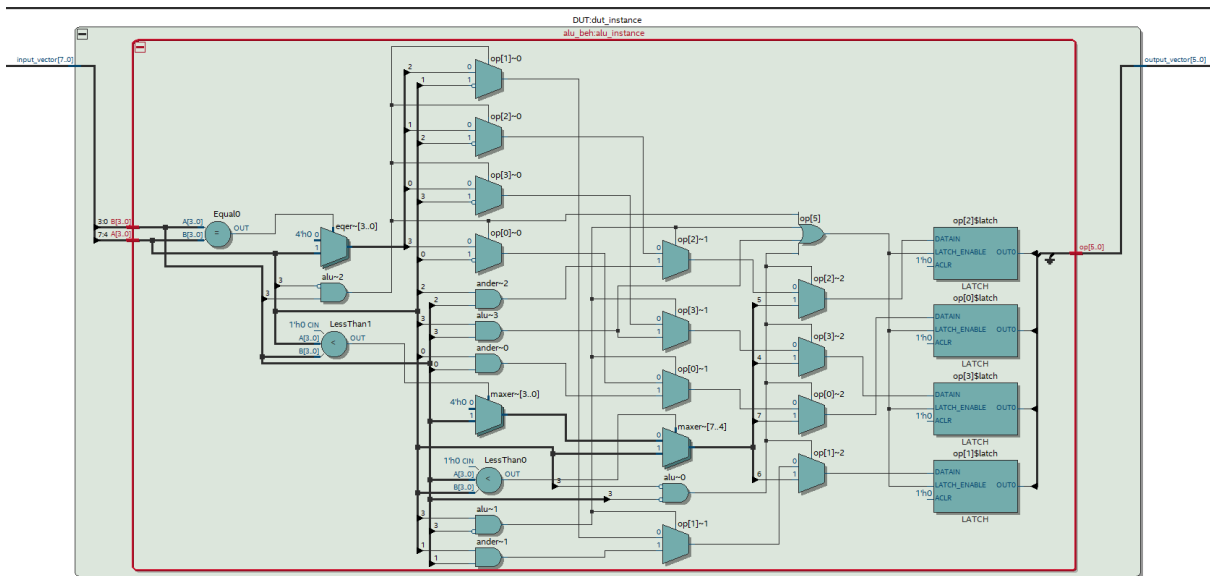
# EE214 Digital Circuits Laboratory

## EXP5 – Behavioral and Dataflow Modelling

Swarup Dasharath Patil

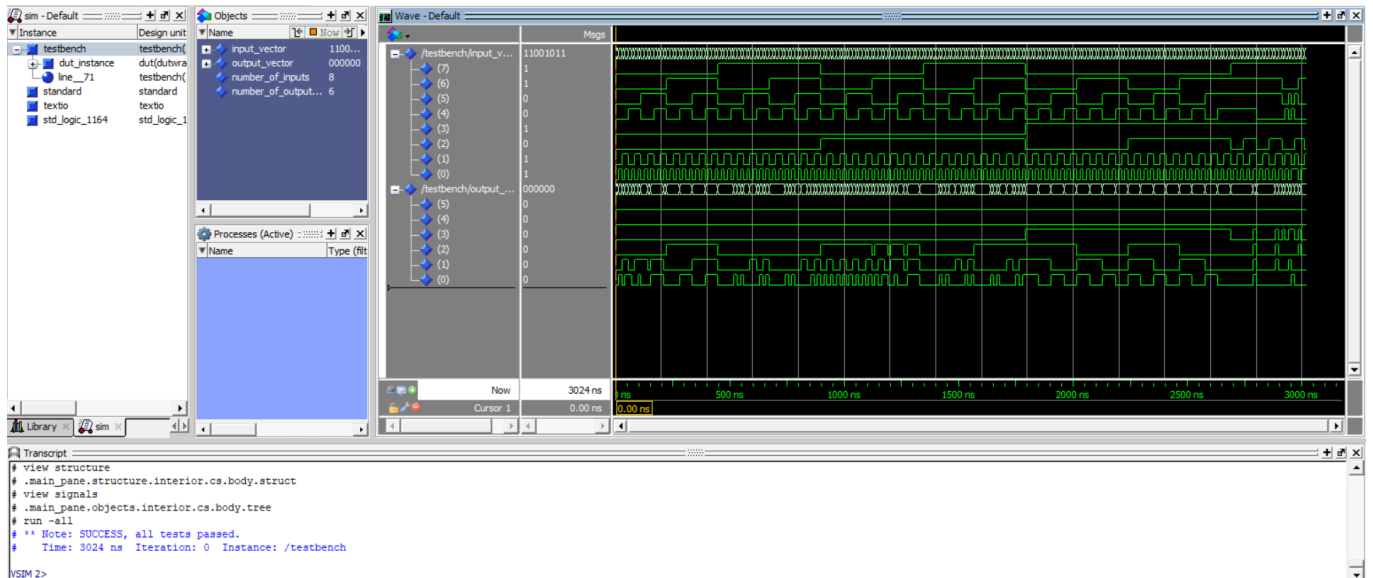
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RTL Viewer:



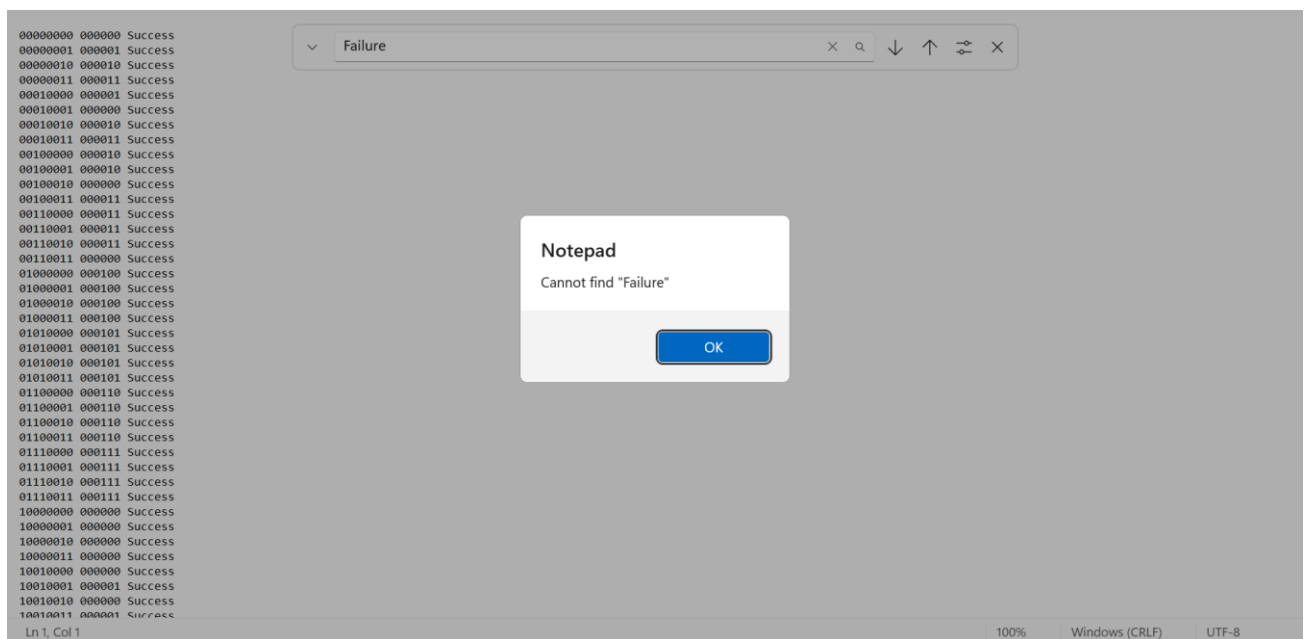
## RTL Simulation:

Note: SUCCESS, all tests passed.



## Scanchain based testing:

All tests were successful.



**Lab Journal:**

A rough pen-paper design of the circuit verified by the TA.

EXPT. NO.

NAME

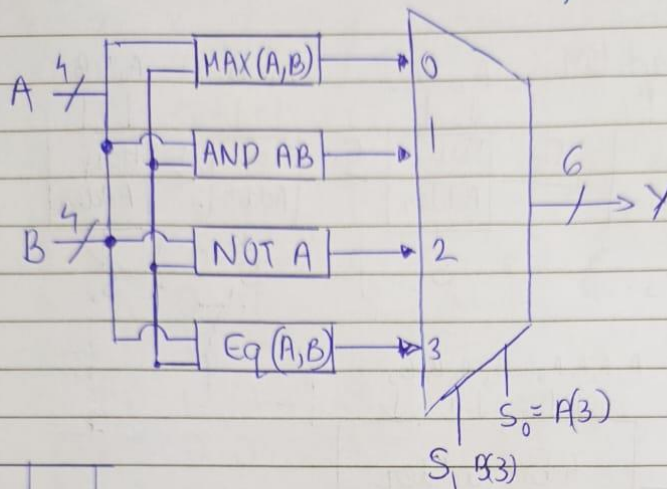
Week 5: ALU- Behavioral and Dataflow modelling

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Date 08/09/2023



B3	A3
S <sub>1</sub>	S <sub>0</sub>
0	0
0	1
1	0
1	1

ALU Output

MAX(A,B): This block outputs larger number between A,B else '0000'

AND(A,B): This block performs AND operation between A,B

NOT(A): This block performs NOT operation of A.

Eq(A,B): This block outputs the number whenever A=B else it should output '0000'

*August 2023*

Teacher's Signature: \_\_\_\_\_