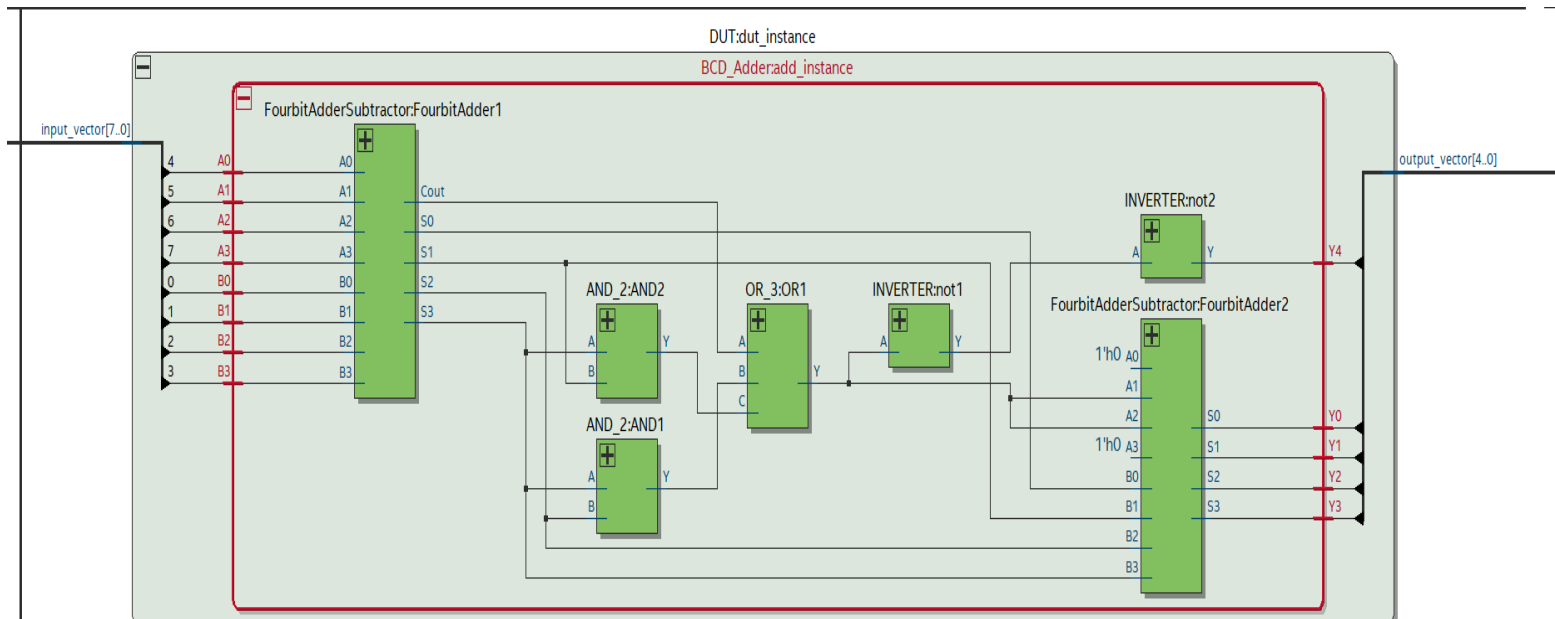
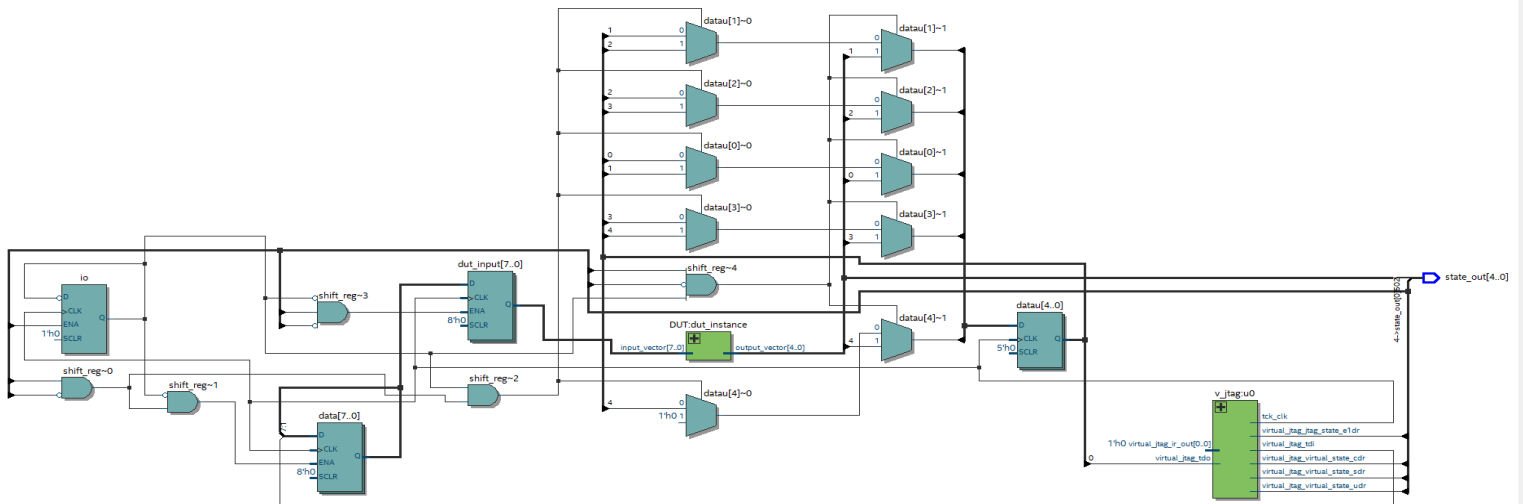


EE214 Digital Circuits Laboratory

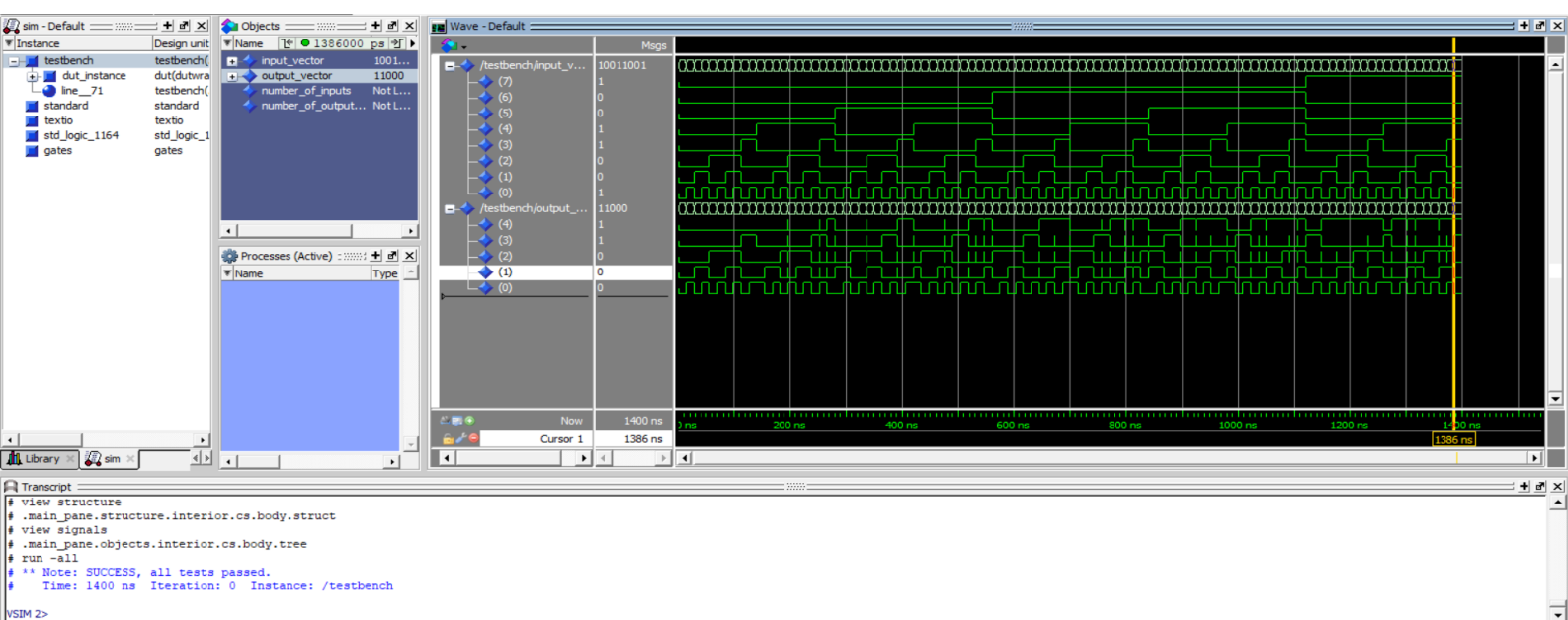
EXP4 – BCD Adder

RTL Viewer:



RTL Simulation:

Note: SUCCESS, all tests passed.



Scanchain based testing:

All tests were successful.

```
output1
File Edit View

00000000 00000 Success
00000001 00001 Success
00000010 00010 Success
00000011 00011 Success
00000100 00100 Success
00000101 00101 Success
00000110 00110 Success
00000111 00111 Success
00001000 01000 Success
00001001 01001 Success
00001010 01010 Success
00001011 01011 Success
00001100 01100 Success
00001101 01101 Success
00001110 01110 Success
00001111 01111 Success
00010000 00001 Success
00010001 00010 Success
00010010 00011 Success
00010011 00100 Success
00010100 00101 Success
00010101 00110 Success
00010110 00111 Success
00010111 01000 Success
00011000 01001 Success
00011001 10000 Success
00100000 00010 Success
00100001 00011 Success
00100010 00100 Success
00100011 00101 Success
00100100 00110 Success
00100101 00111 Success
00100110 01000 Success
00100111 01001 Success
00101000 10000 Success
00101001 10001 Success
00101000 00011 Success
00110001 00100 Success
00110010 00101 Success
00110011 00110 Success
00110100 00111 Success
00110101 01000 Success
00110110 01001 Success
00110111 10000 Success
00111000 10001 Success
00111001 10010 Success
Ln 1, Col 1
100% Windows (CRLF) UTF-8
```

Lab Journal:

A rough pen-paper design of the circuit verified by the TA.

EXPT. NO.	NAME <u>Week 4: BCD Adder</u>	M T W T F S S Page No.: Date: <u>01/09/2023</u>
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*** Four bit adder:**

*** BCD adder:-**

[Signature]

Teacher's Signature: _____