v7.40.0.0
(May 2nd, 2022)

IDesignSpec™ (IDS)

RTL Enhancements

1. B#1659 - Support for resetting a field with some other existing field/signal/parameter value through resetsignal property. More Details
2. F#16618 - Enhanced support of multi-cast/broadcast region in the RTL to allow defining multiple blocks sitting at the base address of the multicast/broadcast region as default access blocks. More Details
3. F#17197 - Support of "vertical_reuse" property with value **"vertical_reuse=verilog"** for verilog output. More Details
4. F#20170 - Support of unaligned memory access using the new property value, **"decode_unaligned=true".** More Details

General Enhancements

1. B#1726 - Support for generating additional outputs through +out switch in IDSBatch command line, i.e., +out uvm, +out xrsl, etc. More Details
2. B#1728 - Support for replacement of tabs and spaces in all the outputs by using switch -pprint "notab" in the command line More Details

UVM Enhancements
1. B#1727 - Support for creation of one package with all the callback classes using the top_property "uvm_opt=4" in the command line. More Details

Bug Fixes

1. F#19733 - Fix for the "ignore_prop=hdl_path_slice" property for external register and section.
2. F#20144 - Fix for presence of redundant signals inside event of 'always' block in case resetsignal property is used and reset type is defined both in field and in signal table for that reset signal.
3. F#19667 & F#20210 - Fix for "hierarchical_decode=true" property usage with the "aggregation_logic=flopped" and "aggregation_logic=flopped_all".

IDS NextGen™ (IDS-NG)

Enhancements

1. G#222 - Support for IPXACT2014 output in IDS-NG configuration window. [More Details](#)

Bug Fixes

1. F#16590 - Fix for the issue in the struct template "insert below" option upon right click.
2. G#132 - Fix for lock expression issue in register name.
3. G#334 - Fix for the issue of update param in case of import yaml file.
4. F#19525 - Fix for the "save as" feature.
5. G#99 - Fix in the update param return line issue.
6. G#344 - Fix for the issue of exception during update parameters in param views.

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Enhancements

1. B#1731 - Parameters and defines are now supported.
2. B#1734 - Support for "ext_error" to enable the user to provide an error from external source to the slave.

Bug fixes

3. B#1735 - Fixes in seq_class when sw=RO and hw=NA.
4. B#1736 - Fixes for iteration issue in case of external mem section.
5. B#1737 - Fixes for the sequence class when reset_level is high at register level.
6. B#1738 - Fixes for external components with repeat.

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Bug fixes

1. B#1644 - Fix for the issue when two values concatenate lvar variables are not generated.