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| 1.1.1.361 | [Distributor\_\_37\_GICD\_IROUTER59\_lower](#bookmark364) | 0x00000000 | 0x61D8 |  |
| 1.1.1.362 | [Distributor\_\_37\_GICD\_IROUTER59\_upper](#bookmark365) | 0x00000000 | 0x61DC |  |
| 1.1.1.363 | [Distributor\_\_37\_GICD\_IROUTER60\_lower](#bookmark366) | 0x00000000 | 0x61E0 |  |
| 1.1.1.364 | [Distributor\_\_37\_GICD\_IROUTER60\_upper](#bookmark367) | 0x00000000 | 0x61E4 |  |
| 1.1.1.365 | [Distributor\_\_37\_GICD\_IROUTER61\_lower](#bookmark368) | 0x00000000 | 0x61E8 |  |
| 1.1.1.366 | [Distributor\_\_37\_GICD\_IROUTER61\_upper](#bookmark369) | 0x00000000 | 0x61EC |  |
| 1.1.1.367 | [Distributor\_\_37\_GICD\_IROUTER62\_lower](#bookmark370) | 0x00000000 | 0x61F0 |  |
| 1.1.1.368 | [Distributor\_\_37\_GICD\_IROUTER62\_upper](#bookmark371) | 0x00000000 | 0x61F4 |  |
| 1.1.1.369 | [Distributor\_\_37\_GICD\_IROUTER63\_lower](#bookmark372) | 0x00000000 | 0x61F8 |  |
| 1.1.1.370 | [Distributor\_\_37\_GICD\_IROUTER63\_upper](#bookmark373) | 0x00000000 | 0x61FC |  |
| 1.1.1.371 | [Distributor\_\_37\_GICD\_IROUTER64\_lower](#bookmark374) | 0x00000000 | 0x6200 |  |
| 1.1.1.372 | [Distributor\_\_37\_GICD\_IROUTER64\_upper](#bookmark375) | 0x00000000 | 0x6204 |  |
| 1.1.1.373 | [Distributor\_\_37\_GICD\_IROUTER65\_lower](#bookmark376) | 0x00000000 | 0x6208 |  |
| 1.1.1.374 | [Distributor\_\_37\_GICD\_IROUTER65\_upper](#bookmark377) | 0x00000000 | 0x620C |  |
| 1.1.1.375 | [Distributor\_\_37\_GICD\_IROUTER66\_lower](#bookmark378) | 0x00000000 | 0x6210 |  |
| 1.1.1.376 | [Distributor\_\_37\_GICD\_IROUTER66\_upper](#bookmark379) | 0x00000000 | 0x6214 |  |
| 1.1.1.377 | [Distributor\_\_37\_GICD\_IROUTER67\_lower](#bookmark380) | 0x00000000 | 0x6218 |  |
| 1.1.1.378 | [Distributor\_\_37\_GICD\_IROUTER67\_upper](#bookmark381) | 0x00000000 | 0x621C |  |
| 1.1.1.379 | [Distributor\_\_37\_GICD\_IROUTER68\_lower](#bookmark382) | 0x00000000 | 0x6220 |  |
| 1.1.1.380 | [Distributor\_\_37\_GICD\_IROUTER68\_upper](#bookmark383) | 0x00000000 | 0x6224 |  |
| 1.1.1.381 | [Distributor\_\_37\_GICD\_IROUTER69\_lower](#bookmark384) | 0x00000000 | 0x6228 |  |
| 1.1.1.382 | [Distributor\_\_37\_GICD\_IROUTER69\_upper](#bookmark385) | 0x00000000 | 0x622C |  |
| 1.1.1.383 | [Distributor\_\_37\_GICD\_IROUTER70\_lower](#bookmark386) | 0x00000000 | 0x6230 |  |
| 1.1.1.384 | [Distributor\_\_37\_GICD\_IROUTER70\_upper](#bookmark387) | 0x00000000 | 0x6234 |  |
| 1.1.1.385 | [Distributor\_\_37\_GICD\_IROUTER71\_lower](#bookmark388) | 0x00000000 | 0x6238 |  |
| 1.1.1.386 | [Distributor\_\_37\_GICD\_IROUTER71\_upper](#bookmark389) | 0x00000000 | 0x623C |  |
| 1.1.1.387 | [Distributor\_\_37\_GICD\_IROUTER72\_lower](#bookmark390) | 0x00000000 | 0x6240 |  |
| 1.1.1.388 | [Distributor\_\_37\_GICD\_IROUTER72\_upper](#bookmark391) | 0x00000000 | 0x6244 |  |
| 1.1.1.389 | [Distributor\_\_37\_GICD\_IROUTER73\_lower](#bookmark392) | 0x00000000 | 0x6248 |  |
| 1.1.1.390 | [Distributor\_\_37\_GICD\_IROUTER73\_upper](#bookmark393) | 0x00000000 | 0x624C |  |
| 1.1.1.391 | [Distributor\_\_37\_GICD\_IROUTER74\_lower](#bookmark394) | 0x00000000 | 0x6250 |  |
| 1.1.1.392 | [Distributor\_\_37\_GICD\_IROUTER74\_upper](#bookmark395) | 0x00000000 | 0x6254 |  |
| 1.1.1.393 | [Distributor\_\_37\_GICD\_IROUTER75\_lower](#bookmark396) | 0x00000000 | 0x6258 |  |
| 1.1.1.394 | [Distributor\_\_37\_GICD\_IROUTER75\_upper](#bookmark397) | 0x00000000 | 0x625C |  |
| 1.1.1.395 | [Distributor\_\_37\_GICD\_IROUTER76\_lower](#bookmark398) | 0x00000000 | 0x6260 |  |
| 1.1.1.396 | [Distributor\_\_37\_GICD\_IROUTER76\_upper](#bookmark399) | 0x00000000 | 0x6264 |  |
| 1.1.1.397 | [Distributor\_\_37\_GICD\_IROUTER77\_lower](#bookmark400) | 0x00000000 | 0x6268 |  |
| 1.1.1.398 | [Distributor\_\_37\_GICD\_IROUTER77\_upper](#bookmark401) | 0x00000000 | 0x626C |  |
| 1.1.1.399 | [Distributor\_\_37\_GICD\_IROUTER78\_lower](#bookmark402) | 0x00000000 | 0x6270 |  |
| 1.1.1.400 | [Distributor\_\_37\_GICD\_IROUTER78\_upper](#bookmark403) | 0x00000000 | 0x6274 |  |
| 1.1.1.401 | [Distributor\_\_37\_GICD\_IROUTER79\_lower](#bookmark404) | 0x00000000 | 0x6278 |  |
| 1.1.1.402 | [Distributor\_\_37\_GICD\_IROUTER79\_upper](#bookmark405) | 0x00000000 | 0x627C |  |
| 1.1.1.403 | [Distributor\_\_37\_GICD\_IROUTER80\_lower](#bookmark406) | 0x00000000 | 0x6280 |  |
| 1.1.1.404 | [Distributor\_\_37\_GICD\_IROUTER80\_upper](#bookmark407) | 0x00000000 | 0x6284 |  |
| 1.1.1.405 | [Distributor\_\_37\_GICD\_IROUTER81\_lower](#bookmark408) | 0x00000000 | 0x6288 |  |
| 1.1.1.406 | [Distributor\_\_37\_GICD\_IROUTER81\_upper](#bookmark409) | 0x00000000 | 0x628C |  |
| 1.1.1.407 | [Distributor\_\_37\_GICD\_IROUTER82\_lower](#bookmark410) | 0x00000000 | 0x6290 |  |
| 1.1.1.408 | [Distributor\_\_37\_GICD\_IROUTER82\_upper](#bookmark411) | 0x00000000 | 0x6294 |  |
| 1.1.1.409 | [Distributor\_\_37\_GICD\_IROUTER83\_lower](#bookmark412) | 0x00000000 | 0x6298 |  |
| 1.1.1.410 | [Distributor\_\_37\_GICD\_IROUTER83\_upper](#bookmark413) | 0x00000000 | 0x629C |  |
| 1.1.1.411 | [Distributor\_\_37\_GICD\_IROUTER84\_lower](#bookmark414) | 0x00000000 | 0x62A0 |  |
| 1.1.1.412 | [Distributor\_\_37\_GICD\_IROUTER84\_upper](#bookmark415) | 0x00000000 | 0x62A4 |  |
| 1.1.1.413 | [Distributor\_\_37\_GICD\_IROUTER85\_lower](#bookmark416) | 0x00000000 | 0x62A8 |  |
| 1.1.1.414 | [Distributor\_\_37\_GICD\_IROUTER85\_upper](#bookmark417) | 0x00000000 | 0x62AC |  |
| 1.1.1.415 | [Distributor\_\_37\_GICD\_IROUTER86\_lower](#bookmark418) | 0x00000000 | 0x62B0 |  |
| 1.1.1.416 | [Distributor\_\_37\_GICD\_IROUTER86\_upper](#bookmark419) | 0x00000000 | 0x62B4 |  |
| 1.1.1.417 | [Distributor\_\_37\_GICD\_IROUTER87\_lower](#bookmark420) | 0x00000000 | 0x62B8 |  |
| 1.1.1.418 | [Distributor\_\_37\_GICD\_IROUTER87\_upper](#bookmark421) | 0x00000000 | 0x62BC |  |
| 1.1.1.419 | [Distributor\_\_37\_GICD\_IROUTER88\_lower](#bookmark422) | 0x00000000 | 0x62C0 |  |
| 1.1.1.420 | [Distributor\_\_37\_GICD\_IROUTER88\_upper](#bookmark423) | 0x00000000 | 0x62C4 |  |
| 1.1.1.421 | [Distributor\_\_37\_GICD\_IROUTER89\_lower](#bookmark424) | 0x00000000 | 0x62C8 |  |
| 1.1.1.422 | [Distributor\_\_37\_GICD\_IROUTER89\_upper](#bookmark425) | 0x00000000 | 0x62CC |  |
| 1.1.1.423 | [Distributor\_\_37\_GICD\_IROUTER90\_lower](#bookmark426) | 0x00000000 | 0x62D0 |  |
| 1.1.1.424 | [Distributor\_\_37\_GICD\_IROUTER90\_upper](#bookmark427) | 0x00000000 | 0x62D4 |  |
| 1.1.1.425 | [Distributor\_\_37\_GICD\_IROUTER91\_lower](#bookmark428) | 0x00000000 | 0x62D8 |  |
| 1.1.1.426 | [Distributor\_\_37\_GICD\_IROUTER91\_upper](#bookmark429) | 0x00000000 | 0x62DC |  |
| 1.1.1.427 | [Distributor\_\_37\_GICD\_IROUTER92\_lower](#bookmark430) | 0x00000000 | 0x62E0 |  |
| 1.1.1.428 | [Distributor\_\_37\_GICD\_IROUTER92\_upper](#bookmark431) | 0x00000000 | 0x62E4 |  |
| 1.1.1.429 | [Distributor\_\_37\_GICD\_IROUTER93\_lower](#bookmark432) | 0x00000000 | 0x62E8 |  |
| 1.1.1.430 | [Distributor\_\_37\_GICD\_IROUTER93\_upper](#bookmark433) | 0x00000000 | 0x62EC |  |
| 1.1.1.431 | [Distributor\_\_37\_GICD\_IROUTER94\_lower](#bookmark434) | 0x00000000 | 0x62F0 |  |
| 1.1.1.432 | [Distributor\_\_37\_GICD\_IROUTER94\_upper](#bookmark435) | 0x00000000 | 0x62F4 |  |
| 1.1.1.433 | [Distributor\_\_37\_GICD\_IROUTER95\_lower](#bookmark436) | 0x00000000 | 0x62F8 |  |
| 1.1.1.434 | [Distributor\_\_37\_GICD\_IROUTER95\_upper](#bookmark437) | 0x00000000 | 0x62FC |  |
| 1.1.1.435 | [Distributor\_\_37\_GICD\_IROUTER96\_lower](#bookmark438) | 0x00000000 | 0x6300 |  |
| 1.1.1.436 | [Distributor\_\_37\_GICD\_IROUTER96\_upper](#bookmark439) | 0x00000000 | 0x6304 |  |
| 1.1.1.437 | [Distributor\_\_37\_GICD\_IROUTER97\_lower](#bookmark440) | 0x00000000 | 0x6308 |  |
| 1.1.1.438 | [Distributor\_\_37\_GICD\_IROUTER97\_upper](#bookmark441) | 0x00000000 | 0x630C |  |
| 1.1.1.439 | [Distributor\_\_37\_GICD\_IROUTER98\_lower](#bookmark442) | 0x00000000 | 0x6310 |  |
| 1.1.1.440 | [Distributor\_\_37\_GICD\_IROUTER98\_upper](#bookmark443) | 0x00000000 | 0x6314 |  |
| 1.1.1.441 | [Distributor\_\_37\_GICD\_IROUTER99\_lower](#bookmark444) | 0x00000000 | 0x6318 |  |
| 1.1.1.442 | [Distributor\_\_37\_GICD\_IROUTER99\_upper](#bookmark445) | 0x00000000 | 0x631C |  |
| 1.1.1.443 | [Distributor\_\_37\_GICD\_IROUTER100\_lower](#bookmark446) | 0x00000000 | 0x6320 |  |
| 1.1.1.444 | [Distributor\_\_37\_GICD\_IROUTER100\_upper](#bookmark447) | 0x00000000 | 0x6324 |  |
| 1.1.1.445 | [Distributor\_\_37\_GICD\_IROUTER101\_lower](#bookmark448) | 0x00000000 | 0x6328 |  |
| 1.1.1.446 | [Distributor\_\_37\_GICD\_IROUTER101\_upper](#bookmark449) | 0x00000000 | 0x632C |  |
| 1.1.1.447 | [Distributor\_\_37\_GICD\_IROUTER102\_lower](#bookmark450) | 0x00000000 | 0x6330 |  |
| 1.1.1.448 | [Distributor\_\_37\_GICD\_IROUTER102\_upper](#bookmark451) | 0x00000000 | 0x6334 |  |
| 1.1.1.449 | [Distributor\_\_37\_GICD\_IROUTER103\_lower](#bookmark452) | 0x00000000 | 0x6338 |  |
| 1.1.1.450 | [Distributor\_\_37\_GICD\_IROUTER103\_upper](#bookmark453) | 0x00000000 | 0x633C |  |
| 1.1.1.451 | [Distributor\_\_37\_GICD\_IROUTER104\_lower](#bookmark454) | 0x00000000 | 0x6340 |  |
| 1.1.1.452 | [Distributor\_\_37\_GICD\_IROUTER104\_upper](#bookmark455) | 0x00000000 | 0x6344 |  |
| 1.1.1.453 | [Distributor\_\_37\_GICD\_IROUTER105\_lower](#bookmark456) | 0x00000000 | 0x6348 |  |
| 1.1.1.454 | [Distributor\_\_37\_GICD\_IROUTER105\_upper](#bookmark457) | 0x00000000 | 0x634C |  |
| 1.1.1.455 | [Distributor\_\_37\_GICD\_IROUTER106\_lower](#bookmark458) | 0x00000000 | 0x6350 |  |
| 1.1.1.456 | [Distributor\_\_37\_GICD\_IROUTER106\_upper](#bookmark459) | 0x00000000 | 0x6354 |  |
| 1.1.1.457 | [Distributor\_\_37\_GICD\_IROUTER107\_lower](#bookmark460) | 0x00000000 | 0x6358 |  |
| 1.1.1.458 | [Distributor\_\_37\_GICD\_IROUTER107\_upper](#bookmark461) | 0x00000000 | 0x635C |  |
| 1.1.1.459 | [Distributor\_\_37\_GICD\_IROUTER108\_lower](#bookmark462) | 0x00000000 | 0x6360 |  |
| 1.1.1.460 | [Distributor\_\_37\_GICD\_IROUTER108\_upper](#bookmark463) | 0x00000000 | 0x6364 |  |
| 1.1.1.461 | [Distributor\_\_37\_GICD\_IROUTER109\_lower](#bookmark464) | 0x00000000 | 0x6368 |  |
| 1.1.1.462 | [Distributor\_\_37\_GICD\_IROUTER109\_upper](#bookmark465) | 0x00000000 | 0x636C |  |
| 1.1.1.463 | [Distributor\_\_37\_GICD\_IROUTER110\_lower](#bookmark466) | 0x00000000 | 0x6370 |  |
| 1.1.1.464 | [Distributor\_\_37\_GICD\_IROUTER110\_upper](#bookmark467) | 0x00000000 | 0x6374 |  |
| 1.1.1.465 | [Distributor\_\_37\_GICD\_IROUTER111\_lower](#bookmark468) | 0x00000000 | 0x6378 |  |
| 1.1.1.466 | [Distributor\_\_37\_GICD\_IROUTER111\_upper](#bookmark469) | 0x00000000 | 0x637C |  |
| 1.1.1.467 | [Distributor\_\_37\_GICD\_IROUTER112\_lower](#bookmark470) | 0x00000000 | 0x6380 |  |
| 1.1.1.468 | [Distributor\_\_37\_GICD\_IROUTER112\_upper](#bookmark471) | 0x00000000 | 0x6384 |  |
| 1.1.1.469 | [Distributor\_\_37\_GICD\_IROUTER113\_lower](#bookmark472) | 0x00000000 | 0x6388 |  |
| 1.1.1.470 | [Distributor\_\_37\_GICD\_IROUTER113\_upper](#bookmark473) | 0x00000000 | 0x638C |  |
| 1.1.1.471 | [Distributor\_\_37\_GICD\_IROUTER114\_lower](#bookmark474) | 0x00000000 | 0x6390 |  |
| 1.1.1.472 | [Distributor\_\_37\_GICD\_IROUTER114\_upper](#bookmark475) | 0x00000000 | 0x6394 |  |
| 1.1.1.473 | [Distributor\_\_37\_GICD\_IROUTER115\_lower](#bookmark476) | 0x00000000 | 0x6398 |  |
| 1.1.1.474 | [Distributor\_\_37\_GICD\_IROUTER115\_upper](#bookmark477) | 0x00000000 | 0x639C |  |
| 1.1.1.475 | [Distributor\_\_37\_GICD\_IROUTER116\_lower](#bookmark478) | 0x00000000 | 0x63A0 |  |
| 1.1.1.476 | [Distributor\_\_37\_GICD\_IROUTER116\_upper](#bookmark479) | 0x00000000 | 0x63A4 |  |
| 1.1.1.477 | [Distributor\_\_37\_GICD\_IROUTER117\_lower](#bookmark480) | 0x00000000 | 0x63A8 |  |
| 1.1.1.478 | [Distributor\_\_37\_GICD\_IROUTER117\_upper](#bookmark481) | 0x00000000 | 0x63AC |  |
| 1.1.1.479 | [Distributor\_\_37\_GICD\_IROUTER118\_lower](#bookmark482) | 0x00000000 | 0x63B0 |  |
| 1.1.1.480 | [Distributor\_\_37\_GICD\_IROUTER118\_upper](#bookmark483) | 0x00000000 | 0x63B4 |  |
| 1.1.1.481 | [Distributor\_\_37\_GICD\_IROUTER119\_lower](#bookmark484) | 0x00000000 | 0x63B8 |  |
| 1.1.1.482 | [Distributor\_\_37\_GICD\_IROUTER119\_upper](#bookmark485) | 0x00000000 | 0x63BC |  |
| 1.1.1.483 | [Distributor\_\_37\_GICD\_IROUTER120\_lower](#bookmark486) | 0x00000000 | 0x63C0 |  |
| 1.1.1.484 | [Distributor\_\_37\_GICD\_IROUTER120\_upper](#bookmark487) | 0x00000000 | 0x63C4 |  |
| 1.1.1.485 | [Distributor\_\_37\_GICD\_IROUTER121\_lower](#bookmark488) | 0x00000000 | 0x63C8 |  |
| 1.1.1.486 | [Distributor\_\_37\_GICD\_IROUTER121\_upper](#bookmark489) | 0x00000000 | 0x63CC |  |
| 1.1.1.487 | [Distributor\_\_37\_GICD\_IROUTER122\_lower](#bookmark490) | 0x00000000 | 0x63D0 |  |
| 1.1.1.488 | [Distributor\_\_37\_GICD\_IROUTER122\_upper](#bookmark491) | 0x00000000 | 0x63D4 |  |
| 1.1.1.489 | [Distributor\_\_37\_GICD\_IROUTER123\_lower](#bookmark492) | 0x00000000 | 0x63D8 |  |
| 1.1.1.490 | [Distributor\_\_37\_GICD\_IROUTER123\_upper](#bookmark493) | 0x00000000 | 0x63DC |  |
| 1.1.1.491 | [Distributor\_\_37\_GICD\_IROUTER124\_lower](#bookmark494) | 0x00000000 | 0x63E0 |  |
| 1.1.1.492 | [Distributor\_\_37\_GICD\_IROUTER124\_upper](#bookmark495) | 0x00000000 | 0x63E4 |  |
| 1.1.1.493 | [Distributor\_\_37\_GICD\_IROUTER125\_lower](#bookmark496) | 0x00000000 | 0x63E8 |  |
| 1.1.1.494 | [Distributor\_\_37\_GICD\_IROUTER125\_upper](#bookmark497) | 0x00000000 | 0x63EC |  |
| 1.1.1.495 | [Distributor\_\_37\_GICD\_IROUTER126\_lower](#bookmark498) | 0x00000000 | 0x63F0 |  |
| 1.1.1.496 | [Distributor\_\_37\_GICD\_IROUTER126\_upper](#bookmark499) | 0x00000000 | 0x63F4 |  |
| 1.1.1.497 | [Distributor\_\_37\_GICD\_IROUTER127\_lower](#bookmark500) | 0x00000000 | 0x63F8 |  |
| 1.1.1.498 | [Distributor\_\_37\_GICD\_IROUTER127\_upper](#bookmark501) | 0x00000000 | 0x63FC |  |
| 1.1.1.499 | [Distributor\_\_37\_GICD\_IROUTER128\_lower](#bookmark502) | 0x00000000 | 0x6400 |  |
| 1.1.1.500 | [Distributor\_\_37\_GICD\_IROUTER128\_upper](#bookmark503) | 0x00000000 | 0x6404 |  |
| 1.1.1.501 | [Distributor\_\_37\_GICD\_IROUTER129\_lower](#bookmark504) | 0x00000000 | 0x6408 |  |
| 1.1.1.502 | [Distributor\_\_37\_GICD\_IROUTER129\_upper](#bookmark505) | 0x00000000 | 0x640C |  |
| 1.1.1.503 | [Distributor\_\_37\_GICD\_IROUTER130\_lower](#bookmark506) | 0x00000000 | 0x6410 |  |
| 1.1.1.504 | [Distributor\_\_37\_GICD\_IROUTER130\_upper](#bookmark507) | 0x00000000 | 0x6414 |  |
| 1.1.1.505 | [Distributor\_\_37\_GICD\_IROUTER131\_lower](#bookmark508) | 0x00000000 | 0x6418 |  |
| 1.1.1.506 | [Distributor\_\_37\_GICD\_IROUTER131\_upper](#bookmark509) | 0x00000000 | 0x641C |  |
| 1.1.1.507 | [Distributor\_\_37\_GICD\_IROUTER132\_lower](#bookmark510) | 0x00000000 | 0x6420 |  |
| 1.1.1.508 | [Distributor\_\_37\_GICD\_IROUTER132\_upper](#bookmark511) | 0x00000000 | 0x6424 |  |
| 1.1.1.509 | [Distributor\_\_37\_GICD\_IROUTER133\_lower](#bookmark512) | 0x00000000 | 0x6428 |  |
| 1.1.1.510 | [Distributor\_\_37\_GICD\_IROUTER133\_upper](#bookmark513) | 0x00000000 | 0x642C |  |
| 1.1.1.511 | [Distributor\_\_37\_GICD\_IROUTER134\_lower](#bookmark514) | 0x00000000 | 0x6430 |  |
| 1.1.1.512 | [Distributor\_\_37\_GICD\_IROUTER134\_upper](#bookmark515) | 0x00000000 | 0x6434 |  |
| 1.1.1.513 | [Distributor\_\_37\_GICD\_IROUTER135\_lower](#bookmark516) | 0x00000000 | 0x6438 |  |
| 1.1.1.514 | [Distributor\_\_37\_GICD\_IROUTER135\_upper](#bookmark517) | 0x00000000 | 0x643C |  |
| 1.1.1.515 | [Distributor\_\_37\_GICD\_IROUTER136\_lower](#bookmark518) | 0x00000000 | 0x6440 |  |
| 1.1.1.516 | [Distributor\_\_37\_GICD\_IROUTER136\_upper](#bookmark519) | 0x00000000 | 0x6444 |  |
| 1.1.1.517 | [Distributor\_\_37\_GICD\_IROUTER137\_lower](#bookmark520) | 0x00000000 | 0x6448 |  |
| 1.1.1.518 | [Distributor\_\_37\_GICD\_IROUTER137\_upper](#bookmark521) | 0x00000000 | 0x644C |  |
| 1.1.1.519 | [Distributor\_\_37\_GICD\_IROUTER138\_lower](#bookmark522) | 0x00000000 | 0x6450 |  |
| 1.1.1.520 | [Distributor\_\_37\_GICD\_IROUTER138\_upper](#bookmark523) | 0x00000000 | 0x6454 |  |
| 1.1.1.521 | [Distributor\_\_37\_GICD\_IROUTER139\_lower](#bookmark524) | 0x00000000 | 0x6458 |  |
| 1.1.1.522 | [Distributor\_\_37\_GICD\_IROUTER139\_upper](#bookmark525) | 0x00000000 | 0x645C |  |
| 1.1.1.523 | [Distributor\_\_37\_GICD\_IROUTER140\_lower](#bookmark526) | 0x00000000 | 0x6460 |  |
| 1.1.1.524 | [Distributor\_\_37\_GICD\_IROUTER140\_upper](#bookmark527) | 0x00000000 | 0x6464 |  |
| 1.1.1.525 | [Distributor\_\_37\_GICD\_IROUTER141\_lower](#bookmark528) | 0x00000000 | 0x6468 |  |
| 1.1.1.526 | [Distributor\_\_37\_GICD\_IROUTER141\_upper](#bookmark529) | 0x00000000 | 0x646C |  |
| 1.1.1.527 | [Distributor\_\_37\_GICD\_IROUTER142\_lower](#bookmark530) | 0x00000000 | 0x6470 |  |
| 1.1.1.528 | [Distributor\_\_37\_GICD\_IROUTER142\_upper](#bookmark531) | 0x00000000 | 0x6474 |  |
| 1.1.1.529 | [Distributor\_\_37\_GICD\_IROUTER143\_lower](#bookmark532) | 0x00000000 | 0x6478 |  |
| 1.1.1.530 | [Distributor\_\_37\_GICD\_IROUTER143\_upper](#bookmark533) | 0x00000000 | 0x647C |  |
| 1.1.1.531 | [Distributor\_\_37\_GICD\_IROUTER144\_lower](#bookmark534) | 0x00000000 | 0x6480 |  |
| 1.1.1.532 | [Distributor\_\_37\_GICD\_IROUTER144\_upper](#bookmark535) | 0x00000000 | 0x6484 |  |
| 1.1.1.533 | [Distributor\_\_37\_GICD\_IROUTER145\_lower](#bookmark536) | 0x00000000 | 0x6488 |  |
| 1.1.1.534 | [Distributor\_\_37\_GICD\_IROUTER145\_upper](#bookmark537) | 0x00000000 | 0x648C |  |
| 1.1.1.535 | [Distributor\_\_37\_GICD\_IROUTER146\_lower](#bookmark538) | 0x00000000 | 0x6490 |  |
| 1.1.1.536 | [Distributor\_\_37\_GICD\_IROUTER146\_upper](#bookmark539) | 0x00000000 | 0x6494 |  |
| 1.1.1.537 | [Distributor\_\_37\_GICD\_IROUTER147\_lower](#bookmark540) | 0x00000000 | 0x6498 |  |
| 1.1.1.538 | [Distributor\_\_37\_GICD\_IROUTER147\_upper](#bookmark541) | 0x00000000 | 0x649C |  |
| 1.1.1.539 | [Distributor\_\_37\_GICD\_IROUTER148\_lower](#bookmark542) | 0x00000000 | 0x64A0 |  |
| 1.1.1.540 | [Distributor\_\_37\_GICD\_IROUTER148\_upper](#bookmark543) | 0x00000000 | 0x64A4 |  |
| 1.1.1.541 | [Distributor\_\_37\_GICD\_IROUTER149\_lower](#bookmark544) | 0x00000000 | 0x64A8 |  |
| 1.1.1.542 | [Distributor\_\_37\_GICD\_IROUTER149\_upper](#bookmark545) | 0x00000000 | 0x64AC |  |
| 1.1.1.543 | [Distributor\_\_37\_GICD\_IROUTER150\_lower](#bookmark546) | 0x00000000 | 0x64B0 |  |
| 1.1.1.544 | [Distributor\_\_37\_GICD\_IROUTER150\_upper](#bookmark547) | 0x00000000 | 0x64B4 |  |
| 1.1.1.545 | [Distributor\_\_37\_GICD\_IROUTER151\_lower](#bookmark548) | 0x00000000 | 0x64B8 |  |
| 1.1.1.546 | [Distributor\_\_37\_GICD\_IROUTER151\_upper](#bookmark549) | 0x00000000 | 0x64BC |  |
| 1.1.1.547 | [Distributor\_\_37\_GICD\_IROUTER152\_lower](#bookmark550) | 0x00000000 | 0x64C0 |  |
| 1.1.1.548 | [Distributor\_\_37\_GICD\_IROUTER152\_upper](#bookmark551) | 0x00000000 | 0x64C4 |  |
| 1.1.1.549 | [Distributor\_\_37\_GICD\_IROUTER153\_lower](#bookmark552) | 0x00000000 | 0x64C8 |  |
| 1.1.1.550 | [Distributor\_\_37\_GICD\_IROUTER153\_upper](#bookmark553) | 0x00000000 | 0x64CC |  |
| 1.1.1.551 | [Distributor\_\_37\_GICD\_IROUTER154\_lower](#bookmark554) | 0x00000000 | 0x64D0 |  |
| 1.1.1.552 | [Distributor\_\_37\_GICD\_IROUTER154\_upper](#bookmark555) | 0x00000000 | 0x64D4 |  |
| 1.1.1.553 | [Distributor\_\_37\_GICD\_IROUTER155\_lower](#bookmark556) | 0x00000000 | 0x64D8 |  |
| 1.1.1.554 | [Distributor\_\_37\_GICD\_IROUTER155\_upper](#bookmark557) | 0x00000000 | 0x64DC |  |
| 1.1.1.555 | [Distributor\_\_37\_GICD\_IROUTER156\_lower](#bookmark558) | 0x00000000 | 0x64E0 |  |
| 1.1.1.556 | [Distributor\_\_37\_GICD\_IROUTER156\_upper](#bookmark559) | 0x00000000 | 0x64E4 |  |
| 1.1.1.557 | [Distributor\_\_37\_GICD\_IROUTER157\_lower](#bookmark560) | 0x00000000 | 0x64E8 |  |
| 1.1.1.558 | [Distributor\_\_37\_GICD\_IROUTER157\_upper](#bookmark561) | 0x00000000 | 0x64EC |  |
| 1.1.1.559 | [Distributor\_\_37\_GICD\_IROUTER158\_lower](#bookmark562) | 0x00000000 | 0x64F0 |  |
| 1.1.1.560 | [Distributor\_\_37\_GICD\_IROUTER158\_upper](#bookmark563) | 0x00000000 | 0x64F4 |  |
| 1.1.1.561 | [Distributor\_\_37\_GICD\_IROUTER159\_lower](#bookmark564) | 0x00000000 | 0x64F8 |  |
| 1.1.1.562 | [Distributor\_\_37\_GICD\_IROUTER159\_upper](#bookmark565) | 0x00000000 | 0x64FC |  |
| 1.1.1.563 | [Distributor\_\_37\_GICD\_IROUTER160\_lower](#bookmark566) | 0x00000000 | 0x6500 |  |
| 1.1.1.564 | [Distributor\_\_37\_GICD\_IROUTER160\_upper](#bookmark567) | 0x00000000 | 0x6504 |  |
| 1.1.1.565 | [Distributor\_\_37\_GICD\_IROUTER161\_lower](#bookmark568) | 0x00000000 | 0x6508 |  |
| 1.1.1.566 | [Distributor\_\_37\_GICD\_IROUTER161\_upper](#bookmark569) | 0x00000000 | 0x650C |  |
| 1.1.1.567 | [Distributor\_\_37\_GICD\_IROUTER162\_lower](#bookmark570) | 0x00000000 | 0x6510 |  |
| 1.1.1.568 | [Distributor\_\_37\_GICD\_IROUTER162\_upper](#bookmark571) | 0x00000000 | 0x6514 |  |
| 1.1.1.569 | [Distributor\_\_37\_GICD\_IROUTER163\_lower](#bookmark572) | 0x00000000 | 0x6518 |  |
| 1.1.1.570 | [Distributor\_\_37\_GICD\_IROUTER163\_upper](#bookmark573) | 0x00000000 | 0x651C |  |
| 1.1.1.571 | [Distributor\_\_37\_GICD\_IROUTER164\_lower](#bookmark574) | 0x00000000 | 0x6520 |  |
| 1.1.1.572 | [Distributor\_\_37\_GICD\_IROUTER164\_upper](#bookmark575) | 0x00000000 | 0x6524 |  |
| 1.1.1.573 | [Distributor\_\_37\_GICD\_IROUTER165\_lower](#bookmark576) | 0x00000000 | 0x6528 |  |
| 1.1.1.574 | [Distributor\_\_37\_GICD\_IROUTER165\_upper](#bookmark577) | 0x00000000 | 0x652C |  |
| 1.1.1.575 | [Distributor\_\_37\_GICD\_IROUTER166\_lower](#bookmark578) | 0x00000000 | 0x6530 |  |
| 1.1.1.576 | [Distributor\_\_37\_GICD\_IROUTER166\_upper](#bookmark579) | 0x00000000 | 0x6534 |  |
| 1.1.1.577 | [Distributor\_\_37\_GICD\_IROUTER167\_lower](#bookmark580) | 0x00000000 | 0x6538 |  |
| 1.1.1.578 | [Distributor\_\_37\_GICD\_IROUTER167\_upper](#bookmark581) | 0x00000000 | 0x653C |  |
| 1.1.1.579 | [Distributor\_\_37\_GICD\_IROUTER168\_lower](#bookmark582) | 0x00000000 | 0x6540 |  |
| 1.1.1.580 | [Distributor\_\_37\_GICD\_IROUTER168\_upper](#bookmark583) | 0x00000000 | 0x6544 |  |
| 1.1.1.581 | [Distributor\_\_37\_GICD\_IROUTER169\_lower](#bookmark584) | 0x00000000 | 0x6548 |  |
| 1.1.1.582 | [Distributor\_\_37\_GICD\_IROUTER169\_upper](#bookmark585) | 0x00000000 | 0x654C |  |
| 1.1.1.583 | [Distributor\_\_37\_GICD\_IROUTER170\_lower](#bookmark586) | 0x00000000 | 0x6550 |  |
| 1.1.1.584 | [Distributor\_\_37\_GICD\_IROUTER170\_upper](#bookmark587) | 0x00000000 | 0x6554 |  |
| 1.1.1.585 | [Distributor\_\_37\_GICD\_IROUTER171\_lower](#bookmark588) | 0x00000000 | 0x6558 |  |
| 1.1.1.586 | [Distributor\_\_37\_GICD\_IROUTER171\_upper](#bookmark589) | 0x00000000 | 0x655C |  |
| 1.1.1.587 | [Distributor\_\_37\_GICD\_IROUTER172\_lower](#bookmark590) | 0x00000000 | 0x6560 |  |
| 1.1.1.588 | [Distributor\_\_37\_GICD\_IROUTER172\_upper](#bookmark591) | 0x00000000 | 0x6564 |  |
| 1.1.1.589 | [Distributor\_\_37\_GICD\_IROUTER173\_lower](#bookmark592) | 0x00000000 | 0x6568 |  |
| 1.1.1.590 | [Distributor\_\_37\_GICD\_IROUTER173\_upper](#bookmark593) | 0x00000000 | 0x656C |  |
| 1.1.1.591 | [Distributor\_\_37\_GICD\_IROUTER174\_lower](#bookmark594) | 0x00000000 | 0x6570 |  |
| 1.1.1.592 | [Distributor\_\_37\_GICD\_IROUTER174\_upper](#bookmark595) | 0x00000000 | 0x6574 |  |
| 1.1.1.593 | [Distributor\_\_37\_GICD\_IROUTER175\_lower](#bookmark596) | 0x00000000 | 0x6578 |  |
| 1.1.1.594 | [Distributor\_\_37\_GICD\_IROUTER175\_upper](#bookmark597) | 0x00000000 | 0x657C |  |
| 1.1.1.595 | [Distributor\_\_37\_GICD\_IROUTER176\_lower](#bookmark598) | 0x00000000 | 0x6580 |  |
| 1.1.1.596 | [Distributor\_\_37\_GICD\_IROUTER176\_upper](#bookmark599) | 0x00000000 | 0x6584 |  |
| 1.1.1.597 | [Distributor\_\_37\_GICD\_IROUTER177\_lower](#bookmark600) | 0x00000000 | 0x6588 |  |
| 1.1.1.598 | [Distributor\_\_37\_GICD\_IROUTER177\_upper](#bookmark601) | 0x00000000 | 0x658C |  |
| 1.1.1.599 | [Distributor\_\_37\_GICD\_IROUTER178\_lower](#bookmark602) | 0x00000000 | 0x6590 |  |
| 1.1.1.600 | [Distributor\_\_37\_GICD\_IROUTER178\_upper](#bookmark603) | 0x00000000 | 0x6594 |  |
| 1.1.1.601 | [Distributor\_\_37\_GICD\_IROUTER179\_lower](#bookmark604) | 0x00000000 | 0x6598 |  |
| 1.1.1.602 | [Distributor\_\_37\_GICD\_IROUTER179\_upper](#bookmark605) | 0x00000000 | 0x659C |  |
| 1.1.1.603 | [Distributor\_\_37\_GICD\_IROUTER180\_lower](#bookmark606) | 0x00000000 | 0x65A0 |  |
| 1.1.1.604 | [Distributor\_\_37\_GICD\_IROUTER180\_upper](#bookmark607) | 0x00000000 | 0x65A4 |  |
| 1.1.1.605 | [Distributor\_\_37\_GICD\_IROUTER181\_lower](#bookmark608) | 0x00000000 | 0x65A8 |  |
| 1.1.1.606 | [Distributor\_\_37\_GICD\_IROUTER181\_upper](#bookmark609) | 0x00000000 | 0x65AC |  |
| 1.1.1.607 | [Distributor\_\_37\_GICD\_IROUTER182\_lower](#bookmark610) | 0x00000000 | 0x65B0 |  |
| 1.1.1.608 | [Distributor\_\_37\_GICD\_IROUTER182\_upper](#bookmark611) | 0x00000000 | 0x65B4 |  |
| 1.1.1.609 | [Distributor\_\_37\_GICD\_IROUTER183\_lower](#bookmark612) | 0x00000000 | 0x65B8 |  |
| 1.1.1.610 | [Distributor\_\_37\_GICD\_IROUTER183\_upper](#bookmark613) | 0x00000000 | 0x65BC |  |
| 1.1.1.611 | [Distributor\_\_37\_GICD\_IROUTER184\_lower](#bookmark614) | 0x00000000 | 0x65C0 |  |
| 1.1.1.612 | [Distributor\_\_37\_GICD\_IROUTER184\_upper](#bookmark615) | 0x00000000 | 0x65C4 |  |
| 1.1.1.613 | [Distributor\_\_37\_GICD\_IROUTER185\_lower](#bookmark616) | 0x00000000 | 0x65C8 |  |
| 1.1.1.614 | [Distributor\_\_37\_GICD\_IROUTER185\_upper](#bookmark617) | 0x00000000 | 0x65CC |  |
| 1.1.1.615 | [Distributor\_\_37\_GICD\_IROUTER186\_lower](#bookmark618) | 0x00000000 | 0x65D0 |  |
| 1.1.1.616 | [Distributor\_\_37\_GICD\_IROUTER186\_upper](#bookmark619) | 0x00000000 | 0x65D4 |  |
| 1.1.1.617 | [Distributor\_\_37\_GICD\_IROUTER187\_lower](#bookmark620) | 0x00000000 | 0x65D8 |  |
| 1.1.1.618 | [Distributor\_\_37\_GICD\_IROUTER187\_upper](#bookmark621) | 0x00000000 | 0x65DC |  |
| 1.1.1.619 | [Distributor\_\_37\_GICD\_IROUTER188\_lower](#bookmark622) | 0x00000000 | 0x65E0 |  |
| 1.1.1.620 | [Distributor\_\_37\_GICD\_IROUTER188\_upper](#bookmark623) | 0x00000000 | 0x65E4 |  |
| 1.1.1.621 | [Distributor\_\_37\_GICD\_IROUTER189\_lower](#bookmark624) | 0x00000000 | 0x65E8 |  |
| 1.1.1.622 | [Distributor\_\_37\_GICD\_IROUTER189\_upper](#bookmark625) | 0x00000000 | 0x65EC |  |
| 1.1.1.623 | [Distributor\_\_37\_GICD\_IROUTER190\_lower](#bookmark626) | 0x00000000 | 0x65F0 |  |
| 1.1.1.624 | [Distributor\_\_37\_GICD\_IROUTER190\_upper](#bookmark627) | 0x00000000 | 0x65F4 |  |
| 1.1.1.625 | [Distributor\_\_37\_GICD\_IROUTER191\_lower](#bookmark628) | 0x00000000 | 0x65F8 |  |
| 1.1.1.626 | [Distributor\_\_37\_GICD\_IROUTER191\_upper](#bookmark629) | 0x00000000 | 0x65FC |  |
| 1.1.1.627 | [Distributor\_\_37\_GICD\_IROUTER192\_lower](#bookmark630) | 0x00000000 | 0x6600 |  |
| 1.1.1.628 | [Distributor\_\_37\_GICD\_IROUTER192\_upper](#bookmark631) | 0x00000000 | 0x6604 |  |
| 1.1.1.629 | [Distributor\_\_37\_GICD\_IROUTER193\_lower](#bookmark632) | 0x00000000 | 0x6608 |  |
| 1.1.1.630 | [Distributor\_\_37\_GICD\_IROUTER193\_upper](#bookmark633) | 0x00000000 | 0x660C |  |
| 1.1.1.631 | [Distributor\_\_37\_GICD\_IROUTER194\_lower](#bookmark634) | 0x00000000 | 0x6610 |  |
| 1.1.1.632 | [Distributor\_\_37\_GICD\_IROUTER194\_upper](#bookmark635) | 0x00000000 | 0x6614 |  |
| 1.1.1.633 | [Distributor\_\_37\_GICD\_IROUTER195\_lower](#bookmark636) | 0x00000000 | 0x6618 |  |
| 1.1.1.634 | [Distributor\_\_37\_GICD\_IROUTER195\_upper](#bookmark637) | 0x00000000 | 0x661C |  |
| 1.1.1.635 | [Distributor\_\_37\_GICD\_IROUTER196\_lower](#bookmark638) | 0x00000000 | 0x6620 |  |
| 1.1.1.636 | [Distributor\_\_37\_GICD\_IROUTER196\_upper](#bookmark639) | 0x00000000 | 0x6624 |  |
| 1.1.1.637 | [Distributor\_\_37\_GICD\_IROUTER197\_lower](#bookmark640) | 0x00000000 | 0x6628 |  |
| 1.1.1.638 | [Distributor\_\_37\_GICD\_IROUTER197\_upper](#bookmark641) | 0x00000000 | 0x662C |  |
| 1.1.1.639 | [Distributor\_\_37\_GICD\_IROUTER198\_lower](#bookmark642) | 0x00000000 | 0x6630 |  |
| 1.1.1.640 | [Distributor\_\_37\_GICD\_IROUTER198\_upper](#bookmark643) | 0x00000000 | 0x6634 |  |
| 1.1.1.641 | [Distributor\_\_37\_GICD\_IROUTER199\_lower](#bookmark644) | 0x00000000 | 0x6638 |  |
| 1.1.1.642 | [Distributor\_\_37\_GICD\_IROUTER199\_upper](#bookmark645) | 0x00000000 | 0x663C |  |
| 1.1.1.643 | [Distributor\_\_37\_GICD\_IROUTER200\_lower](#bookmark646) | 0x00000000 | 0x6640 |  |
| 1.1.1.644 | [Distributor\_\_37\_GICD\_IROUTER200\_upper](#bookmark647) | 0x00000000 | 0x6644 |  |
| 1.1.1.645 | [Distributor\_\_37\_GICD\_IROUTER201\_lower](#bookmark648) | 0x00000000 | 0x6648 |  |
| 1.1.1.646 | [Distributor\_\_37\_GICD\_IROUTER201\_upper](#bookmark649) | 0x00000000 | 0x664C |  |
| 1.1.1.647 | [Distributor\_\_37\_GICD\_IROUTER202\_lower](#bookmark650) | 0x00000000 | 0x6650 |  |
| 1.1.1.648 | [Distributor\_\_37\_GICD\_IROUTER202\_upper](#bookmark651) | 0x00000000 | 0x6654 |  |
| 1.1.1.649 | [Distributor\_\_37\_GICD\_IROUTER203\_lower](#bookmark652) | 0x00000000 | 0x6658 |  |
| 1.1.1.650 | [Distributor\_\_37\_GICD\_IROUTER203\_upper](#bookmark653) | 0x00000000 | 0x665C |  |
| 1.1.1.651 | [Distributor\_\_37\_GICD\_IROUTER204\_lower](#bookmark654) | 0x00000000 | 0x6660 |  |
| 1.1.1.652 | [Distributor\_\_37\_GICD\_IROUTER204\_upper](#bookmark655) | 0x00000000 | 0x6664 |  |
| 1.1.1.653 | [Distributor\_\_37\_GICD\_IROUTER205\_lower](#bookmark656) | 0x00000000 | 0x6668 |  |
| 1.1.1.654 | [Distributor\_\_37\_GICD\_IROUTER205\_upper](#bookmark657) | 0x00000000 | 0x666C |  |
| 1.1.1.655 | [Distributor\_\_37\_GICD\_IROUTER206\_lower](#bookmark658) | 0x00000000 | 0x6670 |  |
| 1.1.1.656 | [Distributor\_\_37\_GICD\_IROUTER206\_upper](#bookmark659) | 0x00000000 | 0x6674 |  |
| 1.1.1.657 | [Distributor\_\_37\_GICD\_IROUTER207\_lower](#bookmark660) | 0x00000000 | 0x6678 |  |
| 1.1.1.658 | [Distributor\_\_37\_GICD\_IROUTER207\_upper](#bookmark661) | 0x00000000 | 0x667C |  |
| 1.1.1.659 | [Distributor\_\_37\_GICD\_IROUTER208\_lower](#bookmark662) | 0x00000000 | 0x6680 |  |
| 1.1.1.660 | [Distributor\_\_37\_GICD\_IROUTER208\_upper](#bookmark663) | 0x00000000 | 0x6684 |  |
| 1.1.1.661 | [Distributor\_\_37\_GICD\_IROUTER209\_lower](#bookmark664) | 0x00000000 | 0x6688 |  |
| 1.1.1.662 | [Distributor\_\_37\_GICD\_IROUTER209\_upper](#bookmark665) | 0x00000000 | 0x668C |  |
| 1.1.1.663 | [Distributor\_\_37\_GICD\_IROUTER210\_lower](#bookmark666) | 0x00000000 | 0x6690 |  |
| 1.1.1.664 | [Distributor\_\_37\_GICD\_IROUTER210\_upper](#bookmark667) | 0x00000000 | 0x6694 |  |
| 1.1.1.665 | [Distributor\_\_37\_GICD\_IROUTER211\_lower](#bookmark668) | 0x00000000 | 0x6698 |  |
| 1.1.1.666 | [Distributor\_\_37\_GICD\_IROUTER211\_upper](#bookmark669) | 0x00000000 | 0x669C |  |
| 1.1.1.667 | [Distributor\_\_37\_GICD\_IROUTER212\_lower](#bookmark670) | 0x00000000 | 0x66A0 |  |
| 1.1.1.668 | [Distributor\_\_37\_GICD\_IROUTER212\_upper](#bookmark671) | 0x00000000 | 0x66A4 |  |
| 1.1.1.669 | [Distributor\_\_37\_GICD\_IROUTER213\_lower](#bookmark672) | 0x00000000 | 0x66A8 |  |
| 1.1.1.670 | [Distributor\_\_37\_GICD\_IROUTER213\_upper](#bookmark673) | 0x00000000 | 0x66AC |  |
| 1.1.1.671 | [Distributor\_\_37\_GICD\_IROUTER214\_lower](#bookmark674) | 0x00000000 | 0x66B0 |  |
| 1.1.1.672 | [Distributor\_\_37\_GICD\_IROUTER214\_upper](#bookmark675) | 0x00000000 | 0x66B4 |  |
| 1.1.1.673 | [Distributor\_\_37\_GICD\_IROUTER215\_lower](#bookmark676) | 0x00000000 | 0x66B8 |  |
| 1.1.1.674 | [Distributor\_\_37\_GICD\_IROUTER215\_upper](#bookmark677) | 0x00000000 | 0x66BC |  |
| 1.1.1.675 | [Distributor\_\_37\_GICD\_IROUTER216\_lower](#bookmark678) | 0x00000000 | 0x66C0 |  |
| 1.1.1.676 | [Distributor\_\_37\_GICD\_IROUTER216\_upper](#bookmark679) | 0x00000000 | 0x66C4 |  |
| 1.1.1.677 | [Distributor\_\_37\_GICD\_IROUTER217\_lower](#bookmark680) | 0x00000000 | 0x66C8 |  |
| 1.1.1.678 | [Distributor\_\_37\_GICD\_IROUTER217\_upper](#bookmark681) | 0x00000000 | 0x66CC |  |
| 1.1.1.679 | [Distributor\_\_37\_GICD\_IROUTER218\_lower](#bookmark682) | 0x00000000 | 0x66D0 |  |
| 1.1.1.680 | [Distributor\_\_37\_GICD\_IROUTER218\_upper](#bookmark683) | 0x00000000 | 0x66D4 |  |
| 1.1.1.681 | [Distributor\_\_37\_GICD\_IROUTER219\_lower](#bookmark684) | 0x00000000 | 0x66D8 |  |
| 1.1.1.682 | [Distributor\_\_37\_GICD\_IROUTER219\_upper](#bookmark685) | 0x00000000 | 0x66DC |  |
| 1.1.1.683 | [Distributor\_\_37\_GICD\_IROUTER220\_lower](#bookmark686) | 0x00000000 | 0x66E0 |  |
| 1.1.1.684 | [Distributor\_\_37\_GICD\_IROUTER220\_upper](#bookmark687) | 0x00000000 | 0x66E4 |  |
| 1.1.1.685 | [Distributor\_\_37\_GICD\_IROUTER221\_lower](#bookmark688) | 0x00000000 | 0x66E8 |  |
| 1.1.1.686 | [Distributor\_\_37\_GICD\_IROUTER221\_upper](#bookmark689) | 0x00000000 | 0x66EC |  |
| 1.1.1.687 | [Distributor\_\_37\_GICD\_IROUTER222\_lower](#bookmark690) | 0x00000000 | 0x66F0 |  |
| 1.1.1.688 | [Distributor\_\_37\_GICD\_IROUTER222\_upper](#bookmark691) | 0x00000000 | 0x66F4 |  |
| 1.1.1.689 | [Distributor\_\_37\_GICD\_IROUTER223\_lower](#bookmark692) | 0x00000000 | 0x66F8 |  |
| 1.1.1.690 | [Distributor\_\_37\_GICD\_IROUTER223\_upper](#bookmark693) | 0x00000000 | 0x66FC |  |
| 1.1.1.691 | [Distributor\_\_37\_GICD\_IROUTER224\_lower](#bookmark694) | 0x00000000 | 0x6700 |  |
| 1.1.1.692 | [Distributor\_\_37\_GICD\_IROUTER224\_upper](#bookmark695) | 0x00000000 | 0x6704 |  |
| 1.1.1.693 | [Distributor\_\_37\_GICD\_IROUTER225\_lower](#bookmark696) | 0x00000000 | 0x6708 |  |
| 1.1.1.694 | [Distributor\_\_37\_GICD\_IROUTER225\_upper](#bookmark697) | 0x00000000 | 0x670C |  |
| 1.1.1.695 | [Distributor\_\_37\_GICD\_IROUTER226\_lower](#bookmark698) | 0x00000000 | 0x6710 |  |
| 1.1.1.696 | [Distributor\_\_37\_GICD\_IROUTER226\_upper](#bookmark699) | 0x00000000 | 0x6714 |  |
| 1.1.1.697 | [Distributor\_\_37\_GICD\_IROUTER227\_lower](#bookmark700) | 0x00000000 | 0x6718 |  |
| 1.1.1.698 | [Distributor\_\_37\_GICD\_IROUTER227\_upper](#bookmark701) | 0x00000000 | 0x671C |  |
| 1.1.1.699 | [Distributor\_\_37\_GICD\_IROUTER228\_lower](#bookmark702) | 0x00000000 | 0x6720 |  |
| 1.1.1.700 | [Distributor\_\_37\_GICD\_IROUTER228\_upper](#bookmark703) | 0x00000000 | 0x6724 |  |
| 1.1.1.701 | [Distributor\_\_37\_GICD\_IROUTER229\_lower](#bookmark704) | 0x00000000 | 0x6728 |  |
| 1.1.1.702 | [Distributor\_\_37\_GICD\_IROUTER229\_upper](#bookmark705) | 0x00000000 | 0x672C |  |
| 1.1.1.703 | [Distributor\_\_37\_GICD\_IROUTER230\_lower](#bookmark706) | 0x00000000 | 0x6730 |  |
| 1.1.1.704 | [Distributor\_\_37\_GICD\_IROUTER230\_upper](#bookmark707) | 0x00000000 | 0x6734 |  |
| 1.1.1.705 | [Distributor\_\_37\_GICD\_IROUTER231\_lower](#bookmark708) | 0x00000000 | 0x6738 |  |
| 1.1.1.706 | [Distributor\_\_37\_GICD\_IROUTER231\_upper](#bookmark709) | 0x00000000 | 0x673C |  |
| 1.1.1.707 | [Distributor\_\_37\_GICD\_IROUTER232\_lower](#bookmark710) | 0x00000000 | 0x6740 |  |
| 1.1.1.708 | [Distributor\_\_37\_GICD\_IROUTER232\_upper](#bookmark711) | 0x00000000 | 0x6744 |  |
| 1.1.1.709 | [Distributor\_\_37\_GICD\_IROUTER233\_lower](#bookmark712) | 0x00000000 | 0x6748 |  |
| 1.1.1.710 | [Distributor\_\_37\_GICD\_IROUTER233\_upper](#bookmark713) | 0x00000000 | 0x674C |  |
| 1.1.1.711 | [Distributor\_\_37\_GICD\_IROUTER234\_lower](#bookmark714) | 0x00000000 | 0x6750 |  |
| 1.1.1.712 | [Distributor\_\_37\_GICD\_IROUTER234\_upper](#bookmark715) | 0x00000000 | 0x6754 |  |
| 1.1.1.713 | [Distributor\_\_37\_GICD\_IROUTER235\_lower](#bookmark716) | 0x00000000 | 0x6758 |  |
| 1.1.1.714 | [Distributor\_\_37\_GICD\_IROUTER235\_upper](#bookmark717) | 0x00000000 | 0x675C |  |
| 1.1.1.715 | [Distributor\_\_37\_GICD\_IROUTER236\_lower](#bookmark718) | 0x00000000 | 0x6760 |  |
| 1.1.1.716 | [Distributor\_\_37\_GICD\_IROUTER236\_upper](#bookmark719) | 0x00000000 | 0x6764 |  |
| 1.1.1.717 | [Distributor\_\_37\_GICD\_IROUTER237\_lower](#bookmark720) | 0x00000000 | 0x6768 |  |
| 1.1.1.718 | [Distributor\_\_37\_GICD\_IROUTER237\_upper](#bookmark721) | 0x00000000 | 0x676C |  |
| 1.1.1.719 | [Distributor\_\_37\_GICD\_IROUTER238\_lower](#bookmark722) | 0x00000000 | 0x6770 |  |
| 1.1.1.720 | [Distributor\_\_37\_GICD\_IROUTER238\_upper](#bookmark723) | 0x00000000 | 0x6774 |  |
| 1.1.1.721 | [Distributor\_\_37\_GICD\_IROUTER239\_lower](#bookmark724) | 0x00000000 | 0x6778 |  |
| 1.1.1.722 | [Distributor\_\_37\_GICD\_IROUTER239\_upper](#bookmark725) | 0x00000000 | 0x677C |  |
| 1.1.1.723 | [Distributor\_\_37\_GICD\_IROUTER240\_lower](#bookmark726) | 0x00000000 | 0x6780 |  |
| 1.1.1.724 | [Distributor\_\_37\_GICD\_IROUTER240\_upper](#bookmark727) | 0x00000000 | 0x6784 |  |
| 1.1.1.725 | [Distributor\_\_37\_GICD\_IROUTER241\_lower](#bookmark728) | 0x00000000 | 0x6788 |  |
| 1.1.1.726 | [Distributor\_\_37\_GICD\_IROUTER241\_upper](#bookmark729) | 0x00000000 | 0x678C |  |
| 1.1.1.727 | [Distributor\_\_37\_GICD\_IROUTER242\_lower](#bookmark730) | 0x00000000 | 0x6790 |  |
| 1.1.1.728 | [Distributor\_\_37\_GICD\_IROUTER242\_upper](#bookmark731) | 0x00000000 | 0x6794 |  |
| 1.1.1.729 | [Distributor\_\_37\_GICD\_IROUTER243\_lower](#bookmark732) | 0x00000000 | 0x6798 |  |
| 1.1.1.730 | [Distributor\_\_37\_GICD\_IROUTER243\_upper](#bookmark733) | 0x00000000 | 0x679C |  |
| 1.1.1.731 | [Distributor\_\_37\_GICD\_IROUTER244\_lower](#bookmark734) | 0x00000000 | 0x67A0 |  |
| 1.1.1.732 | [Distributor\_\_37\_GICD\_IROUTER244\_upper](#bookmark735) | 0x00000000 | 0x67A4 |  |
| 1.1.1.733 | [Distributor\_\_37\_GICD\_IROUTER245\_lower](#bookmark736) | 0x00000000 | 0x67A8 |  |
| 1.1.1.734 | [Distributor\_\_37\_GICD\_IROUTER245\_upper](#bookmark737) | 0x00000000 | 0x67AC |  |
| 1.1.1.735 | [Distributor\_\_37\_GICD\_IROUTER246\_lower](#bookmark738) | 0x00000000 | 0x67B0 |  |
| 1.1.1.736 | [Distributor\_\_37\_GICD\_IROUTER246\_upper](#bookmark739) | 0x00000000 | 0x67B4 |  |
| 1.1.1.737 | [Distributor\_\_37\_GICD\_IROUTER247\_lower](#bookmark740) | 0x00000000 | 0x67B8 |  |
| 1.1.1.738 | [Distributor\_\_37\_GICD\_IROUTER247\_upper](#bookmark741) | 0x00000000 | 0x67BC |  |
| 1.1.1.739 | [Distributor\_\_37\_GICD\_IROUTER248\_lower](#bookmark742) | 0x00000000 | 0x67C0 |  |
| 1.1.1.740 | [Distributor\_\_37\_GICD\_IROUTER248\_upper](#bookmark743) | 0x00000000 | 0x67C4 |  |
| 1.1.1.741 | [Distributor\_\_37\_GICD\_IROUTER249\_lower](#bookmark744) | 0x00000000 | 0x67C8 |  |
| 1.1.1.742 | [Distributor\_\_37\_GICD\_IROUTER249\_upper](#bookmark745) | 0x00000000 | 0x67CC |  |
| 1.1.1.743 | [Distributor\_\_37\_GICD\_IROUTER250\_lower](#bookmark746) | 0x00000000 | 0x67D0 |  |
| 1.1.1.744 | [Distributor\_\_37\_GICD\_IROUTER250\_upper](#bookmark747) | 0x00000000 | 0x67D4 |  |
| 1.1.1.745 | [Distributor\_\_37\_GICD\_IROUTER251\_lower](#bookmark748) | 0x00000000 | 0x67D8 |  |
| 1.1.1.746 | [Distributor\_\_37\_GICD\_IROUTER251\_upper](#bookmark749) | 0x00000000 | 0x67DC |  |
| 1.1.1.747 | [Distributor\_\_37\_GICD\_IROUTER252\_lower](#bookmark750) | 0x00000000 | 0x67E0 |  |
| 1.1.1.748 | [Distributor\_\_37\_GICD\_IROUTER252\_upper](#bookmark751) | 0x00000000 | 0x67E4 |  |
| 1.1.1.749 | [Distributor\_\_37\_GICD\_IROUTER253\_lower](#bookmark752) | 0x00000000 | 0x67E8 |  |
| 1.1.1.750 | [Distributor\_\_37\_GICD\_IROUTER253\_upper](#bookmark753) | 0x00000000 | 0x67EC |  |
| 1.1.1.751 | [Distributor\_\_37\_GICD\_IROUTER254\_lower](#bookmark754) | 0x00000000 | 0x67F0 |  |
| 1.1.1.752 | [Distributor\_\_37\_GICD\_IROUTER254\_upper](#bookmark755) | 0x00000000 | 0x67F4 |  |
| 1.1.1.753 | [Distributor\_\_37\_GICD\_IROUTER255\_lower](#bookmark756) | 0x00000000 | 0x67F8 |  |
| 1.1.1.754 | [Distributor\_\_37\_GICD\_IROUTER255\_upper](#bookmark757) | 0x00000000 | 0x67FC |  |
| 1.1.1.755 | [Distributor\_\_37\_GICD\_IROUTER256\_lower](#bookmark758) | 0x00000000 | 0x6800 |  |
| 1.1.1.756 | [Distributor\_\_37\_GICD\_IROUTER256\_upper](#bookmark759) | 0x00000000 | 0x6804 |  |
| 1.1.1.757 | [Distributor\_\_37\_GICD\_IROUTER257\_lower](#bookmark760) | 0x00000000 | 0x6808 |  |
| 1.1.1.758 | [Distributor\_\_37\_GICD\_IROUTER257\_upper](#bookmark761) | 0x00000000 | 0x680C |  |
| 1.1.1.759 | [Distributor\_\_37\_GICD\_IROUTER258\_lower](#bookmark762) | 0x00000000 | 0x6810 |  |
| 1.1.1.760 | [Distributor\_\_37\_GICD\_IROUTER258\_upper](#bookmark763) | 0x00000000 | 0x6814 |  |
| 1.1.1.761 | [Distributor\_\_37\_GICD\_IROUTER259\_lower](#bookmark764) | 0x00000000 | 0x6818 |  |
| 1.1.1.762 | [Distributor\_\_37\_GICD\_IROUTER259\_upper](#bookmark765) | 0x00000000 | 0x681C |  |
| 1.1.1.763 | [Distributor\_\_37\_GICD\_IROUTER260\_lower](#bookmark766) | 0x00000000 | 0x6820 |  |
| 1.1.1.764 | [Distributor\_\_37\_GICD\_IROUTER260\_upper](#bookmark767) | 0x00000000 | 0x6824 |  |
| 1.1.1.765 | [Distributor\_\_37\_GICD\_IROUTER261\_lower](#bookmark768) | 0x00000000 | 0x6828 |  |
| 1.1.1.766 | [Distributor\_\_37\_GICD\_IROUTER261\_upper](#bookmark769) | 0x00000000 | 0x682C |  |
| 1.1.1.767 | [Distributor\_\_37\_GICD\_IROUTER262\_lower](#bookmark770) | 0x00000000 | 0x6830 |  |
| 1.1.1.768 | [Distributor\_\_37\_GICD\_IROUTER262\_upper](#bookmark771) | 0x00000000 | 0x6834 |  |
| 1.1.1.769 | [Distributor\_\_37\_GICD\_IROUTER263\_lower](#bookmark772) | 0x00000000 | 0x6838 |  |
| 1.1.1.770 | [Distributor\_\_37\_GICD\_IROUTER263\_upper](#bookmark773) | 0x00000000 | 0x683C |  |
| 1.1.1.771 | [Distributor\_\_37\_GICD\_IROUTER264\_lower](#bookmark774) | 0x00000000 | 0x6840 |  |
| 1.1.1.772 | [Distributor\_\_37\_GICD\_IROUTER264\_upper](#bookmark775) | 0x00000000 | 0x6844 |  |
| 1.1.1.773 | [Distributor\_\_37\_GICD\_IROUTER265\_lower](#bookmark776) | 0x00000000 | 0x6848 |  |
| 1.1.1.774 | [Distributor\_\_37\_GICD\_IROUTER265\_upper](#bookmark777) | 0x00000000 | 0x684C |  |
| 1.1.1.775 | [Distributor\_\_37\_GICD\_IROUTER266\_lower](#bookmark778) | 0x00000000 | 0x6850 |  |
| 1.1.1.776 | [Distributor\_\_37\_GICD\_IROUTER266\_upper](#bookmark779) | 0x00000000 | 0x6854 |  |
| 1.1.1.777 | [Distributor\_\_37\_GICD\_IROUTER267\_lower](#bookmark780) | 0x00000000 | 0x6858 |  |
| 1.1.1.778 | [Distributor\_\_37\_GICD\_IROUTER267\_upper](#bookmark781) | 0x00000000 | 0x685C |  |
| 1.1.1.779 | [Distributor\_\_37\_GICD\_IROUTER268\_lower](#bookmark782) | 0x00000000 | 0x6860 |  |
| 1.1.1.780 | [Distributor\_\_37\_GICD\_IROUTER268\_upper](#bookmark783) | 0x00000000 | 0x6864 |  |
| 1.1.1.781 | [Distributor\_\_37\_GICD\_IROUTER269\_lower](#bookmark784) | 0x00000000 | 0x6868 |  |
| 1.1.1.782 | [Distributor\_\_37\_GICD\_IROUTER269\_upper](#bookmark785) | 0x00000000 | 0x686C |  |
| 1.1.1.783 | [Distributor\_\_37\_GICD\_IROUTER270\_lower](#bookmark786) | 0x00000000 | 0x6870 |  |
| 1.1.1.784 | [Distributor\_\_37\_GICD\_IROUTER270\_upper](#bookmark787) | 0x00000000 | 0x6874 |  |
| 1.1.1.785 | [Distributor\_\_37\_GICD\_IROUTER271\_lower](#bookmark788) | 0x00000000 | 0x6878 |  |
| 1.1.1.786 | [Distributor\_\_37\_GICD\_IROUTER271\_upper](#bookmark789) | 0x00000000 | 0x687C |  |
| 1.1.1.787 | [Distributor\_\_37\_GICD\_IROUTER272\_lower](#bookmark790) | 0x00000000 | 0x6880 |  |
| 1.1.1.788 | [Distributor\_\_37\_GICD\_IROUTER272\_upper](#bookmark791) | 0x00000000 | 0x6884 |  |
| 1.1.1.789 | [Distributor\_\_37\_GICD\_IROUTER273\_lower](#bookmark792) | 0x00000000 | 0x6888 |  |
| 1.1.1.790 | [Distributor\_\_37\_GICD\_IROUTER273\_upper](#bookmark793) | 0x00000000 | 0x688C |  |
| 1.1.1.791 | [Distributor\_\_37\_GICD\_IROUTER274\_lower](#bookmark794) | 0x00000000 | 0x6890 |  |
| 1.1.1.792 | [Distributor\_\_37\_GICD\_IROUTER274\_upper](#bookmark795) | 0x00000000 | 0x6894 |  |
| 1.1.1.793 | [Distributor\_\_37\_GICD\_IROUTER275\_lower](#bookmark796) | 0x00000000 | 0x6898 |  |
| 1.1.1.794 | [Distributor\_\_37\_GICD\_IROUTER275\_upper](#bookmark797) | 0x00000000 | 0x689C |  |
| 1.1.1.795 | [Distributor\_\_37\_GICD\_IROUTER276\_lower](#bookmark798) | 0x00000000 | 0x68A0 |  |
| 1.1.1.796 | [Distributor\_\_37\_GICD\_IROUTER276\_upper](#bookmark799) | 0x00000000 | 0x68A4 |  |
| 1.1.1.797 | [Distributor\_\_37\_GICD\_IROUTER277\_lower](#bookmark800) | 0x00000000 | 0x68A8 |  |
| 1.1.1.798 | [Distributor\_\_37\_GICD\_IROUTER277\_upper](#bookmark801) | 0x00000000 | 0x68AC |  |
| 1.1.1.799 | [Distributor\_\_37\_GICD\_IROUTER278\_lower](#bookmark802) | 0x00000000 | 0x68B0 |  |
| 1.1.1.800 | [Distributor\_\_37\_GICD\_IROUTER278\_upper](#bookmark803) | 0x00000000 | 0x68B4 |  |
| 1.1.1.801 | [Distributor\_\_37\_GICD\_IROUTER279\_lower](#bookmark804) | 0x00000000 | 0x68B8 |  |
| 1.1.1.802 | [Distributor\_\_37\_GICD\_IROUTER279\_upper](#bookmark805) | 0x00000000 | 0x68BC |  |
| 1.1.1.803 | [Distributor\_\_37\_GICD\_IROUTER280\_lower](#bookmark806) | 0x00000000 | 0x68C0 |  |
| 1.1.1.804 | [Distributor\_\_37\_GICD\_IROUTER280\_upper](#bookmark807) | 0x00000000 | 0x68C4 |  |
| 1.1.1.805 | [Distributor\_\_37\_GICD\_IROUTER281\_lower](#bookmark808) | 0x00000000 | 0x68C8 |  |
| 1.1.1.806 | [Distributor\_\_37\_GICD\_IROUTER281\_upper](#bookmark809) | 0x00000000 | 0x68CC |  |
| 1.1.1.807 | [Distributor\_\_37\_GICD\_IROUTER282\_lower](#bookmark810) | 0x00000000 | 0x68D0 |  |
| 1.1.1.808 | [Distributor\_\_37\_GICD\_IROUTER282\_upper](#bookmark811) | 0x00000000 | 0x68D4 |  |
| 1.1.1.809 | [Distributor\_\_37\_GICD\_IROUTER283\_lower](#bookmark812) | 0x00000000 | 0x68D8 |  |
| 1.1.1.810 | [Distributor\_\_37\_GICD\_IROUTER283\_upper](#bookmark813) | 0x00000000 | 0x68DC |  |
| 1.1.1.811 | [Distributor\_\_37\_GICD\_IROUTER284\_lower](#bookmark814) | 0x00000000 | 0x68E0 |  |
| 1.1.1.812 | [Distributor\_\_37\_GICD\_IROUTER284\_upper](#bookmark815) | 0x00000000 | 0x68E4 |  |
| 1.1.1.813 | [Distributor\_\_37\_GICD\_IROUTER285\_lower](#bookmark816) | 0x00000000 | 0x68E8 |  |
| 1.1.1.814 | [Distributor\_\_37\_GICD\_IROUTER285\_upper](#bookmark817) | 0x00000000 | 0x68EC |  |
| 1.1.1.815 | [Distributor\_\_37\_GICD\_IROUTER286\_lower](#bookmark818) | 0x00000000 | 0x68F0 |  |
| 1.1.1.816 | [Distributor\_\_37\_GICD\_IROUTER286\_upper](#bookmark819) | 0x00000000 | 0x68F4 |  |
| 1.1.1.817 | [Distributor\_\_37\_GICD\_IROUTER287\_lower](#bookmark820) | 0x00000000 | 0x68F8 |  |
| 1.1.1.818 | [Distributor\_\_37\_GICD\_IROUTER287\_upper](#bookmark821) | 0x00000000 | 0x68FC |  |
| 1.1.1.819 | [Distributor\_\_37\_GICD\_IROUTER288\_lower](#bookmark822) | 0x00000000 | 0x6900 |  |
| 1.1.1.820 | [Distributor\_\_37\_GICD\_IROUTER288\_upper](#bookmark823) | 0x00000000 | 0x6904 |  |
| 1.1.1.821 | [Distributor\_\_37\_GICD\_IROUTER289\_lower](#bookmark824) | 0x00000000 | 0x6908 |  |
| 1.1.1.822 | [Distributor\_\_37\_GICD\_IROUTER289\_upper](#bookmark825) | 0x00000000 | 0x690C |  |
| 1.1.1.823 | [Distributor\_\_37\_GICD\_IROUTER290\_lower](#bookmark826) | 0x00000000 | 0x6910 |  |
| 1.1.1.824 | [Distributor\_\_37\_GICD\_IROUTER290\_upper](#bookmark827) | 0x00000000 | 0x6914 |  |
| 1.1.1.825 | [Distributor\_\_37\_GICD\_IROUTER291\_lower](#bookmark828) | 0x00000000 | 0x6918 |  |
| 1.1.1.826 | [Distributor\_\_37\_GICD\_IROUTER291\_upper](#bookmark829) | 0x00000000 | 0x691C |  |
| 1.1.1.827 | [Distributor\_\_37\_GICD\_IROUTER292\_lower](#bookmark830) | 0x00000000 | 0x6920 |  |
| 1.1.1.828 | [Distributor\_\_37\_GICD\_IROUTER292\_upper](#bookmark831) | 0x00000000 | 0x6924 |  |
| 1.1.1.829 | [Distributor\_\_37\_GICD\_IROUTER293\_lower](#bookmark832) | 0x00000000 | 0x6928 |  |
| 1.1.1.830 | [Distributor\_\_37\_GICD\_IROUTER293\_upper](#bookmark833) | 0x00000000 | 0x692C |  |
| 1.1.1.831 | [Distributor\_\_37\_GICD\_IROUTER294\_lower](#bookmark834) | 0x00000000 | 0x6930 |  |
| 1.1.1.832 | [Distributor\_\_37\_GICD\_IROUTER294\_upper](#bookmark835) | 0x00000000 | 0x6934 |  |
| 1.1.1.833 | [Distributor\_\_37\_GICD\_IROUTER295\_lower](#bookmark836) | 0x00000000 | 0x6938 |  |
| 1.1.1.834 | [Distributor\_\_37\_GICD\_IROUTER295\_upper](#bookmark837) | 0x00000000 | 0x693C |  |
| 1.1.1.835 | [Distributor\_\_37\_GICD\_IROUTER296\_lower](#bookmark838) | 0x00000000 | 0x6940 |  |
| 1.1.1.836 | [Distributor\_\_37\_GICD\_IROUTER296\_upper](#bookmark839) | 0x00000000 | 0x6944 |  |
| 1.1.1.837 | [Distributor\_\_37\_GICD\_IROUTER297\_lower](#bookmark840) | 0x00000000 | 0x6948 |  |
| 1.1.1.838 | [Distributor\_\_37\_GICD\_IROUTER297\_upper](#bookmark841) | 0x00000000 | 0x694C |  |
| 1.1.1.839 | [Distributor\_\_37\_GICD\_IROUTER298\_lower](#bookmark842) | 0x00000000 | 0x6950 |  |
| 1.1.1.840 | [Distributor\_\_37\_GICD\_IROUTER298\_upper](#bookmark843) | 0x00000000 | 0x6954 |  |
| 1.1.1.841 | [Distributor\_\_37\_GICD\_IROUTER299\_lower](#bookmark844) | 0x00000000 | 0x6958 |  |
| 1.1.1.842 | [Distributor\_\_37\_GICD\_IROUTER299\_upper](#bookmark845) | 0x00000000 | 0x695C |  |
| 1.1.1.843 | [Distributor\_\_37\_GICD\_IROUTER300\_lower](#bookmark846) | 0x00000000 | 0x6960 |  |
| 1.1.1.844 | [Distributor\_\_37\_GICD\_IROUTER300\_upper](#bookmark847) | 0x00000000 | 0x6964 |  |
| 1.1.1.845 | [Distributor\_\_37\_GICD\_IROUTER301\_lower](#bookmark848) | 0x00000000 | 0x6968 |  |
| 1.1.1.846 | [Distributor\_\_37\_GICD\_IROUTER301\_upper](#bookmark849) | 0x00000000 | 0x696C |  |
| 1.1.1.847 | [Distributor\_\_37\_GICD\_IROUTER302\_lower](#bookmark850) | 0x00000000 | 0x6970 |  |
| 1.1.1.848 | [Distributor\_\_37\_GICD\_IROUTER302\_upper](#bookmark851) | 0x00000000 | 0x6974 |  |
| 1.1.1.849 | [Distributor\_\_37\_GICD\_IROUTER303\_lower](#bookmark852) | 0x00000000 | 0x6978 |  |
| 1.1.1.850 | [Distributor\_\_37\_GICD\_IROUTER303\_upper](#bookmark853) | 0x00000000 | 0x697C |  |
| 1.1.1.851 | [Distributor\_\_37\_GICD\_IROUTER304\_lower](#bookmark854) | 0x00000000 | 0x6980 |  |
| 1.1.1.852 | [Distributor\_\_37\_GICD\_IROUTER304\_upper](#bookmark855) | 0x00000000 | 0x6984 |  |
| 1.1.1.853 | [Distributor\_\_37\_GICD\_IROUTER305\_lower](#bookmark856) | 0x00000000 | 0x6988 |  |
| 1.1.1.854 | [Distributor\_\_37\_GICD\_IROUTER305\_upper](#bookmark857) | 0x00000000 | 0x698C |  |
| 1.1.1.855 | [Distributor\_\_37\_GICD\_IROUTER306\_lower](#bookmark858) | 0x00000000 | 0x6990 |  |
| 1.1.1.856 | [Distributor\_\_37\_GICD\_IROUTER306\_upper](#bookmark859) | 0x00000000 | 0x6994 |  |
| 1.1.1.857 | [Distributor\_\_37\_GICD\_IROUTER307\_lower](#bookmark860) | 0x00000000 | 0x6998 |  |
| 1.1.1.858 | [Distributor\_\_37\_GICD\_IROUTER307\_upper](#bookmark861) | 0x00000000 | 0x699C |  |
| 1.1.1.859 | [Distributor\_\_37\_GICD\_IROUTER308\_lower](#bookmark862) | 0x00000000 | 0x69A0 |  |
| 1.1.1.860 | [Distributor\_\_37\_GICD\_IROUTER308\_upper](#bookmark863) | 0x00000000 | 0x69A4 |  |
| 1.1.1.861 | [Distributor\_\_37\_GICD\_IROUTER309\_lower](#bookmark864) | 0x00000000 | 0x69A8 |  |
| 1.1.1.862 | [Distributor\_\_37\_GICD\_IROUTER309\_upper](#bookmark865) | 0x00000000 | 0x69AC |  |
| 1.1.1.863 | [Distributor\_\_37\_GICD\_IROUTER310\_lower](#bookmark866) | 0x00000000 | 0x69B0 |  |
| 1.1.1.864 | [Distributor\_\_37\_GICD\_IROUTER310\_upper](#bookmark867) | 0x00000000 | 0x69B4 |  |
| 1.1.1.865 | [Distributor\_\_37\_GICD\_IROUTER311\_lower](#bookmark868) | 0x00000000 | 0x69B8 |  |
| 1.1.1.866 | [Distributor\_\_37\_GICD\_IROUTER311\_upper](#bookmark869) | 0x00000000 | 0x69BC |  |
| 1.1.1.867 | [Distributor\_\_37\_GICD\_IROUTER312\_lower](#bookmark870) | 0x00000000 | 0x69C0 |  |
| 1.1.1.868 | [Distributor\_\_37\_GICD\_IROUTER312\_upper](#bookmark871) | 0x00000000 | 0x69C4 |  |
| 1.1.1.869 | [Distributor\_\_37\_GICD\_IROUTER313\_lower](#bookmark872) | 0x00000000 | 0x69C8 |  |
| 1.1.1.870 | [Distributor\_\_37\_GICD\_IROUTER313\_upper](#bookmark873) | 0x00000000 | 0x69CC |  |
| 1.1.1.871 | [Distributor\_\_37\_GICD\_IROUTER314\_lower](#bookmark874) | 0x00000000 | 0x69D0 |  |
| 1.1.1.872 | [Distributor\_\_37\_GICD\_IROUTER314\_upper](#bookmark875) | 0x00000000 | 0x69D4 |  |
| 1.1.1.873 | [Distributor\_\_37\_GICD\_IROUTER315\_lower](#bookmark876) | 0x00000000 | 0x69D8 |  |
| 1.1.1.874 | [Distributor\_\_37\_GICD\_IROUTER315\_upper](#bookmark877) | 0x00000000 | 0x69DC |  |
| 1.1.1.875 | [Distributor\_\_37\_GICD\_IROUTER316\_lower](#bookmark878) | 0x00000000 | 0x69E0 |  |
| 1.1.1.876 | [Distributor\_\_37\_GICD\_IROUTER316\_upper](#bookmark879) | 0x00000000 | 0x69E4 |  |
| 1.1.1.877 | [Distributor\_\_37\_GICD\_IROUTER317\_lower](#bookmark880) | 0x00000000 | 0x69E8 |  |
| 1.1.1.878 | [Distributor\_\_37\_GICD\_IROUTER317\_upper](#bookmark881) | 0x00000000 | 0x69EC |  |
| 1.1.1.879 | [Distributor\_\_37\_GICD\_IROUTER318\_lower](#bookmark882) | 0x00000000 | 0x69F0 |  |
| 1.1.1.880 | [Distributor\_\_37\_GICD\_IROUTER318\_upper](#bookmark883) | 0x00000000 | 0x69F4 |  |
| 1.1.1.881 | [Distributor\_\_37\_GICD\_IROUTER319\_lower](#bookmark884) | 0x00000000 | 0x69F8 |  |
| 1.1.1.882 | [Distributor\_\_37\_GICD\_IROUTER319\_upper](#bookmark885) | 0x00000000 | 0x69FC |  |
| 1.1.1.883 | [Distributor\_\_37\_GICD\_IROUTER320\_lower](#bookmark886) | 0x00000000 | 0x6A00 |  |
| 1.1.1.884 | [Distributor\_\_37\_GICD\_IROUTER320\_upper](#bookmark887) | 0x00000000 | 0x6A04 |  |
| 1.1.1.885 | [Distributor\_\_37\_GICD\_IROUTER321\_lower](#bookmark888) | 0x00000000 | 0x6A08 |  |
| 1.1.1.886 | [Distributor\_\_37\_GICD\_IROUTER321\_upper](#bookmark889) | 0x00000000 | 0x6A0C |  |
| 1.1.1.887 | [Distributor\_\_37\_GICD\_IROUTER322\_lower](#bookmark890) | 0x00000000 | 0x6A10 |  |
| 1.1.1.888 | [Distributor\_\_37\_GICD\_IROUTER322\_upper](#bookmark891) | 0x00000000 | 0x6A14 |  |
| 1.1.1.889 | [Distributor\_\_37\_GICD\_IROUTER323\_lower](#bookmark892) | 0x00000000 | 0x6A18 |  |
| 1.1.1.890 | [Distributor\_\_37\_GICD\_IROUTER323\_upper](#bookmark893) | 0x00000000 | 0x6A1C |  |
| 1.1.1.891 | [Distributor\_\_37\_GICD\_IROUTER324\_lower](#bookmark894) | 0x00000000 | 0x6A20 |  |
| 1.1.1.892 | [Distributor\_\_37\_GICD\_IROUTER324\_upper](#bookmark895) | 0x00000000 | 0x6A24 |  |
| 1.1.1.893 | [Distributor\_\_37\_GICD\_IROUTER325\_lower](#bookmark896) | 0x00000000 | 0x6A28 |  |
| 1.1.1.894 | [Distributor\_\_37\_GICD\_IROUTER325\_upper](#bookmark897) | 0x00000000 | 0x6A2C |  |
| 1.1.1.895 | [Distributor\_\_37\_GICD\_IROUTER326\_lower](#bookmark898) | 0x00000000 | 0x6A30 |  |
| 1.1.1.896 | [Distributor\_\_37\_GICD\_IROUTER326\_upper](#bookmark899) | 0x00000000 | 0x6A34 |  |
| 1.1.1.897 | [Distributor\_\_37\_GICD\_IROUTER327\_lower](#bookmark900) | 0x00000000 | 0x6A38 |  |
| 1.1.1.898 | [Distributor\_\_37\_GICD\_IROUTER327\_upper](#bookmark901) | 0x00000000 | 0x6A3C |  |
| 1.1.1.899 | [Distributor\_\_37\_GICD\_IROUTER328\_lower](#bookmark902) | 0x00000000 | 0x6A40 |  |
| 1.1.1.900 | [Distributor\_\_37\_GICD\_IROUTER328\_upper](#bookmark903) | 0x00000000 | 0x6A44 |  |
| 1.1.1.901 | [Distributor\_\_37\_GICD\_IROUTER329\_lower](#bookmark904) | 0x00000000 | 0x6A48 |  |
| 1.1.1.902 | [Distributor\_\_37\_GICD\_IROUTER329\_upper](#bookmark905) | 0x00000000 | 0x6A4C |  |
| 1.1.1.903 | [Distributor\_\_37\_GICD\_IROUTER330\_lower](#bookmark906) | 0x00000000 | 0x6A50 |  |
| 1.1.1.904 | [Distributor\_\_37\_GICD\_IROUTER330\_upper](#bookmark907) | 0x00000000 | 0x6A54 |  |
| 1.1.1.905 | [Distributor\_\_37\_GICD\_IROUTER331\_lower](#bookmark908) | 0x00000000 | 0x6A58 |  |
| 1.1.1.906 | [Distributor\_\_37\_GICD\_IROUTER331\_upper](#bookmark909) | 0x00000000 | 0x6A5C |  |
| 1.1.1.907 | [Distributor\_\_37\_GICD\_IROUTER332\_lower](#bookmark910) | 0x00000000 | 0x6A60 |  |
| 1.1.1.908 | [Distributor\_\_37\_GICD\_IROUTER332\_upper](#bookmark911) | 0x00000000 | 0x6A64 |  |
| 1.1.1.909 | [Distributor\_\_37\_GICD\_IROUTER333\_lower](#bookmark912) | 0x00000000 | 0x6A68 |  |
| 1.1.1.910 | [Distributor\_\_37\_GICD\_IROUTER333\_upper](#bookmark913) | 0x00000000 | 0x6A6C |  |
| 1.1.1.911 | [Distributor\_\_37\_GICD\_IROUTER334\_lower](#bookmark914) | 0x00000000 | 0x6A70 |  |
| 1.1.1.912 | [Distributor\_\_37\_GICD\_IROUTER334\_upper](#bookmark915) | 0x00000000 | 0x6A74 |  |
| 1.1.1.913 | [Distributor\_\_37\_GICD\_IROUTER335\_lower](#bookmark916) | 0x00000000 | 0x6A78 |  |
| 1.1.1.914 | [Distributor\_\_37\_GICD\_IROUTER335\_upper](#bookmark917) | 0x00000000 | 0x6A7C |  |
| 1.1.1.915 | [Distributor\_\_37\_GICD\_IROUTER336\_lower](#bookmark918) | 0x00000000 | 0x6A80 |  |
| 1.1.1.916 | [Distributor\_\_37\_GICD\_IROUTER336\_upper](#bookmark919) | 0x00000000 | 0x6A84 |  |
| 1.1.1.917 | [Distributor\_\_37\_GICD\_IROUTER337\_lower](#bookmark920) | 0x00000000 | 0x6A88 |  |
| 1.1.1.918 | [Distributor\_\_37\_GICD\_IROUTER337\_upper](#bookmark921) | 0x00000000 | 0x6A8C |  |
| 1.1.1.919 | [Distributor\_\_37\_GICD\_IROUTER338\_lower](#bookmark922) | 0x00000000 | 0x6A90 |  |
| 1.1.1.920 | [Distributor\_\_37\_GICD\_IROUTER338\_upper](#bookmark923) | 0x00000000 | 0x6A94 |  |
| 1.1.1.921 | [Distributor\_\_37\_GICD\_IROUTER339\_lower](#bookmark924) | 0x00000000 | 0x6A98 |  |
| 1.1.1.922 | [Distributor\_\_37\_GICD\_IROUTER339\_upper](#bookmark925) | 0x00000000 | 0x6A9C |  |
| 1.1.1.923 | [Distributor\_\_37\_GICD\_IROUTER340\_lower](#bookmark926) | 0x00000000 | 0x6AA0 |  |
| 1.1.1.924 | [Distributor\_\_37\_GICD\_IROUTER340\_upper](#bookmark927) | 0x00000000 | 0x6AA4 |  |
| 1.1.1.925 | [Distributor\_\_37\_GICD\_IROUTER341\_lower](#bookmark928) | 0x00000000 | 0x6AA8 |  |
| 1.1.1.926 | [Distributor\_\_37\_GICD\_IROUTER341\_upper](#bookmark929) | 0x00000000 | 0x6AAC |  |
| 1.1.1.927 | [Distributor\_\_37\_GICD\_IROUTER342\_lower](#bookmark930) | 0x00000000 | 0x6AB0 |  |
| 1.1.1.928 | [Distributor\_\_37\_GICD\_IROUTER342\_upper](#bookmark931) | 0x00000000 | 0x6AB4 |  |
| 1.1.1.929 | [Distributor\_\_37\_GICD\_IROUTER343\_lower](#bookmark932) | 0x00000000 | 0x6AB8 |  |
| 1.1.1.930 | [Distributor\_\_37\_GICD\_IROUTER343\_upper](#bookmark933) | 0x00000000 | 0x6ABC |  |
| 1.1.1.931 | [Distributor\_\_37\_GICD\_IROUTER344\_lower](#bookmark934) | 0x00000000 | 0x6AC0 |  |
| 1.1.1.932 | [Distributor\_\_37\_GICD\_IROUTER344\_upper](#bookmark935) | 0x00000000 | 0x6AC4 |  |
| 1.1.1.933 | [Distributor\_\_37\_GICD\_IROUTER345\_lower](#bookmark936) | 0x00000000 | 0x6AC8 |  |
| 1.1.1.934 | [Distributor\_\_37\_GICD\_IROUTER345\_upper](#bookmark937) | 0x00000000 | 0x6ACC |  |
| 1.1.1.935 | [Distributor\_\_37\_GICD\_IROUTER346\_lower](#bookmark938) | 0x00000000 | 0x6AD0 |  |
| 1.1.1.936 | [Distributor\_\_37\_GICD\_IROUTER346\_upper](#bookmark939) | 0x00000000 | 0x6AD4 |  |
| 1.1.1.937 | [Distributor\_\_37\_GICD\_IROUTER347\_lower](#bookmark940) | 0x00000000 | 0x6AD8 |  |
| 1.1.1.938 | [Distributor\_\_37\_GICD\_IROUTER347\_upper](#bookmark941) | 0x00000000 | 0x6ADC |  |
| 1.1.1.939 | [Distributor\_\_37\_GICD\_IROUTER348\_lower](#bookmark942) | 0x00000000 | 0x6AE0 |  |
| 1.1.1.940 | [Distributor\_\_37\_GICD\_IROUTER348\_upper](#bookmark943) | 0x00000000 | 0x6AE4 |  |
| 1.1.1.941 | [Distributor\_\_37\_GICD\_IROUTER349\_lower](#bookmark944) | 0x00000000 | 0x6AE8 |  |
| 1.1.1.942 | [Distributor\_\_37\_GICD\_IROUTER349\_upper](#bookmark945) | 0x00000000 | 0x6AEC |  |
| 1.1.1.943 | [Distributor\_\_37\_GICD\_IROUTER350\_lower](#bookmark946) | 0x00000000 | 0x6AF0 |  |
| 1.1.1.944 | [Distributor\_\_37\_GICD\_IROUTER350\_upper](#bookmark947) | 0x00000000 | 0x6AF4 |  |
| 1.1.1.945 | [Distributor\_\_37\_GICD\_IROUTER351\_lower](#bookmark948) | 0x00000000 | 0x6AF8 |  |
| 1.1.1.946 | [Distributor\_\_37\_GICD\_IROUTER351\_upper](#bookmark949) | 0x00000000 | 0x6AFC |  |
| 1.1.1.947 | [Distributor\_\_37\_GICD\_IROUTER352\_lower](#bookmark950) | 0x00000000 | 0x6B00 |  |
| 1.1.1.948 | [Distributor\_\_37\_GICD\_IROUTER352\_upper](#bookmark951) | 0x00000000 | 0x6B04 |  |
| 1.1.1.949 | [Distributor\_\_37\_GICD\_IROUTER353\_lower](#bookmark952) | 0x00000000 | 0x6B08 |  |
| 1.1.1.950 | [Distributor\_\_37\_GICD\_IROUTER353\_upper](#bookmark953) | 0x00000000 | 0x6B0C |  |
| 1.1.1.951 | [Distributor\_\_37\_GICD\_IROUTER354\_lower](#bookmark954) | 0x00000000 | 0x6B10 |  |
| 1.1.1.952 | [Distributor\_\_37\_GICD\_IROUTER354\_upper](#bookmark955) | 0x00000000 | 0x6B14 |  |
| 1.1.1.953 | [Distributor\_\_37\_GICD\_IROUTER355\_lower](#bookmark956) | 0x00000000 | 0x6B18 |  |
| 1.1.1.954 | [Distributor\_\_37\_GICD\_IROUTER355\_upper](#bookmark957) | 0x00000000 | 0x6B1C |  |
| 1.1.1.955 | [Distributor\_\_37\_GICD\_IROUTER356\_lower](#bookmark958) | 0x00000000 | 0x6B20 |  |
| 1.1.1.956 | [Distributor\_\_37\_GICD\_IROUTER356\_upper](#bookmark959) | 0x00000000 | 0x6B24 |  |
| 1.1.1.957 | [Distributor\_\_37\_GICD\_IROUTER357\_lower](#bookmark960) | 0x00000000 | 0x6B28 |  |
| 1.1.1.958 | [Distributor\_\_37\_GICD\_IROUTER357\_upper](#bookmark961) | 0x00000000 | 0x6B2C |  |
| 1.1.1.959 | [Distributor\_\_37\_GICD\_IROUTER358\_lower](#bookmark962) | 0x00000000 | 0x6B30 |  |
| 1.1.1.960 | [Distributor\_\_37\_GICD\_IROUTER358\_upper](#bookmark963) | 0x00000000 | 0x6B34 |  |
| 1.1.1.961 | [Distributor\_\_37\_GICD\_IROUTER359\_lower](#bookmark964) | 0x00000000 | 0x6B38 |  |
| 1.1.1.962 | [Distributor\_\_37\_GICD\_IROUTER359\_upper](#bookmark965) | 0x00000000 | 0x6B3C |  |
| 1.1.1.963 | [Distributor\_\_37\_GICD\_IROUTER360\_lower](#bookmark966) | 0x00000000 | 0x6B40 |  |
| 1.1.1.964 | [Distributor\_\_37\_GICD\_IROUTER360\_upper](#bookmark967) | 0x00000000 | 0x6B44 |  |
| 1.1.1.965 | [Distributor\_\_37\_GICD\_IROUTER361\_lower](#bookmark968) | 0x00000000 | 0x6B48 |  |
| 1.1.1.966 | [Distributor\_\_37\_GICD\_IROUTER361\_upper](#bookmark969) | 0x00000000 | 0x6B4C |  |
| 1.1.1.967 | [Distributor\_\_37\_GICD\_IROUTER362\_lower](#bookmark970) | 0x00000000 | 0x6B50 |  |
| 1.1.1.968 | [Distributor\_\_37\_GICD\_IROUTER362\_upper](#bookmark971) | 0x00000000 | 0x6B54 |  |
| 1.1.1.969 | [Distributor\_\_37\_GICD\_IROUTER363\_lower](#bookmark972) | 0x00000000 | 0x6B58 |  |
| 1.1.1.970 | [Distributor\_\_37\_GICD\_IROUTER363\_upper](#bookmark973) | 0x00000000 | 0x6B5C |  |
| 1.1.1.971 | [Distributor\_\_37\_GICD\_IROUTER364\_lower](#bookmark974) | 0x00000000 | 0x6B60 |  |
| 1.1.1.972 | [Distributor\_\_37\_GICD\_IROUTER364\_upper](#bookmark975) | 0x00000000 | 0x6B64 |  |
| 1.1.1.973 | [Distributor\_\_37\_GICD\_IROUTER365\_lower](#bookmark976) | 0x00000000 | 0x6B68 |  |
| 1.1.1.974 | [Distributor\_\_37\_GICD\_IROUTER365\_upper](#bookmark977) | 0x00000000 | 0x6B6C |  |
| 1.1.1.975 | [Distributor\_\_37\_GICD\_IROUTER366\_lower](#bookmark978) | 0x00000000 | 0x6B70 |  |
| 1.1.1.976 | [Distributor\_\_37\_GICD\_IROUTER366\_upper](#bookmark979) | 0x00000000 | 0x6B74 |  |
| 1.1.1.977 | [Distributor\_\_37\_GICD\_IROUTER367\_lower](#bookmark980) | 0x00000000 | 0x6B78 |  |
| 1.1.1.978 | [Distributor\_\_37\_GICD\_IROUTER367\_upper](#bookmark981) | 0x00000000 | 0x6B7C |  |
| 1.1.1.979 | [Distributor\_\_37\_GICD\_IROUTER368\_lower](#bookmark982) | 0x00000000 | 0x6B80 |  |
| 1.1.1.980 | [Distributor\_\_37\_GICD\_IROUTER368\_upper](#bookmark983) | 0x00000000 | 0x6B84 |  |
| 1.1.1.981 | [Distributor\_\_37\_GICD\_IROUTER369\_lower](#bookmark984) | 0x00000000 | 0x6B88 |  |
| 1.1.1.982 | [Distributor\_\_37\_GICD\_IROUTER369\_upper](#bookmark985) | 0x00000000 | 0x6B8C |  |
| 1.1.1.983 | [Distributor\_\_37\_GICD\_IROUTER370\_lower](#bookmark986) | 0x00000000 | 0x6B90 |  |
| 1.1.1.984 | [Distributor\_\_37\_GICD\_IROUTER370\_upper](#bookmark987) | 0x00000000 | 0x6B94 |  |
| 1.1.1.985 | [Distributor\_\_37\_GICD\_IROUTER371\_lower](#bookmark988) | 0x00000000 | 0x6B98 |  |
| 1.1.1.986 | [Distributor\_\_37\_GICD\_IROUTER371\_upper](#bookmark989) | 0x00000000 | 0x6B9C |  |
| 1.1.1.987 | [Distributor\_\_37\_GICD\_IROUTER372\_lower](#bookmark990) | 0x00000000 | 0x6BA0 |  |
| 1.1.1.988 | [Distributor\_\_37\_GICD\_IROUTER372\_upper](#bookmark991) | 0x00000000 | 0x6BA4 |  |
| 1.1.1.989 | [Distributor\_\_37\_GICD\_IROUTER373\_lower](#bookmark992) | 0x00000000 | 0x6BA8 |  |
| 1.1.1.990 | [Distributor\_\_37\_GICD\_IROUTER373\_upper](#bookmark993) | 0x00000000 | 0x6BAC |  |
| 1.1.1.991 | [Distributor\_\_37\_GICD\_IROUTER374\_lower](#bookmark994) | 0x00000000 | 0x6BB0 |  |
| 1.1.1.992 | [Distributor\_\_37\_GICD\_IROUTER374\_upper](#bookmark995) | 0x00000000 | 0x6BB4 |  |
| 1.1.1.993 | [Distributor\_\_37\_GICD\_IROUTER375\_lower](#bookmark996) | 0x00000000 | 0x6BB8 |  |
| 1.1.1.994 | [Distributor\_\_37\_GICD\_IROUTER375\_upper](#bookmark997) | 0x00000000 | 0x6BBC |  |
| 1.1.1.995 | [Distributor\_\_37\_GICD\_IROUTER376\_lower](#bookmark998) | 0x00000000 | 0x6BC0 |  |
| 1.1.1.996 | [Distributor\_\_37\_GICD\_IROUTER376\_upper](#bookmark999) | 0x00000000 | 0x6BC4 |  |
| 1.1.1.997 | [Distributor\_\_37\_GICD\_IROUTER377\_lower](#bookmark1000) | 0x00000000 | 0x6BC8 |  |
| 1.1.1.998 | [Distributor\_\_37\_GICD\_IROUTER377\_upper](#bookmark1001) | 0x00000000 | 0x6BCC |  |
| 1.1.1.999 | [Distributor\_\_37\_GICD\_IROUTER378\_lower](#bookmark1002) | 0x00000000 | 0x6BD0 |  |
| 1.1.1.1000 | [Distributor\_\_37\_GICD\_IROUTER378\_upper](#bookmark1003) | 0x00000000 | 0x6BD4 |  |
| 1.1.1.1001 | [Distributor\_\_37\_GICD\_IROUTER379\_lower](#bookmark1004) | 0x00000000 | 0x6BD8 |  |
| 1.1.1.1002 | [Distributor\_\_37\_GICD\_IROUTER379\_upper](#bookmark1005) | 0x00000000 | 0x6BDC |  |
| 1.1.1.1003 | [Distributor\_\_37\_GICD\_IROUTER380\_lower](#bookmark1006) | 0x00000000 | 0x6BE0 |  |
| 1.1.1.1004 | [Distributor\_\_37\_GICD\_IROUTER380\_upper](#bookmark1007) | 0x00000000 | 0x6BE4 |  |
| 1.1.1.1005 | [Distributor\_\_37\_GICD\_IROUTER381\_lower](#bookmark1008) | 0x00000000 | 0x6BE8 |  |
| 1.1.1.1006 | [Distributor\_\_37\_GICD\_IROUTER381\_upper](#bookmark1009) | 0x00000000 | 0x6BEC |  |
| 1.1.1.1007 | [Distributor\_\_37\_GICD\_IROUTER382\_lower](#bookmark1010) | 0x00000000 | 0x6BF0 |  |
| 1.1.1.1008 | [Distributor\_\_37\_GICD\_IROUTER382\_upper](#bookmark1011) | 0x00000000 | 0x6BF4 |  |
| 1.1.1.1009 | [Distributor\_\_37\_GICD\_IROUTER383\_lower](#bookmark1012) | 0x00000000 | 0x6BF8 |  |
| 1.1.1.1010 | [Distributor\_\_37\_GICD\_IROUTER383\_upper](#bookmark1013) | 0x00000000 | 0x6BFC |  |
| 1.1.1.1011 | [Distributor\_\_37\_GICD\_IROUTER384\_lower](#bookmark1014) | 0x00000000 | 0x6C00 |  |
| 1.1.1.1012 | [Distributor\_\_37\_GICD\_IROUTER384\_upper](#bookmark1015) | 0x00000000 | 0x6C04 |  |
| 1.1.1.1013 | [Distributor\_\_37\_GICD\_IROUTER385\_lower](#bookmark1016) | 0x00000000 | 0x6C08 |  |
| 1.1.1.1014 | [Distributor\_\_37\_GICD\_IROUTER385\_upper](#bookmark1017) | 0x00000000 | 0x6C0C |  |
| 1.1.1.1015 | [Distributor\_\_37\_GICD\_IROUTER386\_lower](#bookmark1018) | 0x00000000 | 0x6C10 |  |
| 1.1.1.1016 | [Distributor\_\_37\_GICD\_IROUTER386\_upper](#bookmark1019) | 0x00000000 | 0x6C14 |  |
| 1.1.1.1017 | [Distributor\_\_37\_GICD\_IROUTER387\_lower](#bookmark1020) | 0x00000000 | 0x6C18 |  |
| 1.1.1.1018 | [Distributor\_\_37\_GICD\_IROUTER387\_upper](#bookmark1021) | 0x00000000 | 0x6C1C |  |
| 1.1.1.1019 | [Distributor\_\_37\_GICD\_IROUTER388\_lower](#bookmark1022) | 0x00000000 | 0x6C20 |  |
| 1.1.1.1020 | [Distributor\_\_37\_GICD\_IROUTER388\_upper](#bookmark1023) | 0x00000000 | 0x6C24 |  |
| 1.1.1.1021 | [Distributor\_\_37\_GICD\_IROUTER389\_lower](#bookmark1024) | 0x00000000 | 0x6C28 |  |
| 1.1.1.1022 | [Distributor\_\_37\_GICD\_IROUTER389\_upper](#bookmark1025) | 0x00000000 | 0x6C2C |  |
| 1.1.1.1023 | [Distributor\_\_37\_GICD\_IROUTER390\_lower](#bookmark1026) | 0x00000000 | 0x6C30 |  |
| 1.1.1.1024 | [Distributor\_\_37\_GICD\_IROUTER390\_upper](#bookmark1027) | 0x00000000 | 0x6C34 |  |
| 1.1.1.1025 | [Distributor\_\_37\_GICD\_IROUTER391\_lower](#bookmark1028) | 0x00000000 | 0x6C38 |  |
| 1.1.1.1026 | [Distributor\_\_37\_GICD\_IROUTER391\_upper](#bookmark1029) | 0x00000000 | 0x6C3C |  |
| 1.1.1.1027 | [Distributor\_\_37\_GICD\_IROUTER392\_lower](#bookmark1030) | 0x00000000 | 0x6C40 |  |
| 1.1.1.1028 | [Distributor\_\_37\_GICD\_IROUTER392\_upper](#bookmark1031) | 0x00000000 | 0x6C44 |  |
| 1.1.1.1029 | [Distributor\_\_37\_GICD\_IROUTER393\_lower](#bookmark1032) | 0x00000000 | 0x6C48 |  |
| 1.1.1.1030 | [Distributor\_\_37\_GICD\_IROUTER393\_upper](#bookmark1033) | 0x00000000 | 0x6C4C |  |
| 1.1.1.1031 | [Distributor\_\_37\_GICD\_IROUTER394\_lower](#bookmark1034) | 0x00000000 | 0x6C50 |  |
| 1.1.1.1032 | [Distributor\_\_37\_GICD\_IROUTER394\_upper](#bookmark1035) | 0x00000000 | 0x6C54 |  |
| 1.1.1.1033 | [Distributor\_\_37\_GICD\_IROUTER395\_lower](#bookmark1036) | 0x00000000 | 0x6C58 |  |
| 1.1.1.1034 | [Distributor\_\_37\_GICD\_IROUTER395\_upper](#bookmark1037) | 0x00000000 | 0x6C5C |  |
| 1.1.1.1035 | [Distributor\_\_37\_GICD\_IROUTER396\_lower](#bookmark1038) | 0x00000000 | 0x6C60 |  |
| 1.1.1.1036 | [Distributor\_\_37\_GICD\_IROUTER396\_upper](#bookmark1039) | 0x00000000 | 0x6C64 |  |
| 1.1.1.1037 | [Distributor\_\_37\_GICD\_IROUTER397\_lower](#bookmark1040) | 0x00000000 | 0x6C68 |  |
| 1.1.1.1038 | [Distributor\_\_37\_GICD\_IROUTER397\_upper](#bookmark1041) | 0x00000000 | 0x6C6C |  |
| 1.1.1.1039 | [Distributor\_\_37\_GICD\_IROUTER398\_lower](#bookmark1042) | 0x00000000 | 0x6C70 |  |
| 1.1.1.1040 | [Distributor\_\_37\_GICD\_IROUTER398\_upper](#bookmark1043) | 0x00000000 | 0x6C74 |  |
| 1.1.1.1041 | [Distributor\_\_37\_GICD\_IROUTER399\_lower](#bookmark1044) | 0x00000000 | 0x6C78 |  |
| 1.1.1.1042 | [Distributor\_\_37\_GICD\_IROUTER399\_upper](#bookmark1045) | 0x00000000 | 0x6C7C |  |
| 1.1.1.1043 | [Distributor\_\_37\_GICD\_IROUTER400\_lower](#bookmark1046) | 0x00000000 | 0x6C80 |  |
| 1.1.1.1044 | [Distributor\_\_37\_GICD\_IROUTER400\_upper](#bookmark1047) | 0x00000000 | 0x6C84 |  |
| 1.1.1.1045 | [Distributor\_\_37\_GICD\_IROUTER401\_lower](#bookmark1048) | 0x00000000 | 0x6C88 |  |
| 1.1.1.1046 | [Distributor\_\_37\_GICD\_IROUTER401\_upper](#bookmark1049) | 0x00000000 | 0x6C8C |  |
| 1.1.1.1047 | [Distributor\_\_37\_GICD\_IROUTER402\_lower](#bookmark1050) | 0x00000000 | 0x6C90 |  |
| 1.1.1.1048 | [Distributor\_\_37\_GICD\_IROUTER402\_upper](#bookmark1051) | 0x00000000 | 0x6C94 |  |
| 1.1.1.1049 | [Distributor\_\_37\_GICD\_IROUTER403\_lower](#bookmark1052) | 0x00000000 | 0x6C98 |  |
| 1.1.1.1050 | [Distributor\_\_37\_GICD\_IROUTER403\_upper](#bookmark1053) | 0x00000000 | 0x6C9C |  |
| 1.1.1.1051 | [Distributor\_\_37\_GICD\_IROUTER404\_lower](#bookmark1054) | 0x00000000 | 0x6CA0 |  |
| 1.1.1.1052 | [Distributor\_\_37\_GICD\_IROUTER404\_upper](#bookmark1055) | 0x00000000 | 0x6CA4 |  |
| 1.1.1.1053 | [Distributor\_\_37\_GICD\_IROUTER405\_lower](#bookmark1056) | 0x00000000 | 0x6CA8 |  |
| 1.1.1.1054 | [Distributor\_\_37\_GICD\_IROUTER405\_upper](#bookmark1057) | 0x00000000 | 0x6CAC |  |
| 1.1.1.1055 | [Distributor\_\_37\_GICD\_IROUTER406\_lower](#bookmark1058) | 0x00000000 | 0x6CB0 |  |
| 1.1.1.1056 | [Distributor\_\_37\_GICD\_IROUTER406\_upper](#bookmark1059) | 0x00000000 | 0x6CB4 |  |
| 1.1.1.1057 | [Distributor\_\_37\_GICD\_IROUTER407\_lower](#bookmark1060) | 0x00000000 | 0x6CB8 |  |
| 1.1.1.1058 | [Distributor\_\_37\_GICD\_IROUTER407\_upper](#bookmark1061) | 0x00000000 | 0x6CBC |  |
| 1.1.1.1059 | [Distributor\_\_37\_GICD\_IROUTER408\_lower](#bookmark1062) | 0x00000000 | 0x6CC0 |  |
| 1.1.1.1060 | [Distributor\_\_37\_GICD\_IROUTER408\_upper](#bookmark1063) | 0x00000000 | 0x6CC4 |  |
| 1.1.1.1061 | [Distributor\_\_37\_GICD\_IROUTER409\_lower](#bookmark1064) | 0x00000000 | 0x6CC8 |  |
| 1.1.1.1062 | [Distributor\_\_37\_GICD\_IROUTER409\_upper](#bookmark1065) | 0x00000000 | 0x6CCC |  |
| 1.1.1.1063 | [Distributor\_\_37\_GICD\_IROUTER410\_lower](#bookmark1066) | 0x00000000 | 0x6CD0 |  |
| 1.1.1.1064 | [Distributor\_\_37\_GICD\_IROUTER410\_upper](#bookmark1067) | 0x00000000 | 0x6CD4 |  |
| 1.1.1.1065 | [Distributor\_\_37\_GICD\_IROUTER411\_lower](#bookmark1068) | 0x00000000 | 0x6CD8 |  |
| 1.1.1.1066 | [Distributor\_\_37\_GICD\_IROUTER411\_upper](#bookmark1069) | 0x00000000 | 0x6CDC |  |
| 1.1.1.1067 | [Distributor\_\_37\_GICD\_IROUTER412\_lower](#bookmark1070) | 0x00000000 | 0x6CE0 |  |
| 1.1.1.1068 | [Distributor\_\_37\_GICD\_IROUTER412\_upper](#bookmark1071) | 0x00000000 | 0x6CE4 |  |
| 1.1.1.1069 | [Distributor\_\_37\_GICD\_IROUTER413\_lower](#bookmark1072) | 0x00000000 | 0x6CE8 |  |
| 1.1.1.1070 | [Distributor\_\_37\_GICD\_IROUTER413\_upper](#bookmark1073) | 0x00000000 | 0x6CEC |  |
| 1.1.1.1071 | [Distributor\_\_37\_GICD\_IROUTER414\_lower](#bookmark1074) | 0x00000000 | 0x6CF0 |  |
| 1.1.1.1072 | [Distributor\_\_37\_GICD\_IROUTER414\_upper](#bookmark1075) | 0x00000000 | 0x6CF4 |  |
| 1.1.1.1073 | [Distributor\_\_37\_GICD\_IROUTER415\_lower](#bookmark1076) | 0x00000000 | 0x6CF8 |  |
| 1.1.1.1074 | [Distributor\_\_37\_GICD\_IROUTER415\_upper](#bookmark1077) | 0x00000000 | 0x6CFC |  |
|  |  |  | 0x6D00 - 0xBFFF |  |
| 1.1.1.1075 | [Distributor\_\_38\_GICD\_ESTATUSR](#bookmark1078) | 0x00000000 | 0xC000 |  |
| 1.1.1.1076 | [Distributor\_\_39\_GICD\_ERRTESTR](#bookmark1079) | 0x00000000 | 0xC004 |  |
|  |  |  | 0xC008 - 0xC083 |  |
| 1.1.1.1077 | [Distributor\_\_40\_GICD\_SPISR0](#bookmark1080) | 0x00000000 | 0xC084 |  |
| 1.1.1.1078 | [Distributor\_\_40\_GICD\_SPISR1](#bookmark1081) | 0x00000000 | 0xC088 |  |
| 1.1.1.1079 | [Distributor\_\_40\_GICD\_SPISR2](#bookmark1082) | 0x00000000 | 0xC08C |  |
| 1.1.1.1080 | [Distributor\_\_40\_GICD\_SPISR3](#bookmark1083) | 0x00000000 | 0xC090 |  |
| 1.1.1.1081 | [Distributor\_\_40\_GICD\_SPISR4](#bookmark1084) | 0x00000000 | 0xC094 |  |
| 1.1.1.1082 | [Distributor\_\_40\_GICD\_SPISR5](#bookmark1085) | 0x00000000 | 0xC098 |  |
| 1.1.1.1083 | [Distributor\_\_40\_GICD\_SPISR6](#bookmark1086) | 0x00000000 | 0xC09C |  |
| 1.1.1.1084 | [Distributor\_\_40\_GICD\_SPISR7](#bookmark1087) | 0x00000000 | 0xC0A0 |  |
| 1.1.1.1085 | [Distributor\_\_40\_GICD\_SPISR8](#bookmark1088) | 0x00000000 | 0xC0A4 |  |
| 1.1.1.1086 | [Distributor\_\_40\_GICD\_SPISR9](#bookmark1089) | 0x00000000 | 0xC0A8 |  |
| 1.1.1.1087 | [Distributor\_\_40\_GICD\_SPISR10](#bookmark1090) | 0x00000000 | 0xC0AC |  |
| 1.1.1.1088 | [Distributor\_\_40\_GICD\_SPISR11](#bookmark1091) | 0x00000000 | 0xC0B0 |  |
|  |  |  | 0xC0B4 - 0xFFCF |  |
| 1.1.1.1089 | [Distributor\_\_41\_GICD\_PIDR4](#bookmark1092) | 0x00000044 | 0xFFD0 |  |
| 1.1.1.1090 | [Distributor\_\_42\_GICD\_PIDR5](#bookmark1093) | 0x00000000 | 0xFFD4 |  |
| 1.1.1.1091 | [Distributor\_\_43\_GICD\_PIDR6](#bookmark1094) | 0x00000000 | 0xFFD8 |  |
| 1.1.1.1092 | [Distributor\_\_44\_GICD\_PIDR7](#bookmark1095) | 0x00000000 | 0xFFDC |  |
| 1.1.1.1093 | [Distributor\_\_45\_GICD\_PIDR0](#bookmark1096) | 0x00000092 | 0xFFE0 |  |
| 1.1.1.1094 | [Distributor\_\_46\_GICD\_PIDR1](#bookmark1097) | 0x000000B4 | 0xFFE4 |  |
| 1.1.1.1095 | [Distributor\_\_47\_GICD\_PIDR2](#bookmark1098) | 0x0000003B | 0xFFE8 |  |
| 1.1.1.1096 | [Distributor\_\_48\_GICD\_PIDR3](#bookmark1099) | 0x00000000 | 0xFFEC |  |
| 1.1.1.1097 | [Distributor\_\_49\_GICD\_CIDR0](#bookmark1100) | 0x0000000D | 0xFFF0 |  |
| 1.1.1.1098 | [Distributor\_\_50\_GICD\_CIDR1](#bookmark1101) | 0x000000F0 | 0xFFF4 |  |
| 1.1.1.1099 | [Distributor\_\_51\_GICD\_CIDR2](#bookmark1102) | 0x00000005 | 0xFFF8 |  |
| 1.1.1.1100 | [Distributor\_\_52\_GICD\_CIDR3](#bookmark1103) | 0x000000B1 | 0xFFFC |  |
| 1.1.2 | [Message\_based\_SPIs](#bookmark1104) |  | 0x10000 - 0x1004B |  |
|  |  |  | 0x10000 - 0x1003F |  |
| 1.1.2.1 | [Message\_based\_SPIs\_\_1\_GICD\_SETSPI\_NSR](#bookmark1105) | 0x00000000 | 0x10040 |  |
|  |  |  | 0x10044 - 0x10047 |  |
| 1.1.2.2 | [Message\_based\_SPIs\_\_2\_GICD\_CLRSPI\_NSR](#bookmark1106) | 0x00000000 | 0x10048 |  |
|  |  |  | 0x1004C - 0x1FFFF |  |
| 1.1.3 | [ITS](#bookmark1107) |  | 0x20000 - 0x2FFFF |  |
| 1.1.3.1 | [ITS\_\_1\_GITS\_CTLR](#bookmark1108) | 0x80000000 | 0x20000 |  |
| 1.1.3.2 | [ITS\_\_2\_GITS\_IIDR](#bookmark1109) | 0x0001143B | 0x20004 |  |
| 1.1.3.3 | [ITS\_\_3\_GITS\_TYPER\_lower](#bookmark1110) | 0x84021041 | 0x20008 |  |
| 1.1.3.4 | [ITS\_\_4\_GITS\_TYPER\_upper](#bookmark1111) | 0x00000000 | 0x2000C |  |
|  |  |  | 0x20010 - 0x2007F |  |
| 1.1.3.5 | [ITS\_\_5\_GITS\_CBASER\_lower](#bookmark1112) | 0x00000000 | 0x20080 |  |
| 1.1.3.6 | [ITS\_\_6\_GITS\_CBASER\_upper](#bookmark1113) | 0x00000000 | 0x20084 |  |
| 1.1.3.7 | [ITS\_\_7\_GITS\_CWRITER\_lower](#bookmark1114) | 0x00000000 | 0x20088 |  |
| 1.1.3.8 | [ITS\_\_8\_GITS\_CWRITER\_upper](#bookmark1115) | 0x00000000 | 0x2008C |  |
| 1.1.3.9 | [ITS\_\_9\_GITS\_CREADR\_lower](#bookmark1116) | 0x00000000 | 0x20090 |  |
| 1.1.3.10 | [ITS\_\_10\_GITS\_CREADR\_upper](#bookmark1117) | 0x00000000 | 0x20094 |  |
|  |  |  | 0x20098 - 0x200FF |  |
| 1.1.3.11 | [ITS\_\_11\_GITS\_BASER0\_lower](#bookmark1118) | 0x00000000 | 0x20100 |  |
| 1.1.3.12 | [ITS\_\_12\_GITS\_BASER0\_upper](#bookmark1119) | 0x01070000 | 0x20104 |  |
|  |  |  | 0x20108 - 0x2BFFF |  |
| 1.1.3.13 | [ITS\_\_13\_GITS\_TRKCTLR](#bookmark1120) | 0x00000000 | 0x2C000 |  |
| 1.1.3.14 | [ITS\_\_14\_GITS\_TRKR](#bookmark1121) | 0x00000000 | 0x2C004 |  |
| 1.1.3.15 | [ITS\_\_15\_GITS\_TRKDIDR](#bookmark1122) | 0x00000000 | 0x2C008 |  |
| 1.1.3.16 | [ITS\_\_16\_GITS\_TRKPIDR](#bookmark1123) | 0x00000000 | 0x2C00C |  |
| 1.1.3.17 | [ITS\_\_17\_GITS\_TRKVIDR](#bookmark1124) | 0x00000000 | 0x2C010 |  |
| 1.1.3.18 | [ITS\_\_18\_GITS\_TRKTGTR](#bookmark1125) | 0x00000000 | 0x2C014 |  |
| 1.1.3.19 | [ITS\_\_19\_GITS\_TRKICR](#bookmark1126) | 0x00000000 | 0x2C018 |  |
| 1.1.3.20 | [ITS\_\_20\_GITS\_TRKLCR](#bookmark1127) | 0x00000000 | 0x2C01C |  |
|  |  |  | 0x2C020 - 0x2FFCF |  |
| 1.1.3.21 | [ITS\_\_21\_GITS\_PIDR4](#bookmark1128) | 0x00000044 | 0x2FFD0 |  |
| 1.1.3.22 | [ITS\_\_22\_GITS\_PIDR5](#bookmark1129) | 0x00000000 | 0x2FFD4 |  |
| 1.1.3.23 | [ITS\_\_23\_GITS\_PIDR6](#bookmark1130) | 0x00000000 | 0x2FFD8 |  |
| 1.1.3.24 | [ITS\_\_24\_GITS\_PIDR7](#bookmark1131) | 0x00000000 | 0x2FFDC |  |
| 1.1.3.25 | [ITS\_\_25\_GITS\_PIDR0](#bookmark1132) | 0x00000094 | 0x2FFE0 |  |
| 1.1.3.26 | [ITS\_\_26\_GITS\_PIDR1](#bookmark1133) | 0x000000B4 | 0x2FFE4 |  |
| 1.1.3.27 | [ITS\_\_27\_GITS\_PIDR2](#bookmark1134) | 0x0000003B | 0x2FFE8 |  |
| 1.1.3.28 | [ITS\_\_28\_GITS\_PIDR3](#bookmark1135) | 0x00000000 | 0x2FFEC |  |
| 1.1.3.29 | [ITS\_\_29\_GITS\_CIDR0](#bookmark1136) | 0x0000000D | 0x2FFF0 |  |
| 1.1.3.30 | [ITS\_\_30\_GITS\_CIDR1](#bookmark1137) | 0x000000F0 | 0x2FFF4 |  |
| 1.1.3.31 | [ITS\_\_31\_GITS\_CIDR2](#bookmark1138) | 0x00000005 | 0x2FFF8 |  |
| 1.1.3.32 | [ITS\_\_32\_GITS\_CIDR3](#bookmark1139) | 0x000000B1 | 0x2FFFC |  |
| 1.1.4 | [TRANSLATER](#bookmark1140) |  | 0x30000 - 0x30043 |  |
|  |  |  | 0x30000 - 0x3003F |  |
| 1.1.4.1 | [TRANSLATER\_\_1\_GITS\_TRANSLATER](#bookmark1141) | 0x00000000 | 0x30040 |  |
|  |  |  | 0x30044 - 0x7FFFF |  |
| 1.1.5 | [Redistributor\_control\_LPI\_0](#bookmark1142) |  | 0x80000 - 0x8FFFF |  |
| 1.1.5.1 | [Redistributor\_control\_LPI\_0\_\_2\_GICR\_CTLR](#bookmark1143) | 0x00000000 | 0x80000 |  |
| 1.1.5.2 | [Redistributor\_control\_LPI\_0\_\_3\_GICR\_IIDR](#bookmark1144) | 0x0001143B | 0x80004 |  |
| 1.1.5.3 | [Redistributor\_control\_LPI\_0\_\_4\_GICR\_TYPER\_lower](#bookmark1145) | 0x00000001 | 0x80008 |  |
| 1.1.5.4 | [Redistributor\_control\_LPI\_0\_\_5\_GICR\_TYPER\_upper](#bookmark1146) | 0x00000000 | 0x8000C |  |
|  |  |  | 0x80010 - 0x80013 |  |
| 1.1.5.5 | [Redistributor\_control\_LPI\_0\_\_6\_GICR\_WAKER](#bookmark1147) | 0x00000006 | 0x80014 |  |
|  |  |  | 0x80018 - 0x8006F |  |
| 1.1.5.6 | [Redistributor\_control\_LPI\_0\_\_7\_GICR\_PROPBASER\_lower](#bookmark1148) | 0x00000000 | 0x80070 |  |
| 1.1.5.7 | [Redistributor\_control\_LPI\_0\_\_8\_GICR\_PROPBASER\_upper](#bookmark1149) | 0x00000000 | 0x80074 |  |
| 1.1.5.8 | [Redistributor\_control\_LPI\_0\_\_9\_GICR\_PENDBASER\_lower](#bookmark1150) | 0x00000000 | 0x80078 |  |
| 1.1.5.9 | [Redistributor\_control\_LPI\_0\_\_10\_GICR\_PENDBASER\_upper](#bookmark1151) | 0x00000000 | 0x8007C |  |
|  |  |  | 0x80080 - 0x8FFCF |  |
| 1.1.5.10 | [Redistributor\_control\_LPI\_0\_\_11\_GICR\_PIDR4](#bookmark1152) | 0x00000044 | 0x8FFD0 |  |
| 1.1.5.11 | [Redistributor\_control\_LPI\_0\_\_12\_GICR\_PIDR5](#bookmark1153) | 0x00000000 | 0x8FFD4 |  |
| 1.1.5.12 | [Redistributor\_control\_LPI\_0\_\_13\_GICR\_PIDR6](#bookmark1154) | 0x00000000 | 0x8FFD8 |  |
| 1.1.5.13 | [Redistributor\_control\_LPI\_0\_\_14\_GICR\_PIDR7](#bookmark1155) | 0x00000000 | 0x8FFDC |  |
| 1.1.5.14 | [Redistributor\_control\_LPI\_0\_\_15\_GICR\_PIDR0](#bookmark1156) | 0x00000093 | 0x8FFE0 |  |
| 1.1.5.15 | [Redistributor\_control\_LPI\_0\_\_16\_GICR\_PIDR1](#bookmark1157) | 0x000000B4 | 0x8FFE4 |  |
| 1.1.5.16 | [Redistributor\_control\_LPI\_0\_\_17\_GICR\_PIDR2](#bookmark1158) | 0x0000003B | 0x8FFE8 |  |
| 1.1.5.17 | [Redistributor\_control\_LPI\_0\_\_18\_GICR\_PIDR3](#bookmark1159) | 0x00000000 | 0x8FFEC |  |
| 1.1.5.18 | [Redistributor\_control\_LPI\_0\_\_19\_GICR\_CIDR0](#bookmark1160) | 0x0000000D | 0x8FFF0 |  |
| 1.1.5.19 | [Redistributor\_control\_LPI\_0\_\_20\_GICR\_CIDR1](#bookmark1161) | 0x000000F0 | 0x8FFF4 |  |
| 1.1.5.20 | [Redistributor\_control\_LPI\_0\_\_21\_GICR\_CIDR2](#bookmark1162) | 0x00000005 | 0x8FFF8 |  |
| 1.1.5.21 | [Redistributor\_control\_LPI\_0\_\_22\_GICR\_CIDR3](#bookmark1163) | 0x000000B1 | 0x8FFFC |  |
| 1.1.6 | [Redistributor\_SGI\_PPI\_0](#bookmark1164) |  | 0x90000 - 0x9C083 |  |
|  |  |  | 0x90000 - 0x9007F |  |
| 1.1.6.1 | [Redistributor\_SGI\_PPI\_0\_\_1\_GICR\_IGROUPR0](#bookmark1165) | 0x00000000 | 0x90080 |  |
|  |  |  | 0x90084 - 0x900FF |  |
| 1.1.6.2 | [Redistributor\_SGI\_PPI\_0\_\_2\_GICR\_ISENABLER0](#bookmark1166) | 0x00000000 | 0x90100 |  |
|  |  |  | 0x90104 - 0x9017F |  |
| 1.1.6.3 | [Redistributor\_SGI\_PPI\_0\_\_3\_GICR\_ICENABLER0](#bookmark1167) | 0x00000000 | 0x90180 |  |
|  |  |  | 0x90184 - 0x901FF |  |
| 1.1.6.4 | [Redistributor\_SGI\_PPI\_0\_\_4\_GICR\_ISPENDR0](#bookmark1168) | 0x00000000 | 0x90200 |  |
|  |  |  | 0x90204 - 0x9027F |  |
| 1.1.6.5 | [Redistributor\_SGI\_PPI\_0\_\_5\_GICR\_ICPENDR0](#bookmark1169) | 0x00000000 | 0x90280 |  |
|  |  |  | 0x90284 - 0x902FF |  |
| 1.1.6.6 | [Redistributor\_SGI\_PPI\_0\_\_6\_GICR\_ISACTIVER0](#bookmark1170) | 0x00000000 | 0x90300 |  |
|  |  |  | 0x90304 - 0x9037F |  |
| 1.1.6.7 | [Redistributor\_SGI\_PPI\_0\_\_7\_GICR\_ICACTIVER0](#bookmark1171) | 0x00000000 | 0x90380 |  |
|  |  |  | 0x90384 - 0x903FF |  |
| 1.1.6.8 | [Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR0](#bookmark1172) | 0x00000000 | 0x90400 |  |
| 1.1.6.9 | [Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR1](#bookmark1173) | 0x00000000 | 0x90404 |  |
| 1.1.6.10 | [Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR2](#bookmark1174) | 0x00000000 | 0x90408 |  |
| 1.1.6.11 | [Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR3](#bookmark1175) | 0x00000000 | 0x9040C |  |
| 1.1.6.12 | [Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR4](#bookmark1176) | 0x00000000 | 0x90410 |  |
| 1.1.6.13 | [Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR5](#bookmark1177) | 0x00000000 | 0x90414 |  |
| 1.1.6.14 | [Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR6](#bookmark1178) | 0x00000000 | 0x90418 |  |
| 1.1.6.15 | [Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR7](#bookmark1179) | 0x00000000 | 0x9041C |  |
|  |  |  | 0x90420 - 0x90BFF |  |
| 1.1.6.16 | [Redistributor\_SGI\_PPI\_0\_\_9\_GICR\_ICFGR0](#bookmark1180) | 0xAAAAAAAA | 0x90C00 |  |
| 1.1.6.17 | [Redistributor\_SGI\_PPI\_0\_\_10\_GICR\_ICFGR1](#bookmark1181) | 0x00000000 | 0x90C04 |  |
|  |  |  | 0x90C08 - 0x9BFFF |  |
| 1.1.6.18 | [Redistributor\_SGI\_PPI\_0\_\_13\_GICR\_MISCSTATUSR](#bookmark1182) | 0x00000000 | 0x9C000 |  |
|  |  |  | 0x9C004 - 0x9C07F |  |
| 1.1.6.19 | [Redistributor\_SGI\_PPI\_0\_\_14\_GICR\_PPISR](#bookmark1183) | 0x00000000 | 0x9C080 |  |
|  |  |  | 0x9C084 - 0x9FFFF |  |
| 1.1.7 | [Redistributor\_control\_LPI\_1](#bookmark1184) |  | 0xA0000 - 0xAFFFF |  |
| 1.1.7.1 | [Redistributor\_control\_LPI\_1\_\_2\_GICR\_CTLR](#bookmark1185) | 0x00000000 | 0xA0000 |  |
| 1.1.7.2 | [Redistributor\_control\_LPI\_1\_\_3\_GICR\_IIDR](#bookmark1186) | 0x0001143B | 0xA0004 |  |
| 1.1.7.3 | [Redistributor\_control\_LPI\_1\_\_4\_GICR\_TYPER\_lower](#bookmark1187) | 0x00000001 | 0xA0008 |  |
| 1.1.7.4 | [Redistributor\_control\_LPI\_1\_\_5\_GICR\_TYPER\_upper](#bookmark1188) | 0x00000000 | 0xA000C |  |
|  |  |  | 0xA0010 - 0xA0013 |  |
| 1.1.7.5 | [Redistributor\_control\_LPI\_1\_\_6\_GICR\_WAKER](#bookmark1189) | 0x00000006 | 0xA0014 |  |
|  |  |  | 0xA0018 - 0xA006F |  |
| 1.1.7.6 | [Redistributor\_control\_LPI\_1\_\_7\_GICR\_PROPBASER\_lower](#bookmark1190) | 0x00000000 | 0xA0070 |  |
| 1.1.7.7 | [Redistributor\_control\_LPI\_1\_\_8\_GICR\_PROPBASER\_upper](#bookmark1191) | 0x00000000 | 0xA0074 |  |
| 1.1.7.8 | [Redistributor\_control\_LPI\_1\_\_9\_GICR\_PENDBASER\_lower](#bookmark1192) | 0x00000000 | 0xA0078 |  |
| 1.1.7.9 | [Redistributor\_control\_LPI\_1\_\_10\_GICR\_PENDBASER\_upper](#bookmark1193) | 0x00000000 | 0xA007C |  |
|  |  |  | 0xA0080 - 0xAFFCF |  |
| 1.1.7.10 | [Redistributor\_control\_LPI\_1\_\_11\_GICR\_PIDR4](#bookmark1194) | 0x00000044 | 0xAFFD0 |  |
| 1.1.7.11 | [Redistributor\_control\_LPI\_1\_\_12\_GICR\_PIDR5](#bookmark1195) | 0x00000000 | 0xAFFD4 |  |
| 1.1.7.12 | [Redistributor\_control\_LPI\_1\_\_13\_GICR\_PIDR6](#bookmark1196) | 0x00000000 | 0xAFFD8 |  |
| 1.1.7.13 | [Redistributor\_control\_LPI\_1\_\_14\_GICR\_PIDR7](#bookmark1197) | 0x00000000 | 0xAFFDC |  |
| 1.1.7.14 | [Redistributor\_control\_LPI\_1\_\_15\_GICR\_PIDR0](#bookmark1198) | 0x00000093 | 0xAFFE0 |  |
| 1.1.7.15 | [Redistributor\_control\_LPI\_1\_\_16\_GICR\_PIDR1](#bookmark1199) | 0x000000B4 | 0xAFFE4 |  |
| 1.1.7.16 | [Redistributor\_control\_LPI\_1\_\_17\_GICR\_PIDR2](#bookmark1200) | 0x0000003B | 0xAFFE8 |  |
| 1.1.7.17 | [Redistributor\_control\_LPI\_1\_\_18\_GICR\_PIDR3](#bookmark1201) | 0x00000000 | 0xAFFEC |  |
| 1.1.7.18 | [Redistributor\_control\_LPI\_1\_\_19\_GICR\_CIDR0](#bookmark1202) | 0x0000000D | 0xAFFF0 |  |
| 1.1.7.19 | [Redistributor\_control\_LPI\_1\_\_20\_GICR\_CIDR1](#bookmark1203) | 0x000000F0 | 0xAFFF4 |  |
| 1.1.7.20 | [Redistributor\_control\_LPI\_1\_\_21\_GICR\_CIDR2](#bookmark1204) | 0x00000005 | 0xAFFF8 |  |
| 1.1.7.21 | [Redistributor\_control\_LPI\_1\_\_22\_GICR\_CIDR3](#bookmark1205) | 0x000000B1 | 0xAFFFC |  |
| 1.1.8 | [Redistributor\_SGI\_PPI\_1](#bookmark1206) |  | 0xB0000 - 0xBC083 |  |
|  |  |  | 0xB0000 - 0xB007F |  |
| 1.1.8.1 | [Redistributor\_SGI\_PPI\_1\_\_1\_GICR\_IGROUPR0](#bookmark1207) | 0x00000000 | 0xB0080 |  |
|  |  |  | 0xB0084 - 0xB00FF |  |
| 1.1.8.2 | [Redistributor\_SGI\_PPI\_1\_\_2\_GICR\_ISENABLER0](#bookmark1208) | 0x00000000 | 0xB0100 |  |
|  |  |  | 0xB0104 - 0xB017F |  |
| 1.1.8.3 | [Redistributor\_SGI\_PPI\_1\_\_3\_GICR\_ICENABLER0](#bookmark1209) | 0x00000000 | 0xB0180 |  |
|  |  |  | 0xB0184 - 0xB01FF |  |
| 1.1.8.4 | [Redistributor\_SGI\_PPI\_1\_\_4\_GICR\_ISPENDR0](#bookmark1210) | 0x00000000 | 0xB0200 |  |
|  |  |  | 0xB0204 - 0xB027F |  |
| 1.1.8.5 | [Redistributor\_SGI\_PPI\_1\_\_5\_GICR\_ICPENDR0](#bookmark1211) | 0x00000000 | 0xB0280 |  |
|  |  |  | 0xB0284 - 0xB02FF |  |
| 1.1.8.6 | [Redistributor\_SGI\_PPI\_1\_\_6\_GICR\_ISACTIVER0](#bookmark1212) | 0x00000000 | 0xB0300 |  |
|  |  |  | 0xB0304 - 0xB037F |  |
| 1.1.8.7 | [Redistributor\_SGI\_PPI\_1\_\_7\_GICR\_ICACTIVER0](#bookmark1213) | 0x00000000 | 0xB0380 |  |
|  |  |  | 0xB0384 - 0xB03FF |  |
| 1.1.8.8 | [Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR0](#bookmark1214) | 0x00000000 | 0xB0400 |  |
| 1.1.8.9 | [Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR1](#bookmark1215) | 0x00000000 | 0xB0404 |  |
| 1.1.8.10 | [Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR2](#bookmark1216) | 0x00000000 | 0xB0408 |  |
| 1.1.8.11 | [Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR3](#bookmark1217) | 0x00000000 | 0xB040C |  |
| 1.1.8.12 | [Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR4](#bookmark1218) | 0x00000000 | 0xB0410 |  |
| 1.1.8.13 | [Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR5](#bookmark1219) | 0x00000000 | 0xB0414 |  |
| 1.1.8.14 | [Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR6](#bookmark1220) | 0x00000000 | 0xB0418 |  |
| 1.1.8.15 | [Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR7](#bookmark1221) | 0x00000000 | 0xB041C |  |
|  |  |  | 0xB0420 - 0xB0BFF |  |
| 1.1.8.16 | [Redistributor\_SGI\_PPI\_1\_\_9\_GICR\_ICFGR0](#bookmark1222) | 0xAAAAAAAA | 0xB0C00 |  |
| 1.1.8.17 | [Redistributor\_SGI\_PPI\_1\_\_10\_GICR\_ICFGR1](#bookmark1223) | 0x00000000 | 0xB0C04 |  |
|  |  |  | 0xB0C08 - 0xBBFFF |  |
| 1.1.8.18 | [Redistributor\_SGI\_PPI\_1\_\_13\_GICR\_MISCSTATUSR](#bookmark1224) | 0x00000000 | 0xBC000 |  |
|  |  |  | 0xBC004 - 0xBC07F |  |
| 1.1.8.19 | [Redistributor\_SGI\_PPI\_1\_\_14\_GICR\_PPISR](#bookmark1225) | 0x00000000 | 0xBC080 |  |
|  |  |  | 0xBC084 - 0xBFFFF |  |
| 1.1.9 | [Redistributor\_control\_LPI\_2](#bookmark1226) |  | 0xC0000 - 0xCFFFF |  |
| 1.1.9.1 | [Redistributor\_control\_LPI\_2\_\_2\_GICR\_CTLR](#bookmark1227) | 0x00000000 | 0xC0000 |  |
| 1.1.9.2 | [Redistributor\_control\_LPI\_2\_\_3\_GICR\_IIDR](#bookmark1228) | 0x0001143B | 0xC0004 |  |
| 1.1.9.3 | [Redistributor\_control\_LPI\_2\_\_4\_GICR\_TYPER\_lower](#bookmark1229) | 0x00000001 | 0xC0008 |  |
| 1.1.9.4 | [Redistributor\_control\_LPI\_2\_\_5\_GICR\_TYPER\_upper](#bookmark1230) | 0x00000000 | 0xC000C |  |
|  |  |  | 0xC0010 - 0xC0013 |  |
| 1.1.9.5 | [Redistributor\_control\_LPI\_2\_\_6\_GICR\_WAKER](#bookmark1231) | 0x00000006 | 0xC0014 |  |
|  |  |  | 0xC0018 - 0xC006F |  |
| 1.1.9.6 | [Redistributor\_control\_LPI\_2\_\_7\_GICR\_PROPBASER\_lower](#bookmark1232) | 0x00000000 | 0xC0070 |  |
| 1.1.9.7 | [Redistributor\_control\_LPI\_2\_\_8\_GICR\_PROPBASER\_upper](#bookmark1233) | 0x00000000 | 0xC0074 |  |
| 1.1.9.8 | [Redistributor\_control\_LPI\_2\_\_9\_GICR\_PENDBASER\_lower](#bookmark1234) | 0x00000000 | 0xC0078 |  |
| 1.1.9.9 | [Redistributor\_control\_LPI\_2\_\_10\_GICR\_PENDBASER\_upper](#bookmark1235) | 0x00000000 | 0xC007C |  |
|  |  |  | 0xC0080 - 0xCFFCF |  |
| 1.1.9.10 | [Redistributor\_control\_LPI\_2\_\_11\_GICR\_PIDR4](#bookmark1236) | 0x00000044 | 0xCFFD0 |  |
| 1.1.9.11 | [Redistributor\_control\_LPI\_2\_\_12\_GICR\_PIDR5](#bookmark1237) | 0x00000000 | 0xCFFD4 |  |
| 1.1.9.12 | [Redistributor\_control\_LPI\_2\_\_13\_GICR\_PIDR6](#bookmark1238) | 0x00000000 | 0xCFFD8 |  |
| 1.1.9.13 | [Redistributor\_control\_LPI\_2\_\_14\_GICR\_PIDR7](#bookmark1239) | 0x00000000 | 0xCFFDC |  |
| 1.1.9.14 | [Redistributor\_control\_LPI\_2\_\_15\_GICR\_PIDR0](#bookmark1240) | 0x00000093 | 0xCFFE0 |  |
| 1.1.9.15 | [Redistributor\_control\_LPI\_2\_\_16\_GICR\_PIDR1](#bookmark1241) | 0x000000B4 | 0xCFFE4 |  |
| 1.1.9.16 | [Redistributor\_control\_LPI\_2\_\_17\_GICR\_PIDR2](#bookmark1242) | 0x0000003B | 0xCFFE8 |  |
| 1.1.9.17 | [Redistributor\_control\_LPI\_2\_\_18\_GICR\_PIDR3](#bookmark1243) | 0x00000000 | 0xCFFEC |  |
| 1.1.9.18 | [Redistributor\_control\_LPI\_2\_\_19\_GICR\_CIDR0](#bookmark1244) | 0x0000000D | 0xCFFF0 |  |
| 1.1.9.19 | [Redistributor\_control\_LPI\_2\_\_20\_GICR\_CIDR1](#bookmark1245) | 0x000000F0 | 0xCFFF4 |  |
| 1.1.9.20 | [Redistributor\_control\_LPI\_2\_\_21\_GICR\_CIDR2](#bookmark1246) | 0x00000005 | 0xCFFF8 |  |
| 1.1.9.21 | [Redistributor\_control\_LPI\_2\_\_22\_GICR\_CIDR3](#bookmark1247) | 0x000000B1 | 0xCFFFC |  |
| 1.1.10 | [Redistributor\_SGI\_PPI\_2](#bookmark1248) |  | 0xD0000 - 0xDC083 |  |
|  |  |  | 0xD0000 - 0xD007F |  |
| 1.1.10.1 | [Redistributor\_SGI\_PPI\_2\_\_1\_GICR\_IGROUPR0](#bookmark1249) | 0x00000000 | 0xD0080 |  |
|  |  |  | 0xD0084 - 0xD00FF |  |
| 1.1.10.2 | [Redistributor\_SGI\_PPI\_2\_\_2\_GICR\_ISENABLER0](#bookmark1250) | 0x00000000 | 0xD0100 |  |
|  |  |  | 0xD0104 - 0xD017F |  |
| 1.1.10.3 | [Redistributor\_SGI\_PPI\_2\_\_3\_GICR\_ICENABLER0](#bookmark1251) | 0x00000000 | 0xD0180 |  |
|  |  |  | 0xD0184 - 0xD01FF |  |
| 1.1.10.4 | [Redistributor\_SGI\_PPI\_2\_\_4\_GICR\_ISPENDR0](#bookmark1252) | 0x00000000 | 0xD0200 |  |
|  |  |  | 0xD0204 - 0xD027F |  |
| 1.1.10.5 | [Redistributor\_SGI\_PPI\_2\_\_5\_GICR\_ICPENDR0](#bookmark1253) | 0x00000000 | 0xD0280 |  |
|  |  |  | 0xD0284 - 0xD02FF |  |
| 1.1.10.6 | [Redistributor\_SGI\_PPI\_2\_\_6\_GICR\_ISACTIVER0](#bookmark1254) | 0x00000000 | 0xD0300 |  |
|  |  |  | 0xD0304 - 0xD037F |  |
| 1.1.10.7 | [Redistributor\_SGI\_PPI\_2\_\_7\_GICR\_ICACTIVER0](#bookmark1255) | 0x00000000 | 0xD0380 |  |
|  |  |  | 0xD0384 - 0xD03FF |  |
| 1.1.10.8 | [Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR0](#bookmark1256) | 0x00000000 | 0xD0400 |  |
| 1.1.10.9 | [Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR1](#bookmark1257) | 0x00000000 | 0xD0404 |  |
| 1.1.10.10 | [Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR2](#bookmark1258) | 0x00000000 | 0xD0408 |  |
| 1.1.10.11 | [Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR3](#bookmark1259) | 0x00000000 | 0xD040C |  |
| 1.1.10.12 | [Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR4](#bookmark1260) | 0x00000000 | 0xD0410 |  |
| 1.1.10.13 | [Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR5](#bookmark1261) | 0x00000000 | 0xD0414 |  |
| 1.1.10.14 | [Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR6](#bookmark1262) | 0x00000000 | 0xD0418 |  |
| 1.1.10.15 | [Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR7](#bookmark1263) | 0x00000000 | 0xD041C |  |
|  |  |  | 0xD0420 - 0xD0BFF |  |
| 1.1.10.16 | [Redistributor\_SGI\_PPI\_2\_\_9\_GICR\_ICFGR0](#bookmark1264) | 0xAAAAAAAA | 0xD0C00 |  |
| 1.1.10.17 | [Redistributor\_SGI\_PPI\_2\_\_10\_GICR\_ICFGR1](#bookmark1265) | 0x00000000 | 0xD0C04 |  |
|  |  |  | 0xD0C08 - 0xDBFFF |  |
| 1.1.10.18 | [Redistributor\_SGI\_PPI\_2\_\_13\_GICR\_MISCSTATUSR](#bookmark1266) | 0x00000000 | 0xDC000 |  |
|  |  |  | 0xDC004 - 0xDC07F |  |
| 1.1.10.19 | [Redistributor\_SGI\_PPI\_2\_\_14\_GICR\_PPISR](#bookmark1267) | 0x00000000 | 0xDC080 |  |
|  |  |  | 0xDC084 - 0xDFFFF |  |
| 1.1.11 | [Redistributor\_control\_LPI\_3](#bookmark1268) |  | 0xE0000 - 0xEFFFF |  |
| 1.1.11.1 | [Redistributor\_control\_LPI\_3\_\_2\_GICR\_CTLR](#bookmark1269) | 0x00000000 | 0xE0000 |  |
| 1.1.11.2 | [Redistributor\_control\_LPI\_3\_\_3\_GICR\_IIDR](#bookmark1270) | 0x0001143B | 0xE0004 |  |
| 1.1.11.3 | [Redistributor\_control\_LPI\_3\_\_4\_GICR\_TYPER\_lower](#bookmark1271) | 0x00000001 | 0xE0008 |  |
| 1.1.11.4 | [Redistributor\_control\_LPI\_3\_\_5\_GICR\_TYPER\_upper](#bookmark1272) | 0x00000000 | 0xE000C |  |
|  |  |  | 0xE0010 - 0xE0013 |  |
| 1.1.11.5 | [Redistributor\_control\_LPI\_3\_\_6\_GICR\_WAKER](#bookmark1273) | 0x00000006 | 0xE0014 |  |
|  |  |  | 0xE0018 - 0xE006F |  |
| 1.1.11.6 | [Redistributor\_control\_LPI\_3\_\_7\_GICR\_PROPBASER\_lower](#bookmark1274) | 0x00000000 | 0xE0070 |  |
| 1.1.11.7 | [Redistributor\_control\_LPI\_3\_\_8\_GICR\_PROPBASER\_upper](#bookmark1275) | 0x00000000 | 0xE0074 |  |
| 1.1.11.8 | [Redistributor\_control\_LPI\_3\_\_9\_GICR\_PENDBASER\_lower](#bookmark1276) | 0x00000000 | 0xE0078 |  |
| 1.1.11.9 | [Redistributor\_control\_LPI\_3\_\_10\_GICR\_PENDBASER\_upper](#bookmark1277) | 0x00000000 | 0xE007C |  |
|  |  |  | 0xE0080 - 0xEFFCF |  |
| 1.1.11.10 | [Redistributor\_control\_LPI\_3\_\_11\_GICR\_PIDR4](#bookmark1278) | 0x00000044 | 0xEFFD0 |  |
| 1.1.11.11 | [Redistributor\_control\_LPI\_3\_\_12\_GICR\_PIDR5](#bookmark1279) | 0x00000000 | 0xEFFD4 |  |
| 1.1.11.12 | [Redistributor\_control\_LPI\_3\_\_13\_GICR\_PIDR6](#bookmark1280) | 0x00000000 | 0xEFFD8 |  |
| 1.1.11.13 | [Redistributor\_control\_LPI\_3\_\_14\_GICR\_PIDR7](#bookmark1281) | 0x00000000 | 0xEFFDC |  |
| 1.1.11.14 | [Redistributor\_control\_LPI\_3\_\_15\_GICR\_PIDR0](#bookmark1282) | 0x00000093 | 0xEFFE0 |  |
| 1.1.11.15 | [Redistributor\_control\_LPI\_3\_\_16\_GICR\_PIDR1](#bookmark1283) | 0x000000B4 | 0xEFFE4 |  |
| 1.1.11.16 | [Redistributor\_control\_LPI\_3\_\_17\_GICR\_PIDR2](#bookmark1284) | 0x0000003B | 0xEFFE8 |  |
| 1.1.11.17 | [Redistributor\_control\_LPI\_3\_\_18\_GICR\_PIDR3](#bookmark1285) | 0x00000000 | 0xEFFEC |  |
| 1.1.11.18 | [Redistributor\_control\_LPI\_3\_\_19\_GICR\_CIDR0](#bookmark1286) | 0x0000000D | 0xEFFF0 |  |
| 1.1.11.19 | [Redistributor\_control\_LPI\_3\_\_20\_GICR\_CIDR1](#bookmark1287) | 0x000000F0 | 0xEFFF4 |  |
| 1.1.11.20 | [Redistributor\_control\_LPI\_3\_\_21\_GICR\_CIDR2](#bookmark1288) | 0x00000005 | 0xEFFF8 |  |
| 1.1.11.21 | [Redistributor\_control\_LPI\_3\_\_22\_GICR\_CIDR3](#bookmark1289) | 0x000000B1 | 0xEFFFC |  |
| 1.1.12 | [Redistributor\_SGI\_PPI\_3](#bookmark1290) |  | 0xF0000 - 0xFC083 |  |
|  |  |  | 0xF0000 - 0xF007F |  |
| 1.1.12.1 | [Redistributor\_SGI\_PPI\_3\_\_1\_GICR\_IGROUPR0](#bookmark1291) | 0x00000000 | 0xF0080 |  |
|  |  |  | 0xF0084 - 0xF00FF |  |
| 1.1.12.2 | [Redistributor\_SGI\_PPI\_3\_\_2\_GICR\_ISENABLER0](#bookmark1292) | 0x00000000 | 0xF0100 |  |
|  |  |  | 0xF0104 - 0xF017F |  |
| 1.1.12.3 | [Redistributor\_SGI\_PPI\_3\_\_3\_GICR\_ICENABLER0](#bookmark1293) | 0x00000000 | 0xF0180 |  |
|  |  |  | 0xF0184 - 0xF01FF |  |
| 1.1.12.4 | [Redistributor\_SGI\_PPI\_3\_\_4\_GICR\_ISPENDR0](#bookmark1294) | 0x00000000 | 0xF0200 |  |
|  |  |  | 0xF0204 - 0xF027F |  |
| 1.1.12.5 | [Redistributor\_SGI\_PPI\_3\_\_5\_GICR\_ICPENDR0](#bookmark1295) | 0x00000000 | 0xF0280 |  |
|  |  |  | 0xF0284 - 0xF02FF |  |
| 1.1.12.6 | [Redistributor\_SGI\_PPI\_3\_\_6\_GICR\_ISACTIVER0](#bookmark1296) | 0x00000000 | 0xF0300 |  |
|  |  |  | 0xF0304 - 0xF037F |  |
| 1.1.12.7 | [Redistributor\_SGI\_PPI\_3\_\_7\_GICR\_ICACTIVER0](#bookmark1297) | 0x00000000 | 0xF0380 |  |
|  |  |  | 0xF0384 - 0xF03FF |  |
| 1.1.12.8 | [Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR0](#bookmark1298) | 0x00000000 | 0xF0400 |  |
| 1.1.12.9 | [Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR1](#bookmark1299) | 0x00000000 | 0xF0404 |  |
| 1.1.12.10 | [Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR2](#bookmark1300) | 0x00000000 | 0xF0408 |  |
| 1.1.12.11 | [Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR3](#bookmark1301) | 0x00000000 | 0xF040C |  |
| 1.1.12.12 | [Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR4](#bookmark1302) | 0x00000000 | 0xF0410 |  |
| 1.1.12.13 | [Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR5](#bookmark1303) | 0x00000000 | 0xF0414 |  |
| 1.1.12.14 | [Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR6](#bookmark1304) | 0x00000000 | 0xF0418 |  |
| 1.1.12.15 | [Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR7](#bookmark1305) | 0x00000000 | 0xF041C |  |
|  |  |  | 0xF0420 - 0xF0BFF |  |
| 1.1.12.16 | [Redistributor\_SGI\_PPI\_3\_\_9\_GICR\_ICFGR0](#bookmark1306) | 0xAAAAAAAA | 0xF0C00 |  |
| 1.1.12.17 | [Redistributor\_SGI\_PPI\_3\_\_10\_GICR\_ICFGR1](#bookmark1307) | 0x00000000 | 0xF0C04 |  |
|  |  |  | 0xF0C08 - 0xFBFFF |  |
| 1.1.12.18 | [Redistributor\_SGI\_PPI\_3\_\_13\_GICR\_MISCSTATUSR](#bookmark1308) | 0x00000000 | 0xFC000 |  |
|  |  |  | 0xFC004 - 0xFC07F |  |
| 1.1.12.19 | [Redistributor\_SGI\_PPI\_3\_\_14\_GICR\_PPISR](#bookmark1309) | 0x00000000 | 0xFC080 |  |

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| 1 gic500\_spi384 | | | gic500\_spi384 | | block | 0x0 |
| offset | 0 | external | |  | size | 1032324 |
|  | | | | | | |
| oid=f47d46c9-e402-4136-af08-3ba205ec4eda | | | | | | |
| {vendor=arm.com;library=CoreLink;version=r1p1\_0} | | | | | | |

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| 1.1 ProgrammingMemMap | | | | ProgrammingMemMap | | | section | | 0x0 | |
| offset | 0 | external |  | | repeat |  | | size | | 1032324 |
|  | | | | | | | | | | |
| oid=69b23e13-222c-4a7b-a5b9-49378518da39 | | | | | | | | | | |
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| 1.1.1 Distributor | | | | Distributor | | | section | | 0x0 | |
| offset | 0 | external |  | | repeat |  | | size | | 65536 |
|  | | | | | | | | | | |
| oid=279a770f-1c2d-404f-8519-edae9229c299 | | | | | | | | | | |
| {range=65536} | | | | | | | | | | |

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| 1.1.1.1 Distributor\_\_2\_GICD\_CTLR | | | | | | | | Distributor\_\_2\_GICD\_CTLR | | | | | reg32 | | 0x0 |
| offset | | 0 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a3a75262-1be7-4252-bf66-3725e49d1628 | | | | | | | | | | | | | | | |
| Distributor Control Register{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Distributor\_\_2\_GICD\_CTLR\_\_0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 1 | Distributor\_\_2\_GICD\_CTLR\_\_1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 4 | Distributor\_\_2\_GICD\_CTLR\_\_4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 6 | Distributor\_\_2\_GICD\_CTLR\_\_6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_2\_GICD\_CTLR\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.2 Distributor\_\_3\_GICD\_TYPER | | | | | | | | Distributor\_\_3\_GICD\_TYPER | | | | | reg32 | | 0x4 |
| offset | | 4 | external | | |  | | | size | | 32 | default | | 0x00061036 | |
|  | | | | | | | | | | | | | | | |
| oid=b7f20807-01ae-4cdc-9f64-ecfadacdb757 | | | | | | | | | | | | | | | |
| Interrupt Controller Type Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 4:0 | Distributor\_\_3\_GICD\_TYPER\_\_0\_5 | | | ro | ro | | 0x16 | | |  | | | | | |
| 7:5 | Distributor\_\_3\_GICD\_TYPER\_\_5\_3 | | | ro | ro | | 0x1 | | |  | | | | | |
| 10 | Distributor\_\_3\_GICD\_TYPER\_\_10\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 15:11 | Distributor\_\_3\_GICD\_TYPER\_\_11\_5 | | | ro | ro | | 0x2 | | |  | | | | | |
| 16 | Distributor\_\_3\_GICD\_TYPER\_\_16\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 17 | Distributor\_\_3\_GICD\_TYPER\_\_17\_1 | | | ro | ro | | 0x1 | | |  | | | | | |
| 18 | Distributor\_\_3\_GICD\_TYPER\_\_18\_1 | | | ro | ro | | 0x1 | | |  | | | | | |
| 23:19 | Distributor\_\_3\_GICD\_TYPER\_\_19\_5 | | | ro | ro | | 0x0 | | |  | | | | | |
| 24 | Distributor\_\_3\_GICD\_TYPER\_\_24\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.3 Distributor\_\_4\_GICD\_IIDR | | | | | | | | Distributor\_\_4\_GICD\_IIDR | | | | | reg32 | | 0x8 |
| offset | | 8 | external | | |  | | | size | | 32 | default | | 0x0001143b | |
|  | | | | | | | | | | | | | | | |
| oid=1a712d75-9222-4435-8ed0-76afea9a82f0 | | | | | | | | | | | | | | | |
| Distributor Implementer Identification Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 11:0 | Distributor\_\_4\_GICD\_IIDR\_\_0\_12 | | | ro | ro | | 0x43B | | |  | | | | | |
| 15:12 | Distributor\_\_4\_GICD\_IIDR\_\_12\_4 | | | ro | ro | | 0x1 | | |  | | | | | |
| 19:16 | Distributor\_\_4\_GICD\_IIDR\_\_16\_4 | | | ro | ro | | 0x1 | | |  | | | | | |
| 31:24 | Distributor\_\_4\_GICD\_IIDR\_\_24\_8 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.4 Distributor\_\_5\_GICD\_SETSPI\_NSR | | | | | | | | Distributor\_\_5\_GICD\_SETSPI\_NSR | | | | | reg32 | | 0x40 |
| offset | | 64 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ba1c968b-0ea3-49e4-8eb3-7f28632127c9 | | | | | | | | | | | | | | | |
| Non-Secure SPI Set Register{access=write-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 9:0 | Distributor\_\_5\_GICD\_SETSPI\_NSR\_\_0\_10 | | | wo | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.5 Distributor\_\_6\_GICD\_CLRSPI\_NSR | | | | | | | | Distributor\_\_6\_GICD\_CLRSPI\_NSR | | | | | reg32 | | 0x48 |
| offset | | 72 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ecc3a026-ca21-47fa-87f2-845ffe289b25 | | | | | | | | | | | | | | | |
| Non-Secure SPI Clear Register{access=write-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 9:0 | Distributor\_\_6\_GICD\_CLRSPI\_NSR\_\_0\_10 | | | wo | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.6 Distributor\_\_9\_GICD\_IGROUPR0 | | | | | | | | Distributor\_\_9\_GICD\_IGROUPR0 | | | | | reg32 | | 0x80 |
| offset | | 128 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=84677841-e3ce-4112-8fd3-0978bc83d0bd | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_9\_GICD\_IGROUPR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.7 Distributor\_\_10\_GICD\_IGROUPR1 | | | | | | | | Distributor\_\_10\_GICD\_IGROUPR1 | | | | | reg32 | | 0x84 |
| offset | | 132 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fe2825a5-1a65-4e52-b911-8e4f488eb8a6 | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_10\_GICD\_IGROUPR1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.8 Distributor\_\_10\_GICD\_IGROUPR2 | | | | | | | | Distributor\_\_10\_GICD\_IGROUPR2 | | | | | reg32 | | 0x88 |
| offset | | 136 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f961880d-f3e7-483a-ac1a-e50916d1b1af | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_10\_GICD\_IGROUPR2\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.9 Distributor\_\_10\_GICD\_IGROUPR3 | | | | | | | | Distributor\_\_10\_GICD\_IGROUPR3 | | | | | reg32 | | 0x8c |
| offset | | 140 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dee6a93f-0adf-4fb7-b96f-ef1e73095f91 | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_10\_GICD\_IGROUPR3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.10 Distributor\_\_10\_GICD\_IGROUPR4 | | | | | | | | Distributor\_\_10\_GICD\_IGROUPR4 | | | | | reg32 | | 0x90 |
| offset | | 144 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=96b64635-65ce-4a8b-af2e-f582f33df0a7 | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_10\_GICD\_IGROUPR4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.11 Distributor\_\_10\_GICD\_IGROUPR5 | | | | | | | | Distributor\_\_10\_GICD\_IGROUPR5 | | | | | reg32 | | 0x94 |
| offset | | 148 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c3919abc-12da-4238-8f05-cdb3547c29ab | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_10\_GICD\_IGROUPR5\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.12 Distributor\_\_10\_GICD\_IGROUPR6 | | | | | | | | Distributor\_\_10\_GICD\_IGROUPR6 | | | | | reg32 | | 0x98 |
| offset | | 152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=24b94789-2ca1-4cef-83b6-7b225917660c | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_10\_GICD\_IGROUPR6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.13 Distributor\_\_10\_GICD\_IGROUPR7 | | | | | | | | Distributor\_\_10\_GICD\_IGROUPR7 | | | | | reg32 | | 0x9c |
| offset | | 156 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=872140a9-b1a1-4b10-921f-7b1644db5fc3 | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_10\_GICD\_IGROUPR7\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.14 Distributor\_\_10\_GICD\_IGROUPR8 | | | | | | | | Distributor\_\_10\_GICD\_IGROUPR8 | | | | | reg32 | | 0xa0 |
| offset | | 160 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e0c51bb7-81b2-4c00-af23-2ebe9d8b32ea | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_10\_GICD\_IGROUPR8\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.15 Distributor\_\_10\_GICD\_IGROUPR9 | | | | | | | | Distributor\_\_10\_GICD\_IGROUPR9 | | | | | reg32 | | 0xa4 |
| offset | | 164 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a78f41fb-ab1b-4a1d-8c01-6519f93f54f5 | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_10\_GICD\_IGROUPR9\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.16 Distributor\_\_10\_GICD\_IGROUPR10 | | | | | | | | Distributor\_\_10\_GICD\_IGROUPR10 | | | | | reg32 | | 0xa8 |
| offset | | 168 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1c3b37f2-937d-485c-b55e-7b1fb48ed4a2 | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_10\_GICD\_IGROUPR10\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.17 Distributor\_\_10\_GICD\_IGROUPR11 | | | | | | | | Distributor\_\_10\_GICD\_IGROUPR11 | | | | | reg32 | | 0xac |
| offset | | 172 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e99c8f20-4911-4b00-aa71-28b791e78613 | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_10\_GICD\_IGROUPR11\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.18 Distributor\_\_10\_GICD\_IGROUPR12 | | | | | | | | Distributor\_\_10\_GICD\_IGROUPR12 | | | | | reg32 | | 0xb0 |
| offset | | 176 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8397583c-c982-4ae9-b95c-974b4c4c7832 | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_10\_GICD\_IGROUPR12\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.19 Distributor\_\_12\_GICD\_ISENABLER1 | | | | | | | | Distributor\_\_12\_GICD\_ISENABLER1 | | | | | reg32 | | 0x104 |
| offset | | 260 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b209aa2b-2474-4a68-91db-82bc7b25d194 | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_12\_GICD\_ISENABLER1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.20 Distributor\_\_12\_GICD\_ISENABLER2 | | | | | | | | Distributor\_\_12\_GICD\_ISENABLER2 | | | | | reg32 | | 0x108 |
| offset | | 264 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a5f8594c-7770-406e-9443-18ae15298d71 | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_12\_GICD\_ISENABLER2\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.21 Distributor\_\_12\_GICD\_ISENABLER3 | | | | | | | | Distributor\_\_12\_GICD\_ISENABLER3 | | | | | reg32 | | 0x10c |
| offset | | 268 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=89be9aa0-0536-4b6d-8e03-34939529d226 | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_12\_GICD\_ISENABLER3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.22 Distributor\_\_12\_GICD\_ISENABLER4 | | | | | | | | Distributor\_\_12\_GICD\_ISENABLER4 | | | | | reg32 | | 0x110 |
| offset | | 272 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5f8726a0-01d5-4dbb-9893-83d1d5b655c3 | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_12\_GICD\_ISENABLER4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.23 Distributor\_\_12\_GICD\_ISENABLER5 | | | | | | | | Distributor\_\_12\_GICD\_ISENABLER5 | | | | | reg32 | | 0x114 |
| offset | | 276 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7ae7e74b-8bc8-44e3-a3ab-8e7fa6aaf044 | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_12\_GICD\_ISENABLER5\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.24 Distributor\_\_12\_GICD\_ISENABLER6 | | | | | | | | Distributor\_\_12\_GICD\_ISENABLER6 | | | | | reg32 | | 0x118 |
| offset | | 280 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=569ef711-38db-4e3b-a802-f7343a18c531 | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_12\_GICD\_ISENABLER6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.25 Distributor\_\_12\_GICD\_ISENABLER7 | | | | | | | | Distributor\_\_12\_GICD\_ISENABLER7 | | | | | reg32 | | 0x11c |
| offset | | 284 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c1354a9d-5bb0-4da8-ba3b-c2a7773fb09a | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_12\_GICD\_ISENABLER7\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.26 Distributor\_\_12\_GICD\_ISENABLER8 | | | | | | | | Distributor\_\_12\_GICD\_ISENABLER8 | | | | | reg32 | | 0x120 |
| offset | | 288 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ac5cedd2-4d13-478a-ae3f-f02d3f235fb6 | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_12\_GICD\_ISENABLER8\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.27 Distributor\_\_12\_GICD\_ISENABLER9 | | | | | | | | Distributor\_\_12\_GICD\_ISENABLER9 | | | | | reg32 | | 0x124 |
| offset | | 292 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=df063f61-1c05-4d4b-a022-b5ff6afe8839 | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_12\_GICD\_ISENABLER9\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.28 Distributor\_\_12\_GICD\_ISENABLER10 | | | | | | | | Distributor\_\_12\_GICD\_ISENABLER10 | | | | | reg32 | | 0x128 |
| offset | | 296 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bb1f449f-3f25-4f75-b823-53cb2af5d422 | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_12\_GICD\_ISENABLER10\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.29 Distributor\_\_12\_GICD\_ISENABLER11 | | | | | | | | Distributor\_\_12\_GICD\_ISENABLER11 | | | | | reg32 | | 0x12c |
| offset | | 300 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ee7af8f6-51d1-462b-a331-54706e4d7584 | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_12\_GICD\_ISENABLER11\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.30 Distributor\_\_12\_GICD\_ISENABLER12 | | | | | | | | Distributor\_\_12\_GICD\_ISENABLER12 | | | | | reg32 | | 0x130 |
| offset | | 304 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4d4c65d0-a97b-41d9-94df-cb16db360ae4 | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_12\_GICD\_ISENABLER12\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.31 Distributor\_\_14\_GICD\_ICENABLER1 | | | | | | | | Distributor\_\_14\_GICD\_ICENABLER1 | | | | | reg32 | | 0x184 |
| offset | | 388 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=47d737ee-f081-4f4a-ae3d-57d588e103ea | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_14\_GICD\_ICENABLER1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.32 Distributor\_\_14\_GICD\_ICENABLER2 | | | | | | | | Distributor\_\_14\_GICD\_ICENABLER2 | | | | | reg32 | | 0x188 |
| offset | | 392 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2f46c8ee-da76-4d7e-83ab-b53298ad7813 | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_14\_GICD\_ICENABLER2\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.33 Distributor\_\_14\_GICD\_ICENABLER3 | | | | | | | | Distributor\_\_14\_GICD\_ICENABLER3 | | | | | reg32 | | 0x18c |
| offset | | 396 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a3c2050c-624c-4be2-82f1-c1afef5359a2 | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_14\_GICD\_ICENABLER3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.34 Distributor\_\_14\_GICD\_ICENABLER4 | | | | | | | | Distributor\_\_14\_GICD\_ICENABLER4 | | | | | reg32 | | 0x190 |
| offset | | 400 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6da19882-c22a-47fd-b838-37264b4b3bea | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_14\_GICD\_ICENABLER4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.35 Distributor\_\_14\_GICD\_ICENABLER5 | | | | | | | | Distributor\_\_14\_GICD\_ICENABLER5 | | | | | reg32 | | 0x194 |
| offset | | 404 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7990ea53-705e-4563-a181-edddf7dc21fd | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_14\_GICD\_ICENABLER5\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.36 Distributor\_\_14\_GICD\_ICENABLER6 | | | | | | | | Distributor\_\_14\_GICD\_ICENABLER6 | | | | | reg32 | | 0x198 |
| offset | | 408 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=22be0f00-0b9a-4a8d-9641-fb0e7f2e598b | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_14\_GICD\_ICENABLER6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.37 Distributor\_\_14\_GICD\_ICENABLER7 | | | | | | | | Distributor\_\_14\_GICD\_ICENABLER7 | | | | | reg32 | | 0x19c |
| offset | | 412 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=223400bc-80da-4d7a-844b-5013b965a832 | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_14\_GICD\_ICENABLER7\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.38 Distributor\_\_14\_GICD\_ICENABLER8 | | | | | | | | Distributor\_\_14\_GICD\_ICENABLER8 | | | | | reg32 | | 0x1a0 |
| offset | | 416 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=abafee0d-1a92-49aa-bf03-cc3719da31ac | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_14\_GICD\_ICENABLER8\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.39 Distributor\_\_14\_GICD\_ICENABLER9 | | | | | | | | Distributor\_\_14\_GICD\_ICENABLER9 | | | | | reg32 | | 0x1a4 |
| offset | | 420 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1f9b7b73-6622-4cb8-b59b-4b78ac7695f7 | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_14\_GICD\_ICENABLER9\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.40 Distributor\_\_14\_GICD\_ICENABLER10 | | | | | | | | Distributor\_\_14\_GICD\_ICENABLER10 | | | | | reg32 | | 0x1a8 |
| offset | | 424 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=11566edf-8fdf-4ef0-8898-6dce24abfcf8 | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_14\_GICD\_ICENABLER10\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.41 Distributor\_\_14\_GICD\_ICENABLER11 | | | | | | | | Distributor\_\_14\_GICD\_ICENABLER11 | | | | | reg32 | | 0x1ac |
| offset | | 428 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d33417b5-20a9-4d93-85b3-e453eb777126 | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_14\_GICD\_ICENABLER11\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.42 Distributor\_\_14\_GICD\_ICENABLER12 | | | | | | | | Distributor\_\_14\_GICD\_ICENABLER12 | | | | | reg32 | | 0x1b0 |
| offset | | 432 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=06f3b41a-2a56-4918-9076-c7360c36b61d | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_14\_GICD\_ICENABLER12\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.43 Distributor\_\_16\_GICD\_ISPENDR1 | | | | | | | | Distributor\_\_16\_GICD\_ISPENDR1 | | | | | reg32 | | 0x204 |
| offset | | 516 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8c3de37b-eb10-42c3-9ce5-da3c037e6763 | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_16\_GICD\_ISPENDR1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.44 Distributor\_\_16\_GICD\_ISPENDR2 | | | | | | | | Distributor\_\_16\_GICD\_ISPENDR2 | | | | | reg32 | | 0x208 |
| offset | | 520 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a2ff8228-4ef7-4653-8148-f80510623b9d | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_16\_GICD\_ISPENDR2\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.45 Distributor\_\_16\_GICD\_ISPENDR3 | | | | | | | | Distributor\_\_16\_GICD\_ISPENDR3 | | | | | reg32 | | 0x20c |
| offset | | 524 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=14473814-391e-4418-a5e1-482b79bf874e | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_16\_GICD\_ISPENDR3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.46 Distributor\_\_16\_GICD\_ISPENDR4 | | | | | | | | Distributor\_\_16\_GICD\_ISPENDR4 | | | | | reg32 | | 0x210 |
| offset | | 528 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=15817439-5084-4b27-95bf-90c2280fff0e | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_16\_GICD\_ISPENDR4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.47 Distributor\_\_16\_GICD\_ISPENDR5 | | | | | | | | Distributor\_\_16\_GICD\_ISPENDR5 | | | | | reg32 | | 0x214 |
| offset | | 532 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=faad5102-e6a4-45d9-8e1e-ce85cbcd8143 | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_16\_GICD\_ISPENDR5\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.48 Distributor\_\_16\_GICD\_ISPENDR6 | | | | | | | | Distributor\_\_16\_GICD\_ISPENDR6 | | | | | reg32 | | 0x218 |
| offset | | 536 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0bbe48b6-6f8d-48d0-918f-7bfa8d6b320c | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_16\_GICD\_ISPENDR6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.49 Distributor\_\_16\_GICD\_ISPENDR7 | | | | | | | | Distributor\_\_16\_GICD\_ISPENDR7 | | | | | reg32 | | 0x21c |
| offset | | 540 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0ff61b4e-742e-4fae-bae5-47c124a36ff1 | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_16\_GICD\_ISPENDR7\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.50 Distributor\_\_16\_GICD\_ISPENDR8 | | | | | | | | Distributor\_\_16\_GICD\_ISPENDR8 | | | | | reg32 | | 0x220 |
| offset | | 544 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1651a9bc-fab0-41c2-ada2-b27b5612438c | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_16\_GICD\_ISPENDR8\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.51 Distributor\_\_16\_GICD\_ISPENDR9 | | | | | | | | Distributor\_\_16\_GICD\_ISPENDR9 | | | | | reg32 | | 0x224 |
| offset | | 548 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=53f0d4d6-7986-49e0-8125-3f964fcde505 | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_16\_GICD\_ISPENDR9\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.52 Distributor\_\_16\_GICD\_ISPENDR10 | | | | | | | | Distributor\_\_16\_GICD\_ISPENDR10 | | | | | reg32 | | 0x228 |
| offset | | 552 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=816e7fa3-5706-4b81-8b77-800648a57e53 | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_16\_GICD\_ISPENDR10\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.53 Distributor\_\_16\_GICD\_ISPENDR11 | | | | | | | | Distributor\_\_16\_GICD\_ISPENDR11 | | | | | reg32 | | 0x22c |
| offset | | 556 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3c178bf6-0873-44e5-82a6-9d0fa3ca7a6d | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_16\_GICD\_ISPENDR11\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.54 Distributor\_\_16\_GICD\_ISPENDR12 | | | | | | | | Distributor\_\_16\_GICD\_ISPENDR12 | | | | | reg32 | | 0x230 |
| offset | | 560 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a10e692a-4770-4acb-ac12-e006073690be | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_16\_GICD\_ISPENDR12\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.55 Distributor\_\_18\_GICD\_ICPENDR1 | | | | | | | | Distributor\_\_18\_GICD\_ICPENDR1 | | | | | reg32 | | 0x284 |
| offset | | 644 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=574322c2-bec3-40d2-8a83-90494faf69eb | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_18\_GICD\_ICPENDR1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.56 Distributor\_\_18\_GICD\_ICPENDR2 | | | | | | | | Distributor\_\_18\_GICD\_ICPENDR2 | | | | | reg32 | | 0x288 |
| offset | | 648 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b3ec1949-c490-4732-8f74-0ab5d70357c1 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_18\_GICD\_ICPENDR2\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.57 Distributor\_\_18\_GICD\_ICPENDR3 | | | | | | | | Distributor\_\_18\_GICD\_ICPENDR3 | | | | | reg32 | | 0x28c |
| offset | | 652 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7e916384-0d78-41a6-92fb-bace288a5f45 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_18\_GICD\_ICPENDR3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.58 Distributor\_\_18\_GICD\_ICPENDR4 | | | | | | | | Distributor\_\_18\_GICD\_ICPENDR4 | | | | | reg32 | | 0x290 |
| offset | | 656 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8b78f893-f44d-4340-bd25-41b0e381b6f8 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_18\_GICD\_ICPENDR4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.59 Distributor\_\_18\_GICD\_ICPENDR5 | | | | | | | | Distributor\_\_18\_GICD\_ICPENDR5 | | | | | reg32 | | 0x294 |
| offset | | 660 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9bd7dc5c-d3a2-4237-9284-01dca196fbe8 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_18\_GICD\_ICPENDR5\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.60 Distributor\_\_18\_GICD\_ICPENDR6 | | | | | | | | Distributor\_\_18\_GICD\_ICPENDR6 | | | | | reg32 | | 0x298 |
| offset | | 664 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0b110260-c4ec-4d98-995a-ff48b612a33b | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_18\_GICD\_ICPENDR6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.61 Distributor\_\_18\_GICD\_ICPENDR7 | | | | | | | | Distributor\_\_18\_GICD\_ICPENDR7 | | | | | reg32 | | 0x29c |
| offset | | 668 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fffa7c7e-d120-43e5-9325-c9b7196324d3 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_18\_GICD\_ICPENDR7\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.62 Distributor\_\_18\_GICD\_ICPENDR8 | | | | | | | | Distributor\_\_18\_GICD\_ICPENDR8 | | | | | reg32 | | 0x2a0 |
| offset | | 672 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f7d064b4-bb92-46d8-b5d1-e749ae5c64fa | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_18\_GICD\_ICPENDR8\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.63 Distributor\_\_18\_GICD\_ICPENDR9 | | | | | | | | Distributor\_\_18\_GICD\_ICPENDR9 | | | | | reg32 | | 0x2a4 |
| offset | | 676 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=294d8f99-26e1-4bae-9d6b-d6857d21e2a0 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_18\_GICD\_ICPENDR9\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.64 Distributor\_\_18\_GICD\_ICPENDR10 | | | | | | | | Distributor\_\_18\_GICD\_ICPENDR10 | | | | | reg32 | | 0x2a8 |
| offset | | 680 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f60e7806-1634-42fb-a4d0-39acaa824a03 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_18\_GICD\_ICPENDR10\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.65 Distributor\_\_18\_GICD\_ICPENDR11 | | | | | | | | Distributor\_\_18\_GICD\_ICPENDR11 | | | | | reg32 | | 0x2ac |
| offset | | 684 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e9303fe3-d87a-4e16-8b0b-34800091eb91 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_18\_GICD\_ICPENDR11\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.66 Distributor\_\_18\_GICD\_ICPENDR12 | | | | | | | | Distributor\_\_18\_GICD\_ICPENDR12 | | | | | reg32 | | 0x2b0 |
| offset | | 688 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9aece159-ce49-4aaa-9bd0-cc369edbac35 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_18\_GICD\_ICPENDR12\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.67 Distributor\_\_20\_GICD\_ISACTIVER1 | | | | | | | | Distributor\_\_20\_GICD\_ISACTIVER1 | | | | | reg32 | | 0x304 |
| offset | | 772 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9b070736-eb2c-47ff-8270-18f94a567b5b | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_20\_GICD\_ISACTIVER1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.68 Distributor\_\_20\_GICD\_ISACTIVER2 | | | | | | | | Distributor\_\_20\_GICD\_ISACTIVER2 | | | | | reg32 | | 0x308 |
| offset | | 776 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=eb372587-0e08-41b6-8740-b4207d8abb47 | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_20\_GICD\_ISACTIVER2\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.69 Distributor\_\_20\_GICD\_ISACTIVER3 | | | | | | | | Distributor\_\_20\_GICD\_ISACTIVER3 | | | | | reg32 | | 0x30c |
| offset | | 780 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=beeae9a9-9014-471a-9842-f87adff2a65b | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_20\_GICD\_ISACTIVER3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.70 Distributor\_\_20\_GICD\_ISACTIVER4 | | | | | | | | Distributor\_\_20\_GICD\_ISACTIVER4 | | | | | reg32 | | 0x310 |
| offset | | 784 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cea45cd5-a140-45f5-a799-b34d7fac2f70 | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_20\_GICD\_ISACTIVER4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.71 Distributor\_\_20\_GICD\_ISACTIVER5 | | | | | | | | Distributor\_\_20\_GICD\_ISACTIVER5 | | | | | reg32 | | 0x314 |
| offset | | 788 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d4cc39b1-8be7-4243-97c5-3f774c2bb124 | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_20\_GICD\_ISACTIVER5\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.72 Distributor\_\_20\_GICD\_ISACTIVER6 | | | | | | | | Distributor\_\_20\_GICD\_ISACTIVER6 | | | | | reg32 | | 0x318 |
| offset | | 792 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=063af778-754b-4ca4-ac1a-9e8d5e1a554d | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_20\_GICD\_ISACTIVER6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.73 Distributor\_\_20\_GICD\_ISACTIVER7 | | | | | | | | Distributor\_\_20\_GICD\_ISACTIVER7 | | | | | reg32 | | 0x31c |
| offset | | 796 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2f065d78-5281-4c77-871c-ab271f787769 | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_20\_GICD\_ISACTIVER7\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.74 Distributor\_\_20\_GICD\_ISACTIVER8 | | | | | | | | Distributor\_\_20\_GICD\_ISACTIVER8 | | | | | reg32 | | 0x320 |
| offset | | 800 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6db4e0f1-2244-4dd6-9158-d6f0083bba11 | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_20\_GICD\_ISACTIVER8\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.75 Distributor\_\_20\_GICD\_ISACTIVER9 | | | | | | | | Distributor\_\_20\_GICD\_ISACTIVER9 | | | | | reg32 | | 0x324 |
| offset | | 804 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=65a91e63-d265-40bc-9b95-10e01b07b1bd | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_20\_GICD\_ISACTIVER9\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.76 Distributor\_\_20\_GICD\_ISACTIVER10 | | | | | | | | Distributor\_\_20\_GICD\_ISACTIVER10 | | | | | reg32 | | 0x328 |
| offset | | 808 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a7a0f821-dcb2-431e-ba34-6759553358aa | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_20\_GICD\_ISACTIVER10\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.77 Distributor\_\_20\_GICD\_ISACTIVER11 | | | | | | | | Distributor\_\_20\_GICD\_ISACTIVER11 | | | | | reg32 | | 0x32c |
| offset | | 812 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=acd85404-e9cc-4a03-a3c8-14da370ff930 | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_20\_GICD\_ISACTIVER11\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.78 Distributor\_\_20\_GICD\_ISACTIVER12 | | | | | | | | Distributor\_\_20\_GICD\_ISACTIVER12 | | | | | reg32 | | 0x330 |
| offset | | 816 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b025161e-e318-42a8-801e-4ebd36730d5b | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_20\_GICD\_ISACTIVER12\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.79 Distributor\_\_22\_GICD\_ICACTIVER1 | | | | | | | | Distributor\_\_22\_GICD\_ICACTIVER1 | | | | | reg32 | | 0x384 |
| offset | | 900 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0fae6408-1b73-452f-a27f-d8b83729f3c6 | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_22\_GICD\_ICACTIVER1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.80 Distributor\_\_22\_GICD\_ICACTIVER2 | | | | | | | | Distributor\_\_22\_GICD\_ICACTIVER2 | | | | | reg32 | | 0x388 |
| offset | | 904 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f4327a9d-629d-44f2-8aed-fe07d71fa786 | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_22\_GICD\_ICACTIVER2\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.81 Distributor\_\_22\_GICD\_ICACTIVER3 | | | | | | | | Distributor\_\_22\_GICD\_ICACTIVER3 | | | | | reg32 | | 0x38c |
| offset | | 908 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fd2fabbb-c6fd-44cc-a68c-65f1530c6128 | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_22\_GICD\_ICACTIVER3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.82 Distributor\_\_22\_GICD\_ICACTIVER4 | | | | | | | | Distributor\_\_22\_GICD\_ICACTIVER4 | | | | | reg32 | | 0x390 |
| offset | | 912 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7baffdfa-d1ec-4eaa-8265-cace3821d7e5 | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_22\_GICD\_ICACTIVER4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.83 Distributor\_\_22\_GICD\_ICACTIVER5 | | | | | | | | Distributor\_\_22\_GICD\_ICACTIVER5 | | | | | reg32 | | 0x394 |
| offset | | 916 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=87c13c4c-9f5f-4b81-a99a-aaa0de156c36 | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_22\_GICD\_ICACTIVER5\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.84 Distributor\_\_22\_GICD\_ICACTIVER6 | | | | | | | | Distributor\_\_22\_GICD\_ICACTIVER6 | | | | | reg32 | | 0x398 |
| offset | | 920 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a3854a97-5370-4053-b9a7-7fdd090d40bc | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_22\_GICD\_ICACTIVER6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.85 Distributor\_\_22\_GICD\_ICACTIVER7 | | | | | | | | Distributor\_\_22\_GICD\_ICACTIVER7 | | | | | reg32 | | 0x39c |
| offset | | 924 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=13c3cbcd-0e94-4748-b05e-984b2822b8b9 | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_22\_GICD\_ICACTIVER7\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.86 Distributor\_\_22\_GICD\_ICACTIVER8 | | | | | | | | Distributor\_\_22\_GICD\_ICACTIVER8 | | | | | reg32 | | 0x3a0 |
| offset | | 928 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e68ad460-84ba-411c-9835-b009bf9fa23c | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_22\_GICD\_ICACTIVER8\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.87 Distributor\_\_22\_GICD\_ICACTIVER9 | | | | | | | | Distributor\_\_22\_GICD\_ICACTIVER9 | | | | | reg32 | | 0x3a4 |
| offset | | 932 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ba1cd1d5-dd5b-4007-8eba-84c8e6d74bd1 | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_22\_GICD\_ICACTIVER9\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.88 Distributor\_\_22\_GICD\_ICACTIVER10 | | | | | | | | Distributor\_\_22\_GICD\_ICACTIVER10 | | | | | reg32 | | 0x3a8 |
| offset | | 936 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=db7985d2-78fd-467b-b5c9-a023e96c7408 | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_22\_GICD\_ICACTIVER10\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.89 Distributor\_\_22\_GICD\_ICACTIVER11 | | | | | | | | Distributor\_\_22\_GICD\_ICACTIVER11 | | | | | reg32 | | 0x3ac |
| offset | | 940 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8290e152-1261-46ca-9c85-58b5f08fa211 | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_22\_GICD\_ICACTIVER11\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.90 Distributor\_\_22\_GICD\_ICACTIVER12 | | | | | | | | Distributor\_\_22\_GICD\_ICACTIVER12 | | | | | reg32 | | 0x3b0 |
| offset | | 944 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b3025abd-6e8b-46d1-8c57-07a0217f9cde | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_22\_GICD\_ICACTIVER12\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.91 Distributor\_\_24\_GICD\_IPRIORITYR8 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR8 | | | | | reg32 | | 0x420 |
| offset | | 1056 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=48eb8ebc-c8a2-4743-bd6d-d1940d5ed25f | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR8\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.92 Distributor\_\_24\_GICD\_IPRIORITYR9 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR9 | | | | | reg32 | | 0x424 |
| offset | | 1060 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=292ce388-da95-4453-b995-5739c10b4283 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR9\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.93 Distributor\_\_24\_GICD\_IPRIORITYR10 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR10 | | | | | reg32 | | 0x428 |
| offset | | 1064 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=67a022be-ffa8-4ad1-843a-e319f96d15ba | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR10\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.94 Distributor\_\_24\_GICD\_IPRIORITYR11 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR11 | | | | | reg32 | | 0x42c |
| offset | | 1068 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=63bfcaf3-9aee-4000-9a38-6388f7481838 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR11\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.95 Distributor\_\_24\_GICD\_IPRIORITYR12 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR12 | | | | | reg32 | | 0x430 |
| offset | | 1072 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a10cae54-ab80-41fc-8ff8-1255f833ed07 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR12\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.96 Distributor\_\_24\_GICD\_IPRIORITYR13 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR13 | | | | | reg32 | | 0x434 |
| offset | | 1076 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d7d7998b-a29d-4f17-8554-2338de57a3b2 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR13\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.97 Distributor\_\_24\_GICD\_IPRIORITYR14 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR14 | | | | | reg32 | | 0x438 |
| offset | | 1080 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=eaf929fa-82f1-4eff-9a49-8c84858a976a | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR14\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.98 Distributor\_\_24\_GICD\_IPRIORITYR15 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR15 | | | | | reg32 | | 0x43c |
| offset | | 1084 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2aca07e7-865b-4d38-944c-f6623a838337 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR15\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.99 Distributor\_\_24\_GICD\_IPRIORITYR16 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR16 | | | | | reg32 | | 0x440 |
| offset | | 1088 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c6d6bf1a-0251-4e7d-b790-950a153d5a36 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR16\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.100 Distributor\_\_24\_GICD\_IPRIORITYR17 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR17 | | | | | reg32 | | 0x444 |
| offset | | 1092 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d6151d9e-f04c-4fd0-8a13-6d618b6e88c6 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR17\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.101 Distributor\_\_24\_GICD\_IPRIORITYR18 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR18 | | | | | reg32 | | 0x448 |
| offset | | 1096 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=36ad9cd9-a089-4bc5-b709-7d1d7a263bcf | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR18\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.102 Distributor\_\_24\_GICD\_IPRIORITYR19 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR19 | | | | | reg32 | | 0x44c |
| offset | | 1100 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d7781532-f3f1-4583-b3f4-d5843fabad65 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR19\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.103 Distributor\_\_24\_GICD\_IPRIORITYR20 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR20 | | | | | reg32 | | 0x450 |
| offset | | 1104 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b9c4f4eb-2823-4483-b326-9e90328d222d | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR20\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.104 Distributor\_\_24\_GICD\_IPRIORITYR21 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR21 | | | | | reg32 | | 0x454 |
| offset | | 1108 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bc3b73a7-8a20-4eca-ba8d-852bbd8f86ae | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR21\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.105 Distributor\_\_24\_GICD\_IPRIORITYR22 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR22 | | | | | reg32 | | 0x458 |
| offset | | 1112 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d922dd83-82ec-481e-833f-3ac6375300d5 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR22\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.106 Distributor\_\_24\_GICD\_IPRIORITYR23 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR23 | | | | | reg32 | | 0x45c |
| offset | | 1116 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b203b222-3efa-4443-87bc-4882a3d26cd6 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR23\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.107 Distributor\_\_24\_GICD\_IPRIORITYR24 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR24 | | | | | reg32 | | 0x460 |
| offset | | 1120 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a824a11e-b28f-4126-a04f-ad0b84f10604 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR24\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.108 Distributor\_\_24\_GICD\_IPRIORITYR25 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR25 | | | | | reg32 | | 0x464 |
| offset | | 1124 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=96542bf4-3eca-4aa5-bcb5-c41e99a89005 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR25\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.109 Distributor\_\_24\_GICD\_IPRIORITYR26 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR26 | | | | | reg32 | | 0x468 |
| offset | | 1128 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b0db9d5a-f082-4544-8509-b4b7d6313d7a | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR26\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.110 Distributor\_\_24\_GICD\_IPRIORITYR27 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR27 | | | | | reg32 | | 0x46c |
| offset | | 1132 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6adfe6b2-5f77-4bfe-beef-f6cf28a0b236 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR27\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.111 Distributor\_\_24\_GICD\_IPRIORITYR28 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR28 | | | | | reg32 | | 0x470 |
| offset | | 1136 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e8915109-5384-4600-b093-acde32c5d03d | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR28\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.112 Distributor\_\_24\_GICD\_IPRIORITYR29 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR29 | | | | | reg32 | | 0x474 |
| offset | | 1140 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2e82e5bb-2f1c-463d-8c3a-dbba07553a15 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR29\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.113 Distributor\_\_24\_GICD\_IPRIORITYR30 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR30 | | | | | reg32 | | 0x478 |
| offset | | 1144 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=39e89d5f-8641-4267-94b8-5a8ff98bf2a8 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR30\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.114 Distributor\_\_24\_GICD\_IPRIORITYR31 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR31 | | | | | reg32 | | 0x47c |
| offset | | 1148 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fd8f99e1-1969-4992-bf65-40a355ba555d | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.115 Distributor\_\_24\_GICD\_IPRIORITYR32 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR32 | | | | | reg32 | | 0x480 |
| offset | | 1152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=da14a97b-8dfd-4c35-86ff-f11ff1ded8c6 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR32\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.116 Distributor\_\_24\_GICD\_IPRIORITYR33 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR33 | | | | | reg32 | | 0x484 |
| offset | | 1156 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=580d7545-12d2-4bec-b92a-1d34bcd18f78 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR33\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.117 Distributor\_\_24\_GICD\_IPRIORITYR34 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR34 | | | | | reg32 | | 0x488 |
| offset | | 1160 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9812fb79-958a-4999-b7ae-abf4c1e916dc | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR34\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.118 Distributor\_\_24\_GICD\_IPRIORITYR35 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR35 | | | | | reg32 | | 0x48c |
| offset | | 1164 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a53369c0-63e2-40c3-a3ca-59c3a042937b | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR35\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.119 Distributor\_\_24\_GICD\_IPRIORITYR36 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR36 | | | | | reg32 | | 0x490 |
| offset | | 1168 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ddf37aa5-8ea2-44a5-b625-9035006f22a6 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR36\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.120 Distributor\_\_24\_GICD\_IPRIORITYR37 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR37 | | | | | reg32 | | 0x494 |
| offset | | 1172 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c00eed36-0b6f-4202-a07b-6eda7d99fe66 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR37\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.121 Distributor\_\_24\_GICD\_IPRIORITYR38 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR38 | | | | | reg32 | | 0x498 |
| offset | | 1176 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=941ac12a-5329-44ee-a74a-1259fef6595f | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR38\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.122 Distributor\_\_24\_GICD\_IPRIORITYR39 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR39 | | | | | reg32 | | 0x49c |
| offset | | 1180 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=aa72add6-fe97-47a1-bcca-49205dd9778d | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR39\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.123 Distributor\_\_24\_GICD\_IPRIORITYR40 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR40 | | | | | reg32 | | 0x4a0 |
| offset | | 1184 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c222c21e-f7e8-4e1f-8c2c-085a6484e040 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR40\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.124 Distributor\_\_24\_GICD\_IPRIORITYR41 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR41 | | | | | reg32 | | 0x4a4 |
| offset | | 1188 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bdd0f90a-0f48-4237-8713-e3464a38274e | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR41\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.125 Distributor\_\_24\_GICD\_IPRIORITYR42 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR42 | | | | | reg32 | | 0x4a8 |
| offset | | 1192 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6a8d359d-1cd1-4ac4-9eb9-ab481e231b7b | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR42\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.126 Distributor\_\_24\_GICD\_IPRIORITYR43 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR43 | | | | | reg32 | | 0x4ac |
| offset | | 1196 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=789c5061-b064-4606-9c2f-a9e90f9de7b8 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR43\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.127 Distributor\_\_24\_GICD\_IPRIORITYR44 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR44 | | | | | reg32 | | 0x4b0 |
| offset | | 1200 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b5e36eb8-45d4-4b55-bb18-36e0142423af | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR44\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.128 Distributor\_\_24\_GICD\_IPRIORITYR45 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR45 | | | | | reg32 | | 0x4b4 |
| offset | | 1204 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d5b1ffc4-94cf-47fc-8c44-3a0a2f961eef | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR45\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.129 Distributor\_\_24\_GICD\_IPRIORITYR46 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR46 | | | | | reg32 | | 0x4b8 |
| offset | | 1208 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b31980ed-4793-476a-9510-613b8d23f744 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR46\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.130 Distributor\_\_24\_GICD\_IPRIORITYR47 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR47 | | | | | reg32 | | 0x4bc |
| offset | | 1212 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5cd191af-d8b1-460b-9fb2-5700473ed748 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR47\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.131 Distributor\_\_24\_GICD\_IPRIORITYR48 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR48 | | | | | reg32 | | 0x4c0 |
| offset | | 1216 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7b3f380d-8e03-4c8a-ac12-a3b2aad6e5c0 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR48\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.132 Distributor\_\_24\_GICD\_IPRIORITYR49 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR49 | | | | | reg32 | | 0x4c4 |
| offset | | 1220 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f1a89ef0-3595-4883-8048-fb26a9ad84e6 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR49\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.133 Distributor\_\_24\_GICD\_IPRIORITYR50 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR50 | | | | | reg32 | | 0x4c8 |
| offset | | 1224 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e112a34f-59d2-4750-9af6-19ec3f08a507 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR50\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.134 Distributor\_\_24\_GICD\_IPRIORITYR51 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR51 | | | | | reg32 | | 0x4cc |
| offset | | 1228 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9bbace18-b8d2-4578-a92f-1d734ac105bc | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR51\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.135 Distributor\_\_24\_GICD\_IPRIORITYR52 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR52 | | | | | reg32 | | 0x4d0 |
| offset | | 1232 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b67c59cc-c807-464a-b39c-76ed8ca02493 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR52\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.136 Distributor\_\_24\_GICD\_IPRIORITYR53 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR53 | | | | | reg32 | | 0x4d4 |
| offset | | 1236 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3d9f408d-5594-49b3-b853-c5676bd37acd | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR53\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.137 Distributor\_\_24\_GICD\_IPRIORITYR54 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR54 | | | | | reg32 | | 0x4d8 |
| offset | | 1240 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0693b759-8b73-4411-a8bd-ce723a3f85a7 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR54\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.138 Distributor\_\_24\_GICD\_IPRIORITYR55 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR55 | | | | | reg32 | | 0x4dc |
| offset | | 1244 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0cdc1d7d-d61c-4f81-a606-2f887077964e | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR55\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.139 Distributor\_\_24\_GICD\_IPRIORITYR56 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR56 | | | | | reg32 | | 0x4e0 |
| offset | | 1248 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f2943a53-0f58-4584-af23-802144f61a94 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR56\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.140 Distributor\_\_24\_GICD\_IPRIORITYR57 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR57 | | | | | reg32 | | 0x4e4 |
| offset | | 1252 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=08df51b3-2773-4a29-beef-401648c93d00 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR57\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.141 Distributor\_\_24\_GICD\_IPRIORITYR58 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR58 | | | | | reg32 | | 0x4e8 |
| offset | | 1256 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fa01c5b3-a72a-48a8-a01d-0cdfacf12df2 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR58\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.142 Distributor\_\_24\_GICD\_IPRIORITYR59 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR59 | | | | | reg32 | | 0x4ec |
| offset | | 1260 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=46542ab5-cdac-4d79-9165-251262220fa4 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR59\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.143 Distributor\_\_24\_GICD\_IPRIORITYR60 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR60 | | | | | reg32 | | 0x4f0 |
| offset | | 1264 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=41435955-82ca-48ee-aa29-da8960f57c26 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR60\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.144 Distributor\_\_24\_GICD\_IPRIORITYR61 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR61 | | | | | reg32 | | 0x4f4 |
| offset | | 1268 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=193766fb-21a4-46d5-b1a5-55e0521a20aa | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR61\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.145 Distributor\_\_24\_GICD\_IPRIORITYR62 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR62 | | | | | reg32 | | 0x4f8 |
| offset | | 1272 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8afa4633-9cef-4f4e-802d-d695694f2a71 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR62\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.146 Distributor\_\_24\_GICD\_IPRIORITYR63 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR63 | | | | | reg32 | | 0x4fc |
| offset | | 1276 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=237b63b0-4f02-48da-908d-f3b22803b9c6 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR63\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.147 Distributor\_\_24\_GICD\_IPRIORITYR64 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR64 | | | | | reg32 | | 0x500 |
| offset | | 1280 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4afb31ab-f58e-44dd-b1e2-d592a72abf00 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR64\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.148 Distributor\_\_24\_GICD\_IPRIORITYR65 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR65 | | | | | reg32 | | 0x504 |
| offset | | 1284 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=84666b20-241c-4b5e-97b0-f91a3494be9f | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR65\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.149 Distributor\_\_24\_GICD\_IPRIORITYR66 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR66 | | | | | reg32 | | 0x508 |
| offset | | 1288 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=557f3b1d-71d9-4de7-b6d9-bc42035312af | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR66\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.150 Distributor\_\_24\_GICD\_IPRIORITYR67 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR67 | | | | | reg32 | | 0x50c |
| offset | | 1292 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1c13ac81-ef0a-4c82-ab48-490a1f230775 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR67\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.151 Distributor\_\_24\_GICD\_IPRIORITYR68 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR68 | | | | | reg32 | | 0x510 |
| offset | | 1296 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ed35a7c7-3cd4-41e0-8f12-759a065b4fab | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR68\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.152 Distributor\_\_24\_GICD\_IPRIORITYR69 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR69 | | | | | reg32 | | 0x514 |
| offset | | 1300 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7c9ea278-e02e-4b9e-a9d8-3e4b6c8d0730 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR69\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.153 Distributor\_\_24\_GICD\_IPRIORITYR70 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR70 | | | | | reg32 | | 0x518 |
| offset | | 1304 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1fcf13e6-7d67-4136-8904-89e8ccf013b6 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR70\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.154 Distributor\_\_24\_GICD\_IPRIORITYR71 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR71 | | | | | reg32 | | 0x51c |
| offset | | 1308 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6f08a626-4da0-49a1-bbd1-cdfbb6a66660 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR71\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.155 Distributor\_\_24\_GICD\_IPRIORITYR72 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR72 | | | | | reg32 | | 0x520 |
| offset | | 1312 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=734ea6f2-c776-444a-bd4a-7d6bf8d132e5 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR72\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.156 Distributor\_\_24\_GICD\_IPRIORITYR73 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR73 | | | | | reg32 | | 0x524 |
| offset | | 1316 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=78302133-8e34-4c2e-9c22-ece40db53512 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR73\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.157 Distributor\_\_24\_GICD\_IPRIORITYR74 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR74 | | | | | reg32 | | 0x528 |
| offset | | 1320 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3c65d25d-a733-41cd-a0ba-cc624b7d9f54 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR74\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.158 Distributor\_\_24\_GICD\_IPRIORITYR75 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR75 | | | | | reg32 | | 0x52c |
| offset | | 1324 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=adcdacc9-03af-43d8-a725-bd559c9cc4c2 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR75\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.159 Distributor\_\_24\_GICD\_IPRIORITYR76 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR76 | | | | | reg32 | | 0x530 |
| offset | | 1328 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1485e5da-2f66-49ad-991a-302b10544bd0 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR76\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.160 Distributor\_\_24\_GICD\_IPRIORITYR77 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR77 | | | | | reg32 | | 0x534 |
| offset | | 1332 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7bf45a9a-3fe9-49f4-957c-89b9c739938b | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR77\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.161 Distributor\_\_24\_GICD\_IPRIORITYR78 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR78 | | | | | reg32 | | 0x538 |
| offset | | 1336 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a2d79fbb-5095-4602-b7a1-dbbf9045ea2b | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR78\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.162 Distributor\_\_24\_GICD\_IPRIORITYR79 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR79 | | | | | reg32 | | 0x53c |
| offset | | 1340 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=66002975-e9d7-44ec-b967-faaf6e739e18 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR79\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.163 Distributor\_\_24\_GICD\_IPRIORITYR80 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR80 | | | | | reg32 | | 0x540 |
| offset | | 1344 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e21d1832-2600-4ec9-add3-20561f857fa1 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR80\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.164 Distributor\_\_24\_GICD\_IPRIORITYR81 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR81 | | | | | reg32 | | 0x544 |
| offset | | 1348 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=41ea25f7-c2fc-40b0-989e-5fc4b81e4dd5 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR81\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.165 Distributor\_\_24\_GICD\_IPRIORITYR82 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR82 | | | | | reg32 | | 0x548 |
| offset | | 1352 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8d8e5133-e2ea-456c-832f-b5a7fa1bcb49 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR82\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.166 Distributor\_\_24\_GICD\_IPRIORITYR83 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR83 | | | | | reg32 | | 0x54c |
| offset | | 1356 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=394deae5-8bd8-461c-a85c-c2276f16a530 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR83\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.167 Distributor\_\_24\_GICD\_IPRIORITYR84 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR84 | | | | | reg32 | | 0x550 |
| offset | | 1360 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bfe2f4c2-9292-44cc-bdd8-4c90de0a7599 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR84\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.168 Distributor\_\_24\_GICD\_IPRIORITYR85 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR85 | | | | | reg32 | | 0x554 |
| offset | | 1364 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5d46225a-6588-4afb-9776-a5738e22e6b2 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR85\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.169 Distributor\_\_24\_GICD\_IPRIORITYR86 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR86 | | | | | reg32 | | 0x558 |
| offset | | 1368 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4864c880-9097-498d-af90-3241f6d179a0 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR86\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.170 Distributor\_\_24\_GICD\_IPRIORITYR87 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR87 | | | | | reg32 | | 0x55c |
| offset | | 1372 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4f13fa76-3822-4ff7-baea-e56a30314c26 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR87\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.171 Distributor\_\_24\_GICD\_IPRIORITYR88 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR88 | | | | | reg32 | | 0x560 |
| offset | | 1376 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0eb9c56a-5c54-42e3-93eb-163c6e95cf00 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR88\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.172 Distributor\_\_24\_GICD\_IPRIORITYR89 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR89 | | | | | reg32 | | 0x564 |
| offset | | 1380 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a9fb5f4e-732f-4317-a5f0-794600d0230f | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR89\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.173 Distributor\_\_24\_GICD\_IPRIORITYR90 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR90 | | | | | reg32 | | 0x568 |
| offset | | 1384 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e89bd4a0-fbdf-433e-a0ea-ea3db0b54e04 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR90\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.174 Distributor\_\_24\_GICD\_IPRIORITYR91 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR91 | | | | | reg32 | | 0x56c |
| offset | | 1388 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b15315ea-6a21-4a47-a242-4d15c3ce3400 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR91\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.175 Distributor\_\_24\_GICD\_IPRIORITYR92 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR92 | | | | | reg32 | | 0x570 |
| offset | | 1392 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=714a9968-caa9-48e3-a5f2-ff4b51265842 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR92\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.176 Distributor\_\_24\_GICD\_IPRIORITYR93 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR93 | | | | | reg32 | | 0x574 |
| offset | | 1396 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=55a8f562-9cd8-4a5b-bd75-abbd4afbe4b2 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR93\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.177 Distributor\_\_24\_GICD\_IPRIORITYR94 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR94 | | | | | reg32 | | 0x578 |
| offset | | 1400 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e23b056f-c534-4555-b49c-3c2e3bd1138c | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR94\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.178 Distributor\_\_24\_GICD\_IPRIORITYR95 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR95 | | | | | reg32 | | 0x57c |
| offset | | 1404 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=53f7caed-8422-4d46-a6fc-3b31e6e11f86 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR95\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.179 Distributor\_\_24\_GICD\_IPRIORITYR96 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR96 | | | | | reg32 | | 0x580 |
| offset | | 1408 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=df3fa7f1-c924-41ba-aa05-6405ffcd7979 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR96\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.180 Distributor\_\_24\_GICD\_IPRIORITYR97 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR97 | | | | | reg32 | | 0x584 |
| offset | | 1412 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f0f9f625-a48e-4e07-9917-e40b4086ef37 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR97\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.181 Distributor\_\_24\_GICD\_IPRIORITYR98 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR98 | | | | | reg32 | | 0x588 |
| offset | | 1416 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=90a96658-452f-4b6d-998f-26af4d46d25b | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR98\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.182 Distributor\_\_24\_GICD\_IPRIORITYR99 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR99 | | | | | reg32 | | 0x58c |
| offset | | 1420 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=67b83720-5f51-4db4-b588-c6ac14b2bff2 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR99\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.183 Distributor\_\_24\_GICD\_IPRIORITYR100 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR100 | | | | | reg32 | | 0x590 |
| offset | | 1424 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b7091b0c-21e0-42fe-86dd-8e5d353a9f00 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR100\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.184 Distributor\_\_24\_GICD\_IPRIORITYR101 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR101 | | | | | reg32 | | 0x594 |
| offset | | 1428 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2ea494e2-8946-4fc0-94a7-9957e43d9c25 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR101\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.185 Distributor\_\_24\_GICD\_IPRIORITYR102 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR102 | | | | | reg32 | | 0x598 |
| offset | | 1432 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=057a3726-0394-470f-9c89-ae5e875c6267 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR102\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.186 Distributor\_\_24\_GICD\_IPRIORITYR103 | | | | | | | | Distributor\_\_24\_GICD\_IPRIORITYR103 | | | | | reg32 | | 0x59c |
| offset | | 1436 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9f6159f7-6176-4c69-89f4-7a39fdc71085 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_24\_GICD\_IPRIORITYR103\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.187 Distributor\_\_26\_GICD\_ITARGETSR8 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR8 | | | | | reg32 | | 0x820 |
| offset | | 2080 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2381eddd-1f73-4484-8ddf-69c44275777f | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR8 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.188 Distributor\_\_26\_GICD\_ITARGETSR9 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR9 | | | | | reg32 | | 0x824 |
| offset | | 2084 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4105d759-cfc0-49fc-bad9-6b037a2c47c3 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR9 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.189 Distributor\_\_26\_GICD\_ITARGETSR10 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR10 | | | | | reg32 | | 0x828 |
| offset | | 2088 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2461d9ee-4c54-4f39-931d-e68ded9c778a | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR10 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.190 Distributor\_\_26\_GICD\_ITARGETSR11 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR11 | | | | | reg32 | | 0x82c |
| offset | | 2092 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=37dd1998-9ce1-4e36-98a5-4509864765cf | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR11 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.191 Distributor\_\_26\_GICD\_ITARGETSR12 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR12 | | | | | reg32 | | 0x830 |
| offset | | 2096 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8411a88f-9025-4e1a-a208-04c58969e0c4 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR12 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.192 Distributor\_\_26\_GICD\_ITARGETSR13 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR13 | | | | | reg32 | | 0x834 |
| offset | | 2100 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3dd2e337-7b61-4834-b73e-1eb63ba3e6ca | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR13 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.193 Distributor\_\_26\_GICD\_ITARGETSR14 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR14 | | | | | reg32 | | 0x838 |
| offset | | 2104 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=959a9af0-464c-411f-8a58-0962eef99320 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR14 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.194 Distributor\_\_26\_GICD\_ITARGETSR15 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR15 | | | | | reg32 | | 0x83c |
| offset | | 2108 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f89091bb-49ca-4c39-85cf-b46f5f34765c | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR15 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.195 Distributor\_\_26\_GICD\_ITARGETSR16 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR16 | | | | | reg32 | | 0x840 |
| offset | | 2112 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a6b4824f-d809-4463-bc4a-bb3fb645978d | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR16 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.196 Distributor\_\_26\_GICD\_ITARGETSR17 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR17 | | | | | reg32 | | 0x844 |
| offset | | 2116 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dc84032c-40a9-4764-82a2-6aadbd29ff43 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR17 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.197 Distributor\_\_26\_GICD\_ITARGETSR18 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR18 | | | | | reg32 | | 0x848 |
| offset | | 2120 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=38ceaca3-b84e-41b3-9a71-74e45df515f6 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR18 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.198 Distributor\_\_26\_GICD\_ITARGETSR19 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR19 | | | | | reg32 | | 0x84c |
| offset | | 2124 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1e41f55d-78d9-4ae8-bfb3-0058cb850d81 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR19 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.199 Distributor\_\_26\_GICD\_ITARGETSR20 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR20 | | | | | reg32 | | 0x850 |
| offset | | 2128 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5a95e4d2-4931-4484-b4b0-02ea2256622f | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR20 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.200 Distributor\_\_26\_GICD\_ITARGETSR21 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR21 | | | | | reg32 | | 0x854 |
| offset | | 2132 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9f17d990-99f4-4161-ad9b-ef36582d5245 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR21 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.201 Distributor\_\_26\_GICD\_ITARGETSR22 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR22 | | | | | reg32 | | 0x858 |
| offset | | 2136 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ac17383c-4f5a-4596-bed0-54b63aec15b9 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR22 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.202 Distributor\_\_26\_GICD\_ITARGETSR23 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR23 | | | | | reg32 | | 0x85c |
| offset | | 2140 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e6ec00eb-c1ac-412d-94b0-4f71853e0d6c | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR23 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.203 Distributor\_\_26\_GICD\_ITARGETSR24 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR24 | | | | | reg32 | | 0x860 |
| offset | | 2144 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=389bab37-f1ba-4da5-a6f0-0daf4798135b | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR24 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.204 Distributor\_\_26\_GICD\_ITARGETSR25 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR25 | | | | | reg32 | | 0x864 |
| offset | | 2148 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9a96a555-41d1-4d62-a7a4-0261b6d20e36 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR25 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.205 Distributor\_\_26\_GICD\_ITARGETSR26 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR26 | | | | | reg32 | | 0x868 |
| offset | | 2152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=22a6a4bb-4bf8-4aab-88a4-010f9ab37183 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR26 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.206 Distributor\_\_26\_GICD\_ITARGETSR27 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR27 | | | | | reg32 | | 0x86c |
| offset | | 2156 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3f01a9a1-004c-41b5-b15e-d1067b39f7af | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR27 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.207 Distributor\_\_26\_GICD\_ITARGETSR28 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR28 | | | | | reg32 | | 0x870 |
| offset | | 2160 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9ebe7e25-6419-4303-931a-3e3858dae321 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR28 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.208 Distributor\_\_26\_GICD\_ITARGETSR29 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR29 | | | | | reg32 | | 0x874 |
| offset | | 2164 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=42449567-0825-4ce3-8cbe-a43087894994 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR29 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.209 Distributor\_\_26\_GICD\_ITARGETSR30 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR30 | | | | | reg32 | | 0x878 |
| offset | | 2168 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2d4f809e-0286-4dcf-a902-6ee91635817d | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR30 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.210 Distributor\_\_26\_GICD\_ITARGETSR31 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR31 | | | | | reg32 | | 0x87c |
| offset | | 2172 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=73c7674b-21af-41b6-92cf-e60e5da52836 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR31 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.211 Distributor\_\_26\_GICD\_ITARGETSR32 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR32 | | | | | reg32 | | 0x880 |
| offset | | 2176 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d1a127f3-6345-4a3c-a909-e592c42f8202 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR32 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.212 Distributor\_\_26\_GICD\_ITARGETSR33 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR33 | | | | | reg32 | | 0x884 |
| offset | | 2180 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cebf233d-f60f-4f72-9604-e719068dc927 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR33 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.213 Distributor\_\_26\_GICD\_ITARGETSR34 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR34 | | | | | reg32 | | 0x888 |
| offset | | 2184 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=30c7b81d-4db9-468e-8e63-3ef9111d5599 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR34 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.214 Distributor\_\_26\_GICD\_ITARGETSR35 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR35 | | | | | reg32 | | 0x88c |
| offset | | 2188 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=39cff362-8fc7-43ed-9947-55d4f59246c5 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR35 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.215 Distributor\_\_26\_GICD\_ITARGETSR36 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR36 | | | | | reg32 | | 0x890 |
| offset | | 2192 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9e8d98f1-0954-4cae-8f86-c9f544d94e19 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR36 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.216 Distributor\_\_26\_GICD\_ITARGETSR37 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR37 | | | | | reg32 | | 0x894 |
| offset | | 2196 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0f6a1d97-d0c2-493b-87eb-c395012022ef | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR37 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.217 Distributor\_\_26\_GICD\_ITARGETSR38 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR38 | | | | | reg32 | | 0x898 |
| offset | | 2200 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b9f08e57-a892-4c28-82a6-94145be47078 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR38 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.218 Distributor\_\_26\_GICD\_ITARGETSR39 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR39 | | | | | reg32 | | 0x89c |
| offset | | 2204 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2074d234-09b8-4a91-9a69-e5e06317c8fb | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR39 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.219 Distributor\_\_26\_GICD\_ITARGETSR40 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR40 | | | | | reg32 | | 0x8a0 |
| offset | | 2208 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d6d09244-68e0-4dfa-ba74-69c135b9c712 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR40 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.220 Distributor\_\_26\_GICD\_ITARGETSR41 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR41 | | | | | reg32 | | 0x8a4 |
| offset | | 2212 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7b2a933c-3a2e-497a-8a7f-8971369acf19 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR41 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.221 Distributor\_\_26\_GICD\_ITARGETSR42 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR42 | | | | | reg32 | | 0x8a8 |
| offset | | 2216 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cccda267-e9fa-4ae7-bc1b-630332002243 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR42 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.222 Distributor\_\_26\_GICD\_ITARGETSR43 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR43 | | | | | reg32 | | 0x8ac |
| offset | | 2220 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=85c8f739-bc46-4851-9538-b7a0e9ad65ad | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR43 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.223 Distributor\_\_26\_GICD\_ITARGETSR44 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR44 | | | | | reg32 | | 0x8b0 |
| offset | | 2224 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6d1daf68-ecb8-446f-8117-f0aaa808ed5d | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR44 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.224 Distributor\_\_26\_GICD\_ITARGETSR45 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR45 | | | | | reg32 | | 0x8b4 |
| offset | | 2228 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=57fdbe19-7b08-4247-b4e0-2b862cc145e6 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR45 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.225 Distributor\_\_26\_GICD\_ITARGETSR46 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR46 | | | | | reg32 | | 0x8b8 |
| offset | | 2232 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f8b11148-e6bc-4dba-84cb-634bc21fe050 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR46 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.226 Distributor\_\_26\_GICD\_ITARGETSR47 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR47 | | | | | reg32 | | 0x8bc |
| offset | | 2236 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=22cc1a93-860b-488f-90b1-77c68307b0ca | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR47 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.227 Distributor\_\_26\_GICD\_ITARGETSR48 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR48 | | | | | reg32 | | 0x8c0 |
| offset | | 2240 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=72ecb367-c77c-4371-a47c-390f6ef854ff | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR48 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.228 Distributor\_\_26\_GICD\_ITARGETSR49 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR49 | | | | | reg32 | | 0x8c4 |
| offset | | 2244 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d7f8245d-1f8c-46b4-8c5a-ec80ece88151 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR49 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.229 Distributor\_\_26\_GICD\_ITARGETSR50 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR50 | | | | | reg32 | | 0x8c8 |
| offset | | 2248 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e881bec4-5e6e-4862-8811-00a717566f3b | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR50 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.230 Distributor\_\_26\_GICD\_ITARGETSR51 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR51 | | | | | reg32 | | 0x8cc |
| offset | | 2252 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0ace824b-c6ed-4feb-864f-e6450d3c7ffb | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR51 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.231 Distributor\_\_26\_GICD\_ITARGETSR52 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR52 | | | | | reg32 | | 0x8d0 |
| offset | | 2256 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=81d8cffb-3f9f-4ce2-8543-e8fc7928381b | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR52 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.232 Distributor\_\_26\_GICD\_ITARGETSR53 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR53 | | | | | reg32 | | 0x8d4 |
| offset | | 2260 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a10421e8-2138-4107-96de-4dbca821173c | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR53 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.233 Distributor\_\_26\_GICD\_ITARGETSR54 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR54 | | | | | reg32 | | 0x8d8 |
| offset | | 2264 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c4d2c97d-7200-4284-aa90-0671f8816094 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR54 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.234 Distributor\_\_26\_GICD\_ITARGETSR55 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR55 | | | | | reg32 | | 0x8dc |
| offset | | 2268 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=57e1d176-f4b0-479e-98f4-df832514f3a7 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR55 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.235 Distributor\_\_26\_GICD\_ITARGETSR56 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR56 | | | | | reg32 | | 0x8e0 |
| offset | | 2272 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3f53fc87-5867-450c-bcca-0a02c8d2ec9f | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR56 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.236 Distributor\_\_26\_GICD\_ITARGETSR57 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR57 | | | | | reg32 | | 0x8e4 |
| offset | | 2276 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=27d1be38-ccba-4d20-b688-9729958d8e4b | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR57 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.237 Distributor\_\_26\_GICD\_ITARGETSR58 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR58 | | | | | reg32 | | 0x8e8 |
| offset | | 2280 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3418e9ba-3606-4313-ba32-ef267f12a05e | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR58 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.238 Distributor\_\_26\_GICD\_ITARGETSR59 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR59 | | | | | reg32 | | 0x8ec |
| offset | | 2284 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=95374902-00f2-4ef2-8185-20a38283333d | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR59 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.239 Distributor\_\_26\_GICD\_ITARGETSR60 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR60 | | | | | reg32 | | 0x8f0 |
| offset | | 2288 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b46cd6e2-a5f6-4b41-943f-05f9882985e5 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR60 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.240 Distributor\_\_26\_GICD\_ITARGETSR61 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR61 | | | | | reg32 | | 0x8f4 |
| offset | | 2292 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=27b9a4b6-2687-488b-9078-e96db92bc836 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR61 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.241 Distributor\_\_26\_GICD\_ITARGETSR62 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR62 | | | | | reg32 | | 0x8f8 |
| offset | | 2296 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9cdc0e00-3445-4843-bbc7-2f507839ef0c | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR62 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.242 Distributor\_\_26\_GICD\_ITARGETSR63 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR63 | | | | | reg32 | | 0x8fc |
| offset | | 2300 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4d1ef185-23da-4087-a8bc-ac55da2a12d3 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR63 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.243 Distributor\_\_26\_GICD\_ITARGETSR64 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR64 | | | | | reg32 | | 0x900 |
| offset | | 2304 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e1b3ae3f-36cb-47c1-92ab-96e8d06822d4 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR64 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.244 Distributor\_\_26\_GICD\_ITARGETSR65 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR65 | | | | | reg32 | | 0x904 |
| offset | | 2308 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=22267089-06c2-4553-b40a-77d862b5c55a | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR65 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.245 Distributor\_\_26\_GICD\_ITARGETSR66 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR66 | | | | | reg32 | | 0x908 |
| offset | | 2312 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d209b268-d861-425a-b0bb-404154497cb8 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR66 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.246 Distributor\_\_26\_GICD\_ITARGETSR67 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR67 | | | | | reg32 | | 0x90c |
| offset | | 2316 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2fdd061c-b5b5-4634-9047-63a43cea8b71 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR67 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.247 Distributor\_\_26\_GICD\_ITARGETSR68 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR68 | | | | | reg32 | | 0x910 |
| offset | | 2320 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ddc32e69-6eec-4f80-ba0b-1e7f7e5aeee7 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR68 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.248 Distributor\_\_26\_GICD\_ITARGETSR69 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR69 | | | | | reg32 | | 0x914 |
| offset | | 2324 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fcbbe83e-9983-42cd-ad51-81cea5b43815 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR69 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.249 Distributor\_\_26\_GICD\_ITARGETSR70 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR70 | | | | | reg32 | | 0x918 |
| offset | | 2328 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f10cc9da-a736-4812-9764-fa3792736b04 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR70 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.250 Distributor\_\_26\_GICD\_ITARGETSR71 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR71 | | | | | reg32 | | 0x91c |
| offset | | 2332 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bc575550-7e47-4e56-a920-3d91225c8706 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR71 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.251 Distributor\_\_26\_GICD\_ITARGETSR72 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR72 | | | | | reg32 | | 0x920 |
| offset | | 2336 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bcb9dd3e-e1de-4f31-af9d-d584959fe611 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR72 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.252 Distributor\_\_26\_GICD\_ITARGETSR73 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR73 | | | | | reg32 | | 0x924 |
| offset | | 2340 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f83dcbeb-e66d-42f1-88fe-769f6972daae | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR73 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.253 Distributor\_\_26\_GICD\_ITARGETSR74 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR74 | | | | | reg32 | | 0x928 |
| offset | | 2344 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b706185d-4ca5-490a-83d5-5f55a0001b22 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR74 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.254 Distributor\_\_26\_GICD\_ITARGETSR75 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR75 | | | | | reg32 | | 0x92c |
| offset | | 2348 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0c5b16a0-ed22-4b7a-a704-9b9e2df99daf | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR75 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.255 Distributor\_\_26\_GICD\_ITARGETSR76 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR76 | | | | | reg32 | | 0x930 |
| offset | | 2352 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=940a027f-b201-46a1-a6c2-518f0312614d | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR76 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.256 Distributor\_\_26\_GICD\_ITARGETSR77 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR77 | | | | | reg32 | | 0x934 |
| offset | | 2356 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=77d62847-1eaf-4b57-8c24-a1ecd6cf8d47 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR77 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.257 Distributor\_\_26\_GICD\_ITARGETSR78 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR78 | | | | | reg32 | | 0x938 |
| offset | | 2360 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3f6ed512-f724-4b94-b12a-0762f3272626 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR78 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.258 Distributor\_\_26\_GICD\_ITARGETSR79 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR79 | | | | | reg32 | | 0x93c |
| offset | | 2364 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=92c75245-095e-4b73-ad98-5b556f01d9f9 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR79 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.259 Distributor\_\_26\_GICD\_ITARGETSR80 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR80 | | | | | reg32 | | 0x940 |
| offset | | 2368 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e2ec2ff0-978b-4aec-b92a-671b2bfb7d13 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR80 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.260 Distributor\_\_26\_GICD\_ITARGETSR81 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR81 | | | | | reg32 | | 0x944 |
| offset | | 2372 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=52312e9a-ad18-4789-9182-a9aa8da84e09 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR81 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.261 Distributor\_\_26\_GICD\_ITARGETSR82 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR82 | | | | | reg32 | | 0x948 |
| offset | | 2376 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8a7e704e-6176-4d20-a381-3038d2216459 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR82 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.262 Distributor\_\_26\_GICD\_ITARGETSR83 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR83 | | | | | reg32 | | 0x94c |
| offset | | 2380 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d026a4ff-cc8d-43b2-b8a4-79bbb17488ca | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR83 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.263 Distributor\_\_26\_GICD\_ITARGETSR84 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR84 | | | | | reg32 | | 0x950 |
| offset | | 2384 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7fb20520-0a52-4699-b1aa-01e026794777 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR84 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.264 Distributor\_\_26\_GICD\_ITARGETSR85 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR85 | | | | | reg32 | | 0x954 |
| offset | | 2388 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f150b15e-4436-4e51-ba22-599f92fc20b0 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR85 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.265 Distributor\_\_26\_GICD\_ITARGETSR86 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR86 | | | | | reg32 | | 0x958 |
| offset | | 2392 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=64314a8d-fc7a-476c-b324-c1584c80958f | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR86 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.266 Distributor\_\_26\_GICD\_ITARGETSR87 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR87 | | | | | reg32 | | 0x95c |
| offset | | 2396 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4f4d121d-3efc-4a5e-b744-f7d7b3334137 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR87 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.267 Distributor\_\_26\_GICD\_ITARGETSR88 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR88 | | | | | reg32 | | 0x960 |
| offset | | 2400 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=635602fc-d3d8-435b-b9ab-33c2dc0b9712 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR88 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.268 Distributor\_\_26\_GICD\_ITARGETSR89 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR89 | | | | | reg32 | | 0x964 |
| offset | | 2404 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f591b118-d099-4ff1-9e13-7bfff20ec41f | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR89 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.269 Distributor\_\_26\_GICD\_ITARGETSR90 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR90 | | | | | reg32 | | 0x968 |
| offset | | 2408 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2c1367e7-78e9-467a-9b34-a9dbd6290404 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR90 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.270 Distributor\_\_26\_GICD\_ITARGETSR91 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR91 | | | | | reg32 | | 0x96c |
| offset | | 2412 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c512ebb8-d817-489d-85c6-24402066240e | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR91 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.271 Distributor\_\_26\_GICD\_ITARGETSR92 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR92 | | | | | reg32 | | 0x970 |
| offset | | 2416 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9cbf4b43-1df6-4618-b569-e49af8d37b47 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR92 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.272 Distributor\_\_26\_GICD\_ITARGETSR93 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR93 | | | | | reg32 | | 0x974 |
| offset | | 2420 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=27e7cecb-05c2-4da9-b2ad-cc4c5401a2c2 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR93 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.273 Distributor\_\_26\_GICD\_ITARGETSR94 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR94 | | | | | reg32 | | 0x978 |
| offset | | 2424 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=21a0e9e4-210c-41c0-832b-ffae04c6c4f0 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR94 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.274 Distributor\_\_26\_GICD\_ITARGETSR95 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR95 | | | | | reg32 | | 0x97c |
| offset | | 2428 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=30a2c002-9c9a-46ed-bf8d-be6a39338b43 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR95 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.275 Distributor\_\_26\_GICD\_ITARGETSR96 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR96 | | | | | reg32 | | 0x980 |
| offset | | 2432 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5d6ad822-0ef3-4c52-a529-44508ac1a9f7 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR96 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.276 Distributor\_\_26\_GICD\_ITARGETSR97 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR97 | | | | | reg32 | | 0x984 |
| offset | | 2436 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b56b9c97-8c74-45e4-b035-caf5ff53ce33 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR97 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.277 Distributor\_\_26\_GICD\_ITARGETSR98 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR98 | | | | | reg32 | | 0x988 |
| offset | | 2440 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=58e78de9-93f1-41fb-a9bb-4ff0357d0ad4 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR98 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.278 Distributor\_\_26\_GICD\_ITARGETSR99 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR99 | | | | | reg32 | | 0x98c |
| offset | | 2444 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0fed46dc-edff-479a-b81f-901e820f84e3 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR99 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.279 Distributor\_\_26\_GICD\_ITARGETSR100 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR100 | | | | | reg32 | | 0x990 |
| offset | | 2448 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5e4b857e-deab-4636-a676-43927fabe29a | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR100 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.280 Distributor\_\_26\_GICD\_ITARGETSR101 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR101 | | | | | reg32 | | 0x994 |
| offset | | 2452 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7dfde43e-f494-4429-9687-81cafd6c507e | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR101 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.281 Distributor\_\_26\_GICD\_ITARGETSR102 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR102 | | | | | reg32 | | 0x998 |
| offset | | 2456 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5056cf4d-7944-46aa-b334-b8e4fde5541f | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR102 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.282 Distributor\_\_26\_GICD\_ITARGETSR103 | | | | | | | | Distributor\_\_26\_GICD\_ITARGETSR103 | | | | | reg32 | | 0x99c |
| offset | | 2460 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c69e40fa-6d22-4ec3-9139-4fd2c7b9f4d8 | | | | | | | | | | | | | | | |
| Interrupt Processor Targets Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_26\_GICD\_ITARGETSR103 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.283 Distributor\_\_29\_GICD\_ICFGR2 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR2 | | | | | reg32 | | 0xc08 |
| offset | | 3080 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0eb82d7f-55e5-4f26-ba66-79a5b3df0acc | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR2\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.284 Distributor\_\_29\_GICD\_ICFGR3 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR3 | | | | | reg32 | | 0xc0c |
| offset | | 3084 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=64de1bc4-cb0b-4702-9505-10301a18866f | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.285 Distributor\_\_29\_GICD\_ICFGR4 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR4 | | | | | reg32 | | 0xc10 |
| offset | | 3088 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a17bd0b2-47b5-4ef9-90cb-56391be1317f | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.286 Distributor\_\_29\_GICD\_ICFGR5 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR5 | | | | | reg32 | | 0xc14 |
| offset | | 3092 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dc08bbf6-d917-4ccd-828f-ae1ddb40d93a | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR5\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.287 Distributor\_\_29\_GICD\_ICFGR6 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR6 | | | | | reg32 | | 0xc18 |
| offset | | 3096 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=38dc6d95-6a85-4a0b-b22a-521895344e57 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.288 Distributor\_\_29\_GICD\_ICFGR7 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR7 | | | | | reg32 | | 0xc1c |
| offset | | 3100 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a589bdfe-3f7a-40f3-a4b7-ea9c100ec43e | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR7\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.289 Distributor\_\_29\_GICD\_ICFGR8 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR8 | | | | | reg32 | | 0xc20 |
| offset | | 3104 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=44109a9f-7a9e-4f2c-9542-a1ccaa035415 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR8\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.290 Distributor\_\_29\_GICD\_ICFGR9 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR9 | | | | | reg32 | | 0xc24 |
| offset | | 3108 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=93e8a750-d985-4f98-9035-180afb02f387 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR9\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.291 Distributor\_\_29\_GICD\_ICFGR10 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR10 | | | | | reg32 | | 0xc28 |
| offset | | 3112 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e33b9545-209f-400c-b48b-ba7b2b2e7fe4 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR10\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.292 Distributor\_\_29\_GICD\_ICFGR11 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR11 | | | | | reg32 | | 0xc2c |
| offset | | 3116 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=38ca17e6-68b2-46cb-9f0a-affab2395451 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR11\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.293 Distributor\_\_29\_GICD\_ICFGR12 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR12 | | | | | reg32 | | 0xc30 |
| offset | | 3120 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6a0c0108-de89-4d17-aad7-07718bcc1e41 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR12\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.294 Distributor\_\_29\_GICD\_ICFGR13 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR13 | | | | | reg32 | | 0xc34 |
| offset | | 3124 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=54df87ad-a77d-4587-856e-a07510d12899 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR13\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.295 Distributor\_\_29\_GICD\_ICFGR14 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR14 | | | | | reg32 | | 0xc38 |
| offset | | 3128 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bf2ae143-0826-4490-b436-07e29a28cc7b | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR14\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.296 Distributor\_\_29\_GICD\_ICFGR15 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR15 | | | | | reg32 | | 0xc3c |
| offset | | 3132 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=305beab5-43a7-439e-8cd0-a33a0f503a82 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR15\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.297 Distributor\_\_29\_GICD\_ICFGR16 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR16 | | | | | reg32 | | 0xc40 |
| offset | | 3136 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=df3a3ecd-d25e-4032-9198-92ade5c9f8ee | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR16\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.298 Distributor\_\_29\_GICD\_ICFGR17 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR17 | | | | | reg32 | | 0xc44 |
| offset | | 3140 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=17ea6bbd-b0b7-46ea-b378-2c49efc3895e | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR17\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.299 Distributor\_\_29\_GICD\_ICFGR18 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR18 | | | | | reg32 | | 0xc48 |
| offset | | 3144 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fea3e638-2008-48ef-bfd9-c9aeed929fce | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR18\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.300 Distributor\_\_29\_GICD\_ICFGR19 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR19 | | | | | reg32 | | 0xc4c |
| offset | | 3148 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=91020bfb-96e9-4be1-9620-0dbac00a6105 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR19\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.301 Distributor\_\_29\_GICD\_ICFGR20 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR20 | | | | | reg32 | | 0xc50 |
| offset | | 3152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ee1d05ba-4e49-461a-a47a-fba48ec15ec5 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR20\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.302 Distributor\_\_29\_GICD\_ICFGR21 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR21 | | | | | reg32 | | 0xc54 |
| offset | | 3156 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0540e483-c828-48f7-804e-d19f98813a62 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR21\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.303 Distributor\_\_29\_GICD\_ICFGR22 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR22 | | | | | reg32 | | 0xc58 |
| offset | | 3160 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=29a63ec9-d0b9-4e53-9018-54cbe348a79e | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR22\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.304 Distributor\_\_29\_GICD\_ICFGR23 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR23 | | | | | reg32 | | 0xc5c |
| offset | | 3164 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=523929a0-0bce-4071-a18e-373dba3070ae | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR23\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.305 Distributor\_\_29\_GICD\_ICFGR24 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR24 | | | | | reg32 | | 0xc60 |
| offset | | 3168 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=45b57099-9b01-4f84-bad1-ec75ec165d7a | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR24\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.306 Distributor\_\_29\_GICD\_ICFGR25 | | | | | | | | Distributor\_\_29\_GICD\_ICFGR25 | | | | | reg32 | | 0xc64 |
| offset | | 3172 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0dfaee5f-1a21-4fc0-95b9-4ee7d0ce7ae2 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_29\_GICD\_ICFGR25\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.307 Distributor\_\_37\_GICD\_IROUTER32\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER32\_lower | | | | | reg32 | | 0x6100 |
| offset | | 24832 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b2e95142-05be-4cea-89cc-8c20184f28ea | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER32\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER32\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER32\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.308 Distributor\_\_37\_GICD\_IROUTER32\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER32\_upper | | | | | reg32 | | 0x6104 |
| offset | | 24836 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=36231ef7-d1ef-4cbe-9472-520101d19c44 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER32\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.309 Distributor\_\_37\_GICD\_IROUTER33\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER33\_lower | | | | | reg32 | | 0x6108 |
| offset | | 24840 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6dbd6f60-3b47-47ad-8177-1fe4f5d2aa5b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER33\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER33\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER33\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.310 Distributor\_\_37\_GICD\_IROUTER33\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER33\_upper | | | | | reg32 | | 0x610c |
| offset | | 24844 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d51c91d9-857a-4062-84c4-c86a681feca7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER33\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.311 Distributor\_\_37\_GICD\_IROUTER34\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER34\_lower | | | | | reg32 | | 0x6110 |
| offset | | 24848 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9feff79c-c3eb-4996-a251-4837fcdc0cd6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER34\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER34\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER34\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.312 Distributor\_\_37\_GICD\_IROUTER34\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER34\_upper | | | | | reg32 | | 0x6114 |
| offset | | 24852 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4491f725-3beb-454b-b77a-262680774a85 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER34\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.313 Distributor\_\_37\_GICD\_IROUTER35\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER35\_lower | | | | | reg32 | | 0x6118 |
| offset | | 24856 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2fc00ec9-000e-4b7f-addb-3d3c8ba4c714 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER35\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER35\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER35\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.314 Distributor\_\_37\_GICD\_IROUTER35\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER35\_upper | | | | | reg32 | | 0x611c |
| offset | | 24860 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=308c96d2-294e-4986-916f-f08a0ef44169 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER35\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.315 Distributor\_\_37\_GICD\_IROUTER36\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER36\_lower | | | | | reg32 | | 0x6120 |
| offset | | 24864 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a15ee810-381e-4bd9-9ba7-adcf31272805 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER36\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER36\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER36\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.316 Distributor\_\_37\_GICD\_IROUTER36\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER36\_upper | | | | | reg32 | | 0x6124 |
| offset | | 24868 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9fd0ef2f-0d54-4b90-929a-edc6d4fd2d0d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER36\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.317 Distributor\_\_37\_GICD\_IROUTER37\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER37\_lower | | | | | reg32 | | 0x6128 |
| offset | | 24872 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9652b462-2da9-4e7b-8f93-23bdc4c1ce5f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER37\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER37\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER37\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.318 Distributor\_\_37\_GICD\_IROUTER37\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER37\_upper | | | | | reg32 | | 0x612c |
| offset | | 24876 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fd1389e2-3f9b-4ce6-b2f3-8758aa7b4750 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER37\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.319 Distributor\_\_37\_GICD\_IROUTER38\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER38\_lower | | | | | reg32 | | 0x6130 |
| offset | | 24880 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=88036539-7822-411b-8807-b2f79b2f9aad | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER38\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER38\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER38\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.320 Distributor\_\_37\_GICD\_IROUTER38\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER38\_upper | | | | | reg32 | | 0x6134 |
| offset | | 24884 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3afde622-8abc-40e6-b232-1a0a75494bd2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER38\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.321 Distributor\_\_37\_GICD\_IROUTER39\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER39\_lower | | | | | reg32 | | 0x6138 |
| offset | | 24888 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b33c4764-20b3-43b4-b532-2bd0c3515ced | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER39\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER39\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER39\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.322 Distributor\_\_37\_GICD\_IROUTER39\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER39\_upper | | | | | reg32 | | 0x613c |
| offset | | 24892 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e4259b85-cdd1-4606-aca0-6553fe1e239f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER39\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.323 Distributor\_\_37\_GICD\_IROUTER40\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER40\_lower | | | | | reg32 | | 0x6140 |
| offset | | 24896 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4dc546ae-17d5-435f-ab91-88560baf085a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER40\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER40\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER40\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.324 Distributor\_\_37\_GICD\_IROUTER40\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER40\_upper | | | | | reg32 | | 0x6144 |
| offset | | 24900 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dd005b44-e0f4-4ceb-8653-97499f099fc2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER40\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.325 Distributor\_\_37\_GICD\_IROUTER41\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER41\_lower | | | | | reg32 | | 0x6148 |
| offset | | 24904 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2fdea0fc-f1be-4155-9c78-187c36804605 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER41\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER41\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER41\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.326 Distributor\_\_37\_GICD\_IROUTER41\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER41\_upper | | | | | reg32 | | 0x614c |
| offset | | 24908 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0da96230-04ae-4477-b2b8-884576b07a9d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER41\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.327 Distributor\_\_37\_GICD\_IROUTER42\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER42\_lower | | | | | reg32 | | 0x6150 |
| offset | | 24912 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=aee973b4-2869-4db6-a806-21e2b02022e7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER42\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER42\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER42\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.328 Distributor\_\_37\_GICD\_IROUTER42\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER42\_upper | | | | | reg32 | | 0x6154 |
| offset | | 24916 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2e9f1c31-b5e7-4c3f-997f-f53104658542 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER42\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.329 Distributor\_\_37\_GICD\_IROUTER43\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER43\_lower | | | | | reg32 | | 0x6158 |
| offset | | 24920 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cb35fe33-a8fb-4741-9084-30a23cf033dd | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER43\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER43\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER43\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.330 Distributor\_\_37\_GICD\_IROUTER43\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER43\_upper | | | | | reg32 | | 0x615c |
| offset | | 24924 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b7b13733-f211-4b26-83e9-767fa76414a1 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER43\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.331 Distributor\_\_37\_GICD\_IROUTER44\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER44\_lower | | | | | reg32 | | 0x6160 |
| offset | | 24928 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5a8c310e-6d75-4dd5-af54-af16b1773b36 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER44\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER44\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER44\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.332 Distributor\_\_37\_GICD\_IROUTER44\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER44\_upper | | | | | reg32 | | 0x6164 |
| offset | | 24932 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1cde8877-55ce-480b-a43e-4e77b8527f1a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER44\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.333 Distributor\_\_37\_GICD\_IROUTER45\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER45\_lower | | | | | reg32 | | 0x6168 |
| offset | | 24936 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e528c1a7-183c-4be0-8872-76e9960f0cd4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER45\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER45\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER45\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.334 Distributor\_\_37\_GICD\_IROUTER45\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER45\_upper | | | | | reg32 | | 0x616c |
| offset | | 24940 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=27667761-f4c0-4ff0-8143-942f922e4b6a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER45\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.335 Distributor\_\_37\_GICD\_IROUTER46\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER46\_lower | | | | | reg32 | | 0x6170 |
| offset | | 24944 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=177e7363-d0ab-42ba-90f3-a3484d92d88f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER46\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER46\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER46\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.336 Distributor\_\_37\_GICD\_IROUTER46\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER46\_upper | | | | | reg32 | | 0x6174 |
| offset | | 24948 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=54eda3d7-f134-4240-a0b8-beb5e1e62fb9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER46\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.337 Distributor\_\_37\_GICD\_IROUTER47\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER47\_lower | | | | | reg32 | | 0x6178 |
| offset | | 24952 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bd99c6d6-f9ee-475d-9e7d-ac20f99852dd | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER47\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER47\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER47\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.338 Distributor\_\_37\_GICD\_IROUTER47\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER47\_upper | | | | | reg32 | | 0x617c |
| offset | | 24956 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=67a6fe6e-6547-4e54-848c-8575b20647d3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER47\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.339 Distributor\_\_37\_GICD\_IROUTER48\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER48\_lower | | | | | reg32 | | 0x6180 |
| offset | | 24960 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d499d6b1-fc33-420e-b5d1-41f64fd438b6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER48\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER48\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER48\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.340 Distributor\_\_37\_GICD\_IROUTER48\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER48\_upper | | | | | reg32 | | 0x6184 |
| offset | | 24964 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=38edebe7-be5d-4723-b9aa-e35fd9fbbdc0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER48\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.341 Distributor\_\_37\_GICD\_IROUTER49\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER49\_lower | | | | | reg32 | | 0x6188 |
| offset | | 24968 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3ca4f20d-c64a-45d9-b3fb-6956eed8afc4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER49\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER49\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER49\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.342 Distributor\_\_37\_GICD\_IROUTER49\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER49\_upper | | | | | reg32 | | 0x618c |
| offset | | 24972 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=34ae1366-e11a-43c2-8326-029c70812844 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER49\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.343 Distributor\_\_37\_GICD\_IROUTER50\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER50\_lower | | | | | reg32 | | 0x6190 |
| offset | | 24976 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=66d54648-bfed-476a-bb51-2bd70fc7ae3e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER50\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER50\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER50\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.344 Distributor\_\_37\_GICD\_IROUTER50\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER50\_upper | | | | | reg32 | | 0x6194 |
| offset | | 24980 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f2cf2063-0079-47e7-8877-c179c2efb6fb | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER50\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.345 Distributor\_\_37\_GICD\_IROUTER51\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER51\_lower | | | | | reg32 | | 0x6198 |
| offset | | 24984 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=af7dc048-3f23-453a-83f2-4dfc56ae017e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER51\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER51\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER51\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.346 Distributor\_\_37\_GICD\_IROUTER51\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER51\_upper | | | | | reg32 | | 0x619c |
| offset | | 24988 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0e64070b-8985-4c00-85a1-7d5ccb217967 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER51\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.347 Distributor\_\_37\_GICD\_IROUTER52\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER52\_lower | | | | | reg32 | | 0x61a0 |
| offset | | 24992 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d9e97a7d-789d-4f45-ab83-6b9c8b670275 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER52\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER52\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER52\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.348 Distributor\_\_37\_GICD\_IROUTER52\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER52\_upper | | | | | reg32 | | 0x61a4 |
| offset | | 24996 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f7bbe98e-dfb7-40a5-a51f-681545402ca9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER52\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.349 Distributor\_\_37\_GICD\_IROUTER53\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER53\_lower | | | | | reg32 | | 0x61a8 |
| offset | | 25000 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dc2fcf48-999a-4d76-94ce-77eceba68c1e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER53\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER53\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER53\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.350 Distributor\_\_37\_GICD\_IROUTER53\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER53\_upper | | | | | reg32 | | 0x61ac |
| offset | | 25004 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fa942620-1bb5-42a8-a95f-4abdf5ce2050 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER53\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.351 Distributor\_\_37\_GICD\_IROUTER54\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER54\_lower | | | | | reg32 | | 0x61b0 |
| offset | | 25008 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=62260cc5-ca24-4349-b471-8d580d4108eb | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER54\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER54\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER54\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.352 Distributor\_\_37\_GICD\_IROUTER54\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER54\_upper | | | | | reg32 | | 0x61b4 |
| offset | | 25012 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5f91ce34-c505-4355-88c5-7e55f914956d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER54\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.353 Distributor\_\_37\_GICD\_IROUTER55\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER55\_lower | | | | | reg32 | | 0x61b8 |
| offset | | 25016 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4fe0b2fe-90a9-42f3-8bfa-a04944e7e6cf | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER55\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER55\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER55\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.354 Distributor\_\_37\_GICD\_IROUTER55\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER55\_upper | | | | | reg32 | | 0x61bc |
| offset | | 25020 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=73eef8ea-9a20-45c1-ad1b-95fad18dbb2b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER55\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.355 Distributor\_\_37\_GICD\_IROUTER56\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER56\_lower | | | | | reg32 | | 0x61c0 |
| offset | | 25024 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f03f1c05-492c-470b-89ab-10f7ea8f14d1 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER56\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER56\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER56\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.356 Distributor\_\_37\_GICD\_IROUTER56\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER56\_upper | | | | | reg32 | | 0x61c4 |
| offset | | 25028 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c7e19290-e690-4558-8afe-ff7a62549040 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER56\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.357 Distributor\_\_37\_GICD\_IROUTER57\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER57\_lower | | | | | reg32 | | 0x61c8 |
| offset | | 25032 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=aaaca7dd-369c-4627-bc48-813cbd12e44b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER57\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER57\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER57\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.358 Distributor\_\_37\_GICD\_IROUTER57\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER57\_upper | | | | | reg32 | | 0x61cc |
| offset | | 25036 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=83714778-d4e8-4c3c-bf58-c30c17ac27c9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER57\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.359 Distributor\_\_37\_GICD\_IROUTER58\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER58\_lower | | | | | reg32 | | 0x61d0 |
| offset | | 25040 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9452cea0-6e65-4bac-875c-24c72a81d958 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER58\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER58\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER58\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.360 Distributor\_\_37\_GICD\_IROUTER58\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER58\_upper | | | | | reg32 | | 0x61d4 |
| offset | | 25044 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=41972de0-e1a5-498b-8cf5-2cb35f62db5a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER58\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.361 Distributor\_\_37\_GICD\_IROUTER59\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER59\_lower | | | | | reg32 | | 0x61d8 |
| offset | | 25048 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2a10550f-a211-459e-83ab-5ac5a5533aaa | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER59\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER59\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER59\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.362 Distributor\_\_37\_GICD\_IROUTER59\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER59\_upper | | | | | reg32 | | 0x61dc |
| offset | | 25052 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fa1d533a-50d1-4679-8bc1-197eb2e89bca | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER59\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.363 Distributor\_\_37\_GICD\_IROUTER60\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER60\_lower | | | | | reg32 | | 0x61e0 |
| offset | | 25056 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=92b7373d-3498-49f8-a5f4-2ca8cf8da57e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER60\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER60\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER60\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.364 Distributor\_\_37\_GICD\_IROUTER60\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER60\_upper | | | | | reg32 | | 0x61e4 |
| offset | | 25060 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d0338ac5-47e0-4b92-ac8b-ce797610e747 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER60\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.365 Distributor\_\_37\_GICD\_IROUTER61\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER61\_lower | | | | | reg32 | | 0x61e8 |
| offset | | 25064 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2c1f9d9c-ba6d-4cd2-8353-a20ca9f95973 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER61\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER61\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER61\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.366 Distributor\_\_37\_GICD\_IROUTER61\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER61\_upper | | | | | reg32 | | 0x61ec |
| offset | | 25068 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=08e6a842-c55c-431b-a2b7-545c1c46bd60 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER61\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.367 Distributor\_\_37\_GICD\_IROUTER62\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER62\_lower | | | | | reg32 | | 0x61f0 |
| offset | | 25072 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=637e1731-fbcc-4bf2-8872-aa6a35dc5aa5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER62\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER62\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER62\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.368 Distributor\_\_37\_GICD\_IROUTER62\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER62\_upper | | | | | reg32 | | 0x61f4 |
| offset | | 25076 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f1f53cf9-fe4d-4826-9d14-963dd68b93e4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER62\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.369 Distributor\_\_37\_GICD\_IROUTER63\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER63\_lower | | | | | reg32 | | 0x61f8 |
| offset | | 25080 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8b70f250-7626-4356-9e3f-5a1284a432e6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER63\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER63\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER63\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.370 Distributor\_\_37\_GICD\_IROUTER63\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER63\_upper | | | | | reg32 | | 0x61fc |
| offset | | 25084 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b73b5085-7a58-4d09-9df3-6cc909b9d919 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER63\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.371 Distributor\_\_37\_GICD\_IROUTER64\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER64\_lower | | | | | reg32 | | 0x6200 |
| offset | | 25088 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e98f6ff3-b553-4b96-9c8b-c4dfc7214a5d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER64\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER64\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER64\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.372 Distributor\_\_37\_GICD\_IROUTER64\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER64\_upper | | | | | reg32 | | 0x6204 |
| offset | | 25092 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7f258dae-0325-492a-bf68-6441b70d1e45 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER64\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.373 Distributor\_\_37\_GICD\_IROUTER65\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER65\_lower | | | | | reg32 | | 0x6208 |
| offset | | 25096 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=763726fe-7070-4b28-bdb6-45f8e38351ce | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER65\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER65\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER65\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.374 Distributor\_\_37\_GICD\_IROUTER65\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER65\_upper | | | | | reg32 | | 0x620c |
| offset | | 25100 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c036361d-9743-43d8-a368-6882328d3b1f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER65\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.375 Distributor\_\_37\_GICD\_IROUTER66\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER66\_lower | | | | | reg32 | | 0x6210 |
| offset | | 25104 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2675f121-c507-42be-8beb-a753a0d0ea9f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER66\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER66\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER66\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.376 Distributor\_\_37\_GICD\_IROUTER66\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER66\_upper | | | | | reg32 | | 0x6214 |
| offset | | 25108 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5f5b34db-9b0f-4330-8f13-f3446aa8a1fa | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER66\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.377 Distributor\_\_37\_GICD\_IROUTER67\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER67\_lower | | | | | reg32 | | 0x6218 |
| offset | | 25112 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=50471336-db0c-4f5c-adf9-e4c3031eda3e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER67\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER67\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER67\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.378 Distributor\_\_37\_GICD\_IROUTER67\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER67\_upper | | | | | reg32 | | 0x621c |
| offset | | 25116 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cea65cd7-26cc-4d6f-9002-8bce9270ec6f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER67\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.379 Distributor\_\_37\_GICD\_IROUTER68\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER68\_lower | | | | | reg32 | | 0x6220 |
| offset | | 25120 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1b1fbf8c-9e89-4070-ae9c-b5c9be605d08 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER68\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER68\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER68\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.380 Distributor\_\_37\_GICD\_IROUTER68\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER68\_upper | | | | | reg32 | | 0x6224 |
| offset | | 25124 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=90d8b5b3-ce24-4a5e-b88a-524a24baab00 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER68\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.381 Distributor\_\_37\_GICD\_IROUTER69\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER69\_lower | | | | | reg32 | | 0x6228 |
| offset | | 25128 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=75682dcb-069f-4932-b303-8501065a103b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER69\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER69\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER69\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.382 Distributor\_\_37\_GICD\_IROUTER69\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER69\_upper | | | | | reg32 | | 0x622c |
| offset | | 25132 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=457b3e1c-78ff-4d6e-b713-4ccc1171789c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER69\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.383 Distributor\_\_37\_GICD\_IROUTER70\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER70\_lower | | | | | reg32 | | 0x6230 |
| offset | | 25136 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b8aa63b1-5e1c-4b01-805b-9c78b4bc72f0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER70\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER70\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER70\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.384 Distributor\_\_37\_GICD\_IROUTER70\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER70\_upper | | | | | reg32 | | 0x6234 |
| offset | | 25140 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cb847926-1fc5-47ee-ad90-a984405161bb | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER70\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.385 Distributor\_\_37\_GICD\_IROUTER71\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER71\_lower | | | | | reg32 | | 0x6238 |
| offset | | 25144 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c34bdad8-46c2-43a7-9f96-4c8baefc9017 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER71\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER71\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER71\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.386 Distributor\_\_37\_GICD\_IROUTER71\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER71\_upper | | | | | reg32 | | 0x623c |
| offset | | 25148 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=69802ba3-1aaf-45f0-afde-67cab979f23d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER71\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.387 Distributor\_\_37\_GICD\_IROUTER72\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER72\_lower | | | | | reg32 | | 0x6240 |
| offset | | 25152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5501f18b-8939-4779-a4f5-50717019f33d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER72\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER72\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER72\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.388 Distributor\_\_37\_GICD\_IROUTER72\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER72\_upper | | | | | reg32 | | 0x6244 |
| offset | | 25156 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=501f5998-9076-41a5-93bd-5bf6dcd434fa | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER72\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.389 Distributor\_\_37\_GICD\_IROUTER73\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER73\_lower | | | | | reg32 | | 0x6248 |
| offset | | 25160 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=49898c14-f7f4-47dc-81b8-49bb95ae9e82 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER73\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER73\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER73\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.390 Distributor\_\_37\_GICD\_IROUTER73\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER73\_upper | | | | | reg32 | | 0x624c |
| offset | | 25164 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a5f800b0-21a7-4511-9b86-4b732d18e3af | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER73\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.391 Distributor\_\_37\_GICD\_IROUTER74\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER74\_lower | | | | | reg32 | | 0x6250 |
| offset | | 25168 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e288429c-ab88-464f-b4b2-c66c1d2f46d4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER74\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER74\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER74\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.392 Distributor\_\_37\_GICD\_IROUTER74\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER74\_upper | | | | | reg32 | | 0x6254 |
| offset | | 25172 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8094e95a-296e-41c3-9bda-7246454bc17d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER74\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.393 Distributor\_\_37\_GICD\_IROUTER75\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER75\_lower | | | | | reg32 | | 0x6258 |
| offset | | 25176 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e607ad7a-2652-49c4-be06-dee0e84835d5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER75\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER75\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER75\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.394 Distributor\_\_37\_GICD\_IROUTER75\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER75\_upper | | | | | reg32 | | 0x625c |
| offset | | 25180 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ea514da8-8ec3-4594-9a21-5f43a6bdddce | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER75\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.395 Distributor\_\_37\_GICD\_IROUTER76\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER76\_lower | | | | | reg32 | | 0x6260 |
| offset | | 25184 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d9058f51-69ca-4a98-bdc4-962966e61901 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER76\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER76\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER76\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.396 Distributor\_\_37\_GICD\_IROUTER76\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER76\_upper | | | | | reg32 | | 0x6264 |
| offset | | 25188 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f9a48faf-66ed-4182-bd73-0bd86473b1e3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER76\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.397 Distributor\_\_37\_GICD\_IROUTER77\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER77\_lower | | | | | reg32 | | 0x6268 |
| offset | | 25192 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fc296102-3e87-4106-a56c-fd2c4b92b5b9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER77\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER77\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER77\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.398 Distributor\_\_37\_GICD\_IROUTER77\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER77\_upper | | | | | reg32 | | 0x626c |
| offset | | 25196 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c7d75632-2d3e-4550-92ea-bc141d888af2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER77\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.399 Distributor\_\_37\_GICD\_IROUTER78\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER78\_lower | | | | | reg32 | | 0x6270 |
| offset | | 25200 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e15f84f3-93bc-41d1-bb84-7eb4b16e2839 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER78\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER78\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER78\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.400 Distributor\_\_37\_GICD\_IROUTER78\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER78\_upper | | | | | reg32 | | 0x6274 |
| offset | | 25204 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=34697710-a547-4bbc-ac2c-0cc096384fcc | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER78\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.401 Distributor\_\_37\_GICD\_IROUTER79\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER79\_lower | | | | | reg32 | | 0x6278 |
| offset | | 25208 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ab075143-d5db-402b-92b1-c42117b1783f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER79\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER79\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER79\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.402 Distributor\_\_37\_GICD\_IROUTER79\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER79\_upper | | | | | reg32 | | 0x627c |
| offset | | 25212 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=35b0ffe3-11b1-4dc9-b84b-2a5466154eca | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER79\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.403 Distributor\_\_37\_GICD\_IROUTER80\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER80\_lower | | | | | reg32 | | 0x6280 |
| offset | | 25216 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1a40cdfb-8947-40ef-b08d-24c8500c6dfc | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER80\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER80\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER80\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.404 Distributor\_\_37\_GICD\_IROUTER80\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER80\_upper | | | | | reg32 | | 0x6284 |
| offset | | 25220 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=593cbfd2-5810-4443-879a-432c4e771e00 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER80\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.405 Distributor\_\_37\_GICD\_IROUTER81\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER81\_lower | | | | | reg32 | | 0x6288 |
| offset | | 25224 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=065068ce-7c25-4141-968b-ec7e96c2b94e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER81\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER81\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER81\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.406 Distributor\_\_37\_GICD\_IROUTER81\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER81\_upper | | | | | reg32 | | 0x628c |
| offset | | 25228 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f4a61944-0448-4117-81b9-2d578ef620c0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER81\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.407 Distributor\_\_37\_GICD\_IROUTER82\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER82\_lower | | | | | reg32 | | 0x6290 |
| offset | | 25232 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8ae91440-3a6d-4601-bc85-61a6f0f99fa6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER82\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER82\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER82\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.408 Distributor\_\_37\_GICD\_IROUTER82\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER82\_upper | | | | | reg32 | | 0x6294 |
| offset | | 25236 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=66d17cf5-2f31-41f6-85af-a709070d999c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER82\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.409 Distributor\_\_37\_GICD\_IROUTER83\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER83\_lower | | | | | reg32 | | 0x6298 |
| offset | | 25240 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=72804718-44a6-4e20-afa5-f5354f4d3ea8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER83\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER83\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER83\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.410 Distributor\_\_37\_GICD\_IROUTER83\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER83\_upper | | | | | reg32 | | 0x629c |
| offset | | 25244 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8cdbfc76-f19a-44a9-aef9-54bec0af1b36 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER83\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.411 Distributor\_\_37\_GICD\_IROUTER84\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER84\_lower | | | | | reg32 | | 0x62a0 |
| offset | | 25248 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ac236be9-130d-436a-a85f-fea4dc4a8ad7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER84\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER84\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER84\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.412 Distributor\_\_37\_GICD\_IROUTER84\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER84\_upper | | | | | reg32 | | 0x62a4 |
| offset | | 25252 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=97c50507-a787-4497-abe8-093fbb98a8fa | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER84\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.413 Distributor\_\_37\_GICD\_IROUTER85\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER85\_lower | | | | | reg32 | | 0x62a8 |
| offset | | 25256 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=34b8ed7a-a6db-4ef9-84ea-3f56fd12f194 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER85\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER85\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER85\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.414 Distributor\_\_37\_GICD\_IROUTER85\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER85\_upper | | | | | reg32 | | 0x62ac |
| offset | | 25260 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b28651a0-b4df-496d-995c-0ab8c2344fa8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER85\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.415 Distributor\_\_37\_GICD\_IROUTER86\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER86\_lower | | | | | reg32 | | 0x62b0 |
| offset | | 25264 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=06e21038-c551-4e75-b10f-1a3d2feca905 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER86\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER86\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER86\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.416 Distributor\_\_37\_GICD\_IROUTER86\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER86\_upper | | | | | reg32 | | 0x62b4 |
| offset | | 25268 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8e382de3-53b0-4d06-970c-f281d18da805 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER86\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.417 Distributor\_\_37\_GICD\_IROUTER87\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER87\_lower | | | | | reg32 | | 0x62b8 |
| offset | | 25272 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0813fc75-a3c8-4a21-be13-b92093549eb3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER87\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER87\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER87\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.418 Distributor\_\_37\_GICD\_IROUTER87\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER87\_upper | | | | | reg32 | | 0x62bc |
| offset | | 25276 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6e383695-94ce-4f9d-84bf-4a51baa41805 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER87\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.419 Distributor\_\_37\_GICD\_IROUTER88\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER88\_lower | | | | | reg32 | | 0x62c0 |
| offset | | 25280 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=edd3bb80-e0b2-424c-8531-de1cfcf0d95a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER88\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER88\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER88\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.420 Distributor\_\_37\_GICD\_IROUTER88\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER88\_upper | | | | | reg32 | | 0x62c4 |
| offset | | 25284 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1074143f-ba93-42a0-8219-0698520dd0bd | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER88\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.421 Distributor\_\_37\_GICD\_IROUTER89\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER89\_lower | | | | | reg32 | | 0x62c8 |
| offset | | 25288 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ae7c63dc-2ac4-43d4-9406-150d300814a9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER89\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER89\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER89\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.422 Distributor\_\_37\_GICD\_IROUTER89\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER89\_upper | | | | | reg32 | | 0x62cc |
| offset | | 25292 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7f17a471-5a40-4ffb-9568-40a1cc807f66 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER89\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.423 Distributor\_\_37\_GICD\_IROUTER90\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER90\_lower | | | | | reg32 | | 0x62d0 |
| offset | | 25296 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c366393c-2765-46a0-93d5-26241e3f11af | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER90\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER90\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER90\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.424 Distributor\_\_37\_GICD\_IROUTER90\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER90\_upper | | | | | reg32 | | 0x62d4 |
| offset | | 25300 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fe091eb8-3813-475d-829d-f7f5ba9b5019 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER90\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.425 Distributor\_\_37\_GICD\_IROUTER91\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER91\_lower | | | | | reg32 | | 0x62d8 |
| offset | | 25304 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6244e6f3-99e2-4995-a779-40d0f541330f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER91\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER91\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER91\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.426 Distributor\_\_37\_GICD\_IROUTER91\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER91\_upper | | | | | reg32 | | 0x62dc |
| offset | | 25308 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8d91fb7d-9aa9-4719-8949-91a736569e06 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER91\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.427 Distributor\_\_37\_GICD\_IROUTER92\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER92\_lower | | | | | reg32 | | 0x62e0 |
| offset | | 25312 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b258d653-daf0-4305-a504-a988d4dca19c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER92\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER92\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER92\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.428 Distributor\_\_37\_GICD\_IROUTER92\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER92\_upper | | | | | reg32 | | 0x62e4 |
| offset | | 25316 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=efd6aa83-bc16-4065-bcee-cac3a886da87 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER92\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.429 Distributor\_\_37\_GICD\_IROUTER93\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER93\_lower | | | | | reg32 | | 0x62e8 |
| offset | | 25320 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=02b8c996-950b-4424-b2f5-11a61818f76e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER93\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER93\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER93\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.430 Distributor\_\_37\_GICD\_IROUTER93\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER93\_upper | | | | | reg32 | | 0x62ec |
| offset | | 25324 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3fdd3967-31be-4c49-89a0-161099ca612b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER93\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.431 Distributor\_\_37\_GICD\_IROUTER94\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER94\_lower | | | | | reg32 | | 0x62f0 |
| offset | | 25328 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ce5870dd-19a6-43bd-b8c8-8c3eab1d0501 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER94\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER94\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER94\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.432 Distributor\_\_37\_GICD\_IROUTER94\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER94\_upper | | | | | reg32 | | 0x62f4 |
| offset | | 25332 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0c9f39f5-3ddc-4284-a209-0588ad598cf8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER94\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.433 Distributor\_\_37\_GICD\_IROUTER95\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER95\_lower | | | | | reg32 | | 0x62f8 |
| offset | | 25336 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f8151bdb-4d99-4276-9332-192039f3d87a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER95\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER95\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER95\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.434 Distributor\_\_37\_GICD\_IROUTER95\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER95\_upper | | | | | reg32 | | 0x62fc |
| offset | | 25340 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3fa668ff-0196-4941-b122-6cce4f7f1189 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER95\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.435 Distributor\_\_37\_GICD\_IROUTER96\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER96\_lower | | | | | reg32 | | 0x6300 |
| offset | | 25344 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0f8abb36-cf89-4627-83ea-955bf2dadacb | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER96\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER96\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER96\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.436 Distributor\_\_37\_GICD\_IROUTER96\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER96\_upper | | | | | reg32 | | 0x6304 |
| offset | | 25348 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=32aea398-6d9c-4268-8d23-095901dfa247 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER96\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.437 Distributor\_\_37\_GICD\_IROUTER97\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER97\_lower | | | | | reg32 | | 0x6308 |
| offset | | 25352 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=932d4a95-be86-4b30-ac19-8620a0ebebea | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER97\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER97\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER97\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.438 Distributor\_\_37\_GICD\_IROUTER97\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER97\_upper | | | | | reg32 | | 0x630c |
| offset | | 25356 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3494239c-2afa-400d-abe1-cdcae5d28499 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER97\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.439 Distributor\_\_37\_GICD\_IROUTER98\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER98\_lower | | | | | reg32 | | 0x6310 |
| offset | | 25360 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3cdb25f4-3ca4-4a0e-9767-71b627049845 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER98\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER98\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER98\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.440 Distributor\_\_37\_GICD\_IROUTER98\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER98\_upper | | | | | reg32 | | 0x6314 |
| offset | | 25364 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bfe63d3d-1182-4f8d-abd6-e0c8ca22e01b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER98\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.441 Distributor\_\_37\_GICD\_IROUTER99\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER99\_lower | | | | | reg32 | | 0x6318 |
| offset | | 25368 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4dbafc8a-1117-4631-8f34-3dace336f053 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER99\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER99\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER99\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.442 Distributor\_\_37\_GICD\_IROUTER99\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER99\_upper | | | | | reg32 | | 0x631c |
| offset | | 25372 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4350f4cc-c370-4918-8199-a049095e509a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER99\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.443 Distributor\_\_37\_GICD\_IROUTER100\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER100\_lower | | | | | reg32 | | 0x6320 |
| offset | | 25376 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=97f0faaf-4c6f-4115-a640-a6fb55b3f063 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER100\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER100\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER100\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.444 Distributor\_\_37\_GICD\_IROUTER100\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER100\_upper | | | | | reg32 | | 0x6324 |
| offset | | 25380 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=115d6c34-1070-4ab7-a38a-cf7a187879ab | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER100\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.445 Distributor\_\_37\_GICD\_IROUTER101\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER101\_lower | | | | | reg32 | | 0x6328 |
| offset | | 25384 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7428fb4d-4f90-40c9-bc9d-256c829e320e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER101\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER101\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER101\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.446 Distributor\_\_37\_GICD\_IROUTER101\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER101\_upper | | | | | reg32 | | 0x632c |
| offset | | 25388 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=83829968-fa28-4bdb-a4dc-1207517a9b95 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER101\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.447 Distributor\_\_37\_GICD\_IROUTER102\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER102\_lower | | | | | reg32 | | 0x6330 |
| offset | | 25392 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=31fb26b4-f861-4d4b-b9ac-aba173479a64 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER102\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER102\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER102\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.448 Distributor\_\_37\_GICD\_IROUTER102\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER102\_upper | | | | | reg32 | | 0x6334 |
| offset | | 25396 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ec42b961-789a-4c46-aef1-0a22f00a3f1c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER102\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.449 Distributor\_\_37\_GICD\_IROUTER103\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER103\_lower | | | | | reg32 | | 0x6338 |
| offset | | 25400 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2888de74-ef8e-4c30-99d8-1ab2a687ca55 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER103\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER103\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER103\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.450 Distributor\_\_37\_GICD\_IROUTER103\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER103\_upper | | | | | reg32 | | 0x633c |
| offset | | 25404 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9089a18a-fbb2-4375-925b-18e17432208d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER103\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.451 Distributor\_\_37\_GICD\_IROUTER104\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER104\_lower | | | | | reg32 | | 0x6340 |
| offset | | 25408 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=14d2018c-6c84-4389-a367-69d4633db820 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER104\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER104\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER104\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.452 Distributor\_\_37\_GICD\_IROUTER104\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER104\_upper | | | | | reg32 | | 0x6344 |
| offset | | 25412 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bfe20ea6-94bd-4677-93dd-7306099c1e28 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER104\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.453 Distributor\_\_37\_GICD\_IROUTER105\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER105\_lower | | | | | reg32 | | 0x6348 |
| offset | | 25416 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ff310265-6c4a-4d10-b9be-e3744b362dc4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER105\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER105\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER105\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.454 Distributor\_\_37\_GICD\_IROUTER105\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER105\_upper | | | | | reg32 | | 0x634c |
| offset | | 25420 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dc9502ac-3b78-4dd4-b1ed-3954130e28f0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER105\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.455 Distributor\_\_37\_GICD\_IROUTER106\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER106\_lower | | | | | reg32 | | 0x6350 |
| offset | | 25424 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=20338f1f-4df3-4e5e-a7b2-f873bf7a5b98 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER106\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER106\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER106\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.456 Distributor\_\_37\_GICD\_IROUTER106\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER106\_upper | | | | | reg32 | | 0x6354 |
| offset | | 25428 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ee61444e-a2a3-40c2-b610-6dd1f07b4169 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER106\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.457 Distributor\_\_37\_GICD\_IROUTER107\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER107\_lower | | | | | reg32 | | 0x6358 |
| offset | | 25432 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dfc2d08f-a859-489a-aadb-1e0d1825fa2a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER107\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER107\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER107\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.458 Distributor\_\_37\_GICD\_IROUTER107\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER107\_upper | | | | | reg32 | | 0x635c |
| offset | | 25436 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a19f3290-c155-4aeb-b8c5-e4101081367a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER107\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.459 Distributor\_\_37\_GICD\_IROUTER108\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER108\_lower | | | | | reg32 | | 0x6360 |
| offset | | 25440 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ed38ae6b-15ef-4b06-9918-86cd48e1a8fa | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER108\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER108\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER108\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.460 Distributor\_\_37\_GICD\_IROUTER108\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER108\_upper | | | | | reg32 | | 0x6364 |
| offset | | 25444 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cb6bd6b4-2778-471a-9ac5-4f1b967c4e7b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER108\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.461 Distributor\_\_37\_GICD\_IROUTER109\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER109\_lower | | | | | reg32 | | 0x6368 |
| offset | | 25448 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ec7ea5f5-d16f-4479-a4b7-09110a7ca3b4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER109\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER109\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER109\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.462 Distributor\_\_37\_GICD\_IROUTER109\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER109\_upper | | | | | reg32 | | 0x636c |
| offset | | 25452 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=090ff1be-1376-4f70-9c6d-905a08641549 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER109\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.463 Distributor\_\_37\_GICD\_IROUTER110\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER110\_lower | | | | | reg32 | | 0x6370 |
| offset | | 25456 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=febe001d-a806-45bc-bd26-65ad9db647a3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER110\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER110\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER110\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.464 Distributor\_\_37\_GICD\_IROUTER110\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER110\_upper | | | | | reg32 | | 0x6374 |
| offset | | 25460 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e38af960-eaee-4d1c-befa-8e1221b8d358 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER110\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.465 Distributor\_\_37\_GICD\_IROUTER111\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER111\_lower | | | | | reg32 | | 0x6378 |
| offset | | 25464 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f9cd4db4-1475-43e5-8a05-0a5258183930 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER111\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER111\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER111\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.466 Distributor\_\_37\_GICD\_IROUTER111\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER111\_upper | | | | | reg32 | | 0x637c |
| offset | | 25468 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4e639602-c0d2-4208-9dc7-ead7f7753825 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER111\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.467 Distributor\_\_37\_GICD\_IROUTER112\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER112\_lower | | | | | reg32 | | 0x6380 |
| offset | | 25472 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1a0a3269-c355-4334-9f54-24dd061d9e7f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER112\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER112\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER112\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.468 Distributor\_\_37\_GICD\_IROUTER112\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER112\_upper | | | | | reg32 | | 0x6384 |
| offset | | 25476 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7e73f0b4-fd08-4ea6-a01d-28615c12d92d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER112\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.469 Distributor\_\_37\_GICD\_IROUTER113\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER113\_lower | | | | | reg32 | | 0x6388 |
| offset | | 25480 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=64a599e8-eb27-4641-a6f2-a9e05ea73f89 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER113\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER113\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER113\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.470 Distributor\_\_37\_GICD\_IROUTER113\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER113\_upper | | | | | reg32 | | 0x638c |
| offset | | 25484 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3b2ff9f6-19a5-4000-8483-518c436c3f63 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER113\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.471 Distributor\_\_37\_GICD\_IROUTER114\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER114\_lower | | | | | reg32 | | 0x6390 |
| offset | | 25488 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=93144982-d894-4601-af7f-ea0e5927ac9d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER114\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER114\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER114\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.472 Distributor\_\_37\_GICD\_IROUTER114\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER114\_upper | | | | | reg32 | | 0x6394 |
| offset | | 25492 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dc96fbed-bf0a-457f-9d41-0919892d34fb | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER114\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.473 Distributor\_\_37\_GICD\_IROUTER115\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER115\_lower | | | | | reg32 | | 0x6398 |
| offset | | 25496 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=de11c421-0cbe-46aa-98ec-69dabbc968fa | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER115\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER115\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER115\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.474 Distributor\_\_37\_GICD\_IROUTER115\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER115\_upper | | | | | reg32 | | 0x639c |
| offset | | 25500 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9afdcbe4-9707-48b7-a4d5-4cfeacf90a38 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER115\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.475 Distributor\_\_37\_GICD\_IROUTER116\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER116\_lower | | | | | reg32 | | 0x63a0 |
| offset | | 25504 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=90106949-18c2-4c57-9079-fda1e92e593e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER116\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER116\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER116\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.476 Distributor\_\_37\_GICD\_IROUTER116\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER116\_upper | | | | | reg32 | | 0x63a4 |
| offset | | 25508 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=234ff5d2-40ba-4ae7-81dc-7c156a23d2db | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER116\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.477 Distributor\_\_37\_GICD\_IROUTER117\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER117\_lower | | | | | reg32 | | 0x63a8 |
| offset | | 25512 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f6397d53-d5c3-4cc3-8089-12f38e3101c5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER117\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER117\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER117\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.478 Distributor\_\_37\_GICD\_IROUTER117\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER117\_upper | | | | | reg32 | | 0x63ac |
| offset | | 25516 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=545a6693-c630-4321-a262-0ea51af9a66e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER117\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.479 Distributor\_\_37\_GICD\_IROUTER118\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER118\_lower | | | | | reg32 | | 0x63b0 |
| offset | | 25520 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f1e7360f-dc59-406c-93d5-131ead78a90c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER118\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER118\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER118\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.480 Distributor\_\_37\_GICD\_IROUTER118\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER118\_upper | | | | | reg32 | | 0x63b4 |
| offset | | 25524 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c319d973-e1f6-49e7-8dfe-795694433fc4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER118\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.481 Distributor\_\_37\_GICD\_IROUTER119\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER119\_lower | | | | | reg32 | | 0x63b8 |
| offset | | 25528 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3231ec91-1009-41b3-9daa-98beec475b2a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER119\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER119\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER119\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.482 Distributor\_\_37\_GICD\_IROUTER119\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER119\_upper | | | | | reg32 | | 0x63bc |
| offset | | 25532 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=589bc96b-54a4-4fe5-94a2-1eb71a10dbf5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER119\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.483 Distributor\_\_37\_GICD\_IROUTER120\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER120\_lower | | | | | reg32 | | 0x63c0 |
| offset | | 25536 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7ff5ab50-edef-4e8a-adf4-fbd11b625708 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER120\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER120\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER120\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.484 Distributor\_\_37\_GICD\_IROUTER120\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER120\_upper | | | | | reg32 | | 0x63c4 |
| offset | | 25540 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6ae95901-b0d8-4288-aad1-44b91fd2ec10 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER120\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.485 Distributor\_\_37\_GICD\_IROUTER121\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER121\_lower | | | | | reg32 | | 0x63c8 |
| offset | | 25544 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=80c5389b-de9f-46d5-ab6c-dc3b8c6274e0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER121\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER121\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER121\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.486 Distributor\_\_37\_GICD\_IROUTER121\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER121\_upper | | | | | reg32 | | 0x63cc |
| offset | | 25548 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5cd436ce-0fc6-475c-9022-b5033934b20e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER121\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.487 Distributor\_\_37\_GICD\_IROUTER122\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER122\_lower | | | | | reg32 | | 0x63d0 |
| offset | | 25552 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4f79f9bd-d9c6-4a6e-93f4-3e55cb57e4c2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER122\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER122\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER122\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.488 Distributor\_\_37\_GICD\_IROUTER122\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER122\_upper | | | | | reg32 | | 0x63d4 |
| offset | | 25556 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9ef6142f-fccf-41e4-b96b-2ad8c3d95956 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER122\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.489 Distributor\_\_37\_GICD\_IROUTER123\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER123\_lower | | | | | reg32 | | 0x63d8 |
| offset | | 25560 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b2f3a762-3345-446f-a002-badf1235e2ce | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER123\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER123\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER123\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.490 Distributor\_\_37\_GICD\_IROUTER123\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER123\_upper | | | | | reg32 | | 0x63dc |
| offset | | 25564 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9eed4457-900e-40ff-9be0-84de034fae66 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER123\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.491 Distributor\_\_37\_GICD\_IROUTER124\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER124\_lower | | | | | reg32 | | 0x63e0 |
| offset | | 25568 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c3c77cdf-dac2-4d19-ab62-2865eaf1f9c8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER124\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER124\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER124\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.492 Distributor\_\_37\_GICD\_IROUTER124\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER124\_upper | | | | | reg32 | | 0x63e4 |
| offset | | 25572 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=308fa91a-0d9a-4458-a1e0-0335aea589d6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER124\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.493 Distributor\_\_37\_GICD\_IROUTER125\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER125\_lower | | | | | reg32 | | 0x63e8 |
| offset | | 25576 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bd74d46a-5678-4c83-8911-33d5fbfe14d4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER125\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER125\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER125\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.494 Distributor\_\_37\_GICD\_IROUTER125\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER125\_upper | | | | | reg32 | | 0x63ec |
| offset | | 25580 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0da12a63-90cf-4c81-8364-355c596729d6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER125\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.495 Distributor\_\_37\_GICD\_IROUTER126\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER126\_lower | | | | | reg32 | | 0x63f0 |
| offset | | 25584 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b17b4b86-d80a-4adf-9f95-d672cf16327a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER126\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER126\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER126\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.496 Distributor\_\_37\_GICD\_IROUTER126\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER126\_upper | | | | | reg32 | | 0x63f4 |
| offset | | 25588 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d2201661-9055-4154-801c-cadaae0bdc9a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER126\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.497 Distributor\_\_37\_GICD\_IROUTER127\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER127\_lower | | | | | reg32 | | 0x63f8 |
| offset | | 25592 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9b0164e8-6f7a-4719-bda2-f537753d0a6a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER127\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER127\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER127\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.498 Distributor\_\_37\_GICD\_IROUTER127\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER127\_upper | | | | | reg32 | | 0x63fc |
| offset | | 25596 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3cde69be-e09a-437a-b760-037e5f706100 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER127\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.499 Distributor\_\_37\_GICD\_IROUTER128\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER128\_lower | | | | | reg32 | | 0x6400 |
| offset | | 25600 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d2ec9281-6064-425c-87c2-3c66f259b93a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER128\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER128\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER128\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.500 Distributor\_\_37\_GICD\_IROUTER128\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER128\_upper | | | | | reg32 | | 0x6404 |
| offset | | 25604 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c7d25aa4-63b0-41bb-97bd-bba8235d66b5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER128\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.501 Distributor\_\_37\_GICD\_IROUTER129\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER129\_lower | | | | | reg32 | | 0x6408 |
| offset | | 25608 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4fbac4f8-92f9-4ee0-bc88-6894f898804a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER129\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER129\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER129\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.502 Distributor\_\_37\_GICD\_IROUTER129\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER129\_upper | | | | | reg32 | | 0x640c |
| offset | | 25612 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1decb39a-8e20-461b-8781-5a43e72e32e4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER129\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.503 Distributor\_\_37\_GICD\_IROUTER130\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER130\_lower | | | | | reg32 | | 0x6410 |
| offset | | 25616 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5fa7f81d-5310-4994-b037-20420866ec28 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER130\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER130\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER130\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.504 Distributor\_\_37\_GICD\_IROUTER130\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER130\_upper | | | | | reg32 | | 0x6414 |
| offset | | 25620 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b580cd26-5b24-48f6-8044-720bb0e933da | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER130\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.505 Distributor\_\_37\_GICD\_IROUTER131\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER131\_lower | | | | | reg32 | | 0x6418 |
| offset | | 25624 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1082388c-2263-4ab4-9d44-11c6a029b2f5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER131\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER131\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER131\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.506 Distributor\_\_37\_GICD\_IROUTER131\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER131\_upper | | | | | reg32 | | 0x641c |
| offset | | 25628 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3174c482-39b0-43a1-8bfb-5beb8cfbe932 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER131\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.507 Distributor\_\_37\_GICD\_IROUTER132\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER132\_lower | | | | | reg32 | | 0x6420 |
| offset | | 25632 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6c2d40a5-4c30-42f8-88de-258103a61e30 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER132\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER132\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER132\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.508 Distributor\_\_37\_GICD\_IROUTER132\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER132\_upper | | | | | reg32 | | 0x6424 |
| offset | | 25636 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3cbb0244-da29-49f4-b245-8d15259ab17b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER132\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.509 Distributor\_\_37\_GICD\_IROUTER133\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER133\_lower | | | | | reg32 | | 0x6428 |
| offset | | 25640 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=42b52138-d518-465e-ab1e-3217897450df | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER133\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER133\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER133\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.510 Distributor\_\_37\_GICD\_IROUTER133\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER133\_upper | | | | | reg32 | | 0x642c |
| offset | | 25644 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d40a5403-a1ca-4699-b576-91e6cd758738 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER133\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.511 Distributor\_\_37\_GICD\_IROUTER134\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER134\_lower | | | | | reg32 | | 0x6430 |
| offset | | 25648 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=067f9aa3-6226-485b-9d7e-311c01f185ae | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER134\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER134\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER134\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.512 Distributor\_\_37\_GICD\_IROUTER134\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER134\_upper | | | | | reg32 | | 0x6434 |
| offset | | 25652 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b8f0ed98-5b9e-4db8-b4ed-09ef2c145d10 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER134\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.513 Distributor\_\_37\_GICD\_IROUTER135\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER135\_lower | | | | | reg32 | | 0x6438 |
| offset | | 25656 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=210c731f-dbcc-40d3-9f78-7f7bcec2fa08 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER135\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER135\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER135\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.514 Distributor\_\_37\_GICD\_IROUTER135\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER135\_upper | | | | | reg32 | | 0x643c |
| offset | | 25660 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c193ddd7-86b7-4192-896f-893d3ebbbc37 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER135\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.515 Distributor\_\_37\_GICD\_IROUTER136\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER136\_lower | | | | | reg32 | | 0x6440 |
| offset | | 25664 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=62551157-8dbc-4d82-a10d-e35d7850e8c7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER136\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER136\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER136\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.516 Distributor\_\_37\_GICD\_IROUTER136\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER136\_upper | | | | | reg32 | | 0x6444 |
| offset | | 25668 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d2211535-0054-46f2-966c-6cca381f15fa | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER136\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.517 Distributor\_\_37\_GICD\_IROUTER137\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER137\_lower | | | | | reg32 | | 0x6448 |
| offset | | 25672 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7cadb7d2-1e25-4113-91c2-8da016fdd437 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER137\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER137\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER137\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.518 Distributor\_\_37\_GICD\_IROUTER137\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER137\_upper | | | | | reg32 | | 0x644c |
| offset | | 25676 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f025cbcf-3724-4450-8cc2-82867512b85c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER137\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.519 Distributor\_\_37\_GICD\_IROUTER138\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER138\_lower | | | | | reg32 | | 0x6450 |
| offset | | 25680 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=51427a03-e6a3-437e-b1f8-ff0d19c87063 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER138\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER138\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER138\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.520 Distributor\_\_37\_GICD\_IROUTER138\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER138\_upper | | | | | reg32 | | 0x6454 |
| offset | | 25684 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=862b2709-d25e-4192-94ae-5ba541001d1b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER138\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.521 Distributor\_\_37\_GICD\_IROUTER139\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER139\_lower | | | | | reg32 | | 0x6458 |
| offset | | 25688 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=60624c01-112f-4aef-b74e-55310736f106 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER139\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER139\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER139\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.522 Distributor\_\_37\_GICD\_IROUTER139\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER139\_upper | | | | | reg32 | | 0x645c |
| offset | | 25692 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ef341765-ba91-4b11-ae56-da06300cc4d2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER139\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.523 Distributor\_\_37\_GICD\_IROUTER140\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER140\_lower | | | | | reg32 | | 0x6460 |
| offset | | 25696 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9512cb36-61c0-48c0-a56f-4d2cda184b1a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER140\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER140\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER140\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.524 Distributor\_\_37\_GICD\_IROUTER140\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER140\_upper | | | | | reg32 | | 0x6464 |
| offset | | 25700 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5327dfc8-c394-4bdd-a458-1cdac5198dd9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER140\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.525 Distributor\_\_37\_GICD\_IROUTER141\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER141\_lower | | | | | reg32 | | 0x6468 |
| offset | | 25704 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6358eb8e-2df6-49ef-b106-802b255b47ca | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER141\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER141\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER141\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.526 Distributor\_\_37\_GICD\_IROUTER141\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER141\_upper | | | | | reg32 | | 0x646c |
| offset | | 25708 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=922edbdf-a820-47d3-ada8-a770cd0ff7f3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER141\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.527 Distributor\_\_37\_GICD\_IROUTER142\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER142\_lower | | | | | reg32 | | 0x6470 |
| offset | | 25712 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=20641407-6c89-4437-84c8-4798924f990b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER142\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER142\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER142\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.528 Distributor\_\_37\_GICD\_IROUTER142\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER142\_upper | | | | | reg32 | | 0x6474 |
| offset | | 25716 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=017bc6a7-b235-4c9f-9fff-7a967c1b6f16 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER142\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.529 Distributor\_\_37\_GICD\_IROUTER143\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER143\_lower | | | | | reg32 | | 0x6478 |
| offset | | 25720 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c90b3cb4-2448-429d-9cc4-12dc3f276f4f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER143\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER143\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER143\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.530 Distributor\_\_37\_GICD\_IROUTER143\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER143\_upper | | | | | reg32 | | 0x647c |
| offset | | 25724 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fe64bf7a-e5fa-426f-a539-035395f1b69b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER143\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.531 Distributor\_\_37\_GICD\_IROUTER144\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER144\_lower | | | | | reg32 | | 0x6480 |
| offset | | 25728 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7840fcce-bee5-4f6d-b179-234cf922467d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER144\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER144\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER144\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.532 Distributor\_\_37\_GICD\_IROUTER144\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER144\_upper | | | | | reg32 | | 0x6484 |
| offset | | 25732 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3a29fa6f-0e62-46c5-b2c4-311917c2adc7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER144\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.533 Distributor\_\_37\_GICD\_IROUTER145\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER145\_lower | | | | | reg32 | | 0x6488 |
| offset | | 25736 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=df31e924-5d74-4212-81fd-dfdca426fddc | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER145\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER145\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER145\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.534 Distributor\_\_37\_GICD\_IROUTER145\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER145\_upper | | | | | reg32 | | 0x648c |
| offset | | 25740 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=350cf6ea-cc0e-4d15-8a94-b19304222cdf | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER145\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.535 Distributor\_\_37\_GICD\_IROUTER146\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER146\_lower | | | | | reg32 | | 0x6490 |
| offset | | 25744 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d33e14c0-d01c-43cf-b306-41a418f14c3b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER146\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER146\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER146\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.536 Distributor\_\_37\_GICD\_IROUTER146\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER146\_upper | | | | | reg32 | | 0x6494 |
| offset | | 25748 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=06301ba0-0b82-4978-bd32-477a5d72d64f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER146\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.537 Distributor\_\_37\_GICD\_IROUTER147\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER147\_lower | | | | | reg32 | | 0x6498 |
| offset | | 25752 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0dfab098-20e3-438a-ad9c-e9d9efebd799 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER147\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER147\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER147\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.538 Distributor\_\_37\_GICD\_IROUTER147\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER147\_upper | | | | | reg32 | | 0x649c |
| offset | | 25756 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=67aa890a-4711-41de-98b4-75f4a0f25d51 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER147\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.539 Distributor\_\_37\_GICD\_IROUTER148\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER148\_lower | | | | | reg32 | | 0x64a0 |
| offset | | 25760 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fe01d98c-fa9f-47e5-87d1-6c447406db9a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER148\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER148\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER148\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.540 Distributor\_\_37\_GICD\_IROUTER148\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER148\_upper | | | | | reg32 | | 0x64a4 |
| offset | | 25764 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dc7d11f4-b1f3-4aec-b6e5-8bf01cd70e09 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER148\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.541 Distributor\_\_37\_GICD\_IROUTER149\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER149\_lower | | | | | reg32 | | 0x64a8 |
| offset | | 25768 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e27987f9-94ce-463f-8f29-5fc5eae3ae1b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER149\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER149\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER149\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.542 Distributor\_\_37\_GICD\_IROUTER149\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER149\_upper | | | | | reg32 | | 0x64ac |
| offset | | 25772 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a8951a51-a717-45fc-ad8e-51d21ad5cdaa | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER149\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.543 Distributor\_\_37\_GICD\_IROUTER150\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER150\_lower | | | | | reg32 | | 0x64b0 |
| offset | | 25776 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a3aa1b02-cc72-46d1-a927-df123632485a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER150\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER150\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER150\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.544 Distributor\_\_37\_GICD\_IROUTER150\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER150\_upper | | | | | reg32 | | 0x64b4 |
| offset | | 25780 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0ae72fad-9ff3-4f92-8531-130a2f40addc | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER150\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.545 Distributor\_\_37\_GICD\_IROUTER151\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER151\_lower | | | | | reg32 | | 0x64b8 |
| offset | | 25784 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=952cfd84-8a5e-4ea9-806b-fd89c3c6f2ff | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER151\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER151\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER151\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.546 Distributor\_\_37\_GICD\_IROUTER151\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER151\_upper | | | | | reg32 | | 0x64bc |
| offset | | 25788 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1d27fd3c-fe00-46e9-b11c-28ef0a5deb6d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER151\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.547 Distributor\_\_37\_GICD\_IROUTER152\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER152\_lower | | | | | reg32 | | 0x64c0 |
| offset | | 25792 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=90146a77-6336-4099-80a9-6eb403c950f1 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER152\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER152\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER152\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.548 Distributor\_\_37\_GICD\_IROUTER152\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER152\_upper | | | | | reg32 | | 0x64c4 |
| offset | | 25796 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bb46ab29-d53d-4eec-9cb8-8b409f6ae0b2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER152\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.549 Distributor\_\_37\_GICD\_IROUTER153\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER153\_lower | | | | | reg32 | | 0x64c8 |
| offset | | 25800 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=354b04fa-0524-44a4-945d-9945c13897f2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER153\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER153\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER153\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.550 Distributor\_\_37\_GICD\_IROUTER153\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER153\_upper | | | | | reg32 | | 0x64cc |
| offset | | 25804 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=caf092dc-5ae3-4113-8dfa-e9b5e379359f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER153\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.551 Distributor\_\_37\_GICD\_IROUTER154\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER154\_lower | | | | | reg32 | | 0x64d0 |
| offset | | 25808 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=08737ec8-216b-43d7-afd9-7299e0bbb6b9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER154\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER154\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER154\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.552 Distributor\_\_37\_GICD\_IROUTER154\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER154\_upper | | | | | reg32 | | 0x64d4 |
| offset | | 25812 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=67926a48-2a5d-4cee-aa57-0ffaa9d620f8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER154\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.553 Distributor\_\_37\_GICD\_IROUTER155\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER155\_lower | | | | | reg32 | | 0x64d8 |
| offset | | 25816 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=660362df-0266-4925-bac1-c4b821dfb00a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER155\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER155\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER155\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.554 Distributor\_\_37\_GICD\_IROUTER155\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER155\_upper | | | | | reg32 | | 0x64dc |
| offset | | 25820 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=db39dc5a-96be-468e-9537-fffa9e4c16c5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER155\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.555 Distributor\_\_37\_GICD\_IROUTER156\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER156\_lower | | | | | reg32 | | 0x64e0 |
| offset | | 25824 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b662bb5c-6b7f-4588-8296-72e33c8ba288 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER156\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER156\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER156\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.556 Distributor\_\_37\_GICD\_IROUTER156\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER156\_upper | | | | | reg32 | | 0x64e4 |
| offset | | 25828 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7626ac60-b32f-4805-9972-bd37214f6e4c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER156\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.557 Distributor\_\_37\_GICD\_IROUTER157\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER157\_lower | | | | | reg32 | | 0x64e8 |
| offset | | 25832 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=71d413a5-b59d-4e07-b80b-91bb7fb27b78 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER157\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER157\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER157\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.558 Distributor\_\_37\_GICD\_IROUTER157\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER157\_upper | | | | | reg32 | | 0x64ec |
| offset | | 25836 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fb20e149-410d-4d28-8353-ea92c0eb51fc | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER157\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.559 Distributor\_\_37\_GICD\_IROUTER158\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER158\_lower | | | | | reg32 | | 0x64f0 |
| offset | | 25840 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fe6f1ccc-3fc7-4cce-b914-ad816fa1e44b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER158\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER158\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER158\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.560 Distributor\_\_37\_GICD\_IROUTER158\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER158\_upper | | | | | reg32 | | 0x64f4 |
| offset | | 25844 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7e7e032c-bb53-4090-8fc8-e54c5ca82a3d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER158\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.561 Distributor\_\_37\_GICD\_IROUTER159\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER159\_lower | | | | | reg32 | | 0x64f8 |
| offset | | 25848 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ea989c00-38fd-47b8-97f4-4f05ab40954f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER159\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER159\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER159\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.562 Distributor\_\_37\_GICD\_IROUTER159\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER159\_upper | | | | | reg32 | | 0x64fc |
| offset | | 25852 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0661e39c-18aa-4415-b478-9726a1a41606 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER159\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.563 Distributor\_\_37\_GICD\_IROUTER160\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER160\_lower | | | | | reg32 | | 0x6500 |
| offset | | 25856 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bc2b5a3c-217a-4793-9f06-7d28ec1cd49d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER160\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER160\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER160\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.564 Distributor\_\_37\_GICD\_IROUTER160\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER160\_upper | | | | | reg32 | | 0x6504 |
| offset | | 25860 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2358ea04-939b-46f4-bf69-3711c5c66e0f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER160\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.565 Distributor\_\_37\_GICD\_IROUTER161\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER161\_lower | | | | | reg32 | | 0x6508 |
| offset | | 25864 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=be03574c-0bd5-4636-9f0d-f462c80b6d3c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER161\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER161\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER161\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.566 Distributor\_\_37\_GICD\_IROUTER161\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER161\_upper | | | | | reg32 | | 0x650c |
| offset | | 25868 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9689e494-67a8-468e-9a82-daf1c655831a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER161\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.567 Distributor\_\_37\_GICD\_IROUTER162\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER162\_lower | | | | | reg32 | | 0x6510 |
| offset | | 25872 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9b039582-d9c2-4e74-ba5d-9c9b01b210a5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER162\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER162\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER162\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.568 Distributor\_\_37\_GICD\_IROUTER162\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER162\_upper | | | | | reg32 | | 0x6514 |
| offset | | 25876 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=52ec763e-f09e-4fdb-9e32-8a4fbe1b48b2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER162\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.569 Distributor\_\_37\_GICD\_IROUTER163\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER163\_lower | | | | | reg32 | | 0x6518 |
| offset | | 25880 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=82a370ec-b591-4d4e-8e84-450b4f12f402 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER163\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER163\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER163\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.570 Distributor\_\_37\_GICD\_IROUTER163\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER163\_upper | | | | | reg32 | | 0x651c |
| offset | | 25884 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ed65a2b8-499f-4098-80d5-8f01ba738d12 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER163\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.571 Distributor\_\_37\_GICD\_IROUTER164\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER164\_lower | | | | | reg32 | | 0x6520 |
| offset | | 25888 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=65d0b9f7-8e27-4b51-afcb-226ac5076499 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER164\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER164\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER164\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.572 Distributor\_\_37\_GICD\_IROUTER164\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER164\_upper | | | | | reg32 | | 0x6524 |
| offset | | 25892 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=65d11afc-1f48-49bc-af0c-b7ae43d7f9b0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER164\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.573 Distributor\_\_37\_GICD\_IROUTER165\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER165\_lower | | | | | reg32 | | 0x6528 |
| offset | | 25896 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f7484035-573a-47f7-8ded-120550bd13ed | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER165\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER165\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER165\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.574 Distributor\_\_37\_GICD\_IROUTER165\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER165\_upper | | | | | reg32 | | 0x652c |
| offset | | 25900 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3f4fd6fe-1ad7-4823-9c2c-d72f605454f3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER165\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.575 Distributor\_\_37\_GICD\_IROUTER166\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER166\_lower | | | | | reg32 | | 0x6530 |
| offset | | 25904 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cbe60314-7886-4f35-8e7e-8bdc88c1241d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER166\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER166\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER166\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.576 Distributor\_\_37\_GICD\_IROUTER166\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER166\_upper | | | | | reg32 | | 0x6534 |
| offset | | 25908 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=05198420-a3f2-4a44-873a-a9c428c1377a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER166\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.577 Distributor\_\_37\_GICD\_IROUTER167\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER167\_lower | | | | | reg32 | | 0x6538 |
| offset | | 25912 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8b1f8d77-0ac6-4a45-b7ec-22d21fbf0087 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER167\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER167\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER167\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.578 Distributor\_\_37\_GICD\_IROUTER167\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER167\_upper | | | | | reg32 | | 0x653c |
| offset | | 25916 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7cd6515c-8469-401c-a03b-607fc5f6f83a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER167\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.579 Distributor\_\_37\_GICD\_IROUTER168\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER168\_lower | | | | | reg32 | | 0x6540 |
| offset | | 25920 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=046a0f51-125c-4aec-9988-100e9606895a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER168\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER168\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER168\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.580 Distributor\_\_37\_GICD\_IROUTER168\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER168\_upper | | | | | reg32 | | 0x6544 |
| offset | | 25924 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5fe20bd5-87f9-41fe-9fb1-2e3e1474474a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER168\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.581 Distributor\_\_37\_GICD\_IROUTER169\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER169\_lower | | | | | reg32 | | 0x6548 |
| offset | | 25928 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ca02085d-0cc6-43b0-811d-74e4501ae7ec | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER169\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER169\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER169\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.582 Distributor\_\_37\_GICD\_IROUTER169\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER169\_upper | | | | | reg32 | | 0x654c |
| offset | | 25932 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a81248cd-ddfd-4ec9-a8ca-8271489e7267 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER169\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.583 Distributor\_\_37\_GICD\_IROUTER170\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER170\_lower | | | | | reg32 | | 0x6550 |
| offset | | 25936 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f35de43d-2ff8-4b83-8794-dbd330eadf3d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER170\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER170\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER170\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.584 Distributor\_\_37\_GICD\_IROUTER170\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER170\_upper | | | | | reg32 | | 0x6554 |
| offset | | 25940 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c5d12bf7-9b88-4f23-b92b-4a7dcefc0f22 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER170\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.585 Distributor\_\_37\_GICD\_IROUTER171\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER171\_lower | | | | | reg32 | | 0x6558 |
| offset | | 25944 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=feaf1112-ec5c-4620-8be1-b3a1cb859c28 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER171\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER171\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER171\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.586 Distributor\_\_37\_GICD\_IROUTER171\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER171\_upper | | | | | reg32 | | 0x655c |
| offset | | 25948 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cc8de13b-27d4-417b-81f2-8ab4d4456d8d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER171\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.587 Distributor\_\_37\_GICD\_IROUTER172\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER172\_lower | | | | | reg32 | | 0x6560 |
| offset | | 25952 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a709b669-5d21-4e48-b498-77c8217d7fb8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER172\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER172\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER172\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.588 Distributor\_\_37\_GICD\_IROUTER172\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER172\_upper | | | | | reg32 | | 0x6564 |
| offset | | 25956 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f33833db-8013-41cc-ad08-0928da44fd03 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER172\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.589 Distributor\_\_37\_GICD\_IROUTER173\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER173\_lower | | | | | reg32 | | 0x6568 |
| offset | | 25960 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8faec6ae-1c15-4c68-8b1a-09a0af2d2dc4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER173\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER173\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER173\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.590 Distributor\_\_37\_GICD\_IROUTER173\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER173\_upper | | | | | reg32 | | 0x656c |
| offset | | 25964 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=44ad2396-305c-41a4-b6e3-28e5b7b765a0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER173\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.591 Distributor\_\_37\_GICD\_IROUTER174\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER174\_lower | | | | | reg32 | | 0x6570 |
| offset | | 25968 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=705dce41-cb8c-4806-b7c9-b145da3d900e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER174\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER174\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER174\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.592 Distributor\_\_37\_GICD\_IROUTER174\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER174\_upper | | | | | reg32 | | 0x6574 |
| offset | | 25972 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2d511a34-f1d8-4801-be8a-7646335f7326 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER174\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.593 Distributor\_\_37\_GICD\_IROUTER175\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER175\_lower | | | | | reg32 | | 0x6578 |
| offset | | 25976 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6f79ec99-cefd-46d7-be9c-945cc65651f8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER175\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER175\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER175\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.594 Distributor\_\_37\_GICD\_IROUTER175\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER175\_upper | | | | | reg32 | | 0x657c |
| offset | | 25980 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=efcb45dc-39b5-4079-9952-511e1e54abe8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER175\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.595 Distributor\_\_37\_GICD\_IROUTER176\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER176\_lower | | | | | reg32 | | 0x6580 |
| offset | | 25984 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=99bda9e7-41b0-4c48-b24a-567d06914ff8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER176\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER176\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER176\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.596 Distributor\_\_37\_GICD\_IROUTER176\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER176\_upper | | | | | reg32 | | 0x6584 |
| offset | | 25988 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8fb21af7-cbe1-4825-8cda-6128417b162f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER176\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.597 Distributor\_\_37\_GICD\_IROUTER177\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER177\_lower | | | | | reg32 | | 0x6588 |
| offset | | 25992 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=715314c6-a632-40bb-9f1d-023027d6c3e2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER177\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER177\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER177\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.598 Distributor\_\_37\_GICD\_IROUTER177\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER177\_upper | | | | | reg32 | | 0x658c |
| offset | | 25996 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=438d4c4d-c1c1-4281-9c1e-1f2ea0c66b27 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER177\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.599 Distributor\_\_37\_GICD\_IROUTER178\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER178\_lower | | | | | reg32 | | 0x6590 |
| offset | | 26000 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=958971e3-6a04-4aa5-82e5-9ca6f0b37d2c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER178\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER178\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER178\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.600 Distributor\_\_37\_GICD\_IROUTER178\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER178\_upper | | | | | reg32 | | 0x6594 |
| offset | | 26004 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=396fe2a4-9215-45fd-ba07-55774505b710 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER178\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.601 Distributor\_\_37\_GICD\_IROUTER179\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER179\_lower | | | | | reg32 | | 0x6598 |
| offset | | 26008 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=844a18ad-ac27-4a28-8f35-5b1ce7edec34 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER179\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER179\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER179\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.602 Distributor\_\_37\_GICD\_IROUTER179\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER179\_upper | | | | | reg32 | | 0x659c |
| offset | | 26012 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f259bff1-dcf5-48a9-b6b9-d36435275749 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER179\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.603 Distributor\_\_37\_GICD\_IROUTER180\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER180\_lower | | | | | reg32 | | 0x65a0 |
| offset | | 26016 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=576b184f-24ab-496d-8032-edac324253d7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER180\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER180\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER180\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.604 Distributor\_\_37\_GICD\_IROUTER180\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER180\_upper | | | | | reg32 | | 0x65a4 |
| offset | | 26020 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a7b2a2d4-e3e3-4e90-89bd-5606f285f4da | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER180\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.605 Distributor\_\_37\_GICD\_IROUTER181\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER181\_lower | | | | | reg32 | | 0x65a8 |
| offset | | 26024 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=82d80046-ac53-47de-8d60-5e791a78d764 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER181\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER181\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER181\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.606 Distributor\_\_37\_GICD\_IROUTER181\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER181\_upper | | | | | reg32 | | 0x65ac |
| offset | | 26028 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=75703905-0d87-4d40-8723-d47e9d5e9f54 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER181\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.607 Distributor\_\_37\_GICD\_IROUTER182\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER182\_lower | | | | | reg32 | | 0x65b0 |
| offset | | 26032 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=693ba68b-ec3a-4cce-9fef-9166fac92233 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER182\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER182\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER182\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.608 Distributor\_\_37\_GICD\_IROUTER182\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER182\_upper | | | | | reg32 | | 0x65b4 |
| offset | | 26036 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4d1bfa17-a7aa-4b30-86c9-4e46b44b89b8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER182\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.609 Distributor\_\_37\_GICD\_IROUTER183\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER183\_lower | | | | | reg32 | | 0x65b8 |
| offset | | 26040 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=085a4c4c-9e32-4b3d-aa73-e35d41591059 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER183\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER183\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER183\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.610 Distributor\_\_37\_GICD\_IROUTER183\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER183\_upper | | | | | reg32 | | 0x65bc |
| offset | | 26044 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=18f5bb81-15ea-4ae8-9544-0b79a38c2b06 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER183\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.611 Distributor\_\_37\_GICD\_IROUTER184\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER184\_lower | | | | | reg32 | | 0x65c0 |
| offset | | 26048 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0631dc6e-d56f-4de0-93df-20aadfaf2a7c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER184\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER184\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER184\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.612 Distributor\_\_37\_GICD\_IROUTER184\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER184\_upper | | | | | reg32 | | 0x65c4 |
| offset | | 26052 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b5d63802-065c-46f0-9bd3-42ac0d1cb463 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER184\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.613 Distributor\_\_37\_GICD\_IROUTER185\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER185\_lower | | | | | reg32 | | 0x65c8 |
| offset | | 26056 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3184908c-9762-4c47-9d3c-afe7509ce6c6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER185\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER185\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER185\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.614 Distributor\_\_37\_GICD\_IROUTER185\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER185\_upper | | | | | reg32 | | 0x65cc |
| offset | | 26060 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a3c5e9dc-e9f6-4335-8c1e-7a79b4361c1b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER185\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.615 Distributor\_\_37\_GICD\_IROUTER186\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER186\_lower | | | | | reg32 | | 0x65d0 |
| offset | | 26064 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=96157bbe-75a8-4562-b9fc-849e9ac18754 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER186\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER186\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER186\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.616 Distributor\_\_37\_GICD\_IROUTER186\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER186\_upper | | | | | reg32 | | 0x65d4 |
| offset | | 26068 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ff23a7fc-3a75-42e9-a105-89ccd3fec05d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER186\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.617 Distributor\_\_37\_GICD\_IROUTER187\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER187\_lower | | | | | reg32 | | 0x65d8 |
| offset | | 26072 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=17ce2c63-faf2-4ea7-b8e0-332694265603 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER187\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER187\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER187\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.618 Distributor\_\_37\_GICD\_IROUTER187\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER187\_upper | | | | | reg32 | | 0x65dc |
| offset | | 26076 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f794960a-3972-4ae4-ab72-be4bd2d352a1 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER187\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.619 Distributor\_\_37\_GICD\_IROUTER188\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER188\_lower | | | | | reg32 | | 0x65e0 |
| offset | | 26080 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f8062074-931c-415d-b9a9-888249bb29da | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER188\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER188\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER188\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.620 Distributor\_\_37\_GICD\_IROUTER188\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER188\_upper | | | | | reg32 | | 0x65e4 |
| offset | | 26084 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f270c48c-1607-4353-9889-de7c3f25d7f5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER188\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.621 Distributor\_\_37\_GICD\_IROUTER189\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER189\_lower | | | | | reg32 | | 0x65e8 |
| offset | | 26088 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e300c710-f808-4b06-bf2a-02ad58097fed | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER189\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER189\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER189\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.622 Distributor\_\_37\_GICD\_IROUTER189\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER189\_upper | | | | | reg32 | | 0x65ec |
| offset | | 26092 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a3175e3b-fd23-4e30-949a-e1ef6d4d1b4c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER189\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.623 Distributor\_\_37\_GICD\_IROUTER190\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER190\_lower | | | | | reg32 | | 0x65f0 |
| offset | | 26096 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d62b7dee-e176-4406-a759-4915a95eac25 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER190\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER190\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER190\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.624 Distributor\_\_37\_GICD\_IROUTER190\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER190\_upper | | | | | reg32 | | 0x65f4 |
| offset | | 26100 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=00b323da-65af-445b-84fb-eb64ed31defc | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER190\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.625 Distributor\_\_37\_GICD\_IROUTER191\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER191\_lower | | | | | reg32 | | 0x65f8 |
| offset | | 26104 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2c9c503d-2495-400a-badb-f5b7deb24cf4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER191\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER191\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER191\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.626 Distributor\_\_37\_GICD\_IROUTER191\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER191\_upper | | | | | reg32 | | 0x65fc |
| offset | | 26108 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c99fb278-cbe1-4cb1-a8e1-e93949ff3c70 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER191\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.627 Distributor\_\_37\_GICD\_IROUTER192\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER192\_lower | | | | | reg32 | | 0x6600 |
| offset | | 26112 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a09504ce-b751-4f26-a59f-72e01e21e183 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER192\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER192\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER192\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.628 Distributor\_\_37\_GICD\_IROUTER192\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER192\_upper | | | | | reg32 | | 0x6604 |
| offset | | 26116 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=24b15b86-bac2-4c1b-8895-103b805d309d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER192\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.629 Distributor\_\_37\_GICD\_IROUTER193\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER193\_lower | | | | | reg32 | | 0x6608 |
| offset | | 26120 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8249a160-fa43-4f45-aba3-e08cfbea0fc1 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER193\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER193\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER193\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.630 Distributor\_\_37\_GICD\_IROUTER193\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER193\_upper | | | | | reg32 | | 0x660c |
| offset | | 26124 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e1819307-79b8-44fa-8050-a5d73cb8325d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER193\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.631 Distributor\_\_37\_GICD\_IROUTER194\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER194\_lower | | | | | reg32 | | 0x6610 |
| offset | | 26128 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2b118a5f-863a-413a-abe9-780f6762474e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER194\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER194\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER194\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.632 Distributor\_\_37\_GICD\_IROUTER194\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER194\_upper | | | | | reg32 | | 0x6614 |
| offset | | 26132 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c66c7f2e-a63f-4f33-9160-c35fb552faf0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER194\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.633 Distributor\_\_37\_GICD\_IROUTER195\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER195\_lower | | | | | reg32 | | 0x6618 |
| offset | | 26136 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c0130286-05be-478b-8801-27760e93023a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER195\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER195\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER195\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.634 Distributor\_\_37\_GICD\_IROUTER195\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER195\_upper | | | | | reg32 | | 0x661c |
| offset | | 26140 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0feb3065-e538-4fc5-8b94-b1f922cba8e7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER195\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.635 Distributor\_\_37\_GICD\_IROUTER196\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER196\_lower | | | | | reg32 | | 0x6620 |
| offset | | 26144 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=670437a8-61eb-4b55-bda7-de195e4a6d46 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER196\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER196\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER196\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.636 Distributor\_\_37\_GICD\_IROUTER196\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER196\_upper | | | | | reg32 | | 0x6624 |
| offset | | 26148 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ac2e5970-09d9-412d-b507-cddaf0e8885c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER196\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.637 Distributor\_\_37\_GICD\_IROUTER197\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER197\_lower | | | | | reg32 | | 0x6628 |
| offset | | 26152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dd70afee-740f-4e95-997e-922ded2ee871 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER197\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER197\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER197\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.638 Distributor\_\_37\_GICD\_IROUTER197\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER197\_upper | | | | | reg32 | | 0x662c |
| offset | | 26156 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f675f66a-4ed9-4cdb-891a-764eedb6e938 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER197\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.639 Distributor\_\_37\_GICD\_IROUTER198\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER198\_lower | | | | | reg32 | | 0x6630 |
| offset | | 26160 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4025f470-12a9-44b9-ad7d-ed7c408c58f9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER198\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER198\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER198\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.640 Distributor\_\_37\_GICD\_IROUTER198\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER198\_upper | | | | | reg32 | | 0x6634 |
| offset | | 26164 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9bf9cfd8-9d18-4892-aa69-505dccc2ffeb | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER198\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.641 Distributor\_\_37\_GICD\_IROUTER199\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER199\_lower | | | | | reg32 | | 0x6638 |
| offset | | 26168 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=72779d22-37ae-4733-b669-ae24c52641d3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER199\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER199\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER199\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.642 Distributor\_\_37\_GICD\_IROUTER199\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER199\_upper | | | | | reg32 | | 0x663c |
| offset | | 26172 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=92efc968-6c46-4d1d-8f75-d2b1ff4d85db | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER199\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.643 Distributor\_\_37\_GICD\_IROUTER200\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER200\_lower | | | | | reg32 | | 0x6640 |
| offset | | 26176 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=266103e5-29b5-4e8b-96cc-97cace465499 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER200\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER200\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER200\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.644 Distributor\_\_37\_GICD\_IROUTER200\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER200\_upper | | | | | reg32 | | 0x6644 |
| offset | | 26180 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e3bd1dd5-4655-4e5b-a6b1-e4012596c7ec | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER200\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.645 Distributor\_\_37\_GICD\_IROUTER201\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER201\_lower | | | | | reg32 | | 0x6648 |
| offset | | 26184 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e49f98b9-edef-467d-8db2-36c84e34c612 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER201\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER201\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER201\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.646 Distributor\_\_37\_GICD\_IROUTER201\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER201\_upper | | | | | reg32 | | 0x664c |
| offset | | 26188 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ddb6648e-967e-4b61-b589-79795713f120 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER201\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.647 Distributor\_\_37\_GICD\_IROUTER202\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER202\_lower | | | | | reg32 | | 0x6650 |
| offset | | 26192 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0c1a5a7a-3416-4987-ad7e-b3a595968253 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER202\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER202\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER202\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.648 Distributor\_\_37\_GICD\_IROUTER202\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER202\_upper | | | | | reg32 | | 0x6654 |
| offset | | 26196 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=495f7c39-c90d-4a0a-9be4-5c393dd675ff | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER202\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.649 Distributor\_\_37\_GICD\_IROUTER203\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER203\_lower | | | | | reg32 | | 0x6658 |
| offset | | 26200 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8059a577-6a73-4894-b671-163ec8e25ff8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER203\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER203\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER203\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.650 Distributor\_\_37\_GICD\_IROUTER203\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER203\_upper | | | | | reg32 | | 0x665c |
| offset | | 26204 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e7e45a08-0a8d-4a41-b959-7e61a5302e14 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER203\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.651 Distributor\_\_37\_GICD\_IROUTER204\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER204\_lower | | | | | reg32 | | 0x6660 |
| offset | | 26208 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9050d6cb-c929-4458-a46b-49819b76deff | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER204\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER204\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER204\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.652 Distributor\_\_37\_GICD\_IROUTER204\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER204\_upper | | | | | reg32 | | 0x6664 |
| offset | | 26212 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8b9ab117-c36b-47b5-9299-8a0818ae2f6d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER204\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.653 Distributor\_\_37\_GICD\_IROUTER205\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER205\_lower | | | | | reg32 | | 0x6668 |
| offset | | 26216 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1ece112f-c29a-44ba-8d10-0abeb19a77ba | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER205\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER205\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER205\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.654 Distributor\_\_37\_GICD\_IROUTER205\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER205\_upper | | | | | reg32 | | 0x666c |
| offset | | 26220 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fc777bfc-0520-4a77-a930-a5d688da1d2f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER205\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.655 Distributor\_\_37\_GICD\_IROUTER206\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER206\_lower | | | | | reg32 | | 0x6670 |
| offset | | 26224 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=43a95b0b-a6ab-489a-9caa-a3ec8c42a8ee | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER206\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER206\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER206\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.656 Distributor\_\_37\_GICD\_IROUTER206\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER206\_upper | | | | | reg32 | | 0x6674 |
| offset | | 26228 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ed4cbe50-10da-443d-b136-ca47997e079d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER206\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.657 Distributor\_\_37\_GICD\_IROUTER207\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER207\_lower | | | | | reg32 | | 0x6678 |
| offset | | 26232 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a4fe6feb-0b7c-4bdd-97d9-19cbae37632e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER207\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER207\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER207\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.658 Distributor\_\_37\_GICD\_IROUTER207\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER207\_upper | | | | | reg32 | | 0x667c |
| offset | | 26236 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a51c5ef4-1a51-426b-bd9a-544204ec1fd4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER207\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.659 Distributor\_\_37\_GICD\_IROUTER208\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER208\_lower | | | | | reg32 | | 0x6680 |
| offset | | 26240 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=17fb21d9-b037-40b9-902b-f8943e948e69 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER208\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER208\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER208\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.660 Distributor\_\_37\_GICD\_IROUTER208\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER208\_upper | | | | | reg32 | | 0x6684 |
| offset | | 26244 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=33924fd5-8694-442c-aa11-b06b0a6d281c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER208\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.661 Distributor\_\_37\_GICD\_IROUTER209\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER209\_lower | | | | | reg32 | | 0x6688 |
| offset | | 26248 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=17b5f400-2fe1-4f5c-8df9-b7487233c1da | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER209\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER209\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER209\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.662 Distributor\_\_37\_GICD\_IROUTER209\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER209\_upper | | | | | reg32 | | 0x668c |
| offset | | 26252 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=95816197-9b6a-47ea-83c9-819c71507663 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER209\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.663 Distributor\_\_37\_GICD\_IROUTER210\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER210\_lower | | | | | reg32 | | 0x6690 |
| offset | | 26256 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2d0486a9-6b3c-425b-b3ed-fbfa2dcfe31a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER210\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER210\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER210\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.664 Distributor\_\_37\_GICD\_IROUTER210\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER210\_upper | | | | | reg32 | | 0x6694 |
| offset | | 26260 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3178f989-457f-4916-a5ff-395e127d7905 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER210\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.665 Distributor\_\_37\_GICD\_IROUTER211\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER211\_lower | | | | | reg32 | | 0x6698 |
| offset | | 26264 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f8fa7761-7f14-4429-827a-f804a5694b1f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER211\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER211\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER211\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.666 Distributor\_\_37\_GICD\_IROUTER211\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER211\_upper | | | | | reg32 | | 0x669c |
| offset | | 26268 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5dc42de3-37e3-4d34-a209-d4a18facc45c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER211\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.667 Distributor\_\_37\_GICD\_IROUTER212\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER212\_lower | | | | | reg32 | | 0x66a0 |
| offset | | 26272 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=33619b65-b2aa-47cb-af8f-8f4cfc80dc4a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER212\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER212\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER212\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.668 Distributor\_\_37\_GICD\_IROUTER212\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER212\_upper | | | | | reg32 | | 0x66a4 |
| offset | | 26276 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ae4943cc-451b-4c3e-9cbe-3295313413c2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER212\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.669 Distributor\_\_37\_GICD\_IROUTER213\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER213\_lower | | | | | reg32 | | 0x66a8 |
| offset | | 26280 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6ea65c7a-77de-4d11-8e78-8162c718f98b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER213\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER213\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER213\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.670 Distributor\_\_37\_GICD\_IROUTER213\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER213\_upper | | | | | reg32 | | 0x66ac |
| offset | | 26284 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d67936ca-28ce-4f4f-a07e-316c689d5dec | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER213\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.671 Distributor\_\_37\_GICD\_IROUTER214\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER214\_lower | | | | | reg32 | | 0x66b0 |
| offset | | 26288 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f59f8f40-e861-4097-864d-c3620d66548d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER214\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER214\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER214\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.672 Distributor\_\_37\_GICD\_IROUTER214\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER214\_upper | | | | | reg32 | | 0x66b4 |
| offset | | 26292 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0dadb8a2-994d-46a4-a796-7f1a741782e9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER214\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.673 Distributor\_\_37\_GICD\_IROUTER215\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER215\_lower | | | | | reg32 | | 0x66b8 |
| offset | | 26296 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=39758520-1889-4996-80b3-ea31a244ab33 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER215\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER215\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER215\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.674 Distributor\_\_37\_GICD\_IROUTER215\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER215\_upper | | | | | reg32 | | 0x66bc |
| offset | | 26300 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=76eda74a-f530-4777-a134-85d5be873f8d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER215\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.675 Distributor\_\_37\_GICD\_IROUTER216\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER216\_lower | | | | | reg32 | | 0x66c0 |
| offset | | 26304 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=42c1155f-f44d-4f7d-8207-5406965f616b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER216\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER216\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER216\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.676 Distributor\_\_37\_GICD\_IROUTER216\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER216\_upper | | | | | reg32 | | 0x66c4 |
| offset | | 26308 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5f5c660e-0fc4-461f-9d52-558be188a8ce | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER216\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.677 Distributor\_\_37\_GICD\_IROUTER217\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER217\_lower | | | | | reg32 | | 0x66c8 |
| offset | | 26312 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=22603243-ea60-4e05-b665-13f46affea45 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER217\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER217\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER217\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.678 Distributor\_\_37\_GICD\_IROUTER217\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER217\_upper | | | | | reg32 | | 0x66cc |
| offset | | 26316 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3c52c933-1e6a-42e1-b7bf-f0c0c014c1a8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER217\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.679 Distributor\_\_37\_GICD\_IROUTER218\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER218\_lower | | | | | reg32 | | 0x66d0 |
| offset | | 26320 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5085a6eb-8720-4a3e-9356-e922ca375649 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER218\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER218\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER218\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.680 Distributor\_\_37\_GICD\_IROUTER218\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER218\_upper | | | | | reg32 | | 0x66d4 |
| offset | | 26324 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6f543831-c0c2-4522-8c49-02299132e18d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER218\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.681 Distributor\_\_37\_GICD\_IROUTER219\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER219\_lower | | | | | reg32 | | 0x66d8 |
| offset | | 26328 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=aa2e4b02-827a-4cfc-be34-97aef397600a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER219\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER219\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER219\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.682 Distributor\_\_37\_GICD\_IROUTER219\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER219\_upper | | | | | reg32 | | 0x66dc |
| offset | | 26332 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f827e789-a8f1-4205-bef5-f9cb27473873 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER219\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.683 Distributor\_\_37\_GICD\_IROUTER220\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER220\_lower | | | | | reg32 | | 0x66e0 |
| offset | | 26336 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8e014b52-a823-4677-84e2-18ecce673854 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER220\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER220\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER220\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.684 Distributor\_\_37\_GICD\_IROUTER220\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER220\_upper | | | | | reg32 | | 0x66e4 |
| offset | | 26340 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8394da0d-0280-4ed0-8c55-bcb2aac82936 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER220\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.685 Distributor\_\_37\_GICD\_IROUTER221\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER221\_lower | | | | | reg32 | | 0x66e8 |
| offset | | 26344 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7b408f5b-8361-42b3-8154-f394c80c43a2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER221\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER221\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER221\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.686 Distributor\_\_37\_GICD\_IROUTER221\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER221\_upper | | | | | reg32 | | 0x66ec |
| offset | | 26348 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bfab9387-5404-4805-8fef-89553515c1d2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER221\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.687 Distributor\_\_37\_GICD\_IROUTER222\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER222\_lower | | | | | reg32 | | 0x66f0 |
| offset | | 26352 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=44598190-3df2-44f4-ac9a-9e7910376b59 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER222\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER222\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER222\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.688 Distributor\_\_37\_GICD\_IROUTER222\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER222\_upper | | | | | reg32 | | 0x66f4 |
| offset | | 26356 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=aa02282b-e404-4dbf-aae0-6e90ebc8c3f5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER222\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.689 Distributor\_\_37\_GICD\_IROUTER223\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER223\_lower | | | | | reg32 | | 0x66f8 |
| offset | | 26360 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a205a596-862b-4d2f-9ab7-d528b63a1761 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER223\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER223\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER223\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.690 Distributor\_\_37\_GICD\_IROUTER223\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER223\_upper | | | | | reg32 | | 0x66fc |
| offset | | 26364 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e39a3603-388f-4c32-b7f8-6d8de73d951d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER223\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.691 Distributor\_\_37\_GICD\_IROUTER224\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER224\_lower | | | | | reg32 | | 0x6700 |
| offset | | 26368 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b49331a3-c588-4009-a9cc-7643a323d113 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER224\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER224\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER224\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.692 Distributor\_\_37\_GICD\_IROUTER224\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER224\_upper | | | | | reg32 | | 0x6704 |
| offset | | 26372 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c6f17e77-8b52-47ed-9686-5c10b789b809 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER224\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.693 Distributor\_\_37\_GICD\_IROUTER225\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER225\_lower | | | | | reg32 | | 0x6708 |
| offset | | 26376 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8868b100-ccc7-4ea9-bf80-519a27c03bd9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER225\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER225\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER225\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.694 Distributor\_\_37\_GICD\_IROUTER225\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER225\_upper | | | | | reg32 | | 0x670c |
| offset | | 26380 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d57ea6ef-dc31-4526-8938-3dfec744de58 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER225\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.695 Distributor\_\_37\_GICD\_IROUTER226\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER226\_lower | | | | | reg32 | | 0x6710 |
| offset | | 26384 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ec20d043-9e58-4c6c-99ad-e7f5fa8e6d4a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER226\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER226\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER226\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.696 Distributor\_\_37\_GICD\_IROUTER226\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER226\_upper | | | | | reg32 | | 0x6714 |
| offset | | 26388 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=86f26a5d-f347-42e9-809b-b4176b32b15f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER226\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.697 Distributor\_\_37\_GICD\_IROUTER227\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER227\_lower | | | | | reg32 | | 0x6718 |
| offset | | 26392 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=79492a67-6821-4649-af58-7b74ae9e3df0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER227\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER227\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER227\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.698 Distributor\_\_37\_GICD\_IROUTER227\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER227\_upper | | | | | reg32 | | 0x671c |
| offset | | 26396 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6cb746e8-fd44-4ac7-a688-4a1ea7a9b174 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER227\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.699 Distributor\_\_37\_GICD\_IROUTER228\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER228\_lower | | | | | reg32 | | 0x6720 |
| offset | | 26400 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d2080d22-d00c-4098-9fb8-cb4d8c689416 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER228\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER228\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER228\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.700 Distributor\_\_37\_GICD\_IROUTER228\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER228\_upper | | | | | reg32 | | 0x6724 |
| offset | | 26404 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4538e004-29c9-4dcb-8cc8-15611d4884ce | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER228\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.701 Distributor\_\_37\_GICD\_IROUTER229\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER229\_lower | | | | | reg32 | | 0x6728 |
| offset | | 26408 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=286dcc47-0fba-45da-8363-5cb66a7269a4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER229\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER229\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER229\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.702 Distributor\_\_37\_GICD\_IROUTER229\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER229\_upper | | | | | reg32 | | 0x672c |
| offset | | 26412 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=08788bd7-49b1-45de-bc60-7d0d71d07ba6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER229\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.703 Distributor\_\_37\_GICD\_IROUTER230\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER230\_lower | | | | | reg32 | | 0x6730 |
| offset | | 26416 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=780d7f69-f1ae-40f4-acb5-27998f892c16 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER230\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER230\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER230\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.704 Distributor\_\_37\_GICD\_IROUTER230\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER230\_upper | | | | | reg32 | | 0x6734 |
| offset | | 26420 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=938f3064-c921-4e4e-bb78-96bc47dc9d28 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER230\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.705 Distributor\_\_37\_GICD\_IROUTER231\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER231\_lower | | | | | reg32 | | 0x6738 |
| offset | | 26424 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f68fbec1-5866-4139-b53b-759177856c8a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER231\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER231\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER231\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.706 Distributor\_\_37\_GICD\_IROUTER231\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER231\_upper | | | | | reg32 | | 0x673c |
| offset | | 26428 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3e82d21c-4080-4f5b-982f-ef29eb227029 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER231\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.707 Distributor\_\_37\_GICD\_IROUTER232\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER232\_lower | | | | | reg32 | | 0x6740 |
| offset | | 26432 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=142f2f72-822b-4714-8b35-710f9254f0ce | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER232\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER232\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER232\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.708 Distributor\_\_37\_GICD\_IROUTER232\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER232\_upper | | | | | reg32 | | 0x6744 |
| offset | | 26436 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bd834443-d067-47be-9778-b8e01634fefb | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER232\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.709 Distributor\_\_37\_GICD\_IROUTER233\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER233\_lower | | | | | reg32 | | 0x6748 |
| offset | | 26440 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7db44224-4ce0-488a-a191-6f75ca5c088a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER233\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER233\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER233\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.710 Distributor\_\_37\_GICD\_IROUTER233\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER233\_upper | | | | | reg32 | | 0x674c |
| offset | | 26444 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f3f7585e-d212-4e71-bd44-351b58911559 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER233\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.711 Distributor\_\_37\_GICD\_IROUTER234\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER234\_lower | | | | | reg32 | | 0x6750 |
| offset | | 26448 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8179d8cf-cbfe-452d-9ca5-fc1255ea4e46 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER234\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER234\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER234\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.712 Distributor\_\_37\_GICD\_IROUTER234\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER234\_upper | | | | | reg32 | | 0x6754 |
| offset | | 26452 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=af395e19-043f-4f3b-97f4-4b685cf9527b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER234\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.713 Distributor\_\_37\_GICD\_IROUTER235\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER235\_lower | | | | | reg32 | | 0x6758 |
| offset | | 26456 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0c2d874b-ac9d-46cf-bff6-1360ef4e4855 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER235\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER235\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER235\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.714 Distributor\_\_37\_GICD\_IROUTER235\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER235\_upper | | | | | reg32 | | 0x675c |
| offset | | 26460 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=62b38e96-230f-4f83-b490-f1970816ee8d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER235\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.715 Distributor\_\_37\_GICD\_IROUTER236\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER236\_lower | | | | | reg32 | | 0x6760 |
| offset | | 26464 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5bdcca44-ec37-4522-8b2e-1b4f4e87605f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER236\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER236\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER236\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.716 Distributor\_\_37\_GICD\_IROUTER236\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER236\_upper | | | | | reg32 | | 0x6764 |
| offset | | 26468 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7378b76d-1ec9-41fa-84e2-ee352b890a4d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER236\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.717 Distributor\_\_37\_GICD\_IROUTER237\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER237\_lower | | | | | reg32 | | 0x6768 |
| offset | | 26472 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8f3dac13-0cb9-4964-a6e2-c582579a2e16 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER237\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER237\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER237\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.718 Distributor\_\_37\_GICD\_IROUTER237\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER237\_upper | | | | | reg32 | | 0x676c |
| offset | | 26476 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2484c75e-39f3-4349-a6c1-748dda11a481 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER237\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.719 Distributor\_\_37\_GICD\_IROUTER238\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER238\_lower | | | | | reg32 | | 0x6770 |
| offset | | 26480 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=74b9a74c-51e7-4465-8bf2-2dddaf791bb1 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER238\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER238\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER238\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.720 Distributor\_\_37\_GICD\_IROUTER238\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER238\_upper | | | | | reg32 | | 0x6774 |
| offset | | 26484 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b3219671-f52c-4595-8253-4354a3696e81 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER238\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.721 Distributor\_\_37\_GICD\_IROUTER239\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER239\_lower | | | | | reg32 | | 0x6778 |
| offset | | 26488 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=797186f2-abef-46b2-aeb1-4eb277470cb1 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER239\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER239\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER239\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.722 Distributor\_\_37\_GICD\_IROUTER239\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER239\_upper | | | | | reg32 | | 0x677c |
| offset | | 26492 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1d43b589-6c64-4336-8ebe-c4705ce826f3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER239\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.723 Distributor\_\_37\_GICD\_IROUTER240\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER240\_lower | | | | | reg32 | | 0x6780 |
| offset | | 26496 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f5e2ec40-9c3c-43ae-8406-ae36bb561b1a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER240\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER240\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER240\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.724 Distributor\_\_37\_GICD\_IROUTER240\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER240\_upper | | | | | reg32 | | 0x6784 |
| offset | | 26500 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dcad3949-89ad-4557-ad7c-fcf72e21788e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER240\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.725 Distributor\_\_37\_GICD\_IROUTER241\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER241\_lower | | | | | reg32 | | 0x6788 |
| offset | | 26504 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7408d2e1-7f90-41f9-a675-24a469f2e8a5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER241\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER241\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER241\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.726 Distributor\_\_37\_GICD\_IROUTER241\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER241\_upper | | | | | reg32 | | 0x678c |
| offset | | 26508 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bba73377-37e9-47b4-9735-aa39c85bbd60 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER241\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.727 Distributor\_\_37\_GICD\_IROUTER242\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER242\_lower | | | | | reg32 | | 0x6790 |
| offset | | 26512 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c7400c91-f1d2-485e-936a-088590285212 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER242\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER242\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER242\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.728 Distributor\_\_37\_GICD\_IROUTER242\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER242\_upper | | | | | reg32 | | 0x6794 |
| offset | | 26516 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f8a406a4-6f43-4976-8e36-4c7d332f172e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER242\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.729 Distributor\_\_37\_GICD\_IROUTER243\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER243\_lower | | | | | reg32 | | 0x6798 |
| offset | | 26520 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a09a0305-960f-49e4-9f9d-21c1ecb476c3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER243\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER243\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER243\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.730 Distributor\_\_37\_GICD\_IROUTER243\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER243\_upper | | | | | reg32 | | 0x679c |
| offset | | 26524 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bfeb74e8-022e-4061-8072-6522828aa844 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER243\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.731 Distributor\_\_37\_GICD\_IROUTER244\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER244\_lower | | | | | reg32 | | 0x67a0 |
| offset | | 26528 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=577e487b-3207-42a3-946c-79df49913aac | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER244\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER244\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER244\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.732 Distributor\_\_37\_GICD\_IROUTER244\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER244\_upper | | | | | reg32 | | 0x67a4 |
| offset | | 26532 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=10f5a668-49b8-45b5-bbe0-ca569a0ebd64 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER244\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.733 Distributor\_\_37\_GICD\_IROUTER245\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER245\_lower | | | | | reg32 | | 0x67a8 |
| offset | | 26536 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8f7f508d-7262-4a41-b815-a6301ce0c2f9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER245\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER245\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER245\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.734 Distributor\_\_37\_GICD\_IROUTER245\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER245\_upper | | | | | reg32 | | 0x67ac |
| offset | | 26540 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2d5fcc9c-7848-4a2b-afea-6cc442faf3cc | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER245\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.735 Distributor\_\_37\_GICD\_IROUTER246\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER246\_lower | | | | | reg32 | | 0x67b0 |
| offset | | 26544 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=aff36229-0b60-475a-a839-2bef2c360b44 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER246\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER246\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER246\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.736 Distributor\_\_37\_GICD\_IROUTER246\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER246\_upper | | | | | reg32 | | 0x67b4 |
| offset | | 26548 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=56adbc0b-2e64-4c9f-a9c9-954bda959df0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER246\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.737 Distributor\_\_37\_GICD\_IROUTER247\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER247\_lower | | | | | reg32 | | 0x67b8 |
| offset | | 26552 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ce318221-496b-47ed-a033-9107f8852127 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER247\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER247\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER247\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.738 Distributor\_\_37\_GICD\_IROUTER247\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER247\_upper | | | | | reg32 | | 0x67bc |
| offset | | 26556 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0111dde0-28b8-4780-a7f3-6ec304340f1d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER247\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.739 Distributor\_\_37\_GICD\_IROUTER248\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER248\_lower | | | | | reg32 | | 0x67c0 |
| offset | | 26560 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9b026588-e48b-456c-ae81-6021db8cdf9d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER248\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER248\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER248\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.740 Distributor\_\_37\_GICD\_IROUTER248\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER248\_upper | | | | | reg32 | | 0x67c4 |
| offset | | 26564 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6e6d8ea0-de36-452e-bb88-9aa8d8ea1d80 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER248\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.741 Distributor\_\_37\_GICD\_IROUTER249\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER249\_lower | | | | | reg32 | | 0x67c8 |
| offset | | 26568 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=73724dff-fab2-4cce-99c7-bea7e81c5f8a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER249\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER249\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER249\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.742 Distributor\_\_37\_GICD\_IROUTER249\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER249\_upper | | | | | reg32 | | 0x67cc |
| offset | | 26572 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=64a78b88-95ca-40a3-bb3e-bea98b5802b1 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER249\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.743 Distributor\_\_37\_GICD\_IROUTER250\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER250\_lower | | | | | reg32 | | 0x67d0 |
| offset | | 26576 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e27f4976-9339-44b4-a813-b06068567c47 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER250\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER250\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER250\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.744 Distributor\_\_37\_GICD\_IROUTER250\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER250\_upper | | | | | reg32 | | 0x67d4 |
| offset | | 26580 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1916d170-bd3e-4a1f-84b0-306ed6283fb3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER250\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.745 Distributor\_\_37\_GICD\_IROUTER251\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER251\_lower | | | | | reg32 | | 0x67d8 |
| offset | | 26584 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=554ebf79-c3c1-420c-b189-4cd253046809 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER251\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER251\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER251\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.746 Distributor\_\_37\_GICD\_IROUTER251\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER251\_upper | | | | | reg32 | | 0x67dc |
| offset | | 26588 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=23f68bab-b5e8-4315-9030-f0fccdd76694 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER251\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.747 Distributor\_\_37\_GICD\_IROUTER252\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER252\_lower | | | | | reg32 | | 0x67e0 |
| offset | | 26592 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=99b5f741-79a7-44e9-8b4e-dac44f79448b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER252\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER252\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER252\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.748 Distributor\_\_37\_GICD\_IROUTER252\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER252\_upper | | | | | reg32 | | 0x67e4 |
| offset | | 26596 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4c6ac3c5-99e3-4a46-9f62-7e21df25c37a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER252\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.749 Distributor\_\_37\_GICD\_IROUTER253\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER253\_lower | | | | | reg32 | | 0x67e8 |
| offset | | 26600 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=124735c3-307a-4543-90b7-73faed09ec3d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER253\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER253\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER253\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.750 Distributor\_\_37\_GICD\_IROUTER253\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER253\_upper | | | | | reg32 | | 0x67ec |
| offset | | 26604 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=36ad8057-4557-44f2-8447-60e3cc979eed | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER253\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.751 Distributor\_\_37\_GICD\_IROUTER254\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER254\_lower | | | | | reg32 | | 0x67f0 |
| offset | | 26608 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=75cdec8c-97da-469f-9339-cbdc8e151561 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER254\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER254\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER254\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.752 Distributor\_\_37\_GICD\_IROUTER254\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER254\_upper | | | | | reg32 | | 0x67f4 |
| offset | | 26612 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e5cb7d32-10a1-4a2e-adcd-70fe5b638dee | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER254\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.753 Distributor\_\_37\_GICD\_IROUTER255\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER255\_lower | | | | | reg32 | | 0x67f8 |
| offset | | 26616 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9393c760-3d51-4da0-868c-0f94aa400893 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER255\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER255\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER255\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.754 Distributor\_\_37\_GICD\_IROUTER255\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER255\_upper | | | | | reg32 | | 0x67fc |
| offset | | 26620 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c1e0132e-42c8-4663-9b99-882b20553814 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER255\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.755 Distributor\_\_37\_GICD\_IROUTER256\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER256\_lower | | | | | reg32 | | 0x6800 |
| offset | | 26624 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=926806a5-a205-43a0-a867-621a14530eed | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER256\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER256\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER256\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.756 Distributor\_\_37\_GICD\_IROUTER256\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER256\_upper | | | | | reg32 | | 0x6804 |
| offset | | 26628 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2b72b350-294c-40de-8342-7650031bbd23 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER256\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.757 Distributor\_\_37\_GICD\_IROUTER257\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER257\_lower | | | | | reg32 | | 0x6808 |
| offset | | 26632 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=97913dc1-24ba-40df-8e4a-5a202ba380ae | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER257\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER257\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER257\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.758 Distributor\_\_37\_GICD\_IROUTER257\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER257\_upper | | | | | reg32 | | 0x680c |
| offset | | 26636 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7b5668ba-9c03-43e6-9498-90b28b310a15 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER257\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.759 Distributor\_\_37\_GICD\_IROUTER258\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER258\_lower | | | | | reg32 | | 0x6810 |
| offset | | 26640 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=620e936f-de68-417a-881d-9438c700b31a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER258\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER258\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER258\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.760 Distributor\_\_37\_GICD\_IROUTER258\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER258\_upper | | | | | reg32 | | 0x6814 |
| offset | | 26644 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f847dbce-1e54-4ade-adaa-97aaebd1dc94 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER258\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.761 Distributor\_\_37\_GICD\_IROUTER259\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER259\_lower | | | | | reg32 | | 0x6818 |
| offset | | 26648 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ba118dab-3f3f-45af-b115-b2ec0b34eb63 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER259\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER259\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER259\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.762 Distributor\_\_37\_GICD\_IROUTER259\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER259\_upper | | | | | reg32 | | 0x681c |
| offset | | 26652 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fb0a70c5-ab26-435e-a3fa-27e1777f1793 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER259\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.763 Distributor\_\_37\_GICD\_IROUTER260\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER260\_lower | | | | | reg32 | | 0x6820 |
| offset | | 26656 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=95fe2234-2521-4053-8315-b6036aef4bae | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER260\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER260\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER260\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.764 Distributor\_\_37\_GICD\_IROUTER260\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER260\_upper | | | | | reg32 | | 0x6824 |
| offset | | 26660 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=46d1c3b6-c4f9-4ed5-8b4b-0458b481d54d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER260\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.765 Distributor\_\_37\_GICD\_IROUTER261\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER261\_lower | | | | | reg32 | | 0x6828 |
| offset | | 26664 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b49cc124-2fb3-43cc-987e-1cebce79a950 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER261\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER261\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER261\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.766 Distributor\_\_37\_GICD\_IROUTER261\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER261\_upper | | | | | reg32 | | 0x682c |
| offset | | 26668 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7eabaa26-0b96-4838-8601-19f321ed42e0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER261\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.767 Distributor\_\_37\_GICD\_IROUTER262\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER262\_lower | | | | | reg32 | | 0x6830 |
| offset | | 26672 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dea5babc-4730-47ea-a571-f57386769160 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER262\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER262\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER262\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.768 Distributor\_\_37\_GICD\_IROUTER262\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER262\_upper | | | | | reg32 | | 0x6834 |
| offset | | 26676 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=38054187-87e7-4d53-a62e-bca2f4370482 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER262\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.769 Distributor\_\_37\_GICD\_IROUTER263\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER263\_lower | | | | | reg32 | | 0x6838 |
| offset | | 26680 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bebed4d6-2047-471f-9ba2-fb71918b491d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER263\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER263\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER263\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.770 Distributor\_\_37\_GICD\_IROUTER263\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER263\_upper | | | | | reg32 | | 0x683c |
| offset | | 26684 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=de79029b-d471-4750-8527-ea170607b8ed | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER263\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.771 Distributor\_\_37\_GICD\_IROUTER264\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER264\_lower | | | | | reg32 | | 0x6840 |
| offset | | 26688 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3f0e851c-b4d4-4696-b735-6402b6fa3f4e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER264\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER264\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER264\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.772 Distributor\_\_37\_GICD\_IROUTER264\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER264\_upper | | | | | reg32 | | 0x6844 |
| offset | | 26692 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=597b68a3-ebd6-4cb7-a670-8b83e104b749 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER264\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.773 Distributor\_\_37\_GICD\_IROUTER265\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER265\_lower | | | | | reg32 | | 0x6848 |
| offset | | 26696 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c07b4709-d4d5-4d23-95eb-990fb3e11639 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER265\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER265\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER265\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.774 Distributor\_\_37\_GICD\_IROUTER265\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER265\_upper | | | | | reg32 | | 0x684c |
| offset | | 26700 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5aba511e-a6cc-4075-9c44-eb446264b5e5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER265\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.775 Distributor\_\_37\_GICD\_IROUTER266\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER266\_lower | | | | | reg32 | | 0x6850 |
| offset | | 26704 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bf05925b-bd38-4037-aa9d-12499c72bb82 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER266\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER266\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER266\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.776 Distributor\_\_37\_GICD\_IROUTER266\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER266\_upper | | | | | reg32 | | 0x6854 |
| offset | | 26708 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=69df62c5-59a3-4b47-be0e-cc2a3b568b8d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER266\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.777 Distributor\_\_37\_GICD\_IROUTER267\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER267\_lower | | | | | reg32 | | 0x6858 |
| offset | | 26712 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8df5b7a4-9fc6-4a2d-a74c-7fb3769b234f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER267\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER267\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER267\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.778 Distributor\_\_37\_GICD\_IROUTER267\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER267\_upper | | | | | reg32 | | 0x685c |
| offset | | 26716 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3754d09b-7dd0-449f-896a-89155250bc5c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER267\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.779 Distributor\_\_37\_GICD\_IROUTER268\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER268\_lower | | | | | reg32 | | 0x6860 |
| offset | | 26720 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5a33adf1-ef60-48fe-b78c-32f2714c2631 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER268\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER268\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER268\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.780 Distributor\_\_37\_GICD\_IROUTER268\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER268\_upper | | | | | reg32 | | 0x6864 |
| offset | | 26724 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7df1476f-b0fc-43d5-a239-a61c29e4662f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER268\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.781 Distributor\_\_37\_GICD\_IROUTER269\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER269\_lower | | | | | reg32 | | 0x6868 |
| offset | | 26728 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=47fc9b24-5ac2-4767-ae84-f0535b7167b7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER269\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER269\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER269\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.782 Distributor\_\_37\_GICD\_IROUTER269\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER269\_upper | | | | | reg32 | | 0x686c |
| offset | | 26732 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=152c1ced-98e7-477b-bbe0-374add31c6f9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER269\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.783 Distributor\_\_37\_GICD\_IROUTER270\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER270\_lower | | | | | reg32 | | 0x6870 |
| offset | | 26736 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c16d0905-aded-434c-b515-6760e8df1796 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER270\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER270\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER270\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.784 Distributor\_\_37\_GICD\_IROUTER270\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER270\_upper | | | | | reg32 | | 0x6874 |
| offset | | 26740 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f93d91f3-2763-42f4-982d-cad67e868e06 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER270\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.785 Distributor\_\_37\_GICD\_IROUTER271\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER271\_lower | | | | | reg32 | | 0x6878 |
| offset | | 26744 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7dc9f37a-e3d2-42ad-a92d-951c0848cab9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER271\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER271\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER271\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.786 Distributor\_\_37\_GICD\_IROUTER271\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER271\_upper | | | | | reg32 | | 0x687c |
| offset | | 26748 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=464f3ad9-baee-46f3-9eef-524516e47311 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER271\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.787 Distributor\_\_37\_GICD\_IROUTER272\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER272\_lower | | | | | reg32 | | 0x6880 |
| offset | | 26752 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=26f4087f-8363-4929-9991-fc5ead22b042 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER272\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER272\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER272\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.788 Distributor\_\_37\_GICD\_IROUTER272\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER272\_upper | | | | | reg32 | | 0x6884 |
| offset | | 26756 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=17033f37-73e4-4b54-aae2-fd0b704cc136 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER272\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.789 Distributor\_\_37\_GICD\_IROUTER273\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER273\_lower | | | | | reg32 | | 0x6888 |
| offset | | 26760 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9e449b63-4766-4718-964f-9a993d5e783f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER273\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER273\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER273\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.790 Distributor\_\_37\_GICD\_IROUTER273\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER273\_upper | | | | | reg32 | | 0x688c |
| offset | | 26764 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2ed31fa4-5348-4d47-a84d-055b5af7d6c4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER273\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.791 Distributor\_\_37\_GICD\_IROUTER274\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER274\_lower | | | | | reg32 | | 0x6890 |
| offset | | 26768 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e937969a-d943-4739-acdb-de822825424f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER274\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER274\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER274\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.792 Distributor\_\_37\_GICD\_IROUTER274\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER274\_upper | | | | | reg32 | | 0x6894 |
| offset | | 26772 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5b8b17b4-3e4f-4976-9288-4faf9258f0e4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER274\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.793 Distributor\_\_37\_GICD\_IROUTER275\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER275\_lower | | | | | reg32 | | 0x6898 |
| offset | | 26776 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d9690d24-4b9c-4d65-82fc-1387c9f26fcf | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER275\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER275\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER275\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.794 Distributor\_\_37\_GICD\_IROUTER275\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER275\_upper | | | | | reg32 | | 0x689c |
| offset | | 26780 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=149c29c7-fda8-4167-9641-1ede4693b46d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER275\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.795 Distributor\_\_37\_GICD\_IROUTER276\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER276\_lower | | | | | reg32 | | 0x68a0 |
| offset | | 26784 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1fafb671-329d-4020-9bb3-dae33465e353 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER276\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER276\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER276\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.796 Distributor\_\_37\_GICD\_IROUTER276\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER276\_upper | | | | | reg32 | | 0x68a4 |
| offset | | 26788 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=49990c51-9fec-49e6-8030-7e4816423412 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER276\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.797 Distributor\_\_37\_GICD\_IROUTER277\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER277\_lower | | | | | reg32 | | 0x68a8 |
| offset | | 26792 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c59d7610-9de4-46f8-ba66-ea6fa40cbf46 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER277\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER277\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER277\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.798 Distributor\_\_37\_GICD\_IROUTER277\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER277\_upper | | | | | reg32 | | 0x68ac |
| offset | | 26796 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b4c5df57-d7a8-4511-8458-9c1cc233900b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER277\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.799 Distributor\_\_37\_GICD\_IROUTER278\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER278\_lower | | | | | reg32 | | 0x68b0 |
| offset | | 26800 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a31edc56-668b-47d2-91e9-0bcdd092d541 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER278\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER278\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER278\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.800 Distributor\_\_37\_GICD\_IROUTER278\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER278\_upper | | | | | reg32 | | 0x68b4 |
| offset | | 26804 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=10bcea96-fd3e-413a-9b13-0b0039d456be | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER278\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.801 Distributor\_\_37\_GICD\_IROUTER279\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER279\_lower | | | | | reg32 | | 0x68b8 |
| offset | | 26808 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=11c00d98-2e55-4175-bc01-8c7c51e99bde | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER279\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER279\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER279\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.802 Distributor\_\_37\_GICD\_IROUTER279\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER279\_upper | | | | | reg32 | | 0x68bc |
| offset | | 26812 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9e9e392e-49a9-4581-80b7-21aea8cc911d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER279\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.803 Distributor\_\_37\_GICD\_IROUTER280\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER280\_lower | | | | | reg32 | | 0x68c0 |
| offset | | 26816 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=063e2460-cb90-43e9-a120-3beffae22ff0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER280\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER280\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER280\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.804 Distributor\_\_37\_GICD\_IROUTER280\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER280\_upper | | | | | reg32 | | 0x68c4 |
| offset | | 26820 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=81ca9bfc-0f39-4fab-b4cf-8ed65b642ca6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER280\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.805 Distributor\_\_37\_GICD\_IROUTER281\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER281\_lower | | | | | reg32 | | 0x68c8 |
| offset | | 26824 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ce2ba722-2f3d-4110-a8f6-f88cc4b864c7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER281\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER281\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER281\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.806 Distributor\_\_37\_GICD\_IROUTER281\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER281\_upper | | | | | reg32 | | 0x68cc |
| offset | | 26828 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1a648a0c-2aee-4ec7-932a-b325c4c793a7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER281\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.807 Distributor\_\_37\_GICD\_IROUTER282\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER282\_lower | | | | | reg32 | | 0x68d0 |
| offset | | 26832 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0facf0d5-e4d9-4d03-8cc0-c1e403d1345f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER282\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER282\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER282\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.808 Distributor\_\_37\_GICD\_IROUTER282\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER282\_upper | | | | | reg32 | | 0x68d4 |
| offset | | 26836 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8758c252-ed05-4741-84d2-d3a697bdcfd3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER282\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.809 Distributor\_\_37\_GICD\_IROUTER283\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER283\_lower | | | | | reg32 | | 0x68d8 |
| offset | | 26840 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5019ed4a-bdad-4524-8db5-a6cde5b8b5aa | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER283\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER283\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER283\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.810 Distributor\_\_37\_GICD\_IROUTER283\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER283\_upper | | | | | reg32 | | 0x68dc |
| offset | | 26844 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b7a31226-62df-49f5-91f5-8cfecd451857 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER283\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.811 Distributor\_\_37\_GICD\_IROUTER284\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER284\_lower | | | | | reg32 | | 0x68e0 |
| offset | | 26848 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=71f4b4a1-f06a-4580-9762-968ec21bb675 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER284\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER284\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER284\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.812 Distributor\_\_37\_GICD\_IROUTER284\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER284\_upper | | | | | reg32 | | 0x68e4 |
| offset | | 26852 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a9ad13fd-cb27-42b0-a8e4-5ad66eb720af | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER284\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.813 Distributor\_\_37\_GICD\_IROUTER285\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER285\_lower | | | | | reg32 | | 0x68e8 |
| offset | | 26856 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d28a81a4-ec9b-4aa2-8fbc-2a623fcdc525 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER285\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER285\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER285\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.814 Distributor\_\_37\_GICD\_IROUTER285\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER285\_upper | | | | | reg32 | | 0x68ec |
| offset | | 26860 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=56caf618-9036-4666-887b-9c1c544cd005 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER285\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.815 Distributor\_\_37\_GICD\_IROUTER286\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER286\_lower | | | | | reg32 | | 0x68f0 |
| offset | | 26864 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e2ed14e5-ee33-4deb-8f90-9e1c854c9702 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER286\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER286\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER286\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.816 Distributor\_\_37\_GICD\_IROUTER286\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER286\_upper | | | | | reg32 | | 0x68f4 |
| offset | | 26868 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4efe7c6a-bdf1-4481-95f6-6705746bae0e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER286\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.817 Distributor\_\_37\_GICD\_IROUTER287\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER287\_lower | | | | | reg32 | | 0x68f8 |
| offset | | 26872 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6a447b84-64a1-4bd7-b702-7aa3bf293dcd | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER287\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER287\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER287\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.818 Distributor\_\_37\_GICD\_IROUTER287\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER287\_upper | | | | | reg32 | | 0x68fc |
| offset | | 26876 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6d217fd2-88ac-4d50-8d68-37752653df94 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER287\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.819 Distributor\_\_37\_GICD\_IROUTER288\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER288\_lower | | | | | reg32 | | 0x6900 |
| offset | | 26880 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6a442410-a0c0-45d3-9079-ac4d3e23f30f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER288\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER288\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER288\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.820 Distributor\_\_37\_GICD\_IROUTER288\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER288\_upper | | | | | reg32 | | 0x6904 |
| offset | | 26884 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2213c9b5-a538-44ad-bf57-a9091857ce83 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER288\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.821 Distributor\_\_37\_GICD\_IROUTER289\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER289\_lower | | | | | reg32 | | 0x6908 |
| offset | | 26888 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=501d00da-d9b6-44d3-9ac3-554926c727ba | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER289\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER289\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER289\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.822 Distributor\_\_37\_GICD\_IROUTER289\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER289\_upper | | | | | reg32 | | 0x690c |
| offset | | 26892 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c3ecf7cb-7253-4ce3-8c36-534f0315dfde | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER289\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.823 Distributor\_\_37\_GICD\_IROUTER290\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER290\_lower | | | | | reg32 | | 0x6910 |
| offset | | 26896 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=865f1fea-a755-42b9-91cb-424472d37cef | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER290\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER290\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER290\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.824 Distributor\_\_37\_GICD\_IROUTER290\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER290\_upper | | | | | reg32 | | 0x6914 |
| offset | | 26900 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5eda3ff1-8955-46dd-88e3-a5498be5ccc7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER290\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.825 Distributor\_\_37\_GICD\_IROUTER291\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER291\_lower | | | | | reg32 | | 0x6918 |
| offset | | 26904 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=33b62019-c8cd-41b6-b65a-4d4651c26284 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER291\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER291\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER291\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.826 Distributor\_\_37\_GICD\_IROUTER291\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER291\_upper | | | | | reg32 | | 0x691c |
| offset | | 26908 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=637a294e-335d-4669-948e-d765a3df27ea | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER291\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.827 Distributor\_\_37\_GICD\_IROUTER292\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER292\_lower | | | | | reg32 | | 0x6920 |
| offset | | 26912 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=464f858a-06fd-4327-b157-e284ad6d3687 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER292\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER292\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER292\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.828 Distributor\_\_37\_GICD\_IROUTER292\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER292\_upper | | | | | reg32 | | 0x6924 |
| offset | | 26916 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a0253841-7dd9-4282-bef3-29443e1098e9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER292\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.829 Distributor\_\_37\_GICD\_IROUTER293\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER293\_lower | | | | | reg32 | | 0x6928 |
| offset | | 26920 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=13a8b257-4424-41f2-a2d1-f52a501c462f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER293\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER293\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER293\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.830 Distributor\_\_37\_GICD\_IROUTER293\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER293\_upper | | | | | reg32 | | 0x692c |
| offset | | 26924 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=88415289-8734-4be2-9616-163ce57cf1e8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER293\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.831 Distributor\_\_37\_GICD\_IROUTER294\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER294\_lower | | | | | reg32 | | 0x6930 |
| offset | | 26928 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=379d4827-0b94-4b1a-be45-aee1b82cf80c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER294\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER294\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER294\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.832 Distributor\_\_37\_GICD\_IROUTER294\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER294\_upper | | | | | reg32 | | 0x6934 |
| offset | | 26932 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=aa470cfb-00d8-4803-b85f-7b77cbf65b00 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER294\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.833 Distributor\_\_37\_GICD\_IROUTER295\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER295\_lower | | | | | reg32 | | 0x6938 |
| offset | | 26936 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d17424c1-73d3-4f6e-b059-3a975ecac26e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER295\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER295\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER295\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.834 Distributor\_\_37\_GICD\_IROUTER295\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER295\_upper | | | | | reg32 | | 0x693c |
| offset | | 26940 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=896fefe4-ad32-4309-b972-161c7f42d4ee | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER295\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.835 Distributor\_\_37\_GICD\_IROUTER296\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER296\_lower | | | | | reg32 | | 0x6940 |
| offset | | 26944 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e91db179-2fd7-4d66-bd94-f0e1862146ac | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER296\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER296\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER296\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.836 Distributor\_\_37\_GICD\_IROUTER296\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER296\_upper | | | | | reg32 | | 0x6944 |
| offset | | 26948 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6fcb518f-b714-4b68-9d71-5bbd595242d6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER296\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.837 Distributor\_\_37\_GICD\_IROUTER297\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER297\_lower | | | | | reg32 | | 0x6948 |
| offset | | 26952 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e2211174-c85e-473f-84fe-c0600f52cd2f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER297\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER297\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER297\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.838 Distributor\_\_37\_GICD\_IROUTER297\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER297\_upper | | | | | reg32 | | 0x694c |
| offset | | 26956 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=52c3ef1a-271a-4fdf-91c4-9b3b007e30db | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER297\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.839 Distributor\_\_37\_GICD\_IROUTER298\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER298\_lower | | | | | reg32 | | 0x6950 |
| offset | | 26960 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cef70146-255b-41c4-8a34-6f6cb968a73b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER298\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER298\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER298\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.840 Distributor\_\_37\_GICD\_IROUTER298\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER298\_upper | | | | | reg32 | | 0x6954 |
| offset | | 26964 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ce776fa2-d4c0-4d5f-b15c-2612989d3130 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER298\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.841 Distributor\_\_37\_GICD\_IROUTER299\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER299\_lower | | | | | reg32 | | 0x6958 |
| offset | | 26968 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0563e91c-a176-4ff7-970c-7d1d1277eba4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER299\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER299\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER299\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.842 Distributor\_\_37\_GICD\_IROUTER299\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER299\_upper | | | | | reg32 | | 0x695c |
| offset | | 26972 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b320b38f-dfe3-4825-9a09-5a3f79bad660 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER299\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.843 Distributor\_\_37\_GICD\_IROUTER300\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER300\_lower | | | | | reg32 | | 0x6960 |
| offset | | 26976 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cbbdd5be-38b1-431b-b153-a806f3ec36e2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER300\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER300\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER300\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.844 Distributor\_\_37\_GICD\_IROUTER300\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER300\_upper | | | | | reg32 | | 0x6964 |
| offset | | 26980 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0b1fcf85-5fd2-4931-82b0-4974efd8e760 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER300\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.845 Distributor\_\_37\_GICD\_IROUTER301\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER301\_lower | | | | | reg32 | | 0x6968 |
| offset | | 26984 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=28d4885a-9bf8-49a0-b764-606920e0ea46 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER301\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER301\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER301\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.846 Distributor\_\_37\_GICD\_IROUTER301\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER301\_upper | | | | | reg32 | | 0x696c |
| offset | | 26988 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=40bdc704-4f7a-41ae-b54b-b9a6015babee | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER301\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.847 Distributor\_\_37\_GICD\_IROUTER302\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER302\_lower | | | | | reg32 | | 0x6970 |
| offset | | 26992 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f114fc21-61cd-46f4-809e-9e7f97a6226b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER302\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER302\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER302\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.848 Distributor\_\_37\_GICD\_IROUTER302\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER302\_upper | | | | | reg32 | | 0x6974 |
| offset | | 26996 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e4ba026b-3991-44b1-b36f-d18f46cb1a08 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER302\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.849 Distributor\_\_37\_GICD\_IROUTER303\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER303\_lower | | | | | reg32 | | 0x6978 |
| offset | | 27000 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d0f00eeb-0f57-4abd-8b1e-75688183f818 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER303\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER303\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER303\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.850 Distributor\_\_37\_GICD\_IROUTER303\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER303\_upper | | | | | reg32 | | 0x697c |
| offset | | 27004 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=09254ae1-6044-4925-941c-9300ad227837 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER303\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.851 Distributor\_\_37\_GICD\_IROUTER304\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER304\_lower | | | | | reg32 | | 0x6980 |
| offset | | 27008 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b9eb11e3-82d3-4eee-ba7f-d6c5156365a2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER304\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER304\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER304\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.852 Distributor\_\_37\_GICD\_IROUTER304\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER304\_upper | | | | | reg32 | | 0x6984 |
| offset | | 27012 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8738a1b8-5eca-4d35-b31e-f55e0602d342 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER304\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.853 Distributor\_\_37\_GICD\_IROUTER305\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER305\_lower | | | | | reg32 | | 0x6988 |
| offset | | 27016 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bbf566d1-c4ac-4807-9de5-dea05d7186a4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER305\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER305\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER305\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.854 Distributor\_\_37\_GICD\_IROUTER305\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER305\_upper | | | | | reg32 | | 0x698c |
| offset | | 27020 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2715fd81-c620-42b6-a2b8-b6099c6c506c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER305\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.855 Distributor\_\_37\_GICD\_IROUTER306\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER306\_lower | | | | | reg32 | | 0x6990 |
| offset | | 27024 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cbcae8c5-e2ce-4afb-b086-621ff3305280 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER306\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER306\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER306\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.856 Distributor\_\_37\_GICD\_IROUTER306\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER306\_upper | | | | | reg32 | | 0x6994 |
| offset | | 27028 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=58ac2aa6-2383-4a6f-b287-93fbfd432964 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER306\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.857 Distributor\_\_37\_GICD\_IROUTER307\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER307\_lower | | | | | reg32 | | 0x6998 |
| offset | | 27032 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e022e8b2-f420-42b7-b55e-2aa7a0e52d73 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER307\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER307\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER307\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.858 Distributor\_\_37\_GICD\_IROUTER307\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER307\_upper | | | | | reg32 | | 0x699c |
| offset | | 27036 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f8b52a38-32b6-48eb-9fae-387ab14da9ef | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER307\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.859 Distributor\_\_37\_GICD\_IROUTER308\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER308\_lower | | | | | reg32 | | 0x69a0 |
| offset | | 27040 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1e1accd4-118e-4c34-8c61-2b05fbf07837 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER308\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER308\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER308\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.860 Distributor\_\_37\_GICD\_IROUTER308\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER308\_upper | | | | | reg32 | | 0x69a4 |
| offset | | 27044 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a8296e49-8759-43c1-9607-227d50dfffa5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER308\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.861 Distributor\_\_37\_GICD\_IROUTER309\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER309\_lower | | | | | reg32 | | 0x69a8 |
| offset | | 27048 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b8e1b934-f638-48e4-924a-2f6dca1f6257 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER309\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER309\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER309\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.862 Distributor\_\_37\_GICD\_IROUTER309\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER309\_upper | | | | | reg32 | | 0x69ac |
| offset | | 27052 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b389fcc4-8aca-4426-8187-28f2b06ede68 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER309\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.863 Distributor\_\_37\_GICD\_IROUTER310\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER310\_lower | | | | | reg32 | | 0x69b0 |
| offset | | 27056 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=37672967-e583-4ff9-94df-1d9279199e5a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER310\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER310\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER310\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.864 Distributor\_\_37\_GICD\_IROUTER310\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER310\_upper | | | | | reg32 | | 0x69b4 |
| offset | | 27060 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=43e24d97-edac-4f85-9cdf-6ca0a579a96c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER310\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.865 Distributor\_\_37\_GICD\_IROUTER311\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER311\_lower | | | | | reg32 | | 0x69b8 |
| offset | | 27064 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1e8805d9-d3a1-4754-934b-841d85d23d1e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER311\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER311\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER311\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.866 Distributor\_\_37\_GICD\_IROUTER311\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER311\_upper | | | | | reg32 | | 0x69bc |
| offset | | 27068 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3fda8438-e2e1-4ebc-bb43-0b32dc163a48 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER311\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.867 Distributor\_\_37\_GICD\_IROUTER312\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER312\_lower | | | | | reg32 | | 0x69c0 |
| offset | | 27072 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ff54499f-c252-4016-981c-caf734134bff | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER312\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER312\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER312\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.868 Distributor\_\_37\_GICD\_IROUTER312\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER312\_upper | | | | | reg32 | | 0x69c4 |
| offset | | 27076 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=52474c7f-8905-4338-835f-4e88cd73f1bf | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER312\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.869 Distributor\_\_37\_GICD\_IROUTER313\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER313\_lower | | | | | reg32 | | 0x69c8 |
| offset | | 27080 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=12d89a01-a742-4d7c-9712-17e76a4a74e1 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER313\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER313\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER313\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.870 Distributor\_\_37\_GICD\_IROUTER313\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER313\_upper | | | | | reg32 | | 0x69cc |
| offset | | 27084 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c8145c04-6114-4cb1-a873-8e823c9be4d2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER313\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.871 Distributor\_\_37\_GICD\_IROUTER314\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER314\_lower | | | | | reg32 | | 0x69d0 |
| offset | | 27088 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=57b8bbd6-e1db-498c-b434-8d198ac012f3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER314\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER314\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER314\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.872 Distributor\_\_37\_GICD\_IROUTER314\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER314\_upper | | | | | reg32 | | 0x69d4 |
| offset | | 27092 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=89962110-6ef6-4f62-b5e1-852ba147590a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER314\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.873 Distributor\_\_37\_GICD\_IROUTER315\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER315\_lower | | | | | reg32 | | 0x69d8 |
| offset | | 27096 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8f22ce18-3079-42ab-a614-a7cb105395d9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER315\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER315\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER315\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.874 Distributor\_\_37\_GICD\_IROUTER315\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER315\_upper | | | | | reg32 | | 0x69dc |
| offset | | 27100 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=75f02c8a-d702-480a-803e-2d1e3eba63d1 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER315\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.875 Distributor\_\_37\_GICD\_IROUTER316\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER316\_lower | | | | | reg32 | | 0x69e0 |
| offset | | 27104 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f34a1936-a148-4020-bbd4-e2f2ebbdb327 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER316\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER316\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER316\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.876 Distributor\_\_37\_GICD\_IROUTER316\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER316\_upper | | | | | reg32 | | 0x69e4 |
| offset | | 27108 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ef5980bf-df9c-4bee-ac54-3d487fb84c03 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER316\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.877 Distributor\_\_37\_GICD\_IROUTER317\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER317\_lower | | | | | reg32 | | 0x69e8 |
| offset | | 27112 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e1c7c271-e185-4692-9d1c-28d61208d786 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER317\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER317\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER317\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.878 Distributor\_\_37\_GICD\_IROUTER317\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER317\_upper | | | | | reg32 | | 0x69ec |
| offset | | 27116 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=23c518cd-ae60-436b-82dc-f782bb9fc0b8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER317\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.879 Distributor\_\_37\_GICD\_IROUTER318\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER318\_lower | | | | | reg32 | | 0x69f0 |
| offset | | 27120 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4bbcd0b8-2c6c-4b67-9ecb-b7e245e60f2c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER318\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER318\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER318\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.880 Distributor\_\_37\_GICD\_IROUTER318\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER318\_upper | | | | | reg32 | | 0x69f4 |
| offset | | 27124 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=83e257f1-a5d0-41d0-833a-ff99a2364670 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER318\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.881 Distributor\_\_37\_GICD\_IROUTER319\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER319\_lower | | | | | reg32 | | 0x69f8 |
| offset | | 27128 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=788628aa-eb15-4af8-9562-26be55988a49 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER319\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER319\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER319\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.882 Distributor\_\_37\_GICD\_IROUTER319\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER319\_upper | | | | | reg32 | | 0x69fc |
| offset | | 27132 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=df9e5d05-f7de-444f-8b07-3dea094a6202 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER319\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.883 Distributor\_\_37\_GICD\_IROUTER320\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER320\_lower | | | | | reg32 | | 0x6a00 |
| offset | | 27136 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9b0f6cc2-af2a-4cad-8dba-9f0881c45ac4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER320\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER320\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER320\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.884 Distributor\_\_37\_GICD\_IROUTER320\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER320\_upper | | | | | reg32 | | 0x6a04 |
| offset | | 27140 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=49b6be3d-49fa-49fe-b7de-56f1b09c339b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER320\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.885 Distributor\_\_37\_GICD\_IROUTER321\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER321\_lower | | | | | reg32 | | 0x6a08 |
| offset | | 27144 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e99722df-03c1-4b3a-b707-2ee7d0e7d986 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER321\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER321\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER321\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.886 Distributor\_\_37\_GICD\_IROUTER321\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER321\_upper | | | | | reg32 | | 0x6a0c |
| offset | | 27148 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=01f54184-3b06-4bd1-8cfa-7a7a5639abeb | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER321\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.887 Distributor\_\_37\_GICD\_IROUTER322\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER322\_lower | | | | | reg32 | | 0x6a10 |
| offset | | 27152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=414e2522-91a1-4ff0-954f-57ed80a283ba | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER322\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER322\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER322\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.888 Distributor\_\_37\_GICD\_IROUTER322\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER322\_upper | | | | | reg32 | | 0x6a14 |
| offset | | 27156 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c5c67192-6812-4e93-917b-a49ade95b278 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER322\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.889 Distributor\_\_37\_GICD\_IROUTER323\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER323\_lower | | | | | reg32 | | 0x6a18 |
| offset | | 27160 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4f739992-9b09-409c-94dc-31c4e0c5c2aa | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER323\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER323\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER323\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.890 Distributor\_\_37\_GICD\_IROUTER323\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER323\_upper | | | | | reg32 | | 0x6a1c |
| offset | | 27164 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=07f0cf4b-dbc2-48a3-bfb2-21c18f0e7733 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER323\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.891 Distributor\_\_37\_GICD\_IROUTER324\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER324\_lower | | | | | reg32 | | 0x6a20 |
| offset | | 27168 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b9b2457e-3d7d-40c2-9249-6d8ae0f0ec15 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER324\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER324\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER324\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.892 Distributor\_\_37\_GICD\_IROUTER324\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER324\_upper | | | | | reg32 | | 0x6a24 |
| offset | | 27172 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=50930254-0c89-408a-9dbc-44773933d13a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER324\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.893 Distributor\_\_37\_GICD\_IROUTER325\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER325\_lower | | | | | reg32 | | 0x6a28 |
| offset | | 27176 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fad576a9-9bcb-47a6-9d7b-1a4df17c47c8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER325\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER325\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER325\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.894 Distributor\_\_37\_GICD\_IROUTER325\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER325\_upper | | | | | reg32 | | 0x6a2c |
| offset | | 27180 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a2b4219c-6252-4261-8396-2394189dd09c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER325\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.895 Distributor\_\_37\_GICD\_IROUTER326\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER326\_lower | | | | | reg32 | | 0x6a30 |
| offset | | 27184 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=02b2d9ec-893b-4009-9fa5-52b424a39d9a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER326\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER326\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER326\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.896 Distributor\_\_37\_GICD\_IROUTER326\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER326\_upper | | | | | reg32 | | 0x6a34 |
| offset | | 27188 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9c127f92-c692-4a99-b91e-6b681dad99a2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER326\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.897 Distributor\_\_37\_GICD\_IROUTER327\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER327\_lower | | | | | reg32 | | 0x6a38 |
| offset | | 27192 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fc28f073-3c26-4c57-905a-c12a5a6fc7fd | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER327\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER327\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER327\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.898 Distributor\_\_37\_GICD\_IROUTER327\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER327\_upper | | | | | reg32 | | 0x6a3c |
| offset | | 27196 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=16d312d4-3e7f-445e-9597-e262900701bc | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER327\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.899 Distributor\_\_37\_GICD\_IROUTER328\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER328\_lower | | | | | reg32 | | 0x6a40 |
| offset | | 27200 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=62c76e72-bab3-43cf-862c-051e878038a4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER328\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER328\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER328\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.900 Distributor\_\_37\_GICD\_IROUTER328\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER328\_upper | | | | | reg32 | | 0x6a44 |
| offset | | 27204 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ed187da0-ceef-4716-8b04-eb4e686e5fdc | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER328\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.901 Distributor\_\_37\_GICD\_IROUTER329\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER329\_lower | | | | | reg32 | | 0x6a48 |
| offset | | 27208 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a3aa021d-7958-43ed-a56f-00b8069d557c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER329\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER329\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER329\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.902 Distributor\_\_37\_GICD\_IROUTER329\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER329\_upper | | | | | reg32 | | 0x6a4c |
| offset | | 27212 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9bbf8f9b-4479-4555-a964-be8fca3928bd | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER329\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.903 Distributor\_\_37\_GICD\_IROUTER330\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER330\_lower | | | | | reg32 | | 0x6a50 |
| offset | | 27216 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=495b12b1-c9f0-4d68-96de-52508bfaf497 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER330\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER330\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER330\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.904 Distributor\_\_37\_GICD\_IROUTER330\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER330\_upper | | | | | reg32 | | 0x6a54 |
| offset | | 27220 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9b1919d9-9ed2-435a-b427-ee7469e86346 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER330\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.905 Distributor\_\_37\_GICD\_IROUTER331\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER331\_lower | | | | | reg32 | | 0x6a58 |
| offset | | 27224 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b16f66bb-93ce-418a-9660-5bd8c99e8fd5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER331\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER331\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER331\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.906 Distributor\_\_37\_GICD\_IROUTER331\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER331\_upper | | | | | reg32 | | 0x6a5c |
| offset | | 27228 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bdb3f7fd-c2a0-48e3-84e8-a0bbbca2da37 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER331\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.907 Distributor\_\_37\_GICD\_IROUTER332\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER332\_lower | | | | | reg32 | | 0x6a60 |
| offset | | 27232 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a9b77439-7b21-4ab7-a0ab-d20133934031 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER332\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER332\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER332\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.908 Distributor\_\_37\_GICD\_IROUTER332\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER332\_upper | | | | | reg32 | | 0x6a64 |
| offset | | 27236 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=67b3062c-ef47-4ec6-b432-6d95fe409d06 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER332\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.909 Distributor\_\_37\_GICD\_IROUTER333\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER333\_lower | | | | | reg32 | | 0x6a68 |
| offset | | 27240 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=47152bf3-2402-4537-8981-d85d112f5c64 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER333\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER333\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER333\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.910 Distributor\_\_37\_GICD\_IROUTER333\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER333\_upper | | | | | reg32 | | 0x6a6c |
| offset | | 27244 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1f0915ea-4201-4753-b284-a5380f242bea | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER333\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.911 Distributor\_\_37\_GICD\_IROUTER334\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER334\_lower | | | | | reg32 | | 0x6a70 |
| offset | | 27248 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f28db2a6-53a6-461e-a175-b65cf36ea86e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER334\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER334\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER334\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.912 Distributor\_\_37\_GICD\_IROUTER334\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER334\_upper | | | | | reg32 | | 0x6a74 |
| offset | | 27252 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ac49d31f-4ef3-4c85-9f30-8cfbce9d72f5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER334\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.913 Distributor\_\_37\_GICD\_IROUTER335\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER335\_lower | | | | | reg32 | | 0x6a78 |
| offset | | 27256 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d170bef2-dbb6-408b-8be1-3817d93e1403 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER335\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER335\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER335\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.914 Distributor\_\_37\_GICD\_IROUTER335\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER335\_upper | | | | | reg32 | | 0x6a7c |
| offset | | 27260 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6d0d8511-3227-4f95-be5f-298712f50523 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER335\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.915 Distributor\_\_37\_GICD\_IROUTER336\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER336\_lower | | | | | reg32 | | 0x6a80 |
| offset | | 27264 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ff2e24d0-f0b6-495d-837f-79e766752d5d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER336\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER336\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER336\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.916 Distributor\_\_37\_GICD\_IROUTER336\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER336\_upper | | | | | reg32 | | 0x6a84 |
| offset | | 27268 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d0b9189f-9877-4bdf-85cf-1051b15e5f00 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER336\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.917 Distributor\_\_37\_GICD\_IROUTER337\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER337\_lower | | | | | reg32 | | 0x6a88 |
| offset | | 27272 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f185ed0e-efb7-4f41-85a0-6e2bf46021c8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER337\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER337\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER337\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.918 Distributor\_\_37\_GICD\_IROUTER337\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER337\_upper | | | | | reg32 | | 0x6a8c |
| offset | | 27276 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6bf31ac1-b7d7-44ac-a146-0095cf589c49 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER337\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.919 Distributor\_\_37\_GICD\_IROUTER338\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER338\_lower | | | | | reg32 | | 0x6a90 |
| offset | | 27280 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c9c85675-eeb1-4bb7-83b6-7f30e2d60011 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER338\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER338\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER338\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.920 Distributor\_\_37\_GICD\_IROUTER338\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER338\_upper | | | | | reg32 | | 0x6a94 |
| offset | | 27284 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=47dfde70-b5df-4dba-a77d-d7a73d5204b6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER338\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.921 Distributor\_\_37\_GICD\_IROUTER339\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER339\_lower | | | | | reg32 | | 0x6a98 |
| offset | | 27288 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8df3ddaf-91fc-4cf7-a700-fa0ab31f8201 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER339\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER339\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER339\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.922 Distributor\_\_37\_GICD\_IROUTER339\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER339\_upper | | | | | reg32 | | 0x6a9c |
| offset | | 27292 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=193a9cb6-07a1-4787-952e-0c9ee2b0cc1a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER339\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.923 Distributor\_\_37\_GICD\_IROUTER340\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER340\_lower | | | | | reg32 | | 0x6aa0 |
| offset | | 27296 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=70b71573-7dbe-4ae0-b93b-cc3b25c126ea | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER340\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER340\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER340\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.924 Distributor\_\_37\_GICD\_IROUTER340\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER340\_upper | | | | | reg32 | | 0x6aa4 |
| offset | | 27300 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8faef410-98aa-4c83-ac46-a5e691564c72 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER340\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.925 Distributor\_\_37\_GICD\_IROUTER341\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER341\_lower | | | | | reg32 | | 0x6aa8 |
| offset | | 27304 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bf565c43-43ba-45b4-ada5-e7dba9bfeb56 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER341\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER341\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER341\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.926 Distributor\_\_37\_GICD\_IROUTER341\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER341\_upper | | | | | reg32 | | 0x6aac |
| offset | | 27308 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=13816496-cbd1-4fd7-b4b8-19e16997e073 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER341\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.927 Distributor\_\_37\_GICD\_IROUTER342\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER342\_lower | | | | | reg32 | | 0x6ab0 |
| offset | | 27312 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4f75a512-1238-469f-8f07-0e49b4ab6855 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER342\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER342\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER342\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.928 Distributor\_\_37\_GICD\_IROUTER342\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER342\_upper | | | | | reg32 | | 0x6ab4 |
| offset | | 27316 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ae4a03e3-55d7-4bf3-9887-8a4b4547c45f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER342\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.929 Distributor\_\_37\_GICD\_IROUTER343\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER343\_lower | | | | | reg32 | | 0x6ab8 |
| offset | | 27320 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=795bcd08-6832-4d36-b905-ab629bb43b02 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER343\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER343\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER343\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.930 Distributor\_\_37\_GICD\_IROUTER343\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER343\_upper | | | | | reg32 | | 0x6abc |
| offset | | 27324 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d53a929f-8fc4-4de7-8e27-79099b31e97c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER343\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.931 Distributor\_\_37\_GICD\_IROUTER344\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER344\_lower | | | | | reg32 | | 0x6ac0 |
| offset | | 27328 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ea290d5b-a29c-4c15-a6c9-0c9808343816 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER344\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER344\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER344\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.932 Distributor\_\_37\_GICD\_IROUTER344\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER344\_upper | | | | | reg32 | | 0x6ac4 |
| offset | | 27332 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b733df3f-15d3-418f-bcc7-9caa28d86eec | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER344\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.933 Distributor\_\_37\_GICD\_IROUTER345\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER345\_lower | | | | | reg32 | | 0x6ac8 |
| offset | | 27336 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=15c30c83-85d6-4ffd-bb71-9efe9c225107 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER345\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER345\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER345\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.934 Distributor\_\_37\_GICD\_IROUTER345\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER345\_upper | | | | | reg32 | | 0x6acc |
| offset | | 27340 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b5821592-c4e6-4a27-9d3f-5316b66449bc | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER345\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.935 Distributor\_\_37\_GICD\_IROUTER346\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER346\_lower | | | | | reg32 | | 0x6ad0 |
| offset | | 27344 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9869006b-fdfc-4fdf-b170-db5d09d0b2de | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER346\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER346\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER346\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.936 Distributor\_\_37\_GICD\_IROUTER346\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER346\_upper | | | | | reg32 | | 0x6ad4 |
| offset | | 27348 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2a912195-ba65-4be4-a67b-fa4d8739a106 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER346\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.937 Distributor\_\_37\_GICD\_IROUTER347\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER347\_lower | | | | | reg32 | | 0x6ad8 |
| offset | | 27352 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c7d36616-ec61-4905-a612-652fddcdc3cc | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER347\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER347\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER347\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.938 Distributor\_\_37\_GICD\_IROUTER347\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER347\_upper | | | | | reg32 | | 0x6adc |
| offset | | 27356 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=285b8d3b-c5c1-4648-80e9-7ea0df2c69be | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER347\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.939 Distributor\_\_37\_GICD\_IROUTER348\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER348\_lower | | | | | reg32 | | 0x6ae0 |
| offset | | 27360 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=160d419a-e45a-4fb2-bbe5-16fa13e5fc4b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER348\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER348\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER348\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.940 Distributor\_\_37\_GICD\_IROUTER348\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER348\_upper | | | | | reg32 | | 0x6ae4 |
| offset | | 27364 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=28237329-f719-4b53-9f34-9278eb8b7449 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER348\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.941 Distributor\_\_37\_GICD\_IROUTER349\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER349\_lower | | | | | reg32 | | 0x6ae8 |
| offset | | 27368 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=45727cc0-f27e-4a9e-9c0d-d3eb89ab28e6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER349\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER349\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER349\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.942 Distributor\_\_37\_GICD\_IROUTER349\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER349\_upper | | | | | reg32 | | 0x6aec |
| offset | | 27372 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e5d4738a-f367-4327-9900-a5324c9d28dd | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER349\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.943 Distributor\_\_37\_GICD\_IROUTER350\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER350\_lower | | | | | reg32 | | 0x6af0 |
| offset | | 27376 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4beeb4ed-074c-4640-9445-c42c56c57340 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER350\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER350\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER350\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.944 Distributor\_\_37\_GICD\_IROUTER350\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER350\_upper | | | | | reg32 | | 0x6af4 |
| offset | | 27380 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e470c986-79b5-4dfe-97b7-7d51f619477d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER350\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.945 Distributor\_\_37\_GICD\_IROUTER351\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER351\_lower | | | | | reg32 | | 0x6af8 |
| offset | | 27384 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=42c9d295-a7d4-4378-9665-f95a360e0fe7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER351\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER351\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER351\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.946 Distributor\_\_37\_GICD\_IROUTER351\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER351\_upper | | | | | reg32 | | 0x6afc |
| offset | | 27388 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8aaeb5af-f525-4145-8568-efd273290532 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER351\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.947 Distributor\_\_37\_GICD\_IROUTER352\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER352\_lower | | | | | reg32 | | 0x6b00 |
| offset | | 27392 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=99d61a2f-c615-44a6-9236-d3c9870f35a3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER352\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER352\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER352\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.948 Distributor\_\_37\_GICD\_IROUTER352\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER352\_upper | | | | | reg32 | | 0x6b04 |
| offset | | 27396 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=72b8950e-f3e0-41d7-b3dc-3376e2296ffb | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER352\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.949 Distributor\_\_37\_GICD\_IROUTER353\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER353\_lower | | | | | reg32 | | 0x6b08 |
| offset | | 27400 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2a6efb33-5884-4f79-84af-86a68662db10 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER353\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER353\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER353\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.950 Distributor\_\_37\_GICD\_IROUTER353\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER353\_upper | | | | | reg32 | | 0x6b0c |
| offset | | 27404 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d20e728c-a40c-44a6-b531-79bf0b433ab7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER353\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.951 Distributor\_\_37\_GICD\_IROUTER354\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER354\_lower | | | | | reg32 | | 0x6b10 |
| offset | | 27408 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=183f198e-2af1-46e0-94c5-717f51b36c21 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER354\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER354\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER354\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.952 Distributor\_\_37\_GICD\_IROUTER354\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER354\_upper | | | | | reg32 | | 0x6b14 |
| offset | | 27412 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0486e347-7ec9-418b-bf17-d812f3dbe982 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER354\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.953 Distributor\_\_37\_GICD\_IROUTER355\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER355\_lower | | | | | reg32 | | 0x6b18 |
| offset | | 27416 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f3f07308-5a80-4b3e-9724-8e31c0a271f8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER355\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER355\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER355\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.954 Distributor\_\_37\_GICD\_IROUTER355\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER355\_upper | | | | | reg32 | | 0x6b1c |
| offset | | 27420 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a4d2d4f0-c060-47fc-9277-7d29cfcc0ca2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER355\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.955 Distributor\_\_37\_GICD\_IROUTER356\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER356\_lower | | | | | reg32 | | 0x6b20 |
| offset | | 27424 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=13ab71a1-691a-449f-ad01-b313645c2f41 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER356\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER356\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER356\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.956 Distributor\_\_37\_GICD\_IROUTER356\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER356\_upper | | | | | reg32 | | 0x6b24 |
| offset | | 27428 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cf963891-c869-4bd5-958a-2d87b228d7ad | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER356\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.957 Distributor\_\_37\_GICD\_IROUTER357\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER357\_lower | | | | | reg32 | | 0x6b28 |
| offset | | 27432 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f5ea51ab-8053-424a-91fb-87211e275e1e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER357\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER357\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER357\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.958 Distributor\_\_37\_GICD\_IROUTER357\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER357\_upper | | | | | reg32 | | 0x6b2c |
| offset | | 27436 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4d89cb23-6d01-4241-b623-a1d61bc5e2fd | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER357\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.959 Distributor\_\_37\_GICD\_IROUTER358\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER358\_lower | | | | | reg32 | | 0x6b30 |
| offset | | 27440 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3d1b518e-64d9-46c0-a0bf-102b94acba0e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER358\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER358\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER358\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.960 Distributor\_\_37\_GICD\_IROUTER358\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER358\_upper | | | | | reg32 | | 0x6b34 |
| offset | | 27444 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b0a4ae32-512a-46c9-95e6-59a56000978f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER358\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.961 Distributor\_\_37\_GICD\_IROUTER359\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER359\_lower | | | | | reg32 | | 0x6b38 |
| offset | | 27448 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=96169fca-5cde-4793-b6d2-cb24790b7956 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER359\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER359\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER359\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.962 Distributor\_\_37\_GICD\_IROUTER359\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER359\_upper | | | | | reg32 | | 0x6b3c |
| offset | | 27452 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=25d3d3f3-5cf2-42f1-84a3-9e8296b36936 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER359\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.963 Distributor\_\_37\_GICD\_IROUTER360\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER360\_lower | | | | | reg32 | | 0x6b40 |
| offset | | 27456 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fc903d28-38ed-4dfd-9a91-f1d14fac2281 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER360\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER360\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER360\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.964 Distributor\_\_37\_GICD\_IROUTER360\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER360\_upper | | | | | reg32 | | 0x6b44 |
| offset | | 27460 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=eb07e26b-d883-4aee-9759-49cbfc8129c4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER360\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.965 Distributor\_\_37\_GICD\_IROUTER361\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER361\_lower | | | | | reg32 | | 0x6b48 |
| offset | | 27464 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cacb5687-bff8-49b0-857c-996a3d6c076a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER361\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER361\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER361\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.966 Distributor\_\_37\_GICD\_IROUTER361\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER361\_upper | | | | | reg32 | | 0x6b4c |
| offset | | 27468 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5531b450-7842-4eca-86e0-6884835e76c3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER361\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.967 Distributor\_\_37\_GICD\_IROUTER362\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER362\_lower | | | | | reg32 | | 0x6b50 |
| offset | | 27472 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8eba0599-3e8f-4eec-be2c-c0c51b3dcd58 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER362\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER362\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER362\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.968 Distributor\_\_37\_GICD\_IROUTER362\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER362\_upper | | | | | reg32 | | 0x6b54 |
| offset | | 27476 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=34cac70a-d169-4fca-8cbe-ada9850808e6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER362\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.969 Distributor\_\_37\_GICD\_IROUTER363\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER363\_lower | | | | | reg32 | | 0x6b58 |
| offset | | 27480 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=aaa1ae42-9316-4192-8574-5312384cda98 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER363\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER363\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER363\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.970 Distributor\_\_37\_GICD\_IROUTER363\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER363\_upper | | | | | reg32 | | 0x6b5c |
| offset | | 27484 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=61f82dc4-a53b-46a1-8329-faa796767fac | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER363\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.971 Distributor\_\_37\_GICD\_IROUTER364\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER364\_lower | | | | | reg32 | | 0x6b60 |
| offset | | 27488 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3079f859-4560-4176-a4aa-c5ec42b5536a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER364\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER364\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER364\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.972 Distributor\_\_37\_GICD\_IROUTER364\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER364\_upper | | | | | reg32 | | 0x6b64 |
| offset | | 27492 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f0c0a863-5248-490a-a32c-19ad79814e89 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER364\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.973 Distributor\_\_37\_GICD\_IROUTER365\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER365\_lower | | | | | reg32 | | 0x6b68 |
| offset | | 27496 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a958f6f1-faf4-4a81-b317-bb6a79125be2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER365\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER365\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER365\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.974 Distributor\_\_37\_GICD\_IROUTER365\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER365\_upper | | | | | reg32 | | 0x6b6c |
| offset | | 27500 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6b7a431a-b6fe-4638-a0f1-a28469f7bad4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER365\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.975 Distributor\_\_37\_GICD\_IROUTER366\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER366\_lower | | | | | reg32 | | 0x6b70 |
| offset | | 27504 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b99d9ae4-a60e-42f5-b677-042bd1610d4e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER366\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER366\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER366\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.976 Distributor\_\_37\_GICD\_IROUTER366\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER366\_upper | | | | | reg32 | | 0x6b74 |
| offset | | 27508 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e511fd7c-49bd-41b1-a503-a5e016bcd703 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER366\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.977 Distributor\_\_37\_GICD\_IROUTER367\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER367\_lower | | | | | reg32 | | 0x6b78 |
| offset | | 27512 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f818e41e-31a5-4e73-94f1-93386fec14da | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER367\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER367\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER367\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.978 Distributor\_\_37\_GICD\_IROUTER367\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER367\_upper | | | | | reg32 | | 0x6b7c |
| offset | | 27516 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2f272e69-9dee-4ec9-9b4a-dc1d570bca8b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER367\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.979 Distributor\_\_37\_GICD\_IROUTER368\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER368\_lower | | | | | reg32 | | 0x6b80 |
| offset | | 27520 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=58a44609-e5bc-488a-96ec-31284885e0f3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER368\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER368\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER368\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.980 Distributor\_\_37\_GICD\_IROUTER368\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER368\_upper | | | | | reg32 | | 0x6b84 |
| offset | | 27524 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c8ed2263-c029-408e-8e86-bfc0df64deb9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER368\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.981 Distributor\_\_37\_GICD\_IROUTER369\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER369\_lower | | | | | reg32 | | 0x6b88 |
| offset | | 27528 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5d2a0f40-f6f2-4d42-85ef-598160d4e829 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER369\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER369\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER369\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.982 Distributor\_\_37\_GICD\_IROUTER369\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER369\_upper | | | | | reg32 | | 0x6b8c |
| offset | | 27532 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2516cc6e-830f-48bb-9868-9015e88c31b2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER369\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.983 Distributor\_\_37\_GICD\_IROUTER370\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER370\_lower | | | | | reg32 | | 0x6b90 |
| offset | | 27536 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f1a27eed-6b15-493b-936b-36b42bef85de | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER370\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER370\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER370\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.984 Distributor\_\_37\_GICD\_IROUTER370\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER370\_upper | | | | | reg32 | | 0x6b94 |
| offset | | 27540 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=40a6322f-fe14-45eb-afeb-fac2e7902eca | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER370\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.985 Distributor\_\_37\_GICD\_IROUTER371\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER371\_lower | | | | | reg32 | | 0x6b98 |
| offset | | 27544 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ae29c150-bf8f-4dbb-bf4a-ba99df1e0c3a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER371\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER371\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER371\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.986 Distributor\_\_37\_GICD\_IROUTER371\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER371\_upper | | | | | reg32 | | 0x6b9c |
| offset | | 27548 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=461407a4-37ad-400b-bbd2-e73ee98c5a76 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER371\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.987 Distributor\_\_37\_GICD\_IROUTER372\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER372\_lower | | | | | reg32 | | 0x6ba0 |
| offset | | 27552 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=57de085f-a820-4115-8f14-942dc5e114b0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER372\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER372\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER372\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.988 Distributor\_\_37\_GICD\_IROUTER372\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER372\_upper | | | | | reg32 | | 0x6ba4 |
| offset | | 27556 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=130fc534-3121-4577-99dd-c6f123f58a67 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER372\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.989 Distributor\_\_37\_GICD\_IROUTER373\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER373\_lower | | | | | reg32 | | 0x6ba8 |
| offset | | 27560 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=96e22a07-63f3-4bc3-bec9-6342b2f0f7d6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER373\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER373\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER373\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.990 Distributor\_\_37\_GICD\_IROUTER373\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER373\_upper | | | | | reg32 | | 0x6bac |
| offset | | 27564 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=db7d44eb-0b3d-47c5-86c5-303fbb75e929 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER373\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.991 Distributor\_\_37\_GICD\_IROUTER374\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER374\_lower | | | | | reg32 | | 0x6bb0 |
| offset | | 27568 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fd286ee3-1651-484a-9feb-d9a4f67fb18b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER374\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER374\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER374\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.992 Distributor\_\_37\_GICD\_IROUTER374\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER374\_upper | | | | | reg32 | | 0x6bb4 |
| offset | | 27572 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e5414018-4eee-4ead-b55b-254317b6e49c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER374\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.993 Distributor\_\_37\_GICD\_IROUTER375\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER375\_lower | | | | | reg32 | | 0x6bb8 |
| offset | | 27576 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ff583226-4e39-4e8b-94f6-cb18a6854457 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER375\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER375\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER375\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.994 Distributor\_\_37\_GICD\_IROUTER375\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER375\_upper | | | | | reg32 | | 0x6bbc |
| offset | | 27580 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5dae1ad6-e2b0-4736-9a44-478313ec556e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER375\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.995 Distributor\_\_37\_GICD\_IROUTER376\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER376\_lower | | | | | reg32 | | 0x6bc0 |
| offset | | 27584 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=550c2e74-75cf-4dc6-952c-3c81ff3087c3 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER376\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER376\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER376\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.996 Distributor\_\_37\_GICD\_IROUTER376\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER376\_upper | | | | | reg32 | | 0x6bc4 |
| offset | | 27588 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b2492c44-cd48-43b9-ab7c-afcd715ba313 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER376\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.997 Distributor\_\_37\_GICD\_IROUTER377\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER377\_lower | | | | | reg32 | | 0x6bc8 |
| offset | | 27592 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4a07e119-6715-4492-bc15-47f742871d30 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER377\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER377\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER377\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.998 Distributor\_\_37\_GICD\_IROUTER377\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER377\_upper | | | | | reg32 | | 0x6bcc |
| offset | | 27596 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=982c2ced-ef6d-4359-b5a7-30f1412f9008 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER377\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.999 Distributor\_\_37\_GICD\_IROUTER378\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER378\_lower | | | | | reg32 | | 0x6bd0 |
| offset | | 27600 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b4326bd6-0c10-41de-8155-20219cbe6023 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER378\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER378\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER378\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1000 Distributor\_\_37\_GICD\_IROUTER378\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER378\_upper | | | | | reg32 | | 0x6bd4 |
| offset | | 27604 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=581c48c4-22c2-4c5b-87d1-1aeea5bef2de | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER378\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1001 Distributor\_\_37\_GICD\_IROUTER379\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER379\_lower | | | | | reg32 | | 0x6bd8 |
| offset | | 27608 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e23cc115-afab-43c0-ae29-1aa28e9b43eb | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER379\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER379\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER379\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1002 Distributor\_\_37\_GICD\_IROUTER379\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER379\_upper | | | | | reg32 | | 0x6bdc |
| offset | | 27612 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8240844a-7f92-4968-894d-1d4dd27e986b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER379\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1003 Distributor\_\_37\_GICD\_IROUTER380\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER380\_lower | | | | | reg32 | | 0x6be0 |
| offset | | 27616 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d1ba8d03-ba33-4f91-b826-c107adecfbf6 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER380\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER380\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER380\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1004 Distributor\_\_37\_GICD\_IROUTER380\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER380\_upper | | | | | reg32 | | 0x6be4 |
| offset | | 27620 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=178154f0-93cf-4055-bc45-77657baa4743 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER380\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1005 Distributor\_\_37\_GICD\_IROUTER381\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER381\_lower | | | | | reg32 | | 0x6be8 |
| offset | | 27624 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5ff7fdf2-acad-433f-8439-62240f002158 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER381\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER381\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER381\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1006 Distributor\_\_37\_GICD\_IROUTER381\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER381\_upper | | | | | reg32 | | 0x6bec |
| offset | | 27628 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f680db7d-0084-4b7e-a502-48ba5a4c9090 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER381\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1007 Distributor\_\_37\_GICD\_IROUTER382\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER382\_lower | | | | | reg32 | | 0x6bf0 |
| offset | | 27632 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=46fb9e73-b439-41ad-86c6-9edb5eec01a4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER382\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER382\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER382\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1008 Distributor\_\_37\_GICD\_IROUTER382\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER382\_upper | | | | | reg32 | | 0x6bf4 |
| offset | | 27636 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=57030632-dd32-49b6-b744-f27611122d57 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER382\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1009 Distributor\_\_37\_GICD\_IROUTER383\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER383\_lower | | | | | reg32 | | 0x6bf8 |
| offset | | 27640 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5dafa813-33c7-4cdd-92b7-46d0af898509 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER383\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER383\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER383\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1010 Distributor\_\_37\_GICD\_IROUTER383\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER383\_upper | | | | | reg32 | | 0x6bfc |
| offset | | 27644 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=01a59892-6bf5-4719-8289-1f5d98bdf381 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER383\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1011 Distributor\_\_37\_GICD\_IROUTER384\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER384\_lower | | | | | reg32 | | 0x6c00 |
| offset | | 27648 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1e95f4ce-7284-40bf-b8ec-da04d8031b0e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER384\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER384\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER384\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1012 Distributor\_\_37\_GICD\_IROUTER384\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER384\_upper | | | | | reg32 | | 0x6c04 |
| offset | | 27652 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cc9faf85-4d78-4f04-8748-ea1899b2eb60 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER384\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1013 Distributor\_\_37\_GICD\_IROUTER385\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER385\_lower | | | | | reg32 | | 0x6c08 |
| offset | | 27656 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3b68e665-4555-400d-85bb-e2837553e7f9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER385\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER385\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER385\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1014 Distributor\_\_37\_GICD\_IROUTER385\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER385\_upper | | | | | reg32 | | 0x6c0c |
| offset | | 27660 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ff88a74f-3133-4b8a-91a1-0427a7b8acd2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER385\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1015 Distributor\_\_37\_GICD\_IROUTER386\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER386\_lower | | | | | reg32 | | 0x6c10 |
| offset | | 27664 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ff36b047-1243-43dd-a499-84d0d8948159 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER386\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER386\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER386\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1016 Distributor\_\_37\_GICD\_IROUTER386\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER386\_upper | | | | | reg32 | | 0x6c14 |
| offset | | 27668 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=92bd222f-c42b-40ca-a6f4-32e6157b3f9e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER386\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1017 Distributor\_\_37\_GICD\_IROUTER387\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER387\_lower | | | | | reg32 | | 0x6c18 |
| offset | | 27672 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b7e8496b-6ddb-46c3-94bf-c849953d1470 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER387\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER387\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER387\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1018 Distributor\_\_37\_GICD\_IROUTER387\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER387\_upper | | | | | reg32 | | 0x6c1c |
| offset | | 27676 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=08bf8159-e455-4238-af42-fa359e6f7eef | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER387\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1019 Distributor\_\_37\_GICD\_IROUTER388\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER388\_lower | | | | | reg32 | | 0x6c20 |
| offset | | 27680 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b1655b85-2d0c-4bca-a736-116c8ff5c46e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER388\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER388\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER388\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1020 Distributor\_\_37\_GICD\_IROUTER388\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER388\_upper | | | | | reg32 | | 0x6c24 |
| offset | | 27684 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=39a65bc5-2eaf-4a11-b24a-17751b9e969a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER388\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1021 Distributor\_\_37\_GICD\_IROUTER389\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER389\_lower | | | | | reg32 | | 0x6c28 |
| offset | | 27688 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c640d734-282a-47cf-831a-17f0c316c196 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER389\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER389\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER389\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1022 Distributor\_\_37\_GICD\_IROUTER389\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER389\_upper | | | | | reg32 | | 0x6c2c |
| offset | | 27692 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b2f75803-a4e2-438d-86a2-204f26bf26e9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER389\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1023 Distributor\_\_37\_GICD\_IROUTER390\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER390\_lower | | | | | reg32 | | 0x6c30 |
| offset | | 27696 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d3453594-e377-435c-ba5e-06d04bdc7af0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER390\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER390\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER390\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1024 Distributor\_\_37\_GICD\_IROUTER390\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER390\_upper | | | | | reg32 | | 0x6c34 |
| offset | | 27700 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7af83d34-72a1-43d2-a0c3-7add62ffb100 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER390\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1025 Distributor\_\_37\_GICD\_IROUTER391\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER391\_lower | | | | | reg32 | | 0x6c38 |
| offset | | 27704 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ff21e252-63bf-4dc6-abfe-83f92b8ce085 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER391\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER391\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER391\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1026 Distributor\_\_37\_GICD\_IROUTER391\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER391\_upper | | | | | reg32 | | 0x6c3c |
| offset | | 27708 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=505843a1-ecc6-4f05-ba0a-6db75be7ef5c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER391\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1027 Distributor\_\_37\_GICD\_IROUTER392\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER392\_lower | | | | | reg32 | | 0x6c40 |
| offset | | 27712 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c75b3814-ac36-4e8f-9840-99ea35704756 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER392\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER392\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER392\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1028 Distributor\_\_37\_GICD\_IROUTER392\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER392\_upper | | | | | reg32 | | 0x6c44 |
| offset | | 27716 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1e9b23d8-f6c4-4f88-bec3-2cf4bee4e7d8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER392\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1029 Distributor\_\_37\_GICD\_IROUTER393\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER393\_lower | | | | | reg32 | | 0x6c48 |
| offset | | 27720 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=aa0d401a-28da-4fc4-85fd-c35291b66d97 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER393\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER393\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER393\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1030 Distributor\_\_37\_GICD\_IROUTER393\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER393\_upper | | | | | reg32 | | 0x6c4c |
| offset | | 27724 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0828c224-1e6a-4e9d-9a1a-1708972134e2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER393\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1031 Distributor\_\_37\_GICD\_IROUTER394\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER394\_lower | | | | | reg32 | | 0x6c50 |
| offset | | 27728 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e3db9b81-0026-4146-ba2e-cb02df8bc925 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER394\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER394\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER394\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1032 Distributor\_\_37\_GICD\_IROUTER394\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER394\_upper | | | | | reg32 | | 0x6c54 |
| offset | | 27732 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=94e995a4-3c36-4e02-8359-6eda69c6ef52 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER394\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1033 Distributor\_\_37\_GICD\_IROUTER395\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER395\_lower | | | | | reg32 | | 0x6c58 |
| offset | | 27736 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ba323751-9438-4c6c-b3c1-0d93add954bf | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER395\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER395\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER395\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1034 Distributor\_\_37\_GICD\_IROUTER395\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER395\_upper | | | | | reg32 | | 0x6c5c |
| offset | | 27740 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1713a595-eb32-42f4-849b-68dd1ff59882 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER395\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1035 Distributor\_\_37\_GICD\_IROUTER396\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER396\_lower | | | | | reg32 | | 0x6c60 |
| offset | | 27744 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fca0d04e-d136-4daf-b4d9-865c8152c2c8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER396\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER396\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER396\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1036 Distributor\_\_37\_GICD\_IROUTER396\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER396\_upper | | | | | reg32 | | 0x6c64 |
| offset | | 27748 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=37812eab-8071-4fdb-830f-b7783482e4a2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER396\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1037 Distributor\_\_37\_GICD\_IROUTER397\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER397\_lower | | | | | reg32 | | 0x6c68 |
| offset | | 27752 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=54223823-4bc7-4e2d-a552-c87b677a34cd | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER397\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER397\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER397\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1038 Distributor\_\_37\_GICD\_IROUTER397\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER397\_upper | | | | | reg32 | | 0x6c6c |
| offset | | 27756 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1b0e9dc2-51d4-4468-9415-047fad489361 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER397\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1039 Distributor\_\_37\_GICD\_IROUTER398\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER398\_lower | | | | | reg32 | | 0x6c70 |
| offset | | 27760 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dc88b457-2055-4a08-8e1f-e50ff22f20f0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER398\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER398\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER398\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1040 Distributor\_\_37\_GICD\_IROUTER398\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER398\_upper | | | | | reg32 | | 0x6c74 |
| offset | | 27764 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=61f18bab-a895-4e89-909a-a8d12446e7d1 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER398\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1041 Distributor\_\_37\_GICD\_IROUTER399\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER399\_lower | | | | | reg32 | | 0x6c78 |
| offset | | 27768 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6c05e26a-2549-4d48-a1ee-a44071dc6377 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER399\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER399\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER399\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1042 Distributor\_\_37\_GICD\_IROUTER399\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER399\_upper | | | | | reg32 | | 0x6c7c |
| offset | | 27772 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=92c980fd-3dea-4d38-ae67-501ee7dc3b71 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER399\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1043 Distributor\_\_37\_GICD\_IROUTER400\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER400\_lower | | | | | reg32 | | 0x6c80 |
| offset | | 27776 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7a44c0f4-9f97-48fa-99c0-7e94624965fb | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER400\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER400\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER400\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1044 Distributor\_\_37\_GICD\_IROUTER400\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER400\_upper | | | | | reg32 | | 0x6c84 |
| offset | | 27780 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9a7fe4b5-8f3f-4262-8c6a-2c5f6dbbebb7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER400\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1045 Distributor\_\_37\_GICD\_IROUTER401\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER401\_lower | | | | | reg32 | | 0x6c88 |
| offset | | 27784 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6278b994-e24a-43d0-a30d-5fc7bbf0f1b5 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER401\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER401\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER401\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1046 Distributor\_\_37\_GICD\_IROUTER401\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER401\_upper | | | | | reg32 | | 0x6c8c |
| offset | | 27788 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=14a754b6-d189-4ba2-b4c4-8322f90d2775 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER401\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1047 Distributor\_\_37\_GICD\_IROUTER402\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER402\_lower | | | | | reg32 | | 0x6c90 |
| offset | | 27792 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=200a8a7c-ef38-4af4-bfb1-d555262a0642 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER402\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER402\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER402\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1048 Distributor\_\_37\_GICD\_IROUTER402\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER402\_upper | | | | | reg32 | | 0x6c94 |
| offset | | 27796 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f9cfefb0-0001-401b-bfcf-c195795a45b4 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER402\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1049 Distributor\_\_37\_GICD\_IROUTER403\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER403\_lower | | | | | reg32 | | 0x6c98 |
| offset | | 27800 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=33571c8c-11eb-4a9a-b738-e2aee336ef0d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER403\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER403\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER403\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1050 Distributor\_\_37\_GICD\_IROUTER403\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER403\_upper | | | | | reg32 | | 0x6c9c |
| offset | | 27804 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=703a3ea3-66df-450c-a950-c3be84e81e66 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER403\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1051 Distributor\_\_37\_GICD\_IROUTER404\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER404\_lower | | | | | reg32 | | 0x6ca0 |
| offset | | 27808 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=dc2609b3-84c9-4fb3-a38c-123b5fdab14e | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER404\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER404\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER404\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1052 Distributor\_\_37\_GICD\_IROUTER404\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER404\_upper | | | | | reg32 | | 0x6ca4 |
| offset | | 27812 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=25090382-5f7f-4e48-9b08-4ca2187af73a | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER404\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1053 Distributor\_\_37\_GICD\_IROUTER405\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER405\_lower | | | | | reg32 | | 0x6ca8 |
| offset | | 27816 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=05baea47-2a76-4000-866e-0286a0d4c37c | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER405\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER405\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER405\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1054 Distributor\_\_37\_GICD\_IROUTER405\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER405\_upper | | | | | reg32 | | 0x6cac |
| offset | | 27820 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cfe94fe2-8d3b-4381-a51f-aaf818cdc853 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER405\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1055 Distributor\_\_37\_GICD\_IROUTER406\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER406\_lower | | | | | reg32 | | 0x6cb0 |
| offset | | 27824 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ea598bc1-5d38-4bde-9b42-7366cd065637 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER406\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER406\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER406\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1056 Distributor\_\_37\_GICD\_IROUTER406\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER406\_upper | | | | | reg32 | | 0x6cb4 |
| offset | | 27828 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a1c21882-07f4-45b7-b145-6d1dfbc85d27 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER406\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1057 Distributor\_\_37\_GICD\_IROUTER407\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER407\_lower | | | | | reg32 | | 0x6cb8 |
| offset | | 27832 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3c00bb36-5583-4941-92df-2a58d8172cd0 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER407\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER407\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER407\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1058 Distributor\_\_37\_GICD\_IROUTER407\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER407\_upper | | | | | reg32 | | 0x6cbc |
| offset | | 27836 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3826704a-7596-4d69-958e-31cd26a9817d | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER407\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1059 Distributor\_\_37\_GICD\_IROUTER408\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER408\_lower | | | | | reg32 | | 0x6cc0 |
| offset | | 27840 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bbb64516-3e4a-4743-844a-1018e57b9357 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER408\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER408\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER408\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1060 Distributor\_\_37\_GICD\_IROUTER408\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER408\_upper | | | | | reg32 | | 0x6cc4 |
| offset | | 27844 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ab8af3e8-4a64-426f-87d5-2f3e5d525fe9 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER408\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1061 Distributor\_\_37\_GICD\_IROUTER409\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER409\_lower | | | | | reg32 | | 0x6cc8 |
| offset | | 27848 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=42b591ca-2a6f-4aa0-b449-1f0dac565ba7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER409\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER409\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER409\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1062 Distributor\_\_37\_GICD\_IROUTER409\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER409\_upper | | | | | reg32 | | 0x6ccc |
| offset | | 27852 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=96d6df4b-89ea-4db4-8412-4fdf5d013ce7 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER409\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1063 Distributor\_\_37\_GICD\_IROUTER410\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER410\_lower | | | | | reg32 | | 0x6cd0 |
| offset | | 27856 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=343d3c94-363d-43b8-8fd1-64539cee97a8 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER410\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER410\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER410\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1064 Distributor\_\_37\_GICD\_IROUTER410\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER410\_upper | | | | | reg32 | | 0x6cd4 |
| offset | | 27860 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=82641735-3ee3-4fac-b262-5b13a0c9c276 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER410\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1065 Distributor\_\_37\_GICD\_IROUTER411\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER411\_lower | | | | | reg32 | | 0x6cd8 |
| offset | | 27864 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=57fc79d5-8919-44f9-a7d7-8c7a432dfcb2 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER411\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER411\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER411\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1066 Distributor\_\_37\_GICD\_IROUTER411\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER411\_upper | | | | | reg32 | | 0x6cdc |
| offset | | 27868 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=34d4ce2a-d477-4256-a73b-5d2cae307082 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER411\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1067 Distributor\_\_37\_GICD\_IROUTER412\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER412\_lower | | | | | reg32 | | 0x6ce0 |
| offset | | 27872 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=687633d8-0b50-4d2f-85c4-8c3e96207741 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER412\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER412\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER412\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1068 Distributor\_\_37\_GICD\_IROUTER412\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER412\_upper | | | | | reg32 | | 0x6ce4 |
| offset | | 27876 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8891aca6-6000-4032-b959-5e28705733ed | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER412\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1069 Distributor\_\_37\_GICD\_IROUTER413\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER413\_lower | | | | | reg32 | | 0x6ce8 |
| offset | | 27880 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f96cbcba-8d90-48af-a781-7b6293756492 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER413\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER413\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER413\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1070 Distributor\_\_37\_GICD\_IROUTER413\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER413\_upper | | | | | reg32 | | 0x6cec |
| offset | | 27884 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1c5fe4c0-51d5-42e5-a84f-436ea1a9505f | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER413\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1071 Distributor\_\_37\_GICD\_IROUTER414\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER414\_lower | | | | | reg32 | | 0x6cf0 |
| offset | | 27888 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b625eaf3-75d9-4da9-b47a-6a38ec9f6100 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER414\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER414\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER414\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1072 Distributor\_\_37\_GICD\_IROUTER414\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER414\_upper | | | | | reg32 | | 0x6cf4 |
| offset | | 27892 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0acc60b0-b056-4b98-ad79-222646011b96 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER414\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1073 Distributor\_\_37\_GICD\_IROUTER415\_lower | | | | | | | | Distributor\_\_37\_GICD\_IROUTER415\_lower | | | | | reg32 | | 0x6cf8 |
| offset | | 27896 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6d421c6e-e93a-49d8-91bf-6b738e390c29 | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_37\_GICD\_IROUTER415\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 15:8 | Distributor\_\_37\_GICD\_IROUTER415\_lower\_\_8\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Distributor\_\_37\_GICD\_IROUTER415\_lower\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1074 Distributor\_\_37\_GICD\_IROUTER415\_upper | | | | | | | | Distributor\_\_37\_GICD\_IROUTER415\_upper | | | | | reg32 | | 0x6cfc |
| offset | | 27900 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=79ed5a30-49db-4772-9c73-afc727e3b43b | | | | | | | | | | | | | | | |
| Interrupt Routing Registers{is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_37\_GICD\_IROUTER415\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1075 Distributor\_\_38\_GICD\_ESTATUSR | | | | | | | | Distributor\_\_38\_GICD\_ESTATUSR | | | | | reg32 | | 0xc000 |
| offset | | 49152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=83029557-6b64-4e27-841b-34c282b2bf35 | | | | | | | | | | | | | | | |
| GIC-500 Extended Status Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31 | Distributor\_\_38\_GICD\_ESTATUSR\_\_31\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1076 Distributor\_\_39\_GICD\_ERRTESTR | | | | | | | | Distributor\_\_39\_GICD\_ERRTESTR | | | | | reg32 | | 0xc004 |
| offset | | 49156 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ef8b257e-e175-4f3a-915b-c3a743bcec00 | | | | | | | | | | | | | | | |
| GIC-500 Error Test Register{access=write-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Distributor\_\_39\_GICD\_ERRTESTR\_\_0\_1 | | | wo | ro | | 0x0 | | |  | | | | | |
| 1 | Distributor\_\_39\_GICD\_ERRTESTR\_\_1\_1 | | | wo | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1077 Distributor\_\_40\_GICD\_SPISR0 | | | | | | | | Distributor\_\_40\_GICD\_SPISR0 | | | | | reg32 | | 0xc084 |
| offset | | 49284 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0b87cf5f-42ca-4547-bc3f-e3122eb20150 | | | | | | | | | | | | | | | |
| GIC-500 Shared Peipheral Interrupt Status Registers{is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_40\_GICD\_SPISR0 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1078 Distributor\_\_40\_GICD\_SPISR1 | | | | | | | | Distributor\_\_40\_GICD\_SPISR1 | | | | | reg32 | | 0xc088 |
| offset | | 49288 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f188080b-8f74-4c52-a07c-0033e4dcd08c | | | | | | | | | | | | | | | |
| GIC-500 Shared Peipheral Interrupt Status Registers{is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_40\_GICD\_SPISR1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1079 Distributor\_\_40\_GICD\_SPISR2 | | | | | | | | Distributor\_\_40\_GICD\_SPISR2 | | | | | reg32 | | 0xc08c |
| offset | | 49292 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=447305e8-2cae-48e2-a05b-07a50362901c | | | | | | | | | | | | | | | |
| GIC-500 Shared Peipheral Interrupt Status Registers{is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_40\_GICD\_SPISR2 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1080 Distributor\_\_40\_GICD\_SPISR3 | | | | | | | | Distributor\_\_40\_GICD\_SPISR3 | | | | | reg32 | | 0xc090 |
| offset | | 49296 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0f4943e8-505a-42ab-896b-cf181311e815 | | | | | | | | | | | | | | | |
| GIC-500 Shared Peipheral Interrupt Status Registers{is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_40\_GICD\_SPISR3 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1081 Distributor\_\_40\_GICD\_SPISR4 | | | | | | | | Distributor\_\_40\_GICD\_SPISR4 | | | | | reg32 | | 0xc094 |
| offset | | 49300 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0f1d121f-9531-4fef-ad0e-0dca28f79aa1 | | | | | | | | | | | | | | | |
| GIC-500 Shared Peipheral Interrupt Status Registers{is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_40\_GICD\_SPISR4 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1082 Distributor\_\_40\_GICD\_SPISR5 | | | | | | | | Distributor\_\_40\_GICD\_SPISR5 | | | | | reg32 | | 0xc098 |
| offset | | 49304 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1a530148-5d1b-40aa-bcd4-9e6b553be9f2 | | | | | | | | | | | | | | | |
| GIC-500 Shared Peipheral Interrupt Status Registers{is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_40\_GICD\_SPISR5 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1083 Distributor\_\_40\_GICD\_SPISR6 | | | | | | | | Distributor\_\_40\_GICD\_SPISR6 | | | | | reg32 | | 0xc09c |
| offset | | 49308 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9409ae74-c628-4c03-948f-d552e1748315 | | | | | | | | | | | | | | | |
| GIC-500 Shared Peipheral Interrupt Status Registers{is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_40\_GICD\_SPISR6 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1084 Distributor\_\_40\_GICD\_SPISR7 | | | | | | | | Distributor\_\_40\_GICD\_SPISR7 | | | | | reg32 | | 0xc0a0 |
| offset | | 49312 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=66a68cf2-e0c5-4c6f-9b96-bfbcbd481e55 | | | | | | | | | | | | | | | |
| GIC-500 Shared Peipheral Interrupt Status Registers{is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_40\_GICD\_SPISR7 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1085 Distributor\_\_40\_GICD\_SPISR8 | | | | | | | | Distributor\_\_40\_GICD\_SPISR8 | | | | | reg32 | | 0xc0a4 |
| offset | | 49316 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=450d7c45-1256-4641-af0d-f3cc2adde6c8 | | | | | | | | | | | | | | | |
| GIC-500 Shared Peipheral Interrupt Status Registers{is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_40\_GICD\_SPISR8 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1086 Distributor\_\_40\_GICD\_SPISR9 | | | | | | | | Distributor\_\_40\_GICD\_SPISR9 | | | | | reg32 | | 0xc0a8 |
| offset | | 49320 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f973dfbe-ee55-47d5-afe5-72f138fb22f8 | | | | | | | | | | | | | | | |
| GIC-500 Shared Peipheral Interrupt Status Registers{is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_40\_GICD\_SPISR9 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1087 Distributor\_\_40\_GICD\_SPISR10 | | | | | | | | Distributor\_\_40\_GICD\_SPISR10 | | | | | reg32 | | 0xc0ac |
| offset | | 49324 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=192c9cdd-eebd-4c01-b227-d64ebb77c225 | | | | | | | | | | | | | | | |
| GIC-500 Shared Peipheral Interrupt Status Registers{is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_40\_GICD\_SPISR10 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1088 Distributor\_\_40\_GICD\_SPISR11 | | | | | | | | Distributor\_\_40\_GICD\_SPISR11 | | | | | reg32 | | 0xc0b0 |
| offset | | 49328 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0bc255ee-9f2e-4e70-9a7a-80edbe532881 | | | | | | | | | | | | | | | |
| GIC-500 Shared Peipheral Interrupt Status Registers{is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Distributor\_\_40\_GICD\_SPISR11 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1089 Distributor\_\_41\_GICD\_PIDR4 | | | | | | | | Distributor\_\_41\_GICD\_PIDR4 | | | | | reg32 | | 0xffd0 |
| offset | | 65488 | external | | |  | | | size | | 32 | default | | 0x00000044 | |
|  | | | | | | | | | | | | | | | |
| oid=8cf7dc95-b400-41b9-abc2-920ebdea64d7 | | | | | | | | | | | | | | | |
| Peripheral ID4 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_41\_GICD\_PIDR4\_1 | | | ro | ro | | 0x44 | | |  | | | | | |

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| 1.1.1.1090 Distributor\_\_42\_GICD\_PIDR5 | | | | | | | | Distributor\_\_42\_GICD\_PIDR5 | | | | | reg32 | | 0xffd4 |
| offset | | 65492 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=87c184ed-f928-4fd1-9676-ed2aed9e0fad | | | | | | | | | | | | | | | |
| Peripheral ID5 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_42\_GICD\_PIDR5\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1091 Distributor\_\_43\_GICD\_PIDR6 | | | | | | | | Distributor\_\_43\_GICD\_PIDR6 | | | | | reg32 | | 0xffd8 |
| offset | | 65496 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9dd29333-9c5c-4fe9-bbab-ad5791488eec | | | | | | | | | | | | | | | |
| Peripheral ID6 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_43\_GICD\_PIDR6\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1092 Distributor\_\_44\_GICD\_PIDR7 | | | | | | | | Distributor\_\_44\_GICD\_PIDR7 | | | | | reg32 | | 0xffdc |
| offset | | 65500 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ff077ead-5cc7-4629-82b6-11e890ba6e2e | | | | | | | | | | | | | | | |
| Peripheral ID7 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_44\_GICD\_PIDR7\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1093 Distributor\_\_45\_GICD\_PIDR0 | | | | | | | | Distributor\_\_45\_GICD\_PIDR0 | | | | | reg32 | | 0xffe0 |
| offset | | 65504 | external | | |  | | | size | | 32 | default | | 0x00000092 | |
|  | | | | | | | | | | | | | | | |
| oid=75389a2c-0f93-47cd-ae53-1115938214aa | | | | | | | | | | | | | | | |
| Peripheral ID0 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_45\_GICD\_PIDR0\_1 | | | ro | ro | | 0x92 | | |  | | | | | |

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| 1.1.1.1094 Distributor\_\_46\_GICD\_PIDR1 | | | | | | | | Distributor\_\_46\_GICD\_PIDR1 | | | | | reg32 | | 0xffe4 |
| offset | | 65508 | external | | |  | | | size | | 32 | default | | 0x000000b4 | |
|  | | | | | | | | | | | | | | | |
| oid=2c853b16-1c9f-4467-8e66-716e887f8126 | | | | | | | | | | | | | | | |
| Peripheral ID1 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_46\_GICD\_PIDR1\_1 | | | ro | ro | | 0xB4 | | |  | | | | | |

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| 1.1.1.1095 Distributor\_\_47\_GICD\_PIDR2 | | | | | | | | Distributor\_\_47\_GICD\_PIDR2 | | | | | reg32 | | 0xffe8 |
| offset | | 65512 | external | | |  | | | size | | 32 | default | | 0x0000003b | |
|  | | | | | | | | | | | | | | | |
| oid=a1194522-1cd8-4123-af4c-425672ac3cf0 | | | | | | | | | | | | | | | |
| Peripheral ID2 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_47\_GICD\_PIDR2\_1 | | | ro | ro | | 0x3B | | |  | | | | | |

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| 1.1.1.1096 Distributor\_\_48\_GICD\_PIDR3 | | | | | | | | Distributor\_\_48\_GICD\_PIDR3 | | | | | reg32 | | 0xffec |
| offset | | 65516 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f1c35fb0-2e0d-4ac6-a949-3a463a7a595b | | | | | | | | | | | | | | | |
| Peripheral ID3 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_48\_GICD\_PIDR3\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.1.1097 Distributor\_\_49\_GICD\_CIDR0 | | | | | | | | Distributor\_\_49\_GICD\_CIDR0 | | | | | reg32 | | 0xfff0 |
| offset | | 65520 | external | | |  | | | size | | 32 | default | | 0x0000000d | |
|  | | | | | | | | | | | | | | | |
| oid=88e74adc-504b-4d5b-a839-93b1d580adfa | | | | | | | | | | | | | | | |
| Component ID0 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_49\_GICD\_CIDR0\_1 | | | ro | ro | | 0xD | | |  | | | | | |

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| 1.1.1.1098 Distributor\_\_50\_GICD\_CIDR1 | | | | | | | | Distributor\_\_50\_GICD\_CIDR1 | | | | | reg32 | | 0xfff4 |
| offset | | 65524 | external | | |  | | | size | | 32 | default | | 0x000000f0 | |
|  | | | | | | | | | | | | | | | |
| oid=86c7f42f-1827-4617-948b-e5277536c859 | | | | | | | | | | | | | | | |
| Component ID1 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_50\_GICD\_CIDR1\_1 | | | ro | ro | | 0xF0 | | |  | | | | | |

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| 1.1.1.1099 Distributor\_\_51\_GICD\_CIDR2 | | | | | | | | Distributor\_\_51\_GICD\_CIDR2 | | | | | reg32 | | 0xfff8 |
| offset | | 65528 | external | | |  | | | size | | 32 | default | | 0x00000005 | |
|  | | | | | | | | | | | | | | | |
| oid=99ab34a0-ad09-4ccf-a010-67a97d096ed8 | | | | | | | | | | | | | | | |
| Component ID2 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_51\_GICD\_CIDR2\_1 | | | ro | ro | | 0x5 | | |  | | | | | |

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| 1.1.1.1100 Distributor\_\_52\_GICD\_CIDR3 | | | | | | | | Distributor\_\_52\_GICD\_CIDR3 | | | | | reg32 | | 0xfffc |
| offset | | 65532 | external | | |  | | | size | | 32 | default | | 0x000000b1 | |
|  | | | | | | | | | | | | | | | |
| oid=cca22a55-928c-4d49-86ae-37e6775e9008 | | | | | | | | | | | | | | | |
| Component ID3 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Distributor\_\_52\_GICD\_CIDR3\_1 | | | ro | ro | | 0xB1 | | |  | | | | | |

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| End RegGroup |

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| 1.1.2 Message\_based\_SPIs | | | | Message\_based\_SPIs | | | section | | 0x10000 | |
| offset | 65536 | external |  | | repeat |  | | size | | 76 |
|  | | | | | | | | | | |
| oid=6c44656a-7f29-4ab4-8dec-8ba0ff5c59bb | | | | | | | | | | |
| {range=65536} | | | | | | | | | | |

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| 1.1.2.1 Message\_based\_SPIs\_\_1\_GICD\_SETSPI\_NSR | | | | | | | | Message\_based\_SPIs\_\_1\_GICD\_SETSPI\_NSR | | | | | reg32 | | 0x10040 |
| offset | | 64 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=105f63dd-f508-43db-8fe3-f8971973f0c0 | | | | | | | | | | | | | | | |
| Non-Secure SPI Set Register{access=write-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 9:0 | Message\_based\_SPIs\_\_1\_GICD\_SETSPI\_NSR\_\_0\_10 | | | wo | ro | | 0x0 | | |  | | | | | |

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| 1.1.2.2 Message\_based\_SPIs\_\_2\_GICD\_CLRSPI\_NSR | | | | | | | | Message\_based\_SPIs\_\_2\_GICD\_CLRSPI\_NSR | | | | | reg32 | | 0x10048 |
| offset | | 72 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a89ab628-c7c9-4f32-a8ef-24f25f4dab48 | | | | | | | | | | | | | | | |
| Non-Secure SPI Clear Register{access=write-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 9:0 | Message\_based\_SPIs\_\_2\_GICD\_CLRSPI\_NSR\_\_0\_10 | | | wo | ro | | 0x0 | | |  | | | | | |

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| End RegGroup |

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| 1.1.3 ITS | | | | ITS | | | section | | 0x20000 | |
| offset | 131072 | external |  | | repeat |  | | size | | 65536 |
|  | | | | | | | | | | |
| oid=00d18963-7fdb-4b60-9102-16c084551f59 | | | | | | | | | | |
| {range=65536} | | | | | | | | | | |

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| 1.1.3.1 ITS\_\_1\_GITS\_CTLR | | | | | | | | ITS\_\_1\_GITS\_CTLR | | | | | reg32 | | 0x20000 |
| offset | | 0 | external | | |  | | | size | | 32 | default | | 0x80000000 | |
|  | | | | | | | | | | | | | | | |
| oid=76547c9c-a0d0-4f94-946d-5b5cdc5f1093 | | | | | | | | | | | | | | | |
| ITS Control Register{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | ITS\_\_1\_GITS\_CTLR\_\_0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | ITS\_\_1\_GITS\_CTLR\_\_31\_1 | | | rw | ro | | 0x1 | | |  | | | | | |

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| 1.1.3.2 ITS\_\_2\_GITS\_IIDR | | | | | | | | ITS\_\_2\_GITS\_IIDR | | | | | reg32 | | 0x20004 |
| offset | | 4 | external | | |  | | | size | | 32 | default | | 0x0001143b | |
|  | | | | | | | | | | | | | | | |
| oid=5c7b894f-9bb6-47c4-9da1-06b05f5da470 | | | | | | | | | | | | | | | |
| ITS Implementer Identification Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 11:0 | ITS\_\_2\_GITS\_IIDR\_\_0\_12 | | | ro | ro | | 0x43B | | |  | | | | | |
| 15:12 | ITS\_\_2\_GITS\_IIDR\_\_12\_4 | | | ro | ro | | 0x1 | | |  | | | | | |
| 19:16 | ITS\_\_2\_GITS\_IIDR\_\_16\_4 | | | ro | ro | | 0x1 | | |  | | | | | |
| 31:24 | ITS\_\_2\_GITS\_IIDR\_\_24\_8 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.3 ITS\_\_3\_GITS\_TYPER\_lower | | | | | | | | ITS\_\_3\_GITS\_TYPER\_lower | | | | | reg32 | | 0x20008 |
| offset | | 8 | external | | |  | | | size | | 32 | default | | 0x84021041 | |
|  | | | | | | | | | | | | | | | |
| oid=474a1220-f04a-4bd5-9823-2a8473b0747d | | | | | | | | | | | | | | | |
| ITS Type Register (lower){access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | ITS\_\_3\_GITS\_TYPER\_lower\_\_0\_1 | | | ro | ro | | 0x1 | | |  | | | | | |
| 1 | ITS\_\_3\_GITS\_TYPER\_lower\_\_1\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 3 | ITS\_\_3\_GITS\_TYPER\_lower\_\_3\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 7:4 | ITS\_\_3\_GITS\_TYPER\_lower\_\_4\_4 | | | ro | ro | | 0x4 | | |  | | | | | |
| 12:8 | ITS\_\_3\_GITS\_TYPER\_lower\_\_8\_5 | | | ro | ro | | 0x10 | | |  | | | | | |
| 17:13 | ITS\_\_3\_GITS\_TYPER\_lower\_\_13\_5 | | | ro | ro | | 0x10 | | |  | | | | | |
| 19 | ITS\_\_3\_GITS\_TYPER\_lower\_\_19\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 31:24 | ITS\_\_3\_GITS\_TYPER\_lower\_\_24\_8 | | | ro | ro | | 0x84 | | |  | | | | | |

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| 1.1.3.4 ITS\_\_4\_GITS\_TYPER\_upper | | | | | | | | ITS\_\_4\_GITS\_TYPER\_upper | | | | | reg32 | | 0x2000C |
| offset | | 12 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e86e4b22-4291-44c3-ac1f-2ae558d49a76 | | | | | | | | | | | | | | | |
| ITS Type Register (upper){is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | ITS\_\_4\_GITS\_TYPER\_upper | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.5 ITS\_\_5\_GITS\_CBASER\_lower | | | | | | | | ITS\_\_5\_GITS\_CBASER\_lower | | | | | reg32 | | 0x20080 |
| offset | | 128 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=12708d5a-8e1e-4f8d-a9a5-1b63f9685639 | | | | | | | | | | | | | | | |
| Command Queue Control Register (lower){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_5\_GITS\_CBASER\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31:12 | ITS\_\_5\_GITS\_CBASER\_lower\_\_12\_20 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.6 ITS\_\_6\_GITS\_CBASER\_upper | | | | | | | | ITS\_\_6\_GITS\_CBASER\_upper | | | | | reg32 | | 0x20084 |
| offset | | 132 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=552656a5-0f76-44cc-9fc3-ccf2ebeab8ee | | | | | | | | | | | | | | | |
| Command Queue Control Register (upper){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | ITS\_\_6\_GITS\_CBASER\_upper\_\_0\_16 | | | rw | ro | | 0x0 | | |  | | | | | |
| 29:27 | ITS\_\_6\_GITS\_CBASER\_upper\_\_27\_3 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | ITS\_\_6\_GITS\_CBASER\_upper\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.7 ITS\_\_7\_GITS\_CWRITER\_lower | | | | | | | | ITS\_\_7\_GITS\_CWRITER\_lower | | | | | reg32 | | 0x20088 |
| offset | | 136 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=17a7ff10-5268-4b73-a755-9cbde631905a | | | | | | | | | | | | | | | |
| Command Queue Write Pointer (lower){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 19:5 | ITS\_\_7\_GITS\_CWRITER\_lower\_\_5\_15 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.8 ITS\_\_8\_GITS\_CWRITER\_upper | | | | | | | | ITS\_\_8\_GITS\_CWRITER\_upper | | | | | reg32 | | 0x2008C |
| offset | | 140 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=16b25111-7e61-40e4-b631-5f511dd1447d | | | | | | | | | | | | | | | |
| Command Queue Write Pointer (upper){is\_rsv=true;access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | ITS\_\_8\_GITS\_CWRITER\_upper | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.9 ITS\_\_9\_GITS\_CREADR\_lower | | | | | | | | ITS\_\_9\_GITS\_CREADR\_lower | | | | | reg32 | | 0x20090 |
| offset | | 144 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8c27ceaa-fa2b-4ab7-9f94-3cc41366f57d | | | | | | | | | | | | | | | |
| Command Queue Read Pointer (lower){access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 19:5 | ITS\_\_9\_GITS\_CREADR\_lower\_\_5\_15 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.10 ITS\_\_10\_GITS\_CREADR\_upper | | | | | | | | ITS\_\_10\_GITS\_CREADR\_upper | | | | | reg32 | | 0x20094 |
| offset | | 148 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=375ecfbb-942a-4cff-bf13-6badf72a1aae | | | | | | | | | | | | | | | |
| Command Queue Read Pointer (upper){is\_rsv=true;access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | ITS\_\_10\_GITS\_CREADR\_upper | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.11 ITS\_\_11\_GITS\_BASER0\_lower | | | | | | | | ITS\_\_11\_GITS\_BASER0\_lower | | | | | reg32 | | 0x20100 |
| offset | | 256 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=817050be-3d34-46af-9dfa-b4447e2f3276 | | | | | | | | | | | | | | | |
| ITS Table Control Register (lower){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_11\_GITS\_BASER0\_lower\_\_0\_8 | | | rw | ro | | 0x0 | | |  | | | | | |
| 9:8 | ITS\_\_11\_GITS\_BASER0\_lower\_\_8\_2 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31:12 | ITS\_\_11\_GITS\_BASER0\_lower\_\_12\_20 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.12 ITS\_\_12\_GITS\_BASER0\_upper | | | | | | | | ITS\_\_12\_GITS\_BASER0\_upper | | | | | reg32 | | 0x20104 |
| offset | | 260 | external | | |  | | | size | | 32 | default | | 0x01070000 | |
|  | | | | | | | | | | | | | | | |
| oid=5986b874-8581-4848-9968-e9686ee4e37c | | | | | | | | | | | | | | | |
| ITS Table Control Register (upper){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | ITS\_\_12\_GITS\_BASER0\_upper\_\_0\_16 | | | rw | ro | | 0x0 | | |  | | | | | |
| 23:16 | ITS\_\_12\_GITS\_BASER0\_upper\_\_16\_8 | | | rw | ro | | 0x7 | | |  | | | | | |
| 26:24 | ITS\_\_12\_GITS\_BASER0\_upper\_\_24\_3 | | | rw | ro | | 0x1 | | |  | | | | | |

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| 1.1.3.13 ITS\_\_13\_GITS\_TRKCTLR | | | | | | | | ITS\_\_13\_GITS\_TRKCTLR | | | | | reg32 | | 0x2C000 |
| offset | | 49152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=16a9258f-9e40-4eb9-80a1-8f7edaaaf288 | | | | | | | | | | | | | | | |
| GIC-500 ITS Tracking Control Register{access=write-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | ITS\_\_13\_GITS\_TRKCTLR\_\_0\_1 | | | wo | ro | | 0x0 | | |  | | | | | |
| 1 | ITS\_\_13\_GITS\_TRKCTLR\_\_1\_1 | | | wo | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.14 ITS\_\_14\_GITS\_TRKR | | | | | | | | ITS\_\_14\_GITS\_TRKR | | | | | reg32 | | 0x2C004 |
| offset | | 49156 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c06d576b-29db-4090-83ef-92cd81dcb795 | | | | | | | | | | | | | | | |
| GIC-500 ITS Tracking Status Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | ITS\_\_14\_GITS\_TRKR\_\_0\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 1 | ITS\_\_14\_GITS\_TRKR\_\_1\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 2 | ITS\_\_14\_GITS\_TRKR\_\_2\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 3 | ITS\_\_14\_GITS\_TRKR\_\_3\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 4 | ITS\_\_14\_GITS\_TRKR\_\_4\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 5 | ITS\_\_14\_GITS\_TRKR\_\_5\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.15 ITS\_\_15\_GITS\_TRKDIDR | | | | | | | | ITS\_\_15\_GITS\_TRKDIDR | | | | | reg32 | | 0x2C008 |
| offset | | 49160 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=04680786-63f7-4183-bc04-b71d610124e6 | | | | | | | | | | | | | | | |
| GIC-500 ITS Tracked DID Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 19:0 | ITS\_\_15\_GITS\_TRKDIDR\_\_0\_20 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.16 ITS\_\_16\_GITS\_TRKPIDR | | | | | | | | ITS\_\_16\_GITS\_TRKPIDR | | | | | reg32 | | 0x2C00C |
| offset | | 49164 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2eb91f59-5070-42cf-95b8-dbdf116abb64 | | | | | | | | | | | | | | | |
| GIC-500 ITS Tracked PID Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | ITS\_\_16\_GITS\_TRKPIDR\_\_0\_16 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.17 ITS\_\_17\_GITS\_TRKVIDR | | | | | | | | ITS\_\_17\_GITS\_TRKVIDR | | | | | reg32 | | 0x2C010 |
| offset | | 49168 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f604c3a3-a9dd-44ce-b6d2-68a385639fbe | | | | | | | | | | | | | | | |
| GIC-500 ITS Tracked ID Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | ITS\_\_17\_GITS\_TRKVIDR\_\_0\_16 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.18 ITS\_\_18\_GITS\_TRKTGTR | | | | | | | | ITS\_\_18\_GITS\_TRKTGTR | | | | | reg32 | | 0x2C014 |
| offset | | 49172 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=af6a1ad2-266b-4ee3-89a2-7d5560f9d063 | | | | | | | | | | | | | | | |
| GIC-500 ITS Tracked TGT Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 6:0 | ITS\_\_18\_GITS\_TRKTGTR\_\_0\_7 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.19 ITS\_\_19\_GITS\_TRKICR | | | | | | | | ITS\_\_19\_GITS\_TRKICR | | | | | reg32 | | 0x2C018 |
| offset | | 49176 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d7e94e9f-7520-4683-ba87-d2dc384fdb25 | | | | | | | | | | | | | | | |
| GIC-500 ITE Cache Statistics Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | ITS\_\_19\_GITS\_TRKICR\_\_0\_16 | | | ro | ro | | 0x0 | | |  | | | | | |
| 31:16 | ITS\_\_19\_GITS\_TRKICR\_\_16\_16 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.20 ITS\_\_20\_GITS\_TRKLCR | | | | | | | | ITS\_\_20\_GITS\_TRKLCR | | | | | reg32 | | 0x2C01C |
| offset | | 49180 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fdbc2990-3e22-4b8d-84da-f635946ccace | | | | | | | | | | | | | | | |
| GIC-500 LPI Cache Statistics Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | ITS\_\_20\_GITS\_TRKLCR\_\_0\_16 | | | ro | ro | | 0x0 | | |  | | | | | |
| 31:16 | ITS\_\_20\_GITS\_TRKLCR\_\_16\_16 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.21 ITS\_\_21\_GITS\_PIDR4 | | | | | | | | ITS\_\_21\_GITS\_PIDR4 | | | | | reg32 | | 0x2FFD0 |
| offset | | 65488 | external | | |  | | | size | | 32 | default | | 0x00000044 | |
|  | | | | | | | | | | | | | | | |
| oid=54bf8d95-977b-469e-82e9-3422c1694d3f | | | | | | | | | | | | | | | |
| Peripheral ID4 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_21\_GITS\_PIDR4\_1 | | | ro | ro | | 0x44 | | |  | | | | | |

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| 1.1.3.22 ITS\_\_22\_GITS\_PIDR5 | | | | | | | | ITS\_\_22\_GITS\_PIDR5 | | | | | reg32 | | 0x2FFD4 |
| offset | | 65492 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=baa00843-2f52-4559-b0e4-bdbbf1422b7d | | | | | | | | | | | | | | | |
| Peripheral ID5 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_22\_GITS\_PIDR5\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.23 ITS\_\_23\_GITS\_PIDR6 | | | | | | | | ITS\_\_23\_GITS\_PIDR6 | | | | | reg32 | | 0x2FFD8 |
| offset | | 65496 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a865aba0-0c6c-4df1-a973-9e4bae28dbe4 | | | | | | | | | | | | | | | |
| Peripheral ID6 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_23\_GITS\_PIDR6\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.24 ITS\_\_24\_GITS\_PIDR7 | | | | | | | | ITS\_\_24\_GITS\_PIDR7 | | | | | reg32 | | 0x2FFDC |
| offset | | 65500 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=702b89a9-a12a-4c6b-a515-d31709115701 | | | | | | | | | | | | | | | |
| Peripheral ID7 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_24\_GITS\_PIDR7\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.25 ITS\_\_25\_GITS\_PIDR0 | | | | | | | | ITS\_\_25\_GITS\_PIDR0 | | | | | reg32 | | 0x2FFE0 |
| offset | | 65504 | external | | |  | | | size | | 32 | default | | 0x00000094 | |
|  | | | | | | | | | | | | | | | |
| oid=d47c0055-b004-4e62-814e-e2efb4d43b81 | | | | | | | | | | | | | | | |
| Peripheral ID0 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_25\_GITS\_PIDR0\_1 | | | ro | ro | | 0x94 | | |  | | | | | |

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| 1.1.3.26 ITS\_\_26\_GITS\_PIDR1 | | | | | | | | ITS\_\_26\_GITS\_PIDR1 | | | | | reg32 | | 0x2FFE4 |
| offset | | 65508 | external | | |  | | | size | | 32 | default | | 0x000000b4 | |
|  | | | | | | | | | | | | | | | |
| oid=551e64fd-5a0d-4379-b736-dbc99838535b | | | | | | | | | | | | | | | |
| Peripheral ID1 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_26\_GITS\_PIDR1\_1 | | | ro | ro | | 0xB4 | | |  | | | | | |

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| 1.1.3.27 ITS\_\_27\_GITS\_PIDR2 | | | | | | | | ITS\_\_27\_GITS\_PIDR2 | | | | | reg32 | | 0x2FFE8 |
| offset | | 65512 | external | | |  | | | size | | 32 | default | | 0x0000003b | |
|  | | | | | | | | | | | | | | | |
| oid=dbc18dfb-a5fa-46d3-bf87-769f98388178 | | | | | | | | | | | | | | | |
| Peripheral ID2 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_27\_GITS\_PIDR2\_1 | | | ro | ro | | 0x3B | | |  | | | | | |

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| 1.1.3.28 ITS\_\_28\_GITS\_PIDR3 | | | | | | | | ITS\_\_28\_GITS\_PIDR3 | | | | | reg32 | | 0x2FFEC |
| offset | | 65516 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=59950624-ed57-48c3-98c7-f9fdb28f7837 | | | | | | | | | | | | | | | |
| Peripheral ID3 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_28\_GITS\_PIDR3\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.3.29 ITS\_\_29\_GITS\_CIDR0 | | | | | | | | ITS\_\_29\_GITS\_CIDR0 | | | | | reg32 | | 0x2FFF0 |
| offset | | 65520 | external | | |  | | | size | | 32 | default | | 0x0000000d | |
|  | | | | | | | | | | | | | | | |
| oid=d8bc1973-dfad-43f3-94b1-81863a594786 | | | | | | | | | | | | | | | |
| Component ID0 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_29\_GITS\_CIDR0\_1 | | | ro | ro | | 0xD | | |  | | | | | |

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| 1.1.3.30 ITS\_\_30\_GITS\_CIDR1 | | | | | | | | ITS\_\_30\_GITS\_CIDR1 | | | | | reg32 | | 0x2FFF4 |
| offset | | 65524 | external | | |  | | | size | | 32 | default | | 0x000000f0 | |
|  | | | | | | | | | | | | | | | |
| oid=fd2253de-4ad0-43da-b5ec-44a02c50a972 | | | | | | | | | | | | | | | |
| Component ID1 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_30\_GITS\_CIDR1\_1 | | | ro | ro | | 0xF0 | | |  | | | | | |

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| 1.1.3.31 ITS\_\_31\_GITS\_CIDR2 | | | | | | | | ITS\_\_31\_GITS\_CIDR2 | | | | | reg32 | | 0x2FFF8 |
| offset | | 65528 | external | | |  | | | size | | 32 | default | | 0x00000005 | |
|  | | | | | | | | | | | | | | | |
| oid=068deab2-ef60-43fb-9304-c7adcf1830a3 | | | | | | | | | | | | | | | |
| Component ID2 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_31\_GITS\_CIDR2\_1 | | | ro | ro | | 0x5 | | |  | | | | | |

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| 1.1.3.32 ITS\_\_32\_GITS\_CIDR3 | | | | | | | | ITS\_\_32\_GITS\_CIDR3 | | | | | reg32 | | 0x2FFFC |
| offset | | 65532 | external | | |  | | | size | | 32 | default | | 0x000000b1 | |
|  | | | | | | | | | | | | | | | |
| oid=2c7f054e-2579-4090-9578-93008626eafd | | | | | | | | | | | | | | | |
| Component ID3 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | ITS\_\_32\_GITS\_CIDR3\_1 | | | ro | ro | | 0xB1 | | |  | | | | | |

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| End RegGroup |

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| 1.1.4 TRANSLATER | | | | TRANSLATER | | | section | | 0x30000 | |
| offset | 196608 | external |  | | repeat |  | | size | | 68 |
|  | | | | | | | | | | |
| oid=ab32cb6c-bb7a-43e6-8a3a-719bf8e41e6f | | | | | | | | | | |
| {range=65536} | | | | | | | | | | |

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| 1.1.4.1 TRANSLATER\_\_1\_GITS\_TRANSLATER | | | | | | | | TRANSLATER\_\_1\_GITS\_TRANSLATER | | | | | reg32 | | 0x30040 |
| offset | | 64 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=173e24a8-569b-4b25-803e-2245b029aaf2 | | | | | | | | | | | | | | | |
| ITS Translation Register{access=write-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | TRANSLATER\_\_1\_GITS\_TRANSLATER\_\_0\_32 | | | wo | ro | | 0x0 | | |  | | | | | |

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| End RegGroup |

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| 1.1.5 Redistributor\_control\_LPI\_0 | | | | Redistributor\_control\_LPI\_0 | | | section | | 0x80000 | |
| offset | 524288 | external |  | | repeat |  | | size | | 65536 |
|  | | | | | | | | | | |
| oid=ef6d8218-0e12-4ec6-8739-0ea925aa131c | | | | | | | | | | |
| {range=65536} | | | | | | | | | | |

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| 1.1.5.1 Redistributor\_control\_LPI\_0\_\_2\_GICR\_CTLR | | | | | | | | Redistributor\_control\_LPI\_0\_\_2\_GICR\_CTLR | | | | | reg32 | | 0x80000 |
| offset | | 0 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7d0b2426-44e8-4975-9e18-3c57a95af2ae | | | | | | | | | | | | | | | |
| Re-Distributor Control Register{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_control\_LPI\_0\_\_2\_GICR\_CTLR\_\_0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 3 | Redistributor\_control\_LPI\_0\_\_2\_GICR\_CTLR\_\_3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Redistributor\_control\_LPI\_0\_\_2\_GICR\_CTLR\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.2 Redistributor\_control\_LPI\_0\_\_3\_GICR\_IIDR | | | | | | | | Redistributor\_control\_LPI\_0\_\_3\_GICR\_IIDR | | | | | reg32 | | 0x80004 |
| offset | | 4 | external | | |  | | | size | | 32 | default | | 0x0001143b | |
|  | | | | | | | | | | | | | | | |
| oid=bf2d8631-1631-4f0f-92e8-1c68bb109927 | | | | | | | | | | | | | | | |
| Re-Distributor Implementer Identification Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 11:0 | Redistributor\_control\_LPI\_0\_\_3\_GICR\_IIDR\_\_0\_12 | | | ro | ro | | 0x43B | | |  | | | | | |
| 15:12 | Redistributor\_control\_LPI\_0\_\_3\_GICR\_IIDR\_\_12\_4 | | | ro | ro | | 0x1 | | |  | | | | | |
| 19:16 | Redistributor\_control\_LPI\_0\_\_3\_GICR\_IIDR\_\_16\_4 | | | ro | ro | | 0x1 | | |  | | | | | |
| 31:24 | Redistributor\_control\_LPI\_0\_\_3\_GICR\_IIDR\_\_24\_8 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.3 Redistributor\_control\_LPI\_0\_\_4\_GICR\_TYPER\_lower | | | | | | | | Redistributor\_control\_LPI\_0\_\_4\_GICR\_TYPER\_lower | | | | | reg32 | | 0x80008 |
| offset | | 8 | external | | |  | | | size | | 32 | default | | 0x00000001 | |
|  | | | | | | | | | | | | | | | |
| oid=d0481a8a-55b0-4236-9004-b3c021d0b1e4 | | | | | | | | | | | | | | | |
| Re-Distributor Type Register (lower){access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_control\_LPI\_0\_\_4\_GICR\_TYPER\_lower\_\_0\_1 | | | ro | ro | | 0x1 | | |  | | | | | |
| 1 | Redistributor\_control\_LPI\_0\_\_4\_GICR\_TYPER\_lower\_\_1\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 3 | Redistributor\_control\_LPI\_0\_\_4\_GICR\_TYPER\_lower\_\_3\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 4 | Redistributor\_control\_LPI\_0\_\_4\_GICR\_TYPER\_lower\_\_4\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 23:8 | Redistributor\_control\_LPI\_0\_\_4\_GICR\_TYPER\_lower\_\_8\_16 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.4 Redistributor\_control\_LPI\_0\_\_5\_GICR\_TYPER\_upper | | | | | | | | Redistributor\_control\_LPI\_0\_\_5\_GICR\_TYPER\_upper | | | | | reg32 | | 0x8000C |
| offset | | 12 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c2c6ec2f-d440-45f6-8a77-f201633a0471 | | | | | | | | | | | | | | | |
| Re-Distributor Type Register (upper){access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_5\_GICR\_TYPER\_upper\_\_0\_8 | | | ro | ro | | 0x0 | | |  | | | | | |
| 15:8 | Redistributor\_control\_LPI\_0\_\_5\_GICR\_TYPER\_upper\_\_8\_8 | | | ro | ro | | 0x0 | | |  | | | | | |
| 23:16 | Redistributor\_control\_LPI\_0\_\_5\_GICR\_TYPER\_upper\_\_16\_8 | | | ro | ro | | 0x0 | | |  | | | | | |
| 31:24 | Redistributor\_control\_LPI\_0\_\_5\_GICR\_TYPER\_upper\_\_24\_8 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.5 Redistributor\_control\_LPI\_0\_\_6\_GICR\_WAKER | | | | | | | | Redistributor\_control\_LPI\_0\_\_6\_GICR\_WAKER | | | | | reg32 | | 0x80014 |
| offset | | 20 | external | | |  | | | size | | 32 | default | | 0x00000006 | |
|  | | | | | | | | | | | | | | | |
| oid=184c8612-6185-43e0-8c6a-7f6b66183146 | | | | | | | | | | | | | | | |
| Power Management Control Register{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_control\_LPI\_0\_\_6\_GICR\_WAKER\_\_0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 1 | Redistributor\_control\_LPI\_0\_\_6\_GICR\_WAKER\_\_1\_1 | | | rw | ro | | 0x1 | | |  | | | | | |
| 2 | Redistributor\_control\_LPI\_0\_\_6\_GICR\_WAKER\_\_2\_1 | | | rw | ro | | 0x1 | | |  | | | | | |
| 31 | Redistributor\_control\_LPI\_0\_\_6\_GICR\_WAKER\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.6 Redistributor\_control\_LPI\_0\_\_7\_GICR\_PROPBASER\_lower | | | | | | | | Redistributor\_control\_LPI\_0\_\_7\_GICR\_PROPBASER\_lower | | | | | reg32 | | 0x80070 |
| offset | | 112 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=be4bc144-e3b0-468f-843c-02e88f9b5ad6 | | | | | | | | | | | | | | | |
| LPI Configuration Table Control Register (lower){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 4:0 | Redistributor\_control\_LPI\_0\_\_7\_GICR\_PROPBASER\_lower\_\_0\_5 | | | rw | ro | | 0x0 | | |  | | | | | |
| 9:7 | Redistributor\_control\_LPI\_0\_\_7\_GICR\_PROPBASER\_lower\_\_7\_3 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31:12 | Redistributor\_control\_LPI\_0\_\_7\_GICR\_PROPBASER\_lower\_\_12\_20 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.7 Redistributor\_control\_LPI\_0\_\_8\_GICR\_PROPBASER\_upper | | | | | | | | Redistributor\_control\_LPI\_0\_\_8\_GICR\_PROPBASER\_upper | | | | | reg32 | | 0x80074 |
| offset | | 116 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9bc7a693-b72b-4325-94a2-2b19c6f5ec68 | | | | | | | | | | | | | | | |
| LPI Configuration Table Control Register (upper){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | Redistributor\_control\_LPI\_0\_\_8\_GICR\_PROPBASER\_upper\_\_0\_16 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.8 Redistributor\_control\_LPI\_0\_\_9\_GICR\_PENDBASER\_lower | | | | | | | | Redistributor\_control\_LPI\_0\_\_9\_GICR\_PENDBASER\_lower | | | | | reg32 | | 0x80078 |
| offset | | 120 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=40020b57-706b-4cc6-9ade-94f3abb4257f | | | | | | | | | | | | | | | |
| LPI Pending Table Control Register (lower){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 9:7 | Redistributor\_control\_LPI\_0\_\_9\_GICR\_PENDBASER\_lower\_\_7\_3 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31:16 | Redistributor\_control\_LPI\_0\_\_9\_GICR\_PENDBASER\_lower\_\_16\_16 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.9 Redistributor\_control\_LPI\_0\_\_10\_GICR\_PENDBASER\_upper | | | | | | | | Redistributor\_control\_LPI\_0\_\_10\_GICR\_PENDBASER\_upper | | | | | reg32 | | 0x8007C |
| offset | | 124 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ad0a2c95-1eba-4142-986c-de1a36cd9e50 | | | | | | | | | | | | | | | |
| LPI Pending Table Control Register (upper){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | Redistributor\_control\_LPI\_0\_\_10\_GICR\_PENDBASER\_upper\_\_0\_16 | | | rw | ro | | 0x0 | | |  | | | | | |
| 30 | Redistributor\_control\_LPI\_0\_\_10\_GICR\_PENDBASER\_upper\_\_30\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.10 Redistributor\_control\_LPI\_0\_\_11\_GICR\_PIDR4 | | | | | | | | Redistributor\_control\_LPI\_0\_\_11\_GICR\_PIDR4 | | | | | reg32 | | 0x8FFD0 |
| offset | | 65488 | external | | |  | | | size | | 32 | default | | 0x00000044 | |
|  | | | | | | | | | | | | | | | |
| oid=a6284853-2eaa-451f-81af-ae86ad1a6c73 | | | | | | | | | | | | | | | |
| Peripheral ID4 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_11\_GICR\_PIDR4\_1 | | | ro | ro | | 0x44 | | |  | | | | | |

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| 1.1.5.11 Redistributor\_control\_LPI\_0\_\_12\_GICR\_PIDR5 | | | | | | | | Redistributor\_control\_LPI\_0\_\_12\_GICR\_PIDR5 | | | | | reg32 | | 0x8FFD4 |
| offset | | 65492 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cfd3b070-a080-49b3-ac61-99a8d09c5712 | | | | | | | | | | | | | | | |
| Peripheral ID5 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_12\_GICR\_PIDR5\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.12 Redistributor\_control\_LPI\_0\_\_13\_GICR\_PIDR6 | | | | | | | | Redistributor\_control\_LPI\_0\_\_13\_GICR\_PIDR6 | | | | | reg32 | | 0x8FFD8 |
| offset | | 65496 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=06bfea0b-c2c6-4caf-8d28-0048ea43a0d2 | | | | | | | | | | | | | | | |
| Peripheral ID6 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_13\_GICR\_PIDR6\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.13 Redistributor\_control\_LPI\_0\_\_14\_GICR\_PIDR7 | | | | | | | | Redistributor\_control\_LPI\_0\_\_14\_GICR\_PIDR7 | | | | | reg32 | | 0x8FFDC |
| offset | | 65500 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=59e98347-4fdd-4c11-a7b9-ea14dbbc8ace | | | | | | | | | | | | | | | |
| Peripheral ID7 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_14\_GICR\_PIDR7\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.14 Redistributor\_control\_LPI\_0\_\_15\_GICR\_PIDR0 | | | | | | | | Redistributor\_control\_LPI\_0\_\_15\_GICR\_PIDR0 | | | | | reg32 | | 0x8FFE0 |
| offset | | 65504 | external | | |  | | | size | | 32 | default | | 0x00000093 | |
|  | | | | | | | | | | | | | | | |
| oid=6ed5ebac-5de1-4623-be9a-e3ec7680605b | | | | | | | | | | | | | | | |
| Peripheral ID0 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_15\_GICR\_PIDR0\_1 | | | ro | ro | | 0x93 | | |  | | | | | |

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| 1.1.5.15 Redistributor\_control\_LPI\_0\_\_16\_GICR\_PIDR1 | | | | | | | | Redistributor\_control\_LPI\_0\_\_16\_GICR\_PIDR1 | | | | | reg32 | | 0x8FFE4 |
| offset | | 65508 | external | | |  | | | size | | 32 | default | | 0x000000b4 | |
|  | | | | | | | | | | | | | | | |
| oid=f87667f6-7bbd-4692-8c9b-8a2942eb574f | | | | | | | | | | | | | | | |
| Peripheral ID1 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_16\_GICR\_PIDR1\_1 | | | ro | ro | | 0xB4 | | |  | | | | | |

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| 1.1.5.16 Redistributor\_control\_LPI\_0\_\_17\_GICR\_PIDR2 | | | | | | | | Redistributor\_control\_LPI\_0\_\_17\_GICR\_PIDR2 | | | | | reg32 | | 0x8FFE8 |
| offset | | 65512 | external | | |  | | | size | | 32 | default | | 0x0000003b | |
|  | | | | | | | | | | | | | | | |
| oid=f1a71151-6b2e-468f-a268-91b2ad2258c7 | | | | | | | | | | | | | | | |
| Peripheral ID2 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_17\_GICR\_PIDR2\_1 | | | ro | ro | | 0x3B | | |  | | | | | |

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| 1.1.5.17 Redistributor\_control\_LPI\_0\_\_18\_GICR\_PIDR3 | | | | | | | | Redistributor\_control\_LPI\_0\_\_18\_GICR\_PIDR3 | | | | | reg32 | | 0x8FFEC |
| offset | | 65516 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9ad53184-11f6-4fb6-8d60-631b01be8323 | | | | | | | | | | | | | | | |
| Peripheral ID3 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_18\_GICR\_PIDR3\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.5.18 Redistributor\_control\_LPI\_0\_\_19\_GICR\_CIDR0 | | | | | | | | Redistributor\_control\_LPI\_0\_\_19\_GICR\_CIDR0 | | | | | reg32 | | 0x8FFF0 |
| offset | | 65520 | external | | |  | | | size | | 32 | default | | 0x0000000d | |
|  | | | | | | | | | | | | | | | |
| oid=7eed007a-b40b-43ca-8870-fc6da9a0dfca | | | | | | | | | | | | | | | |
| Component ID0 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_19\_GICR\_CIDR0\_1 | | | ro | ro | | 0xD | | |  | | | | | |

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| 1.1.5.19 Redistributor\_control\_LPI\_0\_\_20\_GICR\_CIDR1 | | | | | | | | Redistributor\_control\_LPI\_0\_\_20\_GICR\_CIDR1 | | | | | reg32 | | 0x8FFF4 |
| offset | | 65524 | external | | |  | | | size | | 32 | default | | 0x000000f0 | |
|  | | | | | | | | | | | | | | | |
| oid=e5015687-2ef4-4a6f-8118-ed55ffa65560 | | | | | | | | | | | | | | | |
| Component ID1 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_20\_GICR\_CIDR1\_1 | | | ro | ro | | 0xF0 | | |  | | | | | |

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| 1.1.5.20 Redistributor\_control\_LPI\_0\_\_21\_GICR\_CIDR2 | | | | | | | | Redistributor\_control\_LPI\_0\_\_21\_GICR\_CIDR2 | | | | | reg32 | | 0x8FFF8 |
| offset | | 65528 | external | | |  | | | size | | 32 | default | | 0x00000005 | |
|  | | | | | | | | | | | | | | | |
| oid=6758fd93-b40b-49b6-8128-3b38eeac866b | | | | | | | | | | | | | | | |
| Component ID2 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_21\_GICR\_CIDR2\_1 | | | ro | ro | | 0x5 | | |  | | | | | |

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| 1.1.5.21 Redistributor\_control\_LPI\_0\_\_22\_GICR\_CIDR3 | | | | | | | | Redistributor\_control\_LPI\_0\_\_22\_GICR\_CIDR3 | | | | | reg32 | | 0x8FFFC |
| offset | | 65532 | external | | |  | | | size | | 32 | default | | 0x000000b1 | |
|  | | | | | | | | | | | | | | | |
| oid=9c1ee3a2-d0d7-4878-ad95-76af14ce0c43 | | | | | | | | | | | | | | | |
| Component ID3 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_0\_\_22\_GICR\_CIDR3\_1 | | | ro | ro | | 0xB1 | | |  | | | | | |

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| --- |
| End RegGroup |

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| 1.1.6 Redistributor\_SGI\_PPI\_0 | | | | Redistributor\_SGI\_PPI\_0 | | | section | | 0x90000 | |
| offset | 589824 | external |  | | repeat |  | | size | | 49284 |
|  | | | | | | | | | | |
| oid=6eb449ab-c4c7-4837-8d30-35d5721399b2 | | | | | | | | | | |
| {range=65536} | | | | | | | | | | |

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| 1.1.6.1 Redistributor\_SGI\_PPI\_0\_\_1\_GICR\_IGROUPR0 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_1\_GICR\_IGROUPR0 | | | | | reg32 | | 0x90080 |
| offset | | 128 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c4a2b60c-158b-42b6-be34-dd9e383e9217 | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_1\_GICR\_IGROUPR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.2 Redistributor\_SGI\_PPI\_0\_\_2\_GICR\_ISENABLER0 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_2\_GICR\_ISENABLER0 | | | | | reg32 | | 0x90100 |
| offset | | 256 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=44687038-0c6a-4f98-9cd9-1bc1424ba0ba | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_2\_GICR\_ISENABLER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.3 Redistributor\_SGI\_PPI\_0\_\_3\_GICR\_ICENABLER0 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_3\_GICR\_ICENABLER0 | | | | | reg32 | | 0x90180 |
| offset | | 384 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=25b39fc7-b84b-4b2c-9c56-83eb06b96010 | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_3\_GICR\_ICENABLER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.4 Redistributor\_SGI\_PPI\_0\_\_4\_GICR\_ISPENDR0 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_4\_GICR\_ISPENDR0 | | | | | reg32 | | 0x90200 |
| offset | | 512 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f887c2dc-efcd-4976-81e8-c190aceac045 | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_4\_GICR\_ISPENDR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.5 Redistributor\_SGI\_PPI\_0\_\_5\_GICR\_ICPENDR0 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_5\_GICR\_ICPENDR0 | | | | | reg32 | | 0x90280 |
| offset | | 640 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2e815449-cfc2-45b7-96a2-f6f410c777e7 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_5\_GICR\_ICPENDR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.6 Redistributor\_SGI\_PPI\_0\_\_6\_GICR\_ISACTIVER0 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_6\_GICR\_ISACTIVER0 | | | | | reg32 | | 0x90300 |
| offset | | 768 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=a1a15900-c65c-4f0b-868a-c4fe0c3b745b | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_6\_GICR\_ISACTIVER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.7 Redistributor\_SGI\_PPI\_0\_\_7\_GICR\_ICACTIVER0 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_7\_GICR\_ICACTIVER0 | | | | | reg32 | | 0x90380 |
| offset | | 896 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=11a6c36b-f192-4aa9-9769-b48f903d7529 | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_7\_GICR\_ICACTIVER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.8 Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR0 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR0 | | | | | reg32 | | 0x90400 |
| offset | | 1024 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9fe7e05c-970b-48d2-a599-d6f940b2fcbb | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.9 Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR1 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR1 | | | | | reg32 | | 0x90404 |
| offset | | 1028 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e899c20f-a648-458c-a396-1658a9106c1d | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.10 Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR2 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR2 | | | | | reg32 | | 0x90408 |
| offset | | 1032 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=99fc4961-aa3f-4a2d-b86d-2471fe1332e7 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR2\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.11 Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR3 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR3 | | | | | reg32 | | 0x9040C |
| offset | | 1036 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=543aeefb-42b9-4207-ab88-83b4c85a27e9 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.12 Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR4 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR4 | | | | | reg32 | | 0x90410 |
| offset | | 1040 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=de0e7218-5686-4533-aa52-f95814d1f7f1 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.13 Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR5 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR5 | | | | | reg32 | | 0x90414 |
| offset | | 1044 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=36c9b3a5-54eb-44b6-949f-e345f52ac324 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR5\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.14 Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR6 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR6 | | | | | reg32 | | 0x90418 |
| offset | | 1048 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ea8b1ada-ef69-4dd8-ab90-4a36b488ed2a | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.15 Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR7 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR7 | | | | | reg32 | | 0x9041C |
| offset | | 1052 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=bc54c6da-c1c2-4c26-bb39-dbb3f5796888 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_8\_GICR\_IPRIORITYR7\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.16 Redistributor\_SGI\_PPI\_0\_\_9\_GICR\_ICFGR0 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_9\_GICR\_ICFGR0 | | | | | reg32 | | 0x90C00 |
| offset | | 3072 | external | | |  | | | size | | 32 | default | | 0xaaaaaaaa | |
|  | | | | | | | | | | | | | | | |
| oid=cddb68d0-dd6c-4514-9f5e-324379a02b31 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_9\_GICR\_ICFGR0\_1 | | | ro | ro | | 0xAAAAAAAA | | |  | | | | | |

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| 1.1.6.17 Redistributor\_SGI\_PPI\_0\_\_10\_GICR\_ICFGR1 | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_10\_GICR\_ICFGR1 | | | | | reg32 | | 0x90C04 |
| offset | | 3076 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=24a50763-4f87-42c8-bc01-09a11fa96a6c | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_0\_\_10\_GICR\_ICFGR1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.18 Redistributor\_SGI\_PPI\_0\_\_13\_GICR\_MISCSTATUSR | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_13\_GICR\_MISCSTATUSR | | | | | reg32 | | 0x9C000 |
| offset | | 49152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=5eed598f-d9b3-4c14-89d9-6b522fff538e | | | | | | | | | | | | | | | |
| GIC-500 Miscellaneous Status Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_SGI\_PPI\_0\_\_13\_GICR\_MISCSTATUSR\_\_0\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 1 | Redistributor\_SGI\_PPI\_0\_\_13\_GICR\_MISCSTATUSR\_\_1\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 2 | Redistributor\_SGI\_PPI\_0\_\_13\_GICR\_MISCSTATUSR\_\_2\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 31 | Redistributor\_SGI\_PPI\_0\_\_13\_GICR\_MISCSTATUSR\_\_31\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.6.19 Redistributor\_SGI\_PPI\_0\_\_14\_GICR\_PPISR | | | | | | | | Redistributor\_SGI\_PPI\_0\_\_14\_GICR\_PPISR | | | | | reg32 | | 0x9C080 |
| offset | | 49280 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cc69bef7-caca-4a4f-8b17-55efe27392bb | | | | | | | | | | | | | | | |
| GIC-500 Private Peripheral Interrupt Status Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:16 | Redistributor\_SGI\_PPI\_0\_\_14\_GICR\_PPISR\_\_16\_16 | | | ro | ro | | 0x0 | | |  | | | | | |

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| End RegGroup |

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| 1.1.7 Redistributor\_control\_LPI\_1 | | | | Redistributor\_control\_LPI\_1 | | | section | | 0xA0000 | |
| offset | 655360 | external |  | | repeat |  | | size | | 65536 |
|  | | | | | | | | | | |
| oid=373aca3c-d914-4432-ba39-6e16c91d1677 | | | | | | | | | | |
| {range=65536} | | | | | | | | | | |

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| 1.1.7.1 Redistributor\_control\_LPI\_1\_\_2\_GICR\_CTLR | | | | | | | | Redistributor\_control\_LPI\_1\_\_2\_GICR\_CTLR | | | | | reg32 | | 0xA0000 |
| offset | | 0 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e8a02ebb-cd63-46d1-b216-2cc43a5ecf57 | | | | | | | | | | | | | | | |
| Re-Distributor Control Register{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_control\_LPI\_1\_\_2\_GICR\_CTLR\_\_0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 3 | Redistributor\_control\_LPI\_1\_\_2\_GICR\_CTLR\_\_3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Redistributor\_control\_LPI\_1\_\_2\_GICR\_CTLR\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.2 Redistributor\_control\_LPI\_1\_\_3\_GICR\_IIDR | | | | | | | | Redistributor\_control\_LPI\_1\_\_3\_GICR\_IIDR | | | | | reg32 | | 0xA0004 |
| offset | | 4 | external | | |  | | | size | | 32 | default | | 0x0001143b | |
|  | | | | | | | | | | | | | | | |
| oid=c90d2c13-00ed-42f6-befb-76ef5ab5cc81 | | | | | | | | | | | | | | | |
| Re-Distributor Implementer Identification Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 11:0 | Redistributor\_control\_LPI\_1\_\_3\_GICR\_IIDR\_\_0\_12 | | | ro | ro | | 0x43B | | |  | | | | | |
| 15:12 | Redistributor\_control\_LPI\_1\_\_3\_GICR\_IIDR\_\_12\_4 | | | ro | ro | | 0x1 | | |  | | | | | |
| 19:16 | Redistributor\_control\_LPI\_1\_\_3\_GICR\_IIDR\_\_16\_4 | | | ro | ro | | 0x1 | | |  | | | | | |
| 31:24 | Redistributor\_control\_LPI\_1\_\_3\_GICR\_IIDR\_\_24\_8 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.3 Redistributor\_control\_LPI\_1\_\_4\_GICR\_TYPER\_lower | | | | | | | | Redistributor\_control\_LPI\_1\_\_4\_GICR\_TYPER\_lower | | | | | reg32 | | 0xA0008 |
| offset | | 8 | external | | |  | | | size | | 32 | default | | 0x00000001 | |
|  | | | | | | | | | | | | | | | |
| oid=0937afae-54dd-436d-aa1f-afa92c850e6f | | | | | | | | | | | | | | | |
| Re-Distributor Type Register (lower){access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_control\_LPI\_1\_\_4\_GICR\_TYPER\_lower\_\_0\_1 | | | ro | ro | | 0x1 | | |  | | | | | |
| 1 | Redistributor\_control\_LPI\_1\_\_4\_GICR\_TYPER\_lower\_\_1\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 3 | Redistributor\_control\_LPI\_1\_\_4\_GICR\_TYPER\_lower\_\_3\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 4 | Redistributor\_control\_LPI\_1\_\_4\_GICR\_TYPER\_lower\_\_4\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 23:8 | Redistributor\_control\_LPI\_1\_\_4\_GICR\_TYPER\_lower\_\_8\_16 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.4 Redistributor\_control\_LPI\_1\_\_5\_GICR\_TYPER\_upper | | | | | | | | Redistributor\_control\_LPI\_1\_\_5\_GICR\_TYPER\_upper | | | | | reg32 | | 0xA000C |
| offset | | 12 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=110735ba-8d61-466b-9ae6-66644fa52580 | | | | | | | | | | | | | | | |
| Re-Distributor Type Register (upper){access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_5\_GICR\_TYPER\_upper\_\_0\_8 | | | ro | ro | | 0x0 | | |  | | | | | |
| 15:8 | Redistributor\_control\_LPI\_1\_\_5\_GICR\_TYPER\_upper\_\_8\_8 | | | ro | ro | | 0x0 | | |  | | | | | |
| 23:16 | Redistributor\_control\_LPI\_1\_\_5\_GICR\_TYPER\_upper\_\_16\_8 | | | ro | ro | | 0x0 | | |  | | | | | |
| 31:24 | Redistributor\_control\_LPI\_1\_\_5\_GICR\_TYPER\_upper\_\_24\_8 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.5 Redistributor\_control\_LPI\_1\_\_6\_GICR\_WAKER | | | | | | | | Redistributor\_control\_LPI\_1\_\_6\_GICR\_WAKER | | | | | reg32 | | 0xA0014 |
| offset | | 20 | external | | |  | | | size | | 32 | default | | 0x00000006 | |
|  | | | | | | | | | | | | | | | |
| oid=aaa979cf-65e1-45f8-a939-73972878b206 | | | | | | | | | | | | | | | |
| Power Management Control Register{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_control\_LPI\_1\_\_6\_GICR\_WAKER\_\_0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 1 | Redistributor\_control\_LPI\_1\_\_6\_GICR\_WAKER\_\_1\_1 | | | rw | ro | | 0x1 | | |  | | | | | |
| 2 | Redistributor\_control\_LPI\_1\_\_6\_GICR\_WAKER\_\_2\_1 | | | rw | ro | | 0x1 | | |  | | | | | |
| 31 | Redistributor\_control\_LPI\_1\_\_6\_GICR\_WAKER\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.6 Redistributor\_control\_LPI\_1\_\_7\_GICR\_PROPBASER\_lower | | | | | | | | Redistributor\_control\_LPI\_1\_\_7\_GICR\_PROPBASER\_lower | | | | | reg32 | | 0xA0070 |
| offset | | 112 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8b5e183a-a0e9-4fac-9703-f83a0856592f | | | | | | | | | | | | | | | |
| LPI Configuration Table Control Register (lower){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 4:0 | Redistributor\_control\_LPI\_1\_\_7\_GICR\_PROPBASER\_lower\_\_0\_5 | | | rw | ro | | 0x0 | | |  | | | | | |
| 9:7 | Redistributor\_control\_LPI\_1\_\_7\_GICR\_PROPBASER\_lower\_\_7\_3 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31:12 | Redistributor\_control\_LPI\_1\_\_7\_GICR\_PROPBASER\_lower\_\_12\_20 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.7 Redistributor\_control\_LPI\_1\_\_8\_GICR\_PROPBASER\_upper | | | | | | | | Redistributor\_control\_LPI\_1\_\_8\_GICR\_PROPBASER\_upper | | | | | reg32 | | 0xA0074 |
| offset | | 116 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=46a2609d-b435-4b1d-97d3-984cb093c958 | | | | | | | | | | | | | | | |
| LPI Configuration Table Control Register (upper){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | Redistributor\_control\_LPI\_1\_\_8\_GICR\_PROPBASER\_upper\_\_0\_16 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.8 Redistributor\_control\_LPI\_1\_\_9\_GICR\_PENDBASER\_lower | | | | | | | | Redistributor\_control\_LPI\_1\_\_9\_GICR\_PENDBASER\_lower | | | | | reg32 | | 0xA0078 |
| offset | | 120 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=55488b72-635a-4f57-9e0f-a519c535e3a5 | | | | | | | | | | | | | | | |
| LPI Pending Table Control Register (lower){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 9:7 | Redistributor\_control\_LPI\_1\_\_9\_GICR\_PENDBASER\_lower\_\_7\_3 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31:16 | Redistributor\_control\_LPI\_1\_\_9\_GICR\_PENDBASER\_lower\_\_16\_16 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.9 Redistributor\_control\_LPI\_1\_\_10\_GICR\_PENDBASER\_upper | | | | | | | | Redistributor\_control\_LPI\_1\_\_10\_GICR\_PENDBASER\_upper | | | | | reg32 | | 0xA007C |
| offset | | 124 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d2a22570-4cf1-4030-881e-dc7452dbaf2a | | | | | | | | | | | | | | | |
| LPI Pending Table Control Register (upper){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | Redistributor\_control\_LPI\_1\_\_10\_GICR\_PENDBASER\_upper\_\_0\_16 | | | rw | ro | | 0x0 | | |  | | | | | |
| 30 | Redistributor\_control\_LPI\_1\_\_10\_GICR\_PENDBASER\_upper\_\_30\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.10 Redistributor\_control\_LPI\_1\_\_11\_GICR\_PIDR4 | | | | | | | | Redistributor\_control\_LPI\_1\_\_11\_GICR\_PIDR4 | | | | | reg32 | | 0xAFFD0 |
| offset | | 65488 | external | | |  | | | size | | 32 | default | | 0x00000044 | |
|  | | | | | | | | | | | | | | | |
| oid=28f73844-9b44-4ecd-8db2-d741a25e2d77 | | | | | | | | | | | | | | | |
| Peripheral ID4 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_11\_GICR\_PIDR4\_1 | | | ro | ro | | 0x44 | | |  | | | | | |

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| 1.1.7.11 Redistributor\_control\_LPI\_1\_\_12\_GICR\_PIDR5 | | | | | | | | Redistributor\_control\_LPI\_1\_\_12\_GICR\_PIDR5 | | | | | reg32 | | 0xAFFD4 |
| offset | | 65492 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f87bc756-20b0-47ee-b82a-e5ad227ac4df | | | | | | | | | | | | | | | |
| Peripheral ID5 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_12\_GICR\_PIDR5\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.12 Redistributor\_control\_LPI\_1\_\_13\_GICR\_PIDR6 | | | | | | | | Redistributor\_control\_LPI\_1\_\_13\_GICR\_PIDR6 | | | | | reg32 | | 0xAFFD8 |
| offset | | 65496 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0f78f5b6-b07d-4b95-b440-cfa13ca59201 | | | | | | | | | | | | | | | |
| Peripheral ID6 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_13\_GICR\_PIDR6\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.13 Redistributor\_control\_LPI\_1\_\_14\_GICR\_PIDR7 | | | | | | | | Redistributor\_control\_LPI\_1\_\_14\_GICR\_PIDR7 | | | | | reg32 | | 0xAFFDC |
| offset | | 65500 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f2d9c0f4-9fa1-4ec2-8a1f-03a36086f571 | | | | | | | | | | | | | | | |
| Peripheral ID7 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_14\_GICR\_PIDR7\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.14 Redistributor\_control\_LPI\_1\_\_15\_GICR\_PIDR0 | | | | | | | | Redistributor\_control\_LPI\_1\_\_15\_GICR\_PIDR0 | | | | | reg32 | | 0xAFFE0 |
| offset | | 65504 | external | | |  | | | size | | 32 | default | | 0x00000093 | |
|  | | | | | | | | | | | | | | | |
| oid=10f6c212-398d-4c74-8fae-144b9ceeed6e | | | | | | | | | | | | | | | |
| Peripheral ID0 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_15\_GICR\_PIDR0\_1 | | | ro | ro | | 0x93 | | |  | | | | | |

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| 1.1.7.15 Redistributor\_control\_LPI\_1\_\_16\_GICR\_PIDR1 | | | | | | | | Redistributor\_control\_LPI\_1\_\_16\_GICR\_PIDR1 | | | | | reg32 | | 0xAFFE4 |
| offset | | 65508 | external | | |  | | | size | | 32 | default | | 0x000000b4 | |
|  | | | | | | | | | | | | | | | |
| oid=1d665559-b320-4076-8f60-6c5668c8be7b | | | | | | | | | | | | | | | |
| Peripheral ID1 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_16\_GICR\_PIDR1\_1 | | | ro | ro | | 0xB4 | | |  | | | | | |

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| 1.1.7.16 Redistributor\_control\_LPI\_1\_\_17\_GICR\_PIDR2 | | | | | | | | Redistributor\_control\_LPI\_1\_\_17\_GICR\_PIDR2 | | | | | reg32 | | 0xAFFE8 |
| offset | | 65512 | external | | |  | | | size | | 32 | default | | 0x0000003b | |
|  | | | | | | | | | | | | | | | |
| oid=70ba73b4-cbf5-423f-b1f0-0df500683581 | | | | | | | | | | | | | | | |
| Peripheral ID2 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_17\_GICR\_PIDR2\_1 | | | ro | ro | | 0x3B | | |  | | | | | |

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| 1.1.7.17 Redistributor\_control\_LPI\_1\_\_18\_GICR\_PIDR3 | | | | | | | | Redistributor\_control\_LPI\_1\_\_18\_GICR\_PIDR3 | | | | | reg32 | | 0xAFFEC |
| offset | | 65516 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=26a3ecae-800d-4a35-bf4b-baafb5611e81 | | | | | | | | | | | | | | | |
| Peripheral ID3 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_18\_GICR\_PIDR3\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.7.18 Redistributor\_control\_LPI\_1\_\_19\_GICR\_CIDR0 | | | | | | | | Redistributor\_control\_LPI\_1\_\_19\_GICR\_CIDR0 | | | | | reg32 | | 0xAFFF0 |
| offset | | 65520 | external | | |  | | | size | | 32 | default | | 0x0000000d | |
|  | | | | | | | | | | | | | | | |
| oid=92354f85-d2ac-47ac-ab4e-fd754f76b3dc | | | | | | | | | | | | | | | |
| Component ID0 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_19\_GICR\_CIDR0\_1 | | | ro | ro | | 0xD | | |  | | | | | |

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| 1.1.7.19 Redistributor\_control\_LPI\_1\_\_20\_GICR\_CIDR1 | | | | | | | | Redistributor\_control\_LPI\_1\_\_20\_GICR\_CIDR1 | | | | | reg32 | | 0xAFFF4 |
| offset | | 65524 | external | | |  | | | size | | 32 | default | | 0x000000f0 | |
|  | | | | | | | | | | | | | | | |
| oid=032e3ec1-0827-43d2-9ef1-606f7803de43 | | | | | | | | | | | | | | | |
| Component ID1 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_20\_GICR\_CIDR1\_1 | | | ro | ro | | 0xF0 | | |  | | | | | |

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| 1.1.7.20 Redistributor\_control\_LPI\_1\_\_21\_GICR\_CIDR2 | | | | | | | | Redistributor\_control\_LPI\_1\_\_21\_GICR\_CIDR2 | | | | | reg32 | | 0xAFFF8 |
| offset | | 65528 | external | | |  | | | size | | 32 | default | | 0x00000005 | |
|  | | | | | | | | | | | | | | | |
| oid=aec8dffe-8191-48b4-bc20-4caa10e45630 | | | | | | | | | | | | | | | |
| Component ID2 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_21\_GICR\_CIDR2\_1 | | | ro | ro | | 0x5 | | |  | | | | | |

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| 1.1.7.21 Redistributor\_control\_LPI\_1\_\_22\_GICR\_CIDR3 | | | | | | | | Redistributor\_control\_LPI\_1\_\_22\_GICR\_CIDR3 | | | | | reg32 | | 0xAFFFC |
| offset | | 65532 | external | | |  | | | size | | 32 | default | | 0x000000b1 | |
|  | | | | | | | | | | | | | | | |
| oid=b3407337-9927-4252-b54f-4993d808916c | | | | | | | | | | | | | | | |
| Component ID3 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_1\_\_22\_GICR\_CIDR3\_1 | | | ro | ro | | 0xB1 | | |  | | | | | |

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| End RegGroup |

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| 1.1.8 Redistributor\_SGI\_PPI\_1 | | | | Redistributor\_SGI\_PPI\_1 | | | section | | 0xB0000 | |
| offset | 720896 | external |  | | repeat |  | | size | | 49284 |
|  | | | | | | | | | | |
| oid=3b98edd9-932d-4a39-aaac-bb9201c75a19 | | | | | | | | | | |
| {range=65536} | | | | | | | | | | |

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| 1.1.8.1 Redistributor\_SGI\_PPI\_1\_\_1\_GICR\_IGROUPR0 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_1\_GICR\_IGROUPR0 | | | | | reg32 | | 0xB0080 |
| offset | | 128 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e561bbc7-9999-40fc-8b8b-fa45bbc8ada9 | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_1\_GICR\_IGROUPR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.2 Redistributor\_SGI\_PPI\_1\_\_2\_GICR\_ISENABLER0 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_2\_GICR\_ISENABLER0 | | | | | reg32 | | 0xB0100 |
| offset | | 256 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1cc51c79-814d-4a2a-8c0a-80f1867bf55f | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_2\_GICR\_ISENABLER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.3 Redistributor\_SGI\_PPI\_1\_\_3\_GICR\_ICENABLER0 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_3\_GICR\_ICENABLER0 | | | | | reg32 | | 0xB0180 |
| offset | | 384 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=46f69a62-547b-4159-9643-97d6a18a1a6b | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_3\_GICR\_ICENABLER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.4 Redistributor\_SGI\_PPI\_1\_\_4\_GICR\_ISPENDR0 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_4\_GICR\_ISPENDR0 | | | | | reg32 | | 0xB0200 |
| offset | | 512 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fafe872f-9b1c-4e99-ab37-bc1727754a65 | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_4\_GICR\_ISPENDR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.5 Redistributor\_SGI\_PPI\_1\_\_5\_GICR\_ICPENDR0 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_5\_GICR\_ICPENDR0 | | | | | reg32 | | 0xB0280 |
| offset | | 640 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=282e0b84-2ba1-4f3d-bcd0-2600621000d5 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_5\_GICR\_ICPENDR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.6 Redistributor\_SGI\_PPI\_1\_\_6\_GICR\_ISACTIVER0 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_6\_GICR\_ISACTIVER0 | | | | | reg32 | | 0xB0300 |
| offset | | 768 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fc6a1854-dab4-414b-a3dc-48a4c520db52 | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_6\_GICR\_ISACTIVER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.7 Redistributor\_SGI\_PPI\_1\_\_7\_GICR\_ICACTIVER0 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_7\_GICR\_ICACTIVER0 | | | | | reg32 | | 0xB0380 |
| offset | | 896 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=447dffd3-9ec4-45eb-a226-2de710b67f36 | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_7\_GICR\_ICACTIVER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.8 Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR0 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR0 | | | | | reg32 | | 0xB0400 |
| offset | | 1024 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=7d2990b9-ec1c-41a7-9a92-68179813260f | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.9 Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR1 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR1 | | | | | reg32 | | 0xB0404 |
| offset | | 1028 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f14b43a5-5827-4834-91bf-b41188227cad | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.10 Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR2 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR2 | | | | | reg32 | | 0xB0408 |
| offset | | 1032 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=3cf262a0-3353-4d65-a937-9dc9cd414cf3 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR2\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.11 Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR3 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR3 | | | | | reg32 | | 0xB040C |
| offset | | 1036 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cf13fa22-1cc6-42b3-a9ae-78fc6fa29060 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.12 Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR4 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR4 | | | | | reg32 | | 0xB0410 |
| offset | | 1040 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d76c5729-9119-4aec-afa0-ef59ddc4cd7e | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.13 Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR5 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR5 | | | | | reg32 | | 0xB0414 |
| offset | | 1044 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=df812c84-ef0e-4bb4-83af-318562f8f561 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR5\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.14 Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR6 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR6 | | | | | reg32 | | 0xB0418 |
| offset | | 1048 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9877f036-8aa2-4051-ac08-663098921b97 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.15 Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR7 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR7 | | | | | reg32 | | 0xB041C |
| offset | | 1052 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=29fd6e3b-73ba-447c-a9d9-65a2074f13bc | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_8\_GICR\_IPRIORITYR7\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.16 Redistributor\_SGI\_PPI\_1\_\_9\_GICR\_ICFGR0 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_9\_GICR\_ICFGR0 | | | | | reg32 | | 0xB0C00 |
| offset | | 3072 | external | | |  | | | size | | 32 | default | | 0xaaaaaaaa | |
|  | | | | | | | | | | | | | | | |
| oid=0e41338f-101e-4326-a85a-81dc79aea5a5 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_9\_GICR\_ICFGR0\_1 | | | ro | ro | | 0xAAAAAAAA | | |  | | | | | |

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| 1.1.8.17 Redistributor\_SGI\_PPI\_1\_\_10\_GICR\_ICFGR1 | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_10\_GICR\_ICFGR1 | | | | | reg32 | | 0xB0C04 |
| offset | | 3076 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=78d53b30-0a18-48e1-8ffb-30afedb00b91 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_1\_\_10\_GICR\_ICFGR1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.18 Redistributor\_SGI\_PPI\_1\_\_13\_GICR\_MISCSTATUSR | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_13\_GICR\_MISCSTATUSR | | | | | reg32 | | 0xBC000 |
| offset | | 49152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=120a9303-9014-41b0-bb81-77d0aabf8d01 | | | | | | | | | | | | | | | |
| GIC-500 Miscellaneous Status Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_SGI\_PPI\_1\_\_13\_GICR\_MISCSTATUSR\_\_0\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 1 | Redistributor\_SGI\_PPI\_1\_\_13\_GICR\_MISCSTATUSR\_\_1\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 2 | Redistributor\_SGI\_PPI\_1\_\_13\_GICR\_MISCSTATUSR\_\_2\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 31 | Redistributor\_SGI\_PPI\_1\_\_13\_GICR\_MISCSTATUSR\_\_31\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.8.19 Redistributor\_SGI\_PPI\_1\_\_14\_GICR\_PPISR | | | | | | | | Redistributor\_SGI\_PPI\_1\_\_14\_GICR\_PPISR | | | | | reg32 | | 0xBC080 |
| offset | | 49280 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=73770db7-a03b-45ae-b68e-16d49fc481b3 | | | | | | | | | | | | | | | |
| GIC-500 Private Peripheral Interrupt Status Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:16 | Redistributor\_SGI\_PPI\_1\_\_14\_GICR\_PPISR\_\_16\_16 | | | ro | ro | | 0x0 | | |  | | | | | |

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| End RegGroup |

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| 1.1.9 Redistributor\_control\_LPI\_2 | | | | Redistributor\_control\_LPI\_2 | | | section | | 0xC0000 | |
| offset | 786432 | external |  | | repeat |  | | size | | 65536 |
|  | | | | | | | | | | |
| oid=2a7f32d5-571c-469a-8ee2-80bc1354ec0f | | | | | | | | | | |
| {range=65536} | | | | | | | | | | |

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| 1.1.9.1 Redistributor\_control\_LPI\_2\_\_2\_GICR\_CTLR | | | | | | | | Redistributor\_control\_LPI\_2\_\_2\_GICR\_CTLR | | | | | reg32 | | 0xC0000 |
| offset | | 0 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fe04ded6-6222-4f45-b24f-70d6c66b0931 | | | | | | | | | | | | | | | |
| Re-Distributor Control Register{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_control\_LPI\_2\_\_2\_GICR\_CTLR\_\_0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 3 | Redistributor\_control\_LPI\_2\_\_2\_GICR\_CTLR\_\_3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Redistributor\_control\_LPI\_2\_\_2\_GICR\_CTLR\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.2 Redistributor\_control\_LPI\_2\_\_3\_GICR\_IIDR | | | | | | | | Redistributor\_control\_LPI\_2\_\_3\_GICR\_IIDR | | | | | reg32 | | 0xC0004 |
| offset | | 4 | external | | |  | | | size | | 32 | default | | 0x0001143b | |
|  | | | | | | | | | | | | | | | |
| oid=6a32f2d5-0b98-4111-85a6-1a74c3d74704 | | | | | | | | | | | | | | | |
| Re-Distributor Implementer Identification Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 11:0 | Redistributor\_control\_LPI\_2\_\_3\_GICR\_IIDR\_\_0\_12 | | | ro | ro | | 0x43B | | |  | | | | | |
| 15:12 | Redistributor\_control\_LPI\_2\_\_3\_GICR\_IIDR\_\_12\_4 | | | ro | ro | | 0x1 | | |  | | | | | |
| 19:16 | Redistributor\_control\_LPI\_2\_\_3\_GICR\_IIDR\_\_16\_4 | | | ro | ro | | 0x1 | | |  | | | | | |
| 31:24 | Redistributor\_control\_LPI\_2\_\_3\_GICR\_IIDR\_\_24\_8 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.3 Redistributor\_control\_LPI\_2\_\_4\_GICR\_TYPER\_lower | | | | | | | | Redistributor\_control\_LPI\_2\_\_4\_GICR\_TYPER\_lower | | | | | reg32 | | 0xC0008 |
| offset | | 8 | external | | |  | | | size | | 32 | default | | 0x00000001 | |
|  | | | | | | | | | | | | | | | |
| oid=65332212-1f22-4b89-b955-80ba22bbfc32 | | | | | | | | | | | | | | | |
| Re-Distributor Type Register (lower){access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_control\_LPI\_2\_\_4\_GICR\_TYPER\_lower\_\_0\_1 | | | ro | ro | | 0x1 | | |  | | | | | |
| 1 | Redistributor\_control\_LPI\_2\_\_4\_GICR\_TYPER\_lower\_\_1\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 3 | Redistributor\_control\_LPI\_2\_\_4\_GICR\_TYPER\_lower\_\_3\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 4 | Redistributor\_control\_LPI\_2\_\_4\_GICR\_TYPER\_lower\_\_4\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 23:8 | Redistributor\_control\_LPI\_2\_\_4\_GICR\_TYPER\_lower\_\_8\_16 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.4 Redistributor\_control\_LPI\_2\_\_5\_GICR\_TYPER\_upper | | | | | | | | Redistributor\_control\_LPI\_2\_\_5\_GICR\_TYPER\_upper | | | | | reg32 | | 0xC000C |
| offset | | 12 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=eac196fb-61fe-47a4-a4ed-b4711126ab6e | | | | | | | | | | | | | | | |
| Re-Distributor Type Register (upper){access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_5\_GICR\_TYPER\_upper\_\_0\_8 | | | ro | ro | | 0x0 | | |  | | | | | |
| 15:8 | Redistributor\_control\_LPI\_2\_\_5\_GICR\_TYPER\_upper\_\_8\_8 | | | ro | ro | | 0x0 | | |  | | | | | |
| 23:16 | Redistributor\_control\_LPI\_2\_\_5\_GICR\_TYPER\_upper\_\_16\_8 | | | ro | ro | | 0x0 | | |  | | | | | |
| 31:24 | Redistributor\_control\_LPI\_2\_\_5\_GICR\_TYPER\_upper\_\_24\_8 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.5 Redistributor\_control\_LPI\_2\_\_6\_GICR\_WAKER | | | | | | | | Redistributor\_control\_LPI\_2\_\_6\_GICR\_WAKER | | | | | reg32 | | 0xC0014 |
| offset | | 20 | external | | |  | | | size | | 32 | default | | 0x00000006 | |
|  | | | | | | | | | | | | | | | |
| oid=0455d90e-41d5-4359-85ea-ed3cd3f129be | | | | | | | | | | | | | | | |
| Power Management Control Register{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_control\_LPI\_2\_\_6\_GICR\_WAKER\_\_0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 1 | Redistributor\_control\_LPI\_2\_\_6\_GICR\_WAKER\_\_1\_1 | | | rw | ro | | 0x1 | | |  | | | | | |
| 2 | Redistributor\_control\_LPI\_2\_\_6\_GICR\_WAKER\_\_2\_1 | | | rw | ro | | 0x1 | | |  | | | | | |
| 31 | Redistributor\_control\_LPI\_2\_\_6\_GICR\_WAKER\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.6 Redistributor\_control\_LPI\_2\_\_7\_GICR\_PROPBASER\_lower | | | | | | | | Redistributor\_control\_LPI\_2\_\_7\_GICR\_PROPBASER\_lower | | | | | reg32 | | 0xC0070 |
| offset | | 112 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=2c4f5cfd-64f2-4877-9ccd-9c4ec61945c7 | | | | | | | | | | | | | | | |
| LPI Configuration Table Control Register (lower){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 4:0 | Redistributor\_control\_LPI\_2\_\_7\_GICR\_PROPBASER\_lower\_\_0\_5 | | | rw | ro | | 0x0 | | |  | | | | | |
| 9:7 | Redistributor\_control\_LPI\_2\_\_7\_GICR\_PROPBASER\_lower\_\_7\_3 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31:12 | Redistributor\_control\_LPI\_2\_\_7\_GICR\_PROPBASER\_lower\_\_12\_20 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.7 Redistributor\_control\_LPI\_2\_\_8\_GICR\_PROPBASER\_upper | | | | | | | | Redistributor\_control\_LPI\_2\_\_8\_GICR\_PROPBASER\_upper | | | | | reg32 | | 0xC0074 |
| offset | | 116 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f2d1ee1a-a50f-49fb-99b4-c6451f351918 | | | | | | | | | | | | | | | |
| LPI Configuration Table Control Register (upper){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | Redistributor\_control\_LPI\_2\_\_8\_GICR\_PROPBASER\_upper\_\_0\_16 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.8 Redistributor\_control\_LPI\_2\_\_9\_GICR\_PENDBASER\_lower | | | | | | | | Redistributor\_control\_LPI\_2\_\_9\_GICR\_PENDBASER\_lower | | | | | reg32 | | 0xC0078 |
| offset | | 120 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c6d7fd0d-545e-4675-b06d-03ac17f84dba | | | | | | | | | | | | | | | |
| LPI Pending Table Control Register (lower){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 9:7 | Redistributor\_control\_LPI\_2\_\_9\_GICR\_PENDBASER\_lower\_\_7\_3 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31:16 | Redistributor\_control\_LPI\_2\_\_9\_GICR\_PENDBASER\_lower\_\_16\_16 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.9 Redistributor\_control\_LPI\_2\_\_10\_GICR\_PENDBASER\_upper | | | | | | | | Redistributor\_control\_LPI\_2\_\_10\_GICR\_PENDBASER\_upper | | | | | reg32 | | 0xC007C |
| offset | | 124 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=4f8f9557-8cf4-421c-adc5-2f934dcfcb97 | | | | | | | | | | | | | | | |
| LPI Pending Table Control Register (upper){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | Redistributor\_control\_LPI\_2\_\_10\_GICR\_PENDBASER\_upper\_\_0\_16 | | | rw | ro | | 0x0 | | |  | | | | | |
| 30 | Redistributor\_control\_LPI\_2\_\_10\_GICR\_PENDBASER\_upper\_\_30\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.10 Redistributor\_control\_LPI\_2\_\_11\_GICR\_PIDR4 | | | | | | | | Redistributor\_control\_LPI\_2\_\_11\_GICR\_PIDR4 | | | | | reg32 | | 0xCFFD0 |
| offset | | 65488 | external | | |  | | | size | | 32 | default | | 0x00000044 | |
|  | | | | | | | | | | | | | | | |
| oid=1512ebc9-5d23-4081-8629-3cee9d604fe2 | | | | | | | | | | | | | | | |
| Peripheral ID4 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_11\_GICR\_PIDR4\_1 | | | ro | ro | | 0x44 | | |  | | | | | |

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| 1.1.9.11 Redistributor\_control\_LPI\_2\_\_12\_GICR\_PIDR5 | | | | | | | | Redistributor\_control\_LPI\_2\_\_12\_GICR\_PIDR5 | | | | | reg32 | | 0xCFFD4 |
| offset | | 65492 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c89e59d6-6322-4f88-b88e-2cc0e34d47ce | | | | | | | | | | | | | | | |
| Peripheral ID5 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_12\_GICR\_PIDR5\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.12 Redistributor\_control\_LPI\_2\_\_13\_GICR\_PIDR6 | | | | | | | | Redistributor\_control\_LPI\_2\_\_13\_GICR\_PIDR6 | | | | | reg32 | | 0xCFFD8 |
| offset | | 65496 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=25c0617d-4c64-4664-b1b7-4f6c78c15589 | | | | | | | | | | | | | | | |
| Peripheral ID6 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_13\_GICR\_PIDR6\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.13 Redistributor\_control\_LPI\_2\_\_14\_GICR\_PIDR7 | | | | | | | | Redistributor\_control\_LPI\_2\_\_14\_GICR\_PIDR7 | | | | | reg32 | | 0xCFFDC |
| offset | | 65500 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=84fdb578-ed51-4e82-9ffe-dfcc1a3d0aa0 | | | | | | | | | | | | | | | |
| Peripheral ID7 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_14\_GICR\_PIDR7\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.14 Redistributor\_control\_LPI\_2\_\_15\_GICR\_PIDR0 | | | | | | | | Redistributor\_control\_LPI\_2\_\_15\_GICR\_PIDR0 | | | | | reg32 | | 0xCFFE0 |
| offset | | 65504 | external | | |  | | | size | | 32 | default | | 0x00000093 | |
|  | | | | | | | | | | | | | | | |
| oid=9537bda8-fc76-46d2-9123-218f51df73a9 | | | | | | | | | | | | | | | |
| Peripheral ID0 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_15\_GICR\_PIDR0\_1 | | | ro | ro | | 0x93 | | |  | | | | | |

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| 1.1.9.15 Redistributor\_control\_LPI\_2\_\_16\_GICR\_PIDR1 | | | | | | | | Redistributor\_control\_LPI\_2\_\_16\_GICR\_PIDR1 | | | | | reg32 | | 0xCFFE4 |
| offset | | 65508 | external | | |  | | | size | | 32 | default | | 0x000000b4 | |
|  | | | | | | | | | | | | | | | |
| oid=73a07598-8414-4882-9972-b11f227c6ce1 | | | | | | | | | | | | | | | |
| Peripheral ID1 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_16\_GICR\_PIDR1\_1 | | | ro | ro | | 0xB4 | | |  | | | | | |

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| 1.1.9.16 Redistributor\_control\_LPI\_2\_\_17\_GICR\_PIDR2 | | | | | | | | Redistributor\_control\_LPI\_2\_\_17\_GICR\_PIDR2 | | | | | reg32 | | 0xCFFE8 |
| offset | | 65512 | external | | |  | | | size | | 32 | default | | 0x0000003b | |
|  | | | | | | | | | | | | | | | |
| oid=405317eb-4a00-454a-9a5d-177a3a7d3c98 | | | | | | | | | | | | | | | |
| Peripheral ID2 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_17\_GICR\_PIDR2\_1 | | | ro | ro | | 0x3B | | |  | | | | | |

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| 1.1.9.17 Redistributor\_control\_LPI\_2\_\_18\_GICR\_PIDR3 | | | | | | | | Redistributor\_control\_LPI\_2\_\_18\_GICR\_PIDR3 | | | | | reg32 | | 0xCFFEC |
| offset | | 65516 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=cb7e59ac-9068-48f1-8e13-d293bf407ba4 | | | | | | | | | | | | | | | |
| Peripheral ID3 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_18\_GICR\_PIDR3\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.9.18 Redistributor\_control\_LPI\_2\_\_19\_GICR\_CIDR0 | | | | | | | | Redistributor\_control\_LPI\_2\_\_19\_GICR\_CIDR0 | | | | | reg32 | | 0xCFFF0 |
| offset | | 65520 | external | | |  | | | size | | 32 | default | | 0x0000000d | |
|  | | | | | | | | | | | | | | | |
| oid=5810cdf8-6825-4390-a758-d18dc524966e | | | | | | | | | | | | | | | |
| Component ID0 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_19\_GICR\_CIDR0\_1 | | | ro | ro | | 0xD | | |  | | | | | |

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| 1.1.9.19 Redistributor\_control\_LPI\_2\_\_20\_GICR\_CIDR1 | | | | | | | | Redistributor\_control\_LPI\_2\_\_20\_GICR\_CIDR1 | | | | | reg32 | | 0xCFFF4 |
| offset | | 65524 | external | | |  | | | size | | 32 | default | | 0x000000f0 | |
|  | | | | | | | | | | | | | | | |
| oid=53c2970c-8591-4dbd-ad14-50237f150b1a | | | | | | | | | | | | | | | |
| Component ID1 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_20\_GICR\_CIDR1\_1 | | | ro | ro | | 0xF0 | | |  | | | | | |

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| 1.1.9.20 Redistributor\_control\_LPI\_2\_\_21\_GICR\_CIDR2 | | | | | | | | Redistributor\_control\_LPI\_2\_\_21\_GICR\_CIDR2 | | | | | reg32 | | 0xCFFF8 |
| offset | | 65528 | external | | |  | | | size | | 32 | default | | 0x00000005 | |
|  | | | | | | | | | | | | | | | |
| oid=7a1a9486-0def-4ae2-9a24-f55e984829a8 | | | | | | | | | | | | | | | |
| Component ID2 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_21\_GICR\_CIDR2\_1 | | | ro | ro | | 0x5 | | |  | | | | | |

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| 1.1.9.21 Redistributor\_control\_LPI\_2\_\_22\_GICR\_CIDR3 | | | | | | | | Redistributor\_control\_LPI\_2\_\_22\_GICR\_CIDR3 | | | | | reg32 | | 0xCFFFC |
| offset | | 65532 | external | | |  | | | size | | 32 | default | | 0x000000b1 | |
|  | | | | | | | | | | | | | | | |
| oid=6aee14dc-18c4-42b0-8a63-c40f5e846674 | | | | | | | | | | | | | | | |
| Component ID3 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_2\_\_22\_GICR\_CIDR3\_1 | | | ro | ro | | 0xB1 | | |  | | | | | |

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| --- |
| End RegGroup |

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| 1.1.10 Redistributor\_SGI\_PPI\_2 | | | | Redistributor\_SGI\_PPI\_2 | | | section | | 0xD0000 | |
| offset | 851968 | external |  | | repeat |  | | size | | 49284 |
|  | | | | | | | | | | |
| oid=2e34ef99-b4f4-4eb8-8934-87ee53170845 | | | | | | | | | | |
| {range=65536} | | | | | | | | | | |

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| 1.1.10.1 Redistributor\_SGI\_PPI\_2\_\_1\_GICR\_IGROUPR0 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_1\_GICR\_IGROUPR0 | | | | | reg32 | | 0xD0080 |
| offset | | 128 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e542da17-7869-422e-b951-c0d14397c7c4 | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_1\_GICR\_IGROUPR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.2 Redistributor\_SGI\_PPI\_2\_\_2\_GICR\_ISENABLER0 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_2\_GICR\_ISENABLER0 | | | | | reg32 | | 0xD0100 |
| offset | | 256 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d69fde85-de34-4ca3-8d21-3aa5a4dae20b | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_2\_GICR\_ISENABLER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.3 Redistributor\_SGI\_PPI\_2\_\_3\_GICR\_ICENABLER0 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_3\_GICR\_ICENABLER0 | | | | | reg32 | | 0xD0180 |
| offset | | 384 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d7659350-7c95-48f8-b736-94378b4ff1dc | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_3\_GICR\_ICENABLER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.4 Redistributor\_SGI\_PPI\_2\_\_4\_GICR\_ISPENDR0 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_4\_GICR\_ISPENDR0 | | | | | reg32 | | 0xD0200 |
| offset | | 512 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1a972a7e-8cbb-499c-82e8-552d85324309 | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_4\_GICR\_ISPENDR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.5 Redistributor\_SGI\_PPI\_2\_\_5\_GICR\_ICPENDR0 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_5\_GICR\_ICPENDR0 | | | | | reg32 | | 0xD0280 |
| offset | | 640 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=21f10512-c1e2-4eb5-9c07-7127add59d84 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_5\_GICR\_ICPENDR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.6 Redistributor\_SGI\_PPI\_2\_\_6\_GICR\_ISACTIVER0 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_6\_GICR\_ISACTIVER0 | | | | | reg32 | | 0xD0300 |
| offset | | 768 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d0b81a06-0ef4-41f9-a827-b5f4984b31ba | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_6\_GICR\_ISACTIVER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.7 Redistributor\_SGI\_PPI\_2\_\_7\_GICR\_ICACTIVER0 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_7\_GICR\_ICACTIVER0 | | | | | reg32 | | 0xD0380 |
| offset | | 896 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6351961b-3df3-480b-afb9-8ba4fc4c2c9f | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_7\_GICR\_ICACTIVER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.8 Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR0 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR0 | | | | | reg32 | | 0xD0400 |
| offset | | 1024 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e3701b80-ba67-4a99-acd3-1a5bb44f7440 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.9 Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR1 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR1 | | | | | reg32 | | 0xD0404 |
| offset | | 1028 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fc3e50fb-8ec7-44f9-80b9-72671e775c8e | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.10 Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR2 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR2 | | | | | reg32 | | 0xD0408 |
| offset | | 1032 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=aa336509-8f56-43f5-9347-3c605cec8abf | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR2\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.11 Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR3 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR3 | | | | | reg32 | | 0xD040C |
| offset | | 1036 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=06357e40-6fe2-46f0-ba26-69a6736ec205 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.12 Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR4 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR4 | | | | | reg32 | | 0xD0410 |
| offset | | 1040 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=de552d57-16e3-4346-824e-2a0d48e4c06e | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.13 Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR5 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR5 | | | | | reg32 | | 0xD0414 |
| offset | | 1044 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=adf6a9ec-ee9d-4799-ae03-6af28b150096 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR5\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.14 Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR6 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR6 | | | | | reg32 | | 0xD0418 |
| offset | | 1048 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=77d6367c-6dcf-426a-bcd7-063245e098b9 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.15 Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR7 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR7 | | | | | reg32 | | 0xD041C |
| offset | | 1052 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ff610e15-0910-49dc-98b9-888ecff817a4 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_8\_GICR\_IPRIORITYR7\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.16 Redistributor\_SGI\_PPI\_2\_\_9\_GICR\_ICFGR0 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_9\_GICR\_ICFGR0 | | | | | reg32 | | 0xD0C00 |
| offset | | 3072 | external | | |  | | | size | | 32 | default | | 0xaaaaaaaa | |
|  | | | | | | | | | | | | | | | |
| oid=b4eb760c-0cd2-45eb-bd04-f4611ae920d2 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_9\_GICR\_ICFGR0\_1 | | | ro | ro | | 0xAAAAAAAA | | |  | | | | | |

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| 1.1.10.17 Redistributor\_SGI\_PPI\_2\_\_10\_GICR\_ICFGR1 | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_10\_GICR\_ICFGR1 | | | | | reg32 | | 0xD0C04 |
| offset | | 3076 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=63be5adb-5247-4fce-aadd-b995efe106f9 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_2\_\_10\_GICR\_ICFGR1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.18 Redistributor\_SGI\_PPI\_2\_\_13\_GICR\_MISCSTATUSR | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_13\_GICR\_MISCSTATUSR | | | | | reg32 | | 0xDC000 |
| offset | | 49152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=9519993d-c148-4b80-9308-02fd7173a2d7 | | | | | | | | | | | | | | | |
| GIC-500 Miscellaneous Status Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_SGI\_PPI\_2\_\_13\_GICR\_MISCSTATUSR\_\_0\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 1 | Redistributor\_SGI\_PPI\_2\_\_13\_GICR\_MISCSTATUSR\_\_1\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 2 | Redistributor\_SGI\_PPI\_2\_\_13\_GICR\_MISCSTATUSR\_\_2\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 31 | Redistributor\_SGI\_PPI\_2\_\_13\_GICR\_MISCSTATUSR\_\_31\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.10.19 Redistributor\_SGI\_PPI\_2\_\_14\_GICR\_PPISR | | | | | | | | Redistributor\_SGI\_PPI\_2\_\_14\_GICR\_PPISR | | | | | reg32 | | 0xDC080 |
| offset | | 49280 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=b1a4047e-51df-40ec-a57b-a22d08f6cc49 | | | | | | | | | | | | | | | |
| GIC-500 Private Peripheral Interrupt Status Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:16 | Redistributor\_SGI\_PPI\_2\_\_14\_GICR\_PPISR\_\_16\_16 | | | ro | ro | | 0x0 | | |  | | | | | |

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| End RegGroup |

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| 1.1.11 Redistributor\_control\_LPI\_3 | | | | Redistributor\_control\_LPI\_3 | | | section | | 0xE0000 | |
| offset | 917504 | external |  | | repeat |  | | size | | 65536 |
|  | | | | | | | | | | |
| oid=b2849e25-b17f-4577-868f-5dfb68075cf8 | | | | | | | | | | |
| {range=65536} | | | | | | | | | | |

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| 1.1.11.1 Redistributor\_control\_LPI\_3\_\_2\_GICR\_CTLR | | | | | | | | Redistributor\_control\_LPI\_3\_\_2\_GICR\_CTLR | | | | | reg32 | | 0xE0000 |
| offset | | 0 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=0c3de805-eaee-434f-bf74-a56565c00f1b | | | | | | | | | | | | | | | |
| Re-Distributor Control Register{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_control\_LPI\_3\_\_2\_GICR\_CTLR\_\_0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 3 | Redistributor\_control\_LPI\_3\_\_2\_GICR\_CTLR\_\_3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31 | Redistributor\_control\_LPI\_3\_\_2\_GICR\_CTLR\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.2 Redistributor\_control\_LPI\_3\_\_3\_GICR\_IIDR | | | | | | | | Redistributor\_control\_LPI\_3\_\_3\_GICR\_IIDR | | | | | reg32 | | 0xE0004 |
| offset | | 4 | external | | |  | | | size | | 32 | default | | 0x0001143b | |
|  | | | | | | | | | | | | | | | |
| oid=fd804348-da13-4bb7-a951-c0c051c2cf21 | | | | | | | | | | | | | | | |
| Re-Distributor Implementer Identification Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 11:0 | Redistributor\_control\_LPI\_3\_\_3\_GICR\_IIDR\_\_0\_12 | | | ro | ro | | 0x43B | | |  | | | | | |
| 15:12 | Redistributor\_control\_LPI\_3\_\_3\_GICR\_IIDR\_\_12\_4 | | | ro | ro | | 0x1 | | |  | | | | | |
| 19:16 | Redistributor\_control\_LPI\_3\_\_3\_GICR\_IIDR\_\_16\_4 | | | ro | ro | | 0x1 | | |  | | | | | |
| 31:24 | Redistributor\_control\_LPI\_3\_\_3\_GICR\_IIDR\_\_24\_8 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.3 Redistributor\_control\_LPI\_3\_\_4\_GICR\_TYPER\_lower | | | | | | | | Redistributor\_control\_LPI\_3\_\_4\_GICR\_TYPER\_lower | | | | | reg32 | | 0xE0008 |
| offset | | 8 | external | | |  | | | size | | 32 | default | | 0x00000001 | |
|  | | | | | | | | | | | | | | | |
| oid=ce889489-7cdc-41e9-9833-4ecc5a1b9718 | | | | | | | | | | | | | | | |
| Re-Distributor Type Register (lower){access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_control\_LPI\_3\_\_4\_GICR\_TYPER\_lower\_\_0\_1 | | | ro | ro | | 0x1 | | |  | | | | | |
| 1 | Redistributor\_control\_LPI\_3\_\_4\_GICR\_TYPER\_lower\_\_1\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 3 | Redistributor\_control\_LPI\_3\_\_4\_GICR\_TYPER\_lower\_\_3\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 4 | Redistributor\_control\_LPI\_3\_\_4\_GICR\_TYPER\_lower\_\_4\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 23:8 | Redistributor\_control\_LPI\_3\_\_4\_GICR\_TYPER\_lower\_\_8\_16 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.4 Redistributor\_control\_LPI\_3\_\_5\_GICR\_TYPER\_upper | | | | | | | | Redistributor\_control\_LPI\_3\_\_5\_GICR\_TYPER\_upper | | | | | reg32 | | 0xE000C |
| offset | | 12 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=569db4f5-da09-49f5-92a7-46cf84cf2150 | | | | | | | | | | | | | | | |
| Re-Distributor Type Register (upper){access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_5\_GICR\_TYPER\_upper\_\_0\_8 | | | ro | ro | | 0x0 | | |  | | | | | |
| 15:8 | Redistributor\_control\_LPI\_3\_\_5\_GICR\_TYPER\_upper\_\_8\_8 | | | ro | ro | | 0x0 | | |  | | | | | |
| 23:16 | Redistributor\_control\_LPI\_3\_\_5\_GICR\_TYPER\_upper\_\_16\_8 | | | ro | ro | | 0x0 | | |  | | | | | |
| 31:24 | Redistributor\_control\_LPI\_3\_\_5\_GICR\_TYPER\_upper\_\_24\_8 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.5 Redistributor\_control\_LPI\_3\_\_6\_GICR\_WAKER | | | | | | | | Redistributor\_control\_LPI\_3\_\_6\_GICR\_WAKER | | | | | reg32 | | 0xE0014 |
| offset | | 20 | external | | |  | | | size | | 32 | default | | 0x00000006 | |
|  | | | | | | | | | | | | | | | |
| oid=a6d0d92a-99b5-4451-97aa-3211ca4fa1d3 | | | | | | | | | | | | | | | |
| Power Management Control Register{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_control\_LPI\_3\_\_6\_GICR\_WAKER\_\_0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |
| 1 | Redistributor\_control\_LPI\_3\_\_6\_GICR\_WAKER\_\_1\_1 | | | rw | ro | | 0x1 | | |  | | | | | |
| 2 | Redistributor\_control\_LPI\_3\_\_6\_GICR\_WAKER\_\_2\_1 | | | rw | ro | | 0x1 | | |  | | | | | |
| 31 | Redistributor\_control\_LPI\_3\_\_6\_GICR\_WAKER\_\_31\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.6 Redistributor\_control\_LPI\_3\_\_7\_GICR\_PROPBASER\_lower | | | | | | | | Redistributor\_control\_LPI\_3\_\_7\_GICR\_PROPBASER\_lower | | | | | reg32 | | 0xE0070 |
| offset | | 112 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=94c35ff3-38f1-4f62-8cc0-6c0ba4b72db8 | | | | | | | | | | | | | | | |
| LPI Configuration Table Control Register (lower){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 4:0 | Redistributor\_control\_LPI\_3\_\_7\_GICR\_PROPBASER\_lower\_\_0\_5 | | | rw | ro | | 0x0 | | |  | | | | | |
| 9:7 | Redistributor\_control\_LPI\_3\_\_7\_GICR\_PROPBASER\_lower\_\_7\_3 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31:12 | Redistributor\_control\_LPI\_3\_\_7\_GICR\_PROPBASER\_lower\_\_12\_20 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.7 Redistributor\_control\_LPI\_3\_\_8\_GICR\_PROPBASER\_upper | | | | | | | | Redistributor\_control\_LPI\_3\_\_8\_GICR\_PROPBASER\_upper | | | | | reg32 | | 0xE0074 |
| offset | | 116 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1e613399-2aae-4cfc-85e2-5edd3403964d | | | | | | | | | | | | | | | |
| LPI Configuration Table Control Register (upper){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | Redistributor\_control\_LPI\_3\_\_8\_GICR\_PROPBASER\_upper\_\_0\_16 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.8 Redistributor\_control\_LPI\_3\_\_9\_GICR\_PENDBASER\_lower | | | | | | | | Redistributor\_control\_LPI\_3\_\_9\_GICR\_PENDBASER\_lower | | | | | reg32 | | 0xE0078 |
| offset | | 120 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f2630a86-d38c-4661-b655-a8ea1723316d | | | | | | | | | | | | | | | |
| LPI Pending Table Control Register (lower){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 9:7 | Redistributor\_control\_LPI\_3\_\_9\_GICR\_PENDBASER\_lower\_\_7\_3 | | | rw | ro | | 0x0 | | |  | | | | | |
| 31:16 | Redistributor\_control\_LPI\_3\_\_9\_GICR\_PENDBASER\_lower\_\_16\_16 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.9 Redistributor\_control\_LPI\_3\_\_10\_GICR\_PENDBASER\_upper | | | | | | | | Redistributor\_control\_LPI\_3\_\_10\_GICR\_PENDBASER\_upper | | | | | reg32 | | 0xE007C |
| offset | | 124 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=fbd1f591-134c-4ef5-bfa1-c6232d2d2bf7 | | | | | | | | | | | | | | | |
| LPI Pending Table Control Register (upper){access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 15:0 | Redistributor\_control\_LPI\_3\_\_10\_GICR\_PENDBASER\_upper\_\_0\_16 | | | rw | ro | | 0x0 | | |  | | | | | |
| 30 | Redistributor\_control\_LPI\_3\_\_10\_GICR\_PENDBASER\_upper\_\_30\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.10 Redistributor\_control\_LPI\_3\_\_11\_GICR\_PIDR4 | | | | | | | | Redistributor\_control\_LPI\_3\_\_11\_GICR\_PIDR4 | | | | | reg32 | | 0xEFFD0 |
| offset | | 65488 | external | | |  | | | size | | 32 | default | | 0x00000044 | |
|  | | | | | | | | | | | | | | | |
| oid=a0756129-c54f-488d-8a4c-5c7304df2c53 | | | | | | | | | | | | | | | |
| Peripheral ID4 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_11\_GICR\_PIDR4\_1 | | | ro | ro | | 0x44 | | |  | | | | | |

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| 1.1.11.11 Redistributor\_control\_LPI\_3\_\_12\_GICR\_PIDR5 | | | | | | | | Redistributor\_control\_LPI\_3\_\_12\_GICR\_PIDR5 | | | | | reg32 | | 0xEFFD4 |
| offset | | 65492 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=85e01c80-9fbc-431d-9f80-f1b0d8a0daf8 | | | | | | | | | | | | | | | |
| Peripheral ID5 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_12\_GICR\_PIDR5\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.12 Redistributor\_control\_LPI\_3\_\_13\_GICR\_PIDR6 | | | | | | | | Redistributor\_control\_LPI\_3\_\_13\_GICR\_PIDR6 | | | | | reg32 | | 0xEFFD8 |
| offset | | 65496 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c2f8461e-05b1-43cb-b175-636ce1826f87 | | | | | | | | | | | | | | | |
| Peripheral ID6 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_13\_GICR\_PIDR6\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.13 Redistributor\_control\_LPI\_3\_\_14\_GICR\_PIDR7 | | | | | | | | Redistributor\_control\_LPI\_3\_\_14\_GICR\_PIDR7 | | | | | reg32 | | 0xEFFDC |
| offset | | 65500 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=90c2ffb3-315d-4e22-b1d1-272bc9517067 | | | | | | | | | | | | | | | |
| Peripheral ID7 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_14\_GICR\_PIDR7\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.14 Redistributor\_control\_LPI\_3\_\_15\_GICR\_PIDR0 | | | | | | | | Redistributor\_control\_LPI\_3\_\_15\_GICR\_PIDR0 | | | | | reg32 | | 0xEFFE0 |
| offset | | 65504 | external | | |  | | | size | | 32 | default | | 0x00000093 | |
|  | | | | | | | | | | | | | | | |
| oid=9ee0aab6-ea86-4ad6-80d0-15ebc6505b26 | | | | | | | | | | | | | | | |
| Peripheral ID0 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_15\_GICR\_PIDR0\_1 | | | ro | ro | | 0x93 | | |  | | | | | |

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| 1.1.11.15 Redistributor\_control\_LPI\_3\_\_16\_GICR\_PIDR1 | | | | | | | | Redistributor\_control\_LPI\_3\_\_16\_GICR\_PIDR1 | | | | | reg32 | | 0xEFFE4 |
| offset | | 65508 | external | | |  | | | size | | 32 | default | | 0x000000b4 | |
|  | | | | | | | | | | | | | | | |
| oid=088371ac-aa24-409b-80b9-297129ab8863 | | | | | | | | | | | | | | | |
| Peripheral ID1 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_16\_GICR\_PIDR1\_1 | | | ro | ro | | 0xB4 | | |  | | | | | |

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| 1.1.11.16 Redistributor\_control\_LPI\_3\_\_17\_GICR\_PIDR2 | | | | | | | | Redistributor\_control\_LPI\_3\_\_17\_GICR\_PIDR2 | | | | | reg32 | | 0xEFFE8 |
| offset | | 65512 | external | | |  | | | size | | 32 | default | | 0x0000003b | |
|  | | | | | | | | | | | | | | | |
| oid=3e9e6dba-31cb-4c6b-82ab-9da5c396e71e | | | | | | | | | | | | | | | |
| Peripheral ID2 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_17\_GICR\_PIDR2\_1 | | | ro | ro | | 0x3B | | |  | | | | | |

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| 1.1.11.17 Redistributor\_control\_LPI\_3\_\_18\_GICR\_PIDR3 | | | | | | | | Redistributor\_control\_LPI\_3\_\_18\_GICR\_PIDR3 | | | | | reg32 | | 0xEFFEC |
| offset | | 65516 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=1396fb24-154f-44b4-b362-10df2d9e0a66 | | | | | | | | | | | | | | | |
| Peripheral ID3 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_18\_GICR\_PIDR3\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.11.18 Redistributor\_control\_LPI\_3\_\_19\_GICR\_CIDR0 | | | | | | | | Redistributor\_control\_LPI\_3\_\_19\_GICR\_CIDR0 | | | | | reg32 | | 0xEFFF0 |
| offset | | 65520 | external | | |  | | | size | | 32 | default | | 0x0000000d | |
|  | | | | | | | | | | | | | | | |
| oid=89fb26b8-9723-483a-952a-9bdc88855c8b | | | | | | | | | | | | | | | |
| Component ID0 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_19\_GICR\_CIDR0\_1 | | | ro | ro | | 0xD | | |  | | | | | |

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| 1.1.11.19 Redistributor\_control\_LPI\_3\_\_20\_GICR\_CIDR1 | | | | | | | | Redistributor\_control\_LPI\_3\_\_20\_GICR\_CIDR1 | | | | | reg32 | | 0xEFFF4 |
| offset | | 65524 | external | | |  | | | size | | 32 | default | | 0x000000f0 | |
|  | | | | | | | | | | | | | | | |
| oid=f8b648fa-ac4e-4e43-9914-b8b08dfab975 | | | | | | | | | | | | | | | |
| Component ID1 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_20\_GICR\_CIDR1\_1 | | | ro | ro | | 0xF0 | | |  | | | | | |

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| 1.1.11.20 Redistributor\_control\_LPI\_3\_\_21\_GICR\_CIDR2 | | | | | | | | Redistributor\_control\_LPI\_3\_\_21\_GICR\_CIDR2 | | | | | reg32 | | 0xEFFF8 |
| offset | | 65528 | external | | |  | | | size | | 32 | default | | 0x00000005 | |
|  | | | | | | | | | | | | | | | |
| oid=3b79ce7a-9bf3-4942-9aa4-ef8805879e18 | | | | | | | | | | | | | | | |
| Component ID2 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_21\_GICR\_CIDR2\_1 | | | ro | ro | | 0x5 | | |  | | | | | |

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| 1.1.11.21 Redistributor\_control\_LPI\_3\_\_22\_GICR\_CIDR3 | | | | | | | | Redistributor\_control\_LPI\_3\_\_22\_GICR\_CIDR3 | | | | | reg32 | | 0xEFFFC |
| offset | | 65532 | external | | |  | | | size | | 32 | default | | 0x000000b1 | |
|  | | | | | | | | | | | | | | | |
| oid=fe713ddc-ec73-4d9c-82e1-5d175a46fff3 | | | | | | | | | | | | | | | |
| Component ID3 Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 7:0 | Redistributor\_control\_LPI\_3\_\_22\_GICR\_CIDR3\_1 | | | ro | ro | | 0xB1 | | |  | | | | | |

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| --- |
| End RegGroup |

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| 1.1.12 Redistributor\_SGI\_PPI\_3 | | | | Redistributor\_SGI\_PPI\_3 | | | section | | 0xF0000 | |
| offset | 983040 | external |  | | repeat |  | | size | | 49284 |
|  | | | | | | | | | | |
| oid=c82a7029-c7f7-4d60-a50e-6f9c71f4dab8 | | | | | | | | | | |
| {range=65536} | | | | | | | | | | |

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| 1.1.12.1 Redistributor\_SGI\_PPI\_3\_\_1\_GICR\_IGROUPR0 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_1\_GICR\_IGROUPR0 | | | | | reg32 | | 0xF0080 |
| offset | | 128 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=e23418c6-c991-4fba-8dfc-2cd44fbac3dd | | | | | | | | | | | | | | | |
| Interrupt Group Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_1\_GICR\_IGROUPR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.2 Redistributor\_SGI\_PPI\_3\_\_2\_GICR\_ISENABLER0 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_2\_GICR\_ISENABLER0 | | | | | reg32 | | 0xF0100 |
| offset | | 256 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=68665291-e96f-4b86-a81a-eec192a21de2 | | | | | | | | | | | | | | | |
| Interrupt Set-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_2\_GICR\_ISENABLER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.3 Redistributor\_SGI\_PPI\_3\_\_3\_GICR\_ICENABLER0 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_3\_GICR\_ICENABLER0 | | | | | reg32 | | 0xF0180 |
| offset | | 384 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=06a1d0fe-043d-407a-9e58-47c382fa770e | | | | | | | | | | | | | | | |
| Interrupt Clear-Enable Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_3\_GICR\_ICENABLER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.4 Redistributor\_SGI\_PPI\_3\_\_4\_GICR\_ISPENDR0 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_4\_GICR\_ISPENDR0 | | | | | reg32 | | 0xF0200 |
| offset | | 512 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d01591f5-02a1-4ef1-a430-f824a230fbd7 | | | | | | | | | | | | | | | |
| Interrupt Set-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_4\_GICR\_ISPENDR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.5 Redistributor\_SGI\_PPI\_3\_\_5\_GICR\_ICPENDR0 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_5\_GICR\_ICPENDR0 | | | | | reg32 | | 0xF0280 |
| offset | | 640 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=f7dd40d9-dd36-4f01-98a8-99af1f117ee7 | | | | | | | | | | | | | | | |
| Interrupt Clear-Pending Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_5\_GICR\_ICPENDR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.6 Redistributor\_SGI\_PPI\_3\_\_6\_GICR\_ISACTIVER0 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_6\_GICR\_ISACTIVER0 | | | | | reg32 | | 0xF0300 |
| offset | | 768 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=35e78d10-91f9-4190-8b82-691f6b29dab7 | | | | | | | | | | | | | | | |
| Interrupt Set-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_6\_GICR\_ISACTIVER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.7 Redistributor\_SGI\_PPI\_3\_\_7\_GICR\_ICACTIVER0 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_7\_GICR\_ICACTIVER0 | | | | | reg32 | | 0xF0380 |
| offset | | 896 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d95feb55-f9c7-4aa7-b0b2-47c5334649bb | | | | | | | | | | | | | | | |
| Interrupt Clear-Active Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_7\_GICR\_ICACTIVER0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.8 Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR0 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR0 | | | | | reg32 | | 0xF0400 |
| offset | | 1024 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ee748271-cb3e-4dd9-b2ee-1132fee12392 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR0\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.9 Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR1 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR1 | | | | | reg32 | | 0xF0404 |
| offset | | 1028 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=284cedac-21e1-4fee-b087-11a666145aef | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.10 Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR2 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR2 | | | | | reg32 | | 0xF0408 |
| offset | | 1032 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=09541dd3-ec6c-48e5-b24d-d17bf6d6b710 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR2\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.11 Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR3 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR3 | | | | | reg32 | | 0xF040C |
| offset | | 1036 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ae343195-a8f2-41f0-9468-32eae85a008f | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR3\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.12 Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR4 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR4 | | | | | reg32 | | 0xF0410 |
| offset | | 1040 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=8246e976-d20e-46a3-9c8a-f089d6c7316b | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR4\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.13 Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR5 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR5 | | | | | reg32 | | 0xF0414 |
| offset | | 1044 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=d818f555-ff1b-4a1f-8f74-00076208f288 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR5\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.14 Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR6 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR6 | | | | | reg32 | | 0xF0418 |
| offset | | 1048 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ba16876a-b5d9-4520-b1a8-ecfc4b4fca96 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR6\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.15 Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR7 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR7 | | | | | reg32 | | 0xF041C |
| offset | | 1052 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=c6d404df-8764-4c4d-84d9-68cc4d352848 | | | | | | | | | | | | | | | |
| Interrupt Priority Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_8\_GICR\_IPRIORITYR7\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.16 Redistributor\_SGI\_PPI\_3\_\_9\_GICR\_ICFGR0 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_9\_GICR\_ICFGR0 | | | | | reg32 | | 0xF0C00 |
| offset | | 3072 | external | | |  | | | size | | 32 | default | | 0xaaaaaaaa | |
|  | | | | | | | | | | | | | | | |
| oid=53f3d6fa-7023-4513-8f6f-2a7b8490a02a | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_9\_GICR\_ICFGR0\_1 | | | ro | ro | | 0xAAAAAAAA | | |  | | | | | |

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| 1.1.12.17 Redistributor\_SGI\_PPI\_3\_\_10\_GICR\_ICFGR1 | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_10\_GICR\_ICFGR1 | | | | | reg32 | | 0xF0C04 |
| offset | | 3076 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=ffc27950-a1d7-40c1-90f8-191fb27f2f03 | | | | | | | | | | | | | | | |
| Interrupt Configuration Registers{access=read-write} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:0 | Redistributor\_SGI\_PPI\_3\_\_10\_GICR\_ICFGR1\_1 | | | rw | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.18 Redistributor\_SGI\_PPI\_3\_\_13\_GICR\_MISCSTATUSR | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_13\_GICR\_MISCSTATUSR | | | | | reg32 | | 0xFC000 |
| offset | | 49152 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=14ec9a7e-f7aa-425a-bd79-37fc137b6843 | | | | | | | | | | | | | | | |
| GIC-500 Miscellaneous Status Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0 | Redistributor\_SGI\_PPI\_3\_\_13\_GICR\_MISCSTATUSR\_\_0\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 1 | Redistributor\_SGI\_PPI\_3\_\_13\_GICR\_MISCSTATUSR\_\_1\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 2 | Redistributor\_SGI\_PPI\_3\_\_13\_GICR\_MISCSTATUSR\_\_2\_1 | | | ro | ro | | 0x0 | | |  | | | | | |
| 31 | Redistributor\_SGI\_PPI\_3\_\_13\_GICR\_MISCSTATUSR\_\_31\_1 | | | ro | ro | | 0x0 | | |  | | | | | |

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| 1.1.12.19 Redistributor\_SGI\_PPI\_3\_\_14\_GICR\_PPISR | | | | | | | | Redistributor\_SGI\_PPI\_3\_\_14\_GICR\_PPISR | | | | | reg32 | | 0xFC080 |
| offset | | 49280 | external | | |  | | | size | | 32 | default | | 0x00000000 | |
|  | | | | | | | | | | | | | | | |
| oid=6218d0ef-8218-4d2a-bf23-7e56684d0815 | | | | | | | | | | | | | | | |
| GIC-500 Private Peripheral Interrupt Status Register{access=read-only} | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 31:16 | Redistributor\_SGI\_PPI\_3\_\_14\_GICR\_PPISR\_\_16\_16 | | | ro | ro | | 0x0 | | |  | | | | | |

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| End RegGroup |

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