

## Protection

→ memory protection

→ base/bound registers

CPU protection → privileged mode



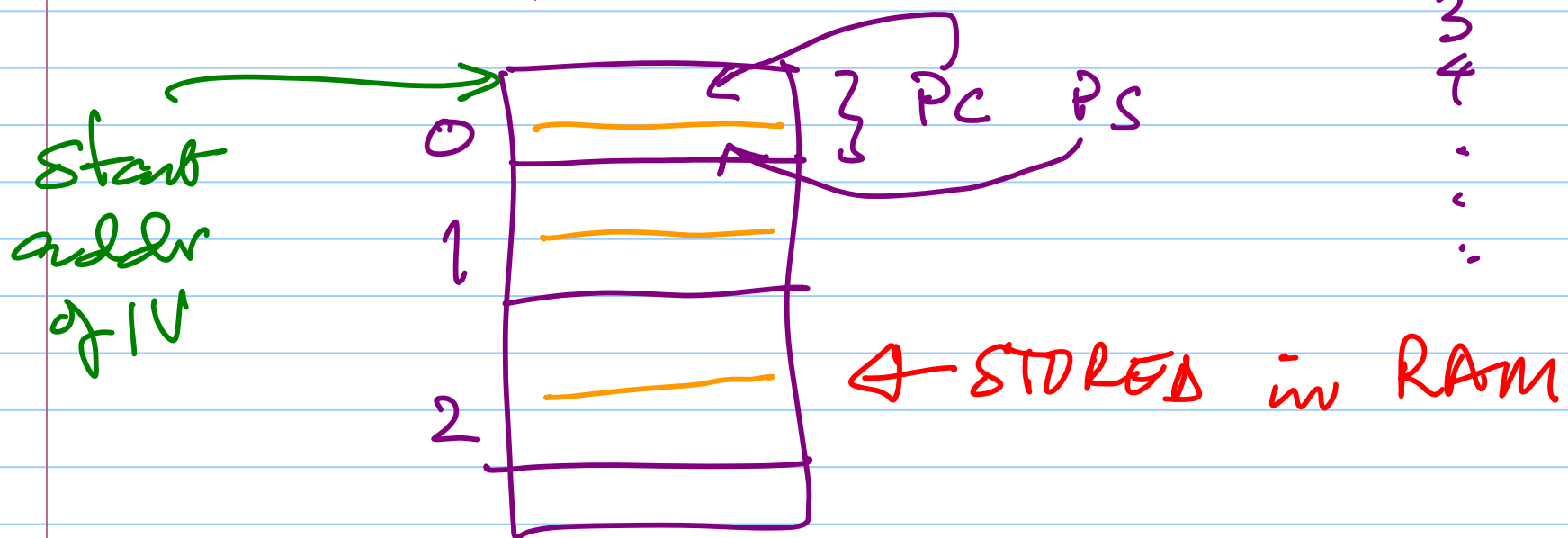
# Interrupts

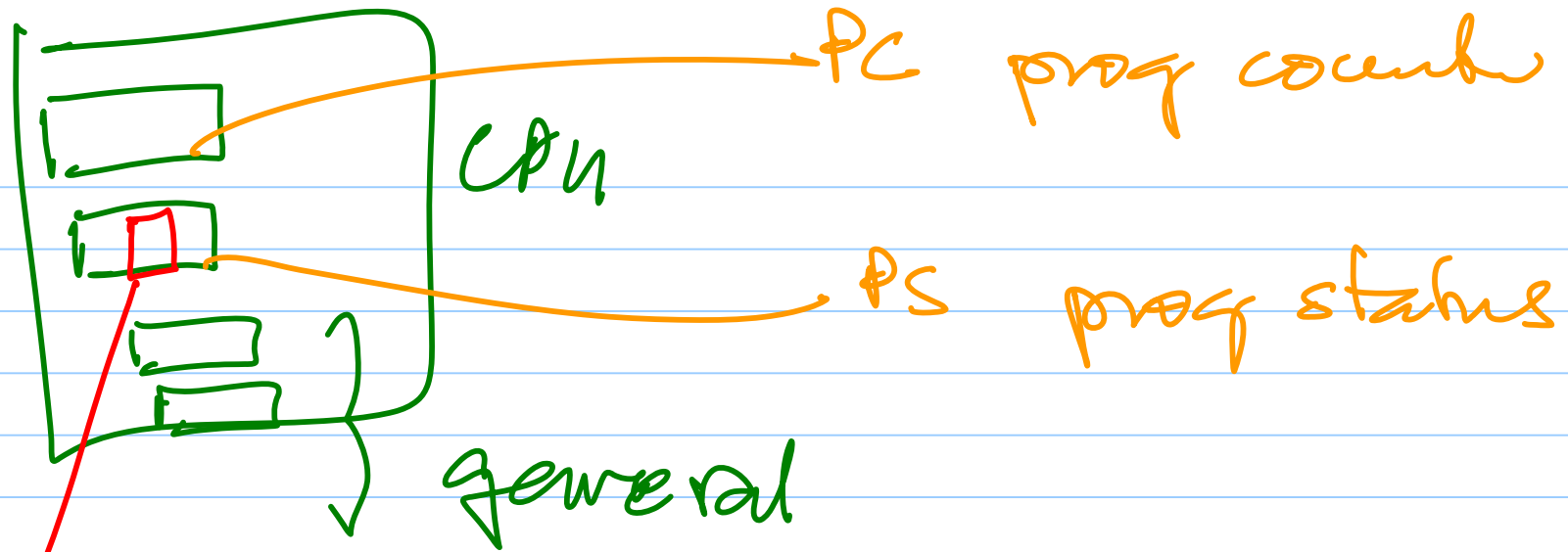
→ "external" → generated by hardware

→ "internal" → generated by software

Interrupts are numbered  $\rightarrow$  0

Interrupt Vector  $\rightarrow$  IV





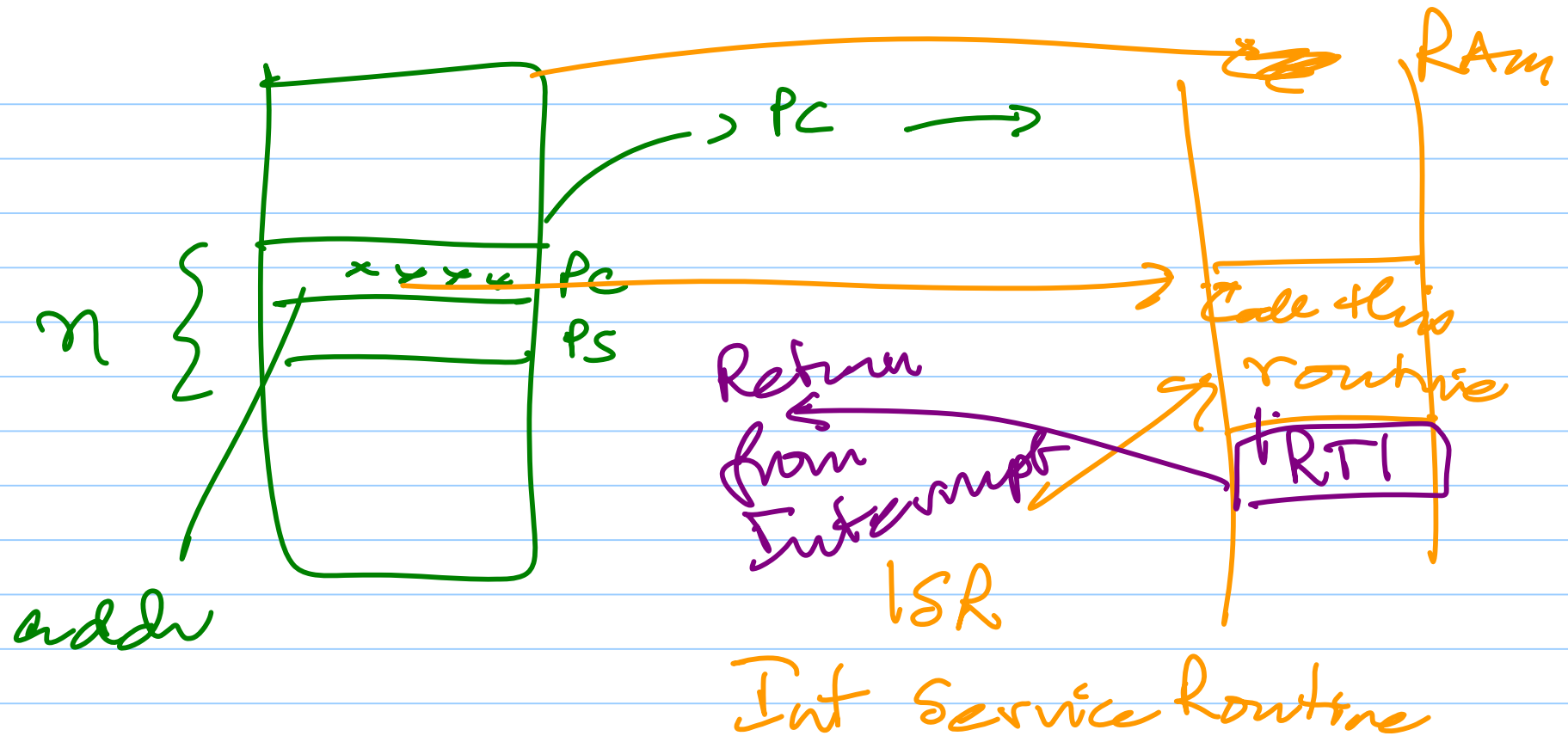
MODE BIT - 0 user

- 1 privileged

→ Interrupt no 'n'

CPU → locate  $IV[n]$

{ push PC on the stack  
" PS " " "  
move  $IV[n].PC \rightarrow PC$   
move  $IV[n].PS \rightarrow PS$



RTI → load PS from  
stack

(pop)

load PC from  
stack

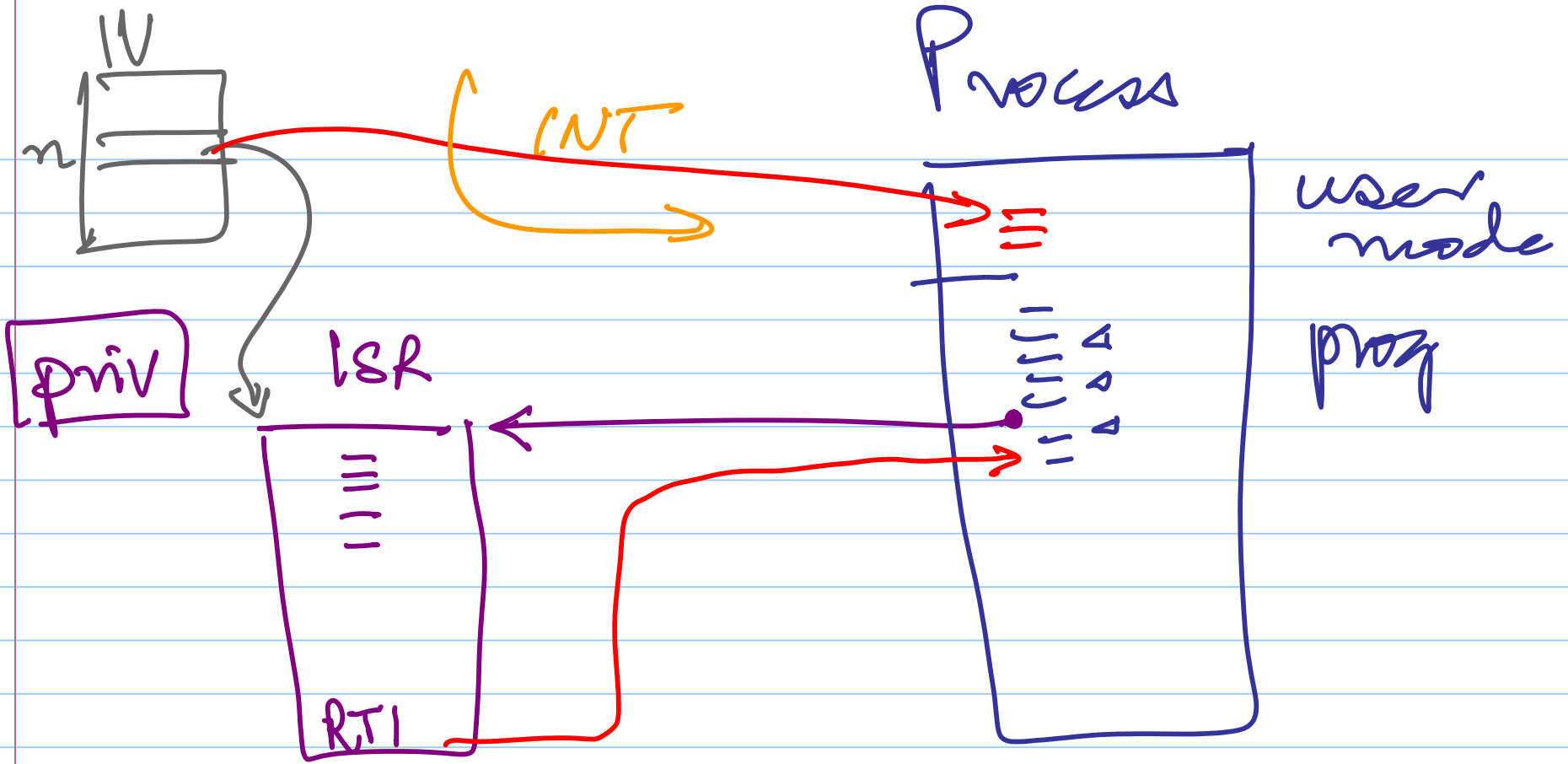
(pop)

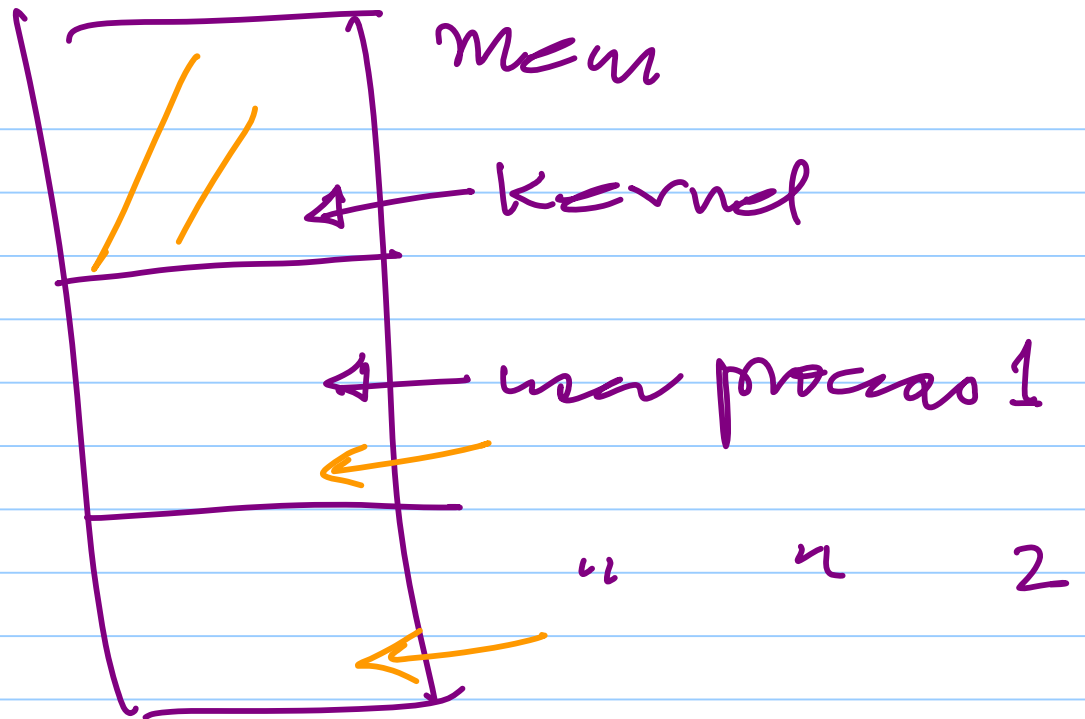
## CPU protection

↳ put a 1 on all mode bits of the IV

→ start a user process with CPU mode bit set to 0







kernel → resident code

→ core part of OS

→ fixed memory location

run in  
priv mode

Contains: IV, all ISR

code, static data (stack  
heap)

protected  
using mem  
prot

## System calls

→ how to write some data  
on ... monitor  
screen  
file etc

```
printf( "Hello world" )
```

↳ library routine

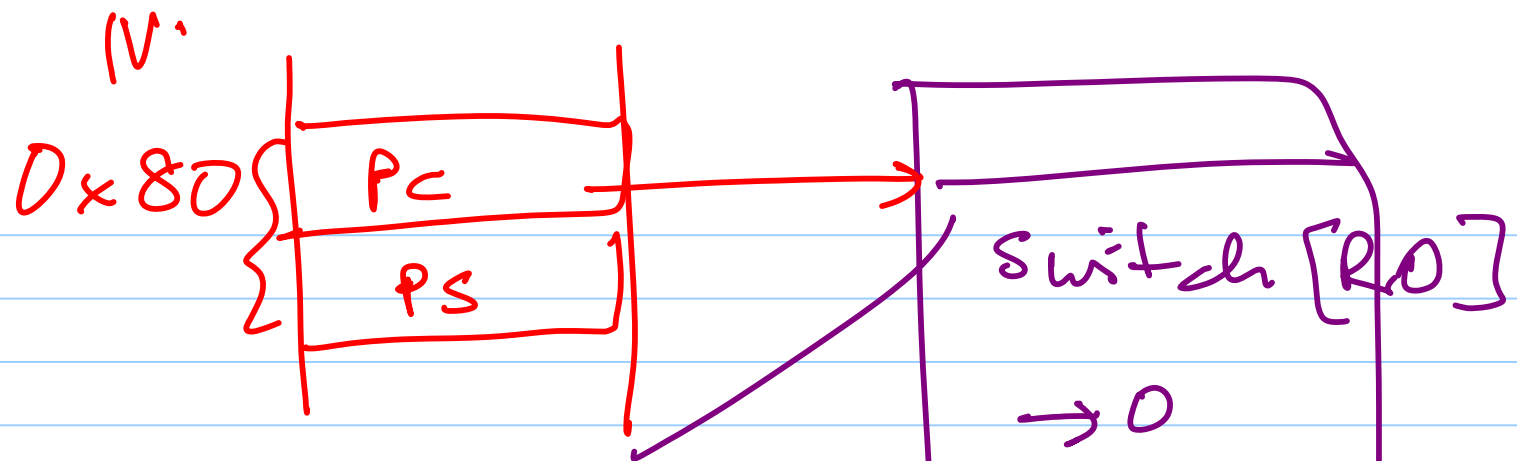
{ puts "hello world" into  
mem & gets a ptr  
& move ptr to R1

puts a syscall code for  
write into R0

printf { sets up R0, R1

INT 0x80 → generates  
an interrupt  
numbered 0x80

←  
Call to the ISR x80



syscall  
routine

→ 3 call write

write() {  
    ↑  
kernel  
routine  
    ↓  
priv mode  
    ≡ finds output  
        device  
    ≡ sets up output  
        ↳ use device  
            driver  
    ≡ // done  
}



