Software Engineering using Formal Methods

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Disclaimer

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Motivation

1 Modeling & Model Checking with Promela & Spin

1.1 PROMELA Introduction

	1.	.2	Verify	/ing	with	SPIN
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1.3	Modeling	g Concurrency
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1.4	Introduction	to Promela/	'SPIN

1.	5	Mode	elina	Dist	ribi	ution
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1.6 Propositional Logic & Temporal Logic (1)

1.7	Temporal	Logic ((2)
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1.8 Channels & Linear Temporal Logic

1.9	1.9 Temporal Model Checking with Spin	

2 Modeling & Verification with JML & KEY

2.1 First-Order Logic (Syntax and Semantics)

2.2	First-	Order	Logic -	· Calcu	lus
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2.3 JML (1)

2.4 JML (2)

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2.5	DVn	amic	LOO	IIC	1
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2.6	Dyna	mic L	ogic	Cal	lcul	us
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2.7	Proof	-Oblic	ations
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2.8	Loop	Invariant	ïS