

# PICO-PI-GL REV:B1

L=6, 85 x 56 mm

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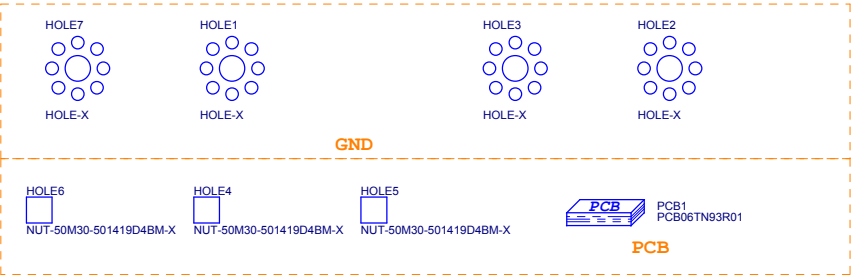
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## WANDBOARD DESIGN AND DISCLAIMER

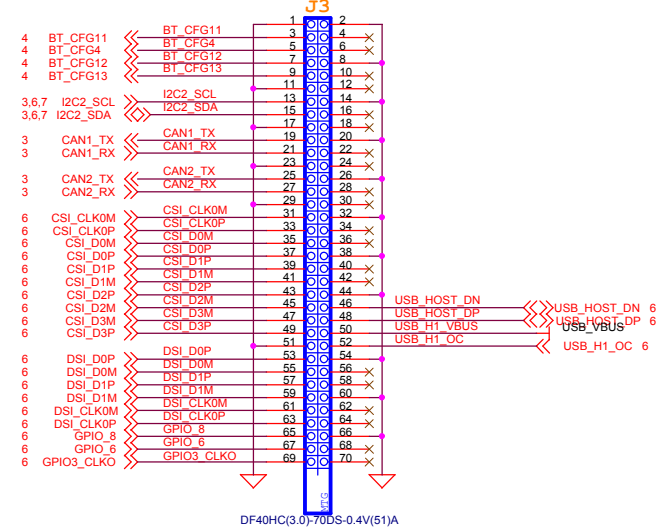
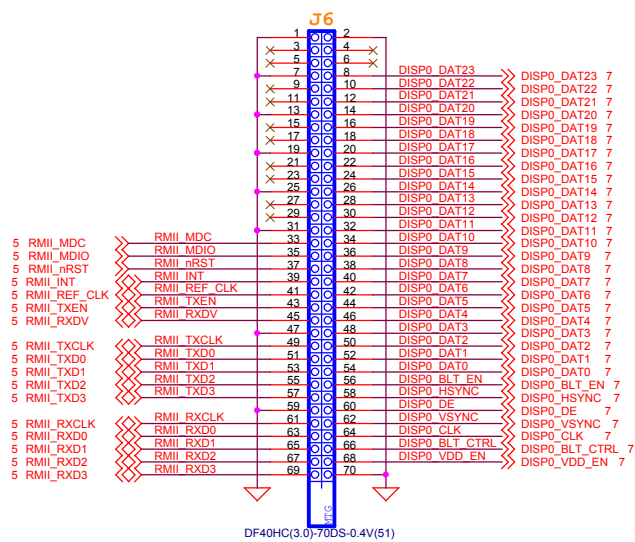
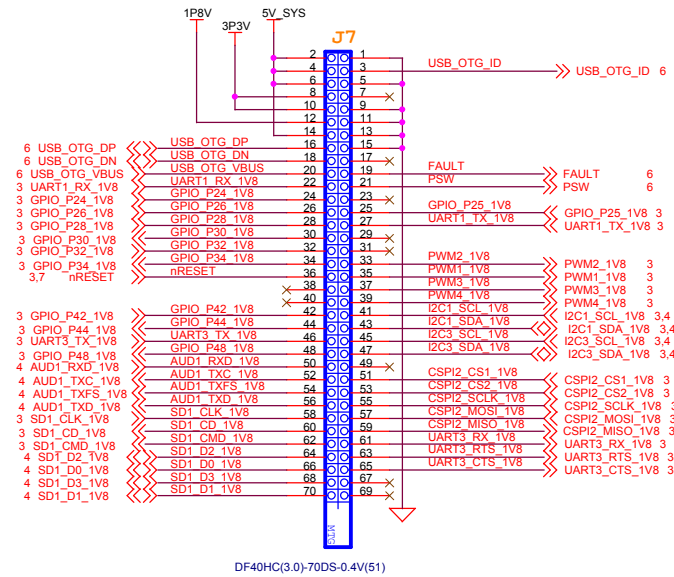
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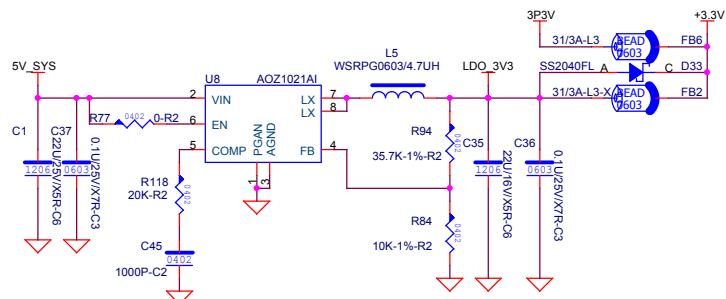
Components marked -X are not populated (DNP)

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Board to Board

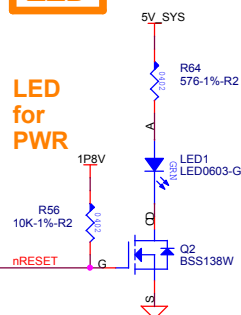


## DC-DC

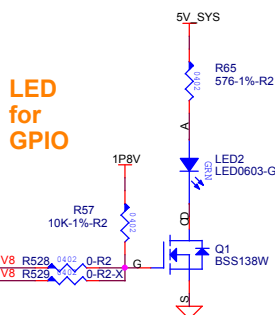


## LED

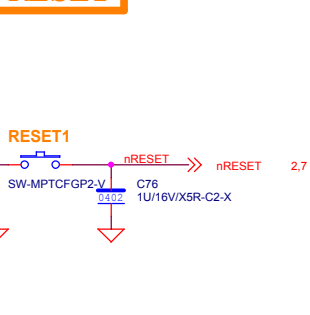
## LED for PWR



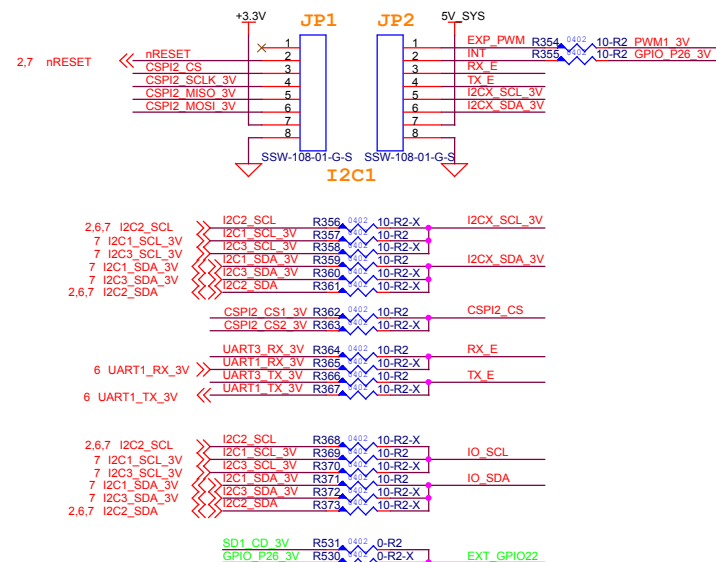
## LED for GPIO



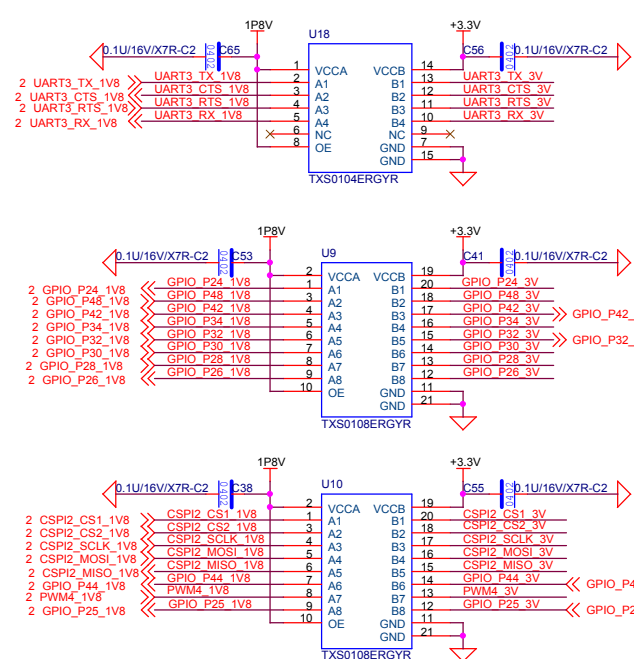
## RESET



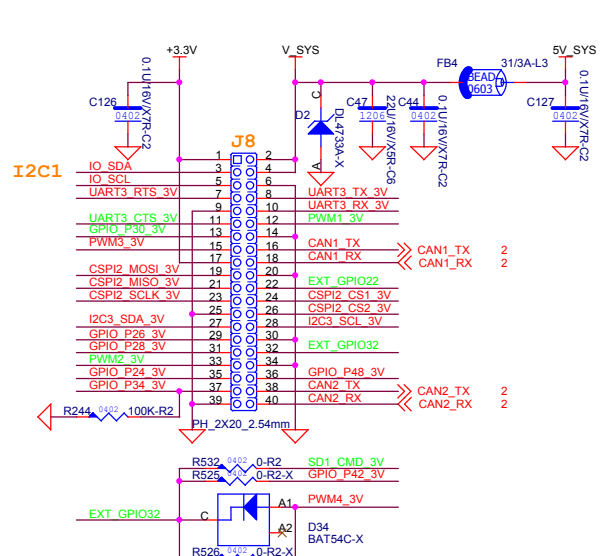
## MIKROBUS



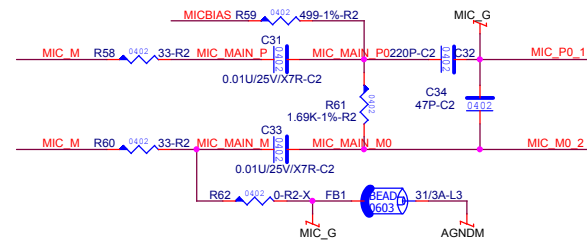
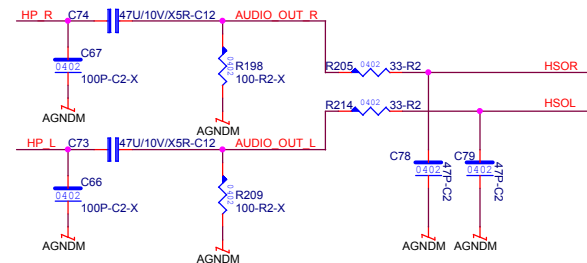
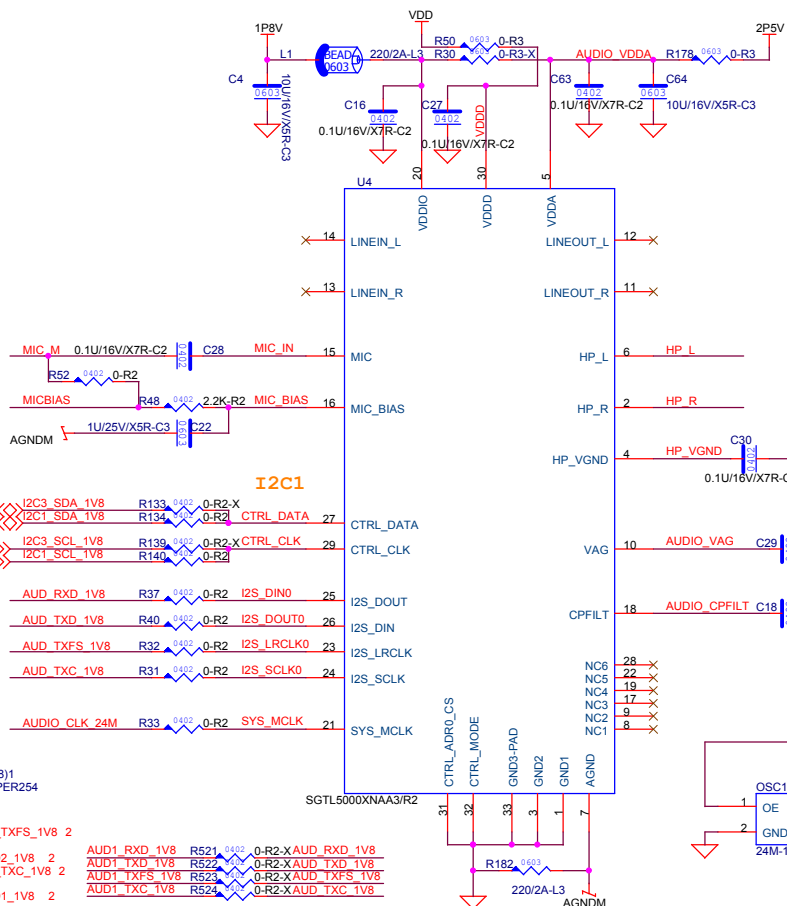
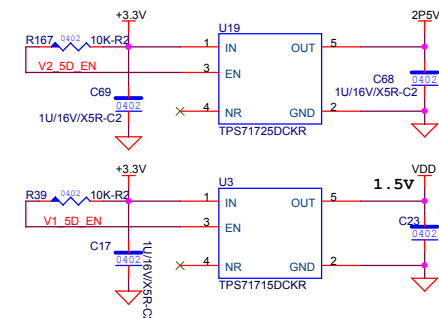
## LEVEL SHIFT



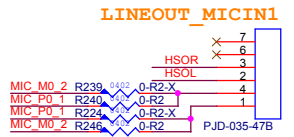
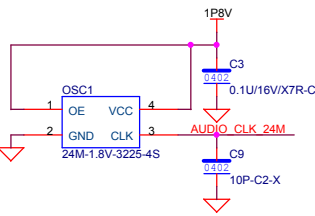
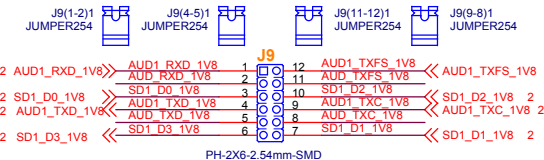
## EXPANSION IO



## AUDIO



IMX7 I2S BUS SETTING	
J9	PIN
I2S1	(1-2), (4-5), (11-12), (9-8)
I2S3	(2-3), (5-6), (11-10), (8-7)



## BOOT HEADER

### I2C LIST

BASEBoard	IMX6	IMX6UL/ULL	IMX7
I2C1	I2C1	I2C2	I2C1
I2C3	I2C2	I2C3	I2C2
I2C2	I2C3	I2C1	I2C4
I2C1	I2C3	I2C2	I2C2
I2C2	I2C3	I2C1	I2C1
I2C1	I2C2	I2C4	I2C4
AUDIO			CAMERA
MIKROBUS			TOUCH
EXPANSION			

### IMX6/IMX6QPOP/IMX7 BOOT Jumper Settings

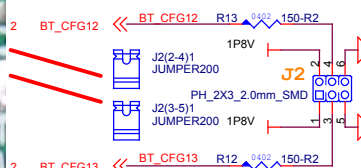
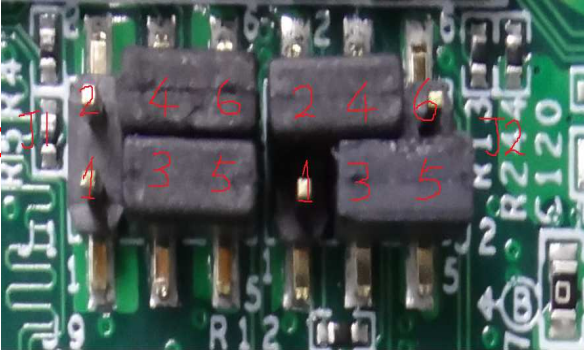
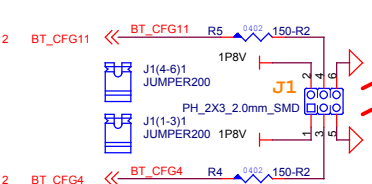
	J1	J2
SD (Module)	2-4	2-4
eMMC	2-4	4-6
Serial Downloader	4-6	2-4
	J1	J2
SD (Module)	3-5	3-5
eMMC	1-3	1-3
Serial Downloader	3-5	1-3

### IMX6UL/ULL-eMMC BOOT Jumper Settings

MODE0	MODE1	BMODE[0:1]
J1	J1	
2-4	3-5	Serial Downloader
4-6	1-3	Internal Boot

### IMX6UL/ULL-NAND BOOT Jumper Settings

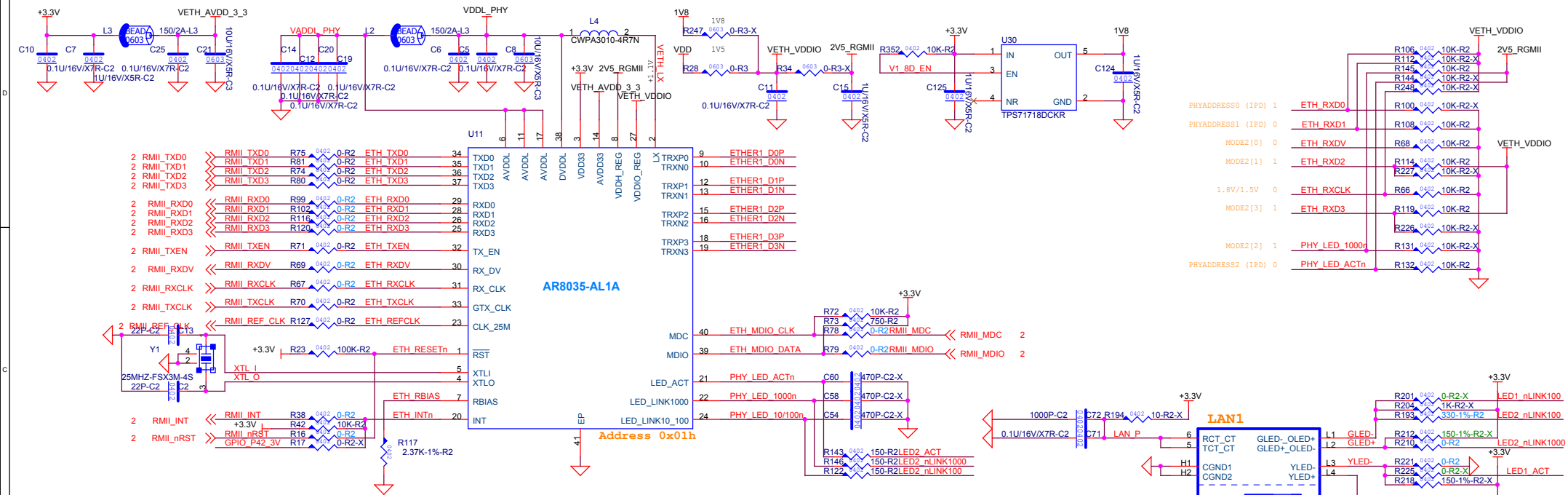
	J1	J2
NAND	2-4	2-4
	J1	J2
NAND	3-5	3-5



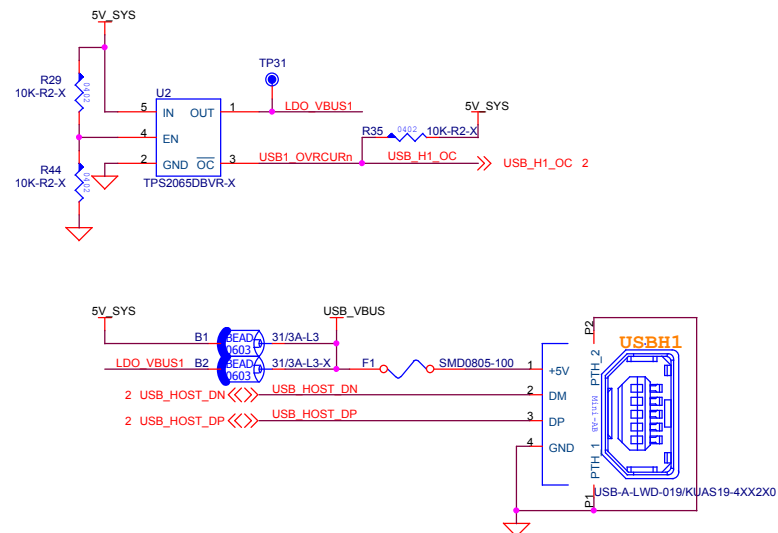
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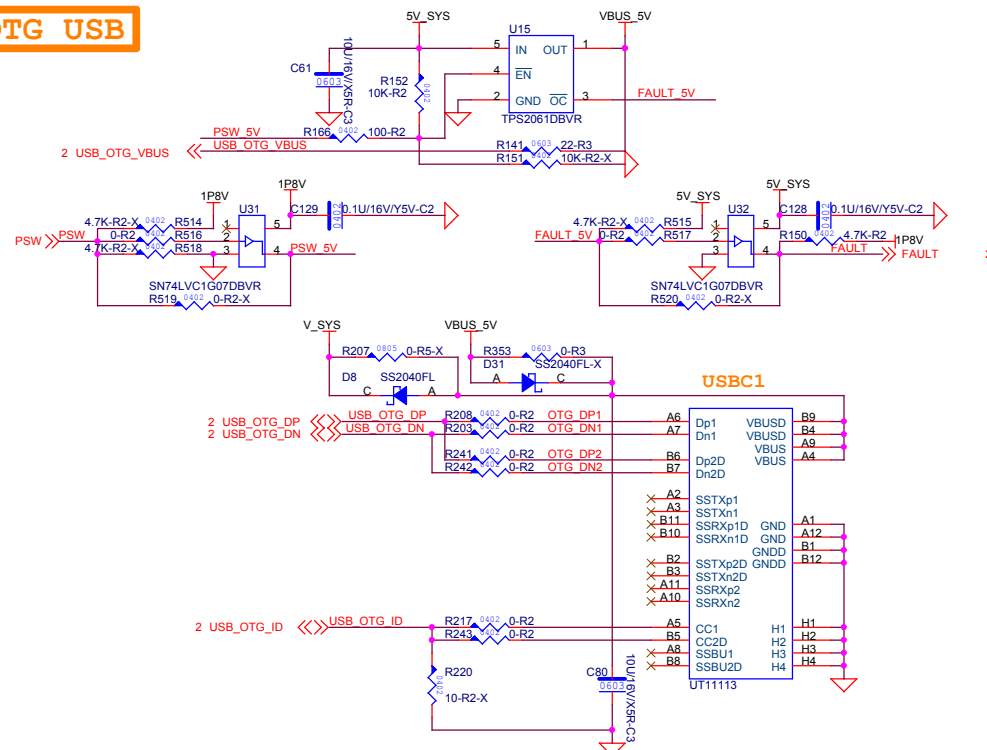
# RGMI I



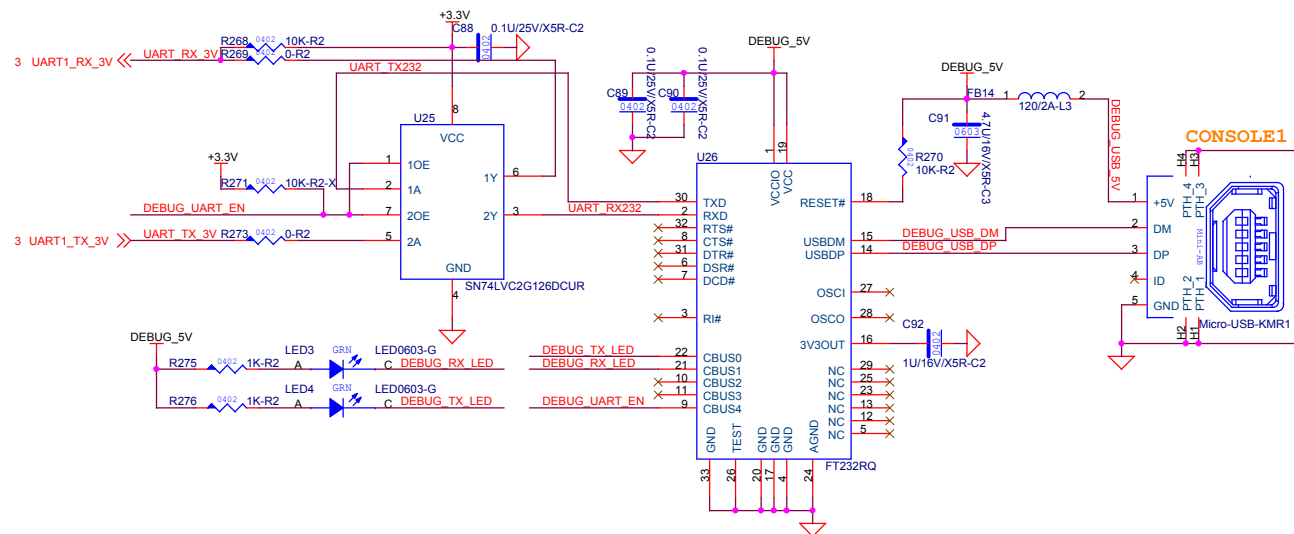
HOST USB



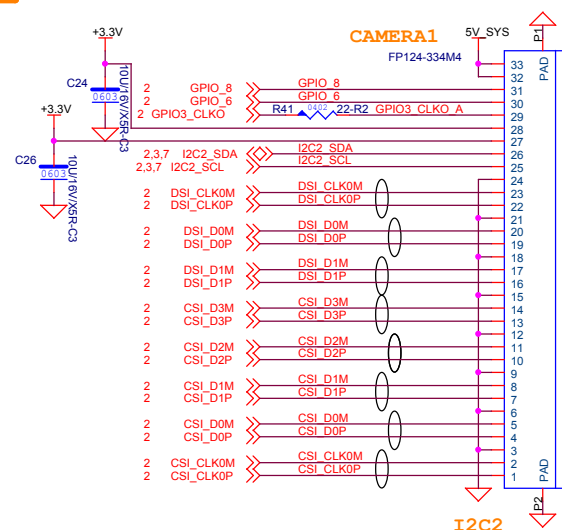
OTG USB



## CONSOLE

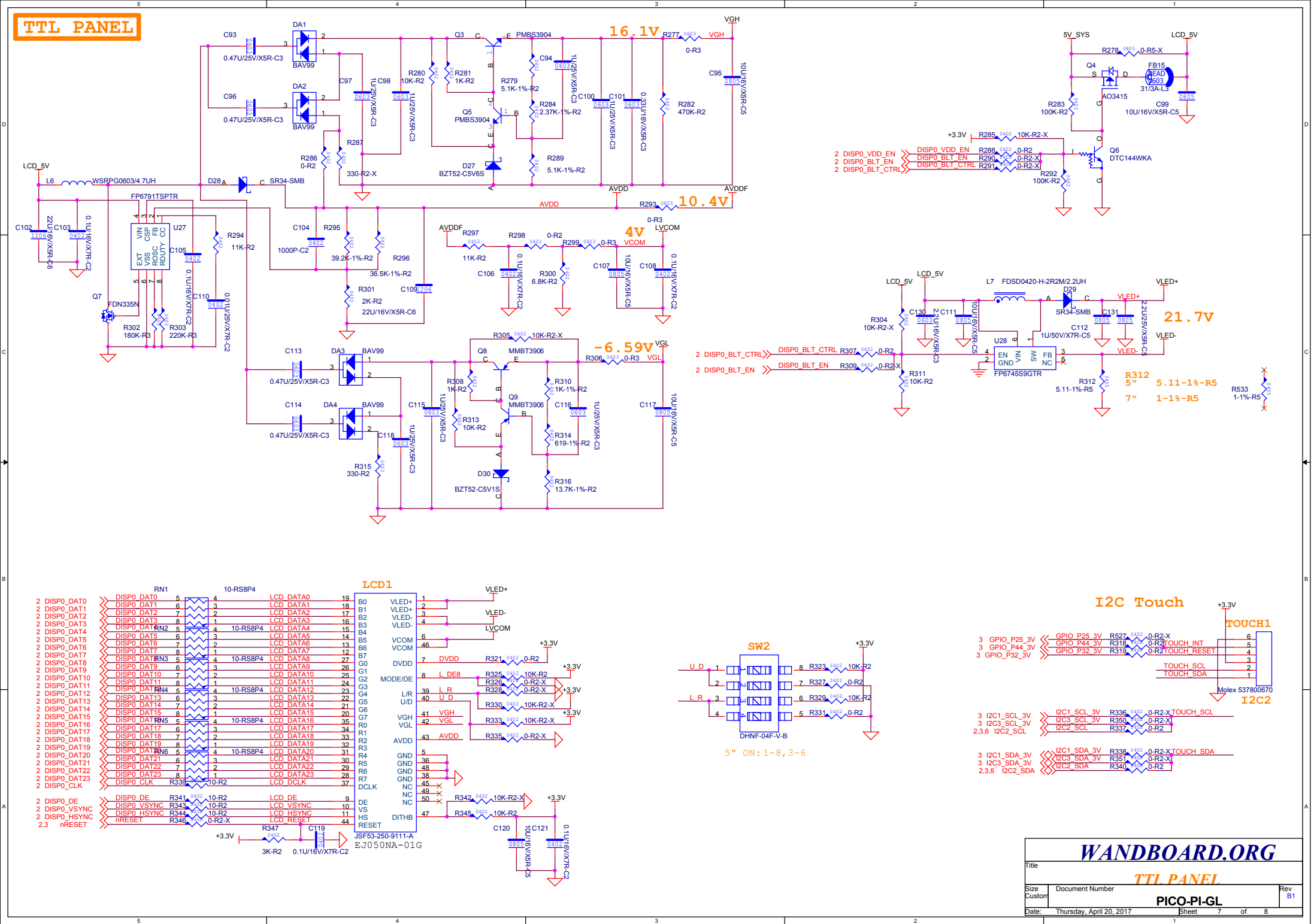


## CAMERA





## TTL PANEL



**WANDBOARD.ORG**

## TTL PANEL

### PICO-PI-GL

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EXPANSION CONN MAPPING TABLE						
JP1	PIN DEFINE	LEVEL	IMX7	CPU MAPPING	IMX6UL	CPU MAPPING
JP1 PIN1	NC	NC				
JP1 PIN2	RESET	3.3V				
JP1 PIN3	CSP12 CS1 3V	3.3V	K3	ECSP13.SS0	J17	ECSP13.SS0
JP1 PIN4	CSP12 SCLK 3V	3.3V	K2	ECSP13.SCLK	J16	ECSP13.SCLK
JP1 PIN5	CSP12 MISO 3V	3.3V	J2	ECSP13.MISO	J15	ECSP13.MISO
JP1 PIN6	CSP12 MOSI 3V	3.3V	K1	ECSP13.MOSI	H14	ECSP13.MOSI
JP1 PIN7	3.3V	3.3V				
JP1 PIN8	GND	GND				
JP2	PIN DEFINE	LEVEL	IMX7	CPU MAPPING	IMX6UL	CPU MAPPING
JP2 PIN1	EXT PWM	3.3V	R1	PWM1.OUT	F14	PWM7.OUT
JP2 PIN2	INT	3.3V	P21	GPIO2.I01	E4	GPIO4.I021
JP2 PIN3	RX E	3.3V	M23	UART6.RX	H16	UART3.RX
JP2 PIN4	TX E	3.3V	L25	UART6.TX	H17	UART3.TX
JP2 PIN5	I2CX_SCL 3V	3.3V	L3	I2C1.SCL	F17	I2C2.SCL
JP2 PIN6	I2CX_SDA 3V	3.3V	L4	I2C1.SDA	G13	I2C2.SDA
JP2 PIN7	5V	5V				
JP2 PIN8	GND	GND				
J8	PIN DEFINE	LEVEL	IMX7	CPU MAPPING	IMX6UL	CPU MAPPING
J8 PIN1	3.3V	3.3V				
J8 PIN2	5V	5V				
J8 PIN3	IO_SDA	3.3V	L4	I2C1.SDA	G13	I2C2.SDA
J8 PIN4	5V	5V				
J8 PIN5	IO_SCL	3.3V	L3	I2C1.SCL	F17	I2C2.SCL
J8 PIN6	GND	GND				
J8 PIN7	UART3_RTS 3V	3.3V	L24	UART6.RTS	G14	UART3.RTS
J8 PIN8	UART3_TX 3V	3.3V	L25	UART6.TX	H17	UART3.TX
J8 PIN9	GND	GND				
J8 PIN10	UART3_RX 3V	3.3V	M23	UART6.RX	H16	UART3.RX
J8 PIN11	UART3_CTS 3V	3.3V	L23	UART6.CTS	H15	UART3.CTS
J8 PIN12	PWM1 3V	3.3V	R1	PWM1.OUT	F14	PWM7.OUT
J8 PIN13	GPIO_P30 3V	3.3V	N21	GPIO2.I03	E2	GPIO4.I023
J8 PIN14	GND	GND				
J8 PIN15	PWM3 3V	3.3V	R5	PWM3.OUT	NC	NC
J8 PIN16	CAN1_TX	3.3V	C11	CAN1.TX	F16	CAN1.TX
J8 PIN17	3.3V	3.3V				
J8 PIN18	CAN1_RX	3.3V	D11	CAN1.RX	E17	CAN1.RX
J8 PIN19	CSP12 MOSI 3V	3.3V	K1	ECSP13.MOSI	H14	ECSP13.MOSI
J8 PIN20	GND	GND				
J8 PIN21	CSP12 MISO 3V	3.3V	J2	ECSP13.MISO	J15	ECSP13.MISO
J8 PIN22	EXT GPIO22	3.3V	C6	GPIO5.I00	J14	GPIO1.I019
J8 PIN23	CSP12 SCLK 3V	3.3V	K2	ECSP13.SCLK	J16	ECSP13.SCLK
J8 PIN24	CSP12 CS1 3V	3.3V	K3	ECSP13.SS0	J17	ECSP13.SS0
J8 PIN25	GND	GND				
J8 PIN26	CSP12 CS2 3V	3.3V	M6	ECSP13.SS1	NC	NC
J8 PIN27	I2C3_SDA 3V	3.3V	L6	I2C2.SDA	K16	I2C3.SDA
J8 PIN28	I2C3_SCL 3V	3.3V	L5	I2C2.SCL	K14	I2C3_SCL
J8 PIN29	GPIO_P26 3V	3.3V	P21	GPIO2.I01	E4	GPIO4.I021
J8 PIN30	GND	GND				
J8 PIN31	GPIO_P28 3V	3.3V	N20	GPIO2.I02	E3	GPIO4.I022
J8 PIN32	EXT GPIO32	3.3V	C9	GPIO5.I04	NC	NC
J8 PIN33	PWM2 3V	3.3V	R2	PWM2.OUT	D15	PWM8.OUT
J8 PIN34	GND	GND				
J8 PIN35	GPIO_P24 3V	3.3V	P20	GPIO2.I00	F2	GPIO4.I021
J8 PIN36	GPIO_P48 3V	3.3V	M22	GPIO2.I07	P11	GPIO5.I02
J8 PIN37	GPIO_P34 3V	3.3V	M20	GPIO2.I05	K15	GPIO1.I018
J8 PIN38	CAN2_TX	3.3V	E11	CAN2.TX	E16	CAN2.TX
J8 PIN39	GND	GND				
J8 PIN40	CAN2_RX	3.3V	D11	CAN2.RX	B15	CAN2.RX