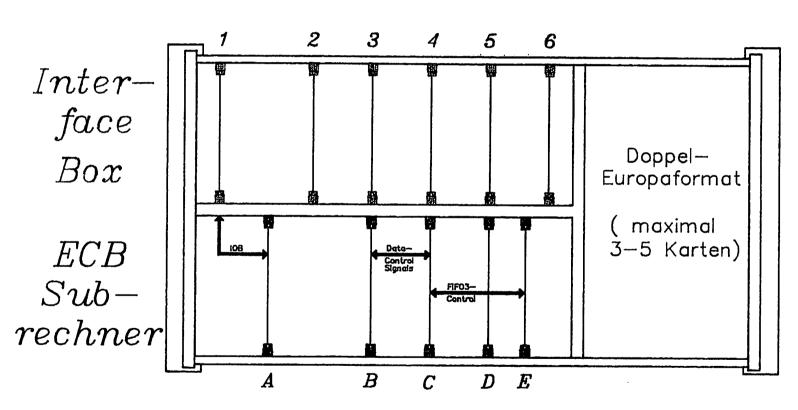


19"-Einschubsystem:



1: I/O-Bus Treiberkarte

2 : VFC-Karte

3 : Zähler 1 (TTL)

4 : Zähler 2 (ECL)

5 : Single Bit I/O

6: HF-Dekade Interface

A : PIO Wrap-Karte (4xZ80PIO)

B: FIFO-Treiberkarte (Line Drivers, Receiv.)

C: FIFO Wrap-Karte (3xZ8038FIO)
D: Timer Wrap-Karte (4xZ80CTC)

 $E: CPU\ IV - Karte\ (Z80, SRAM, EPROM, 2xPIO, CTC)$

VG 64 - Messerleiste Pin 31c : Reset Dekodierung Adress-& Pufferung 14 0000000000 -10--10--10_{10u} FID FID FIO 3 74 LS 74 LS 50polig 0000000000000000 poliq

 $\widehat{}$

FIFO-Karte:

3 x Z8038 FI0 Bus-IN: 16 Bit Bus-OUT: 8 Bit

Tiefe: 128 Bit

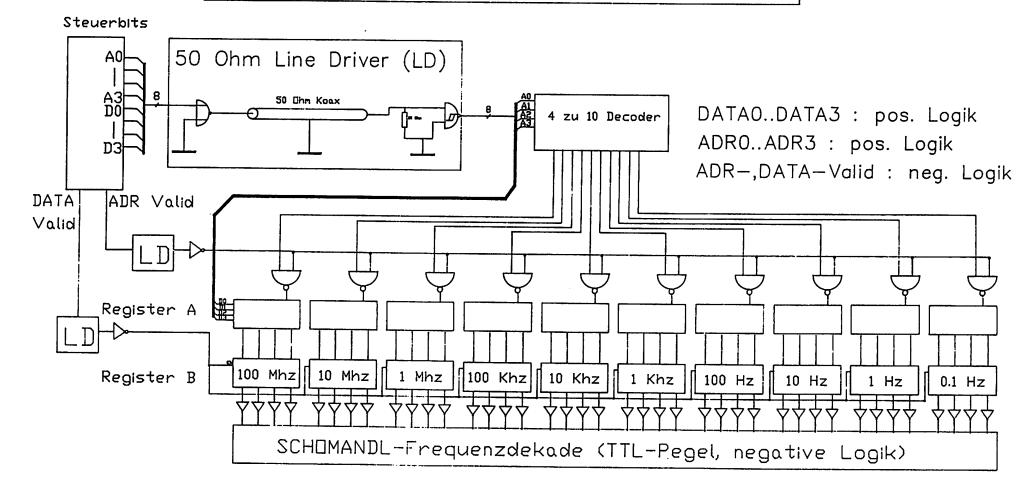
Bestückungsplan

zur CPU-PI01

zur FIFO— Treiber Karte

Frequenz-Dekaden Treiber-Interface

.



PC-I/O Wrapkarte:

Bestueckungsplan

