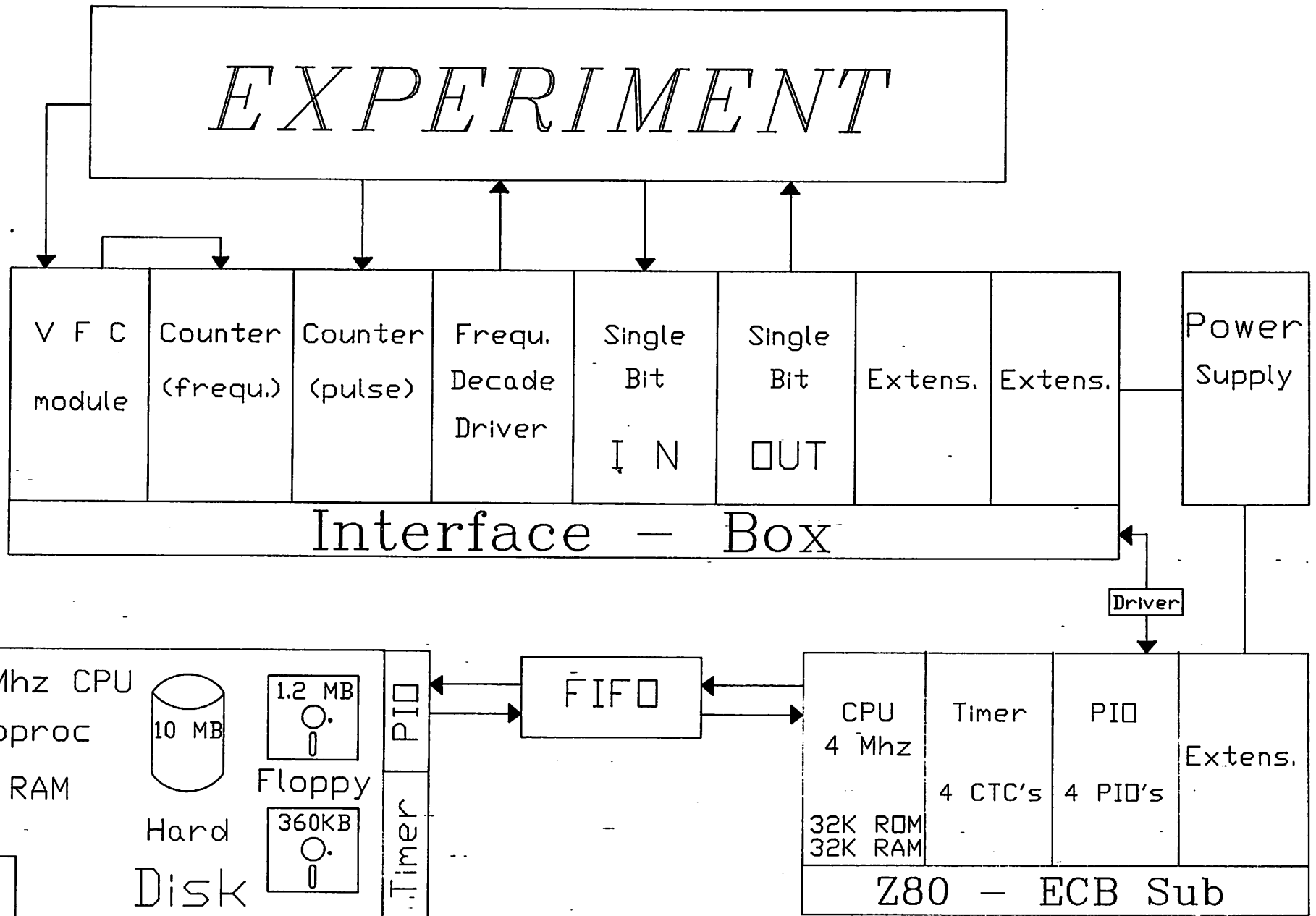
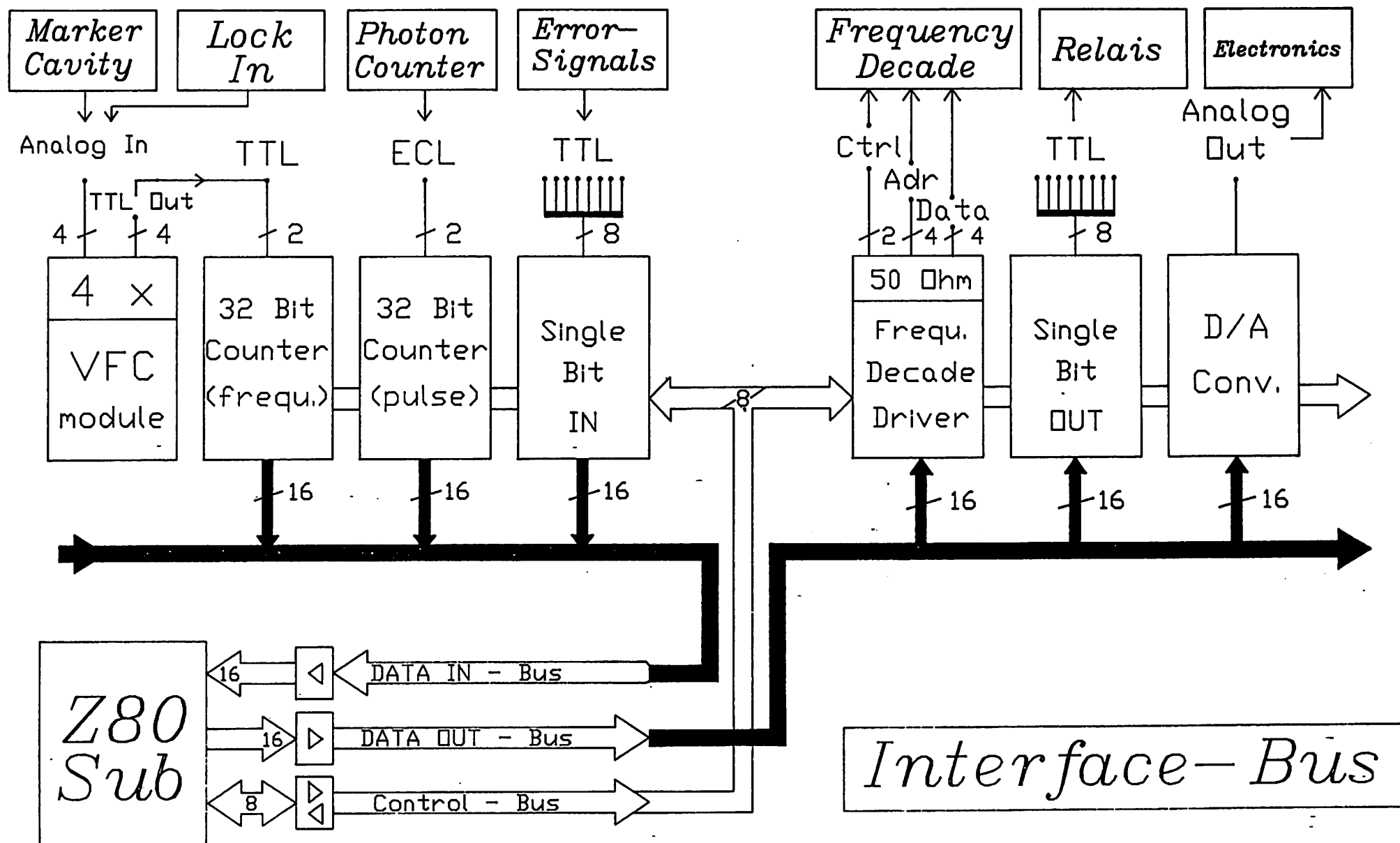
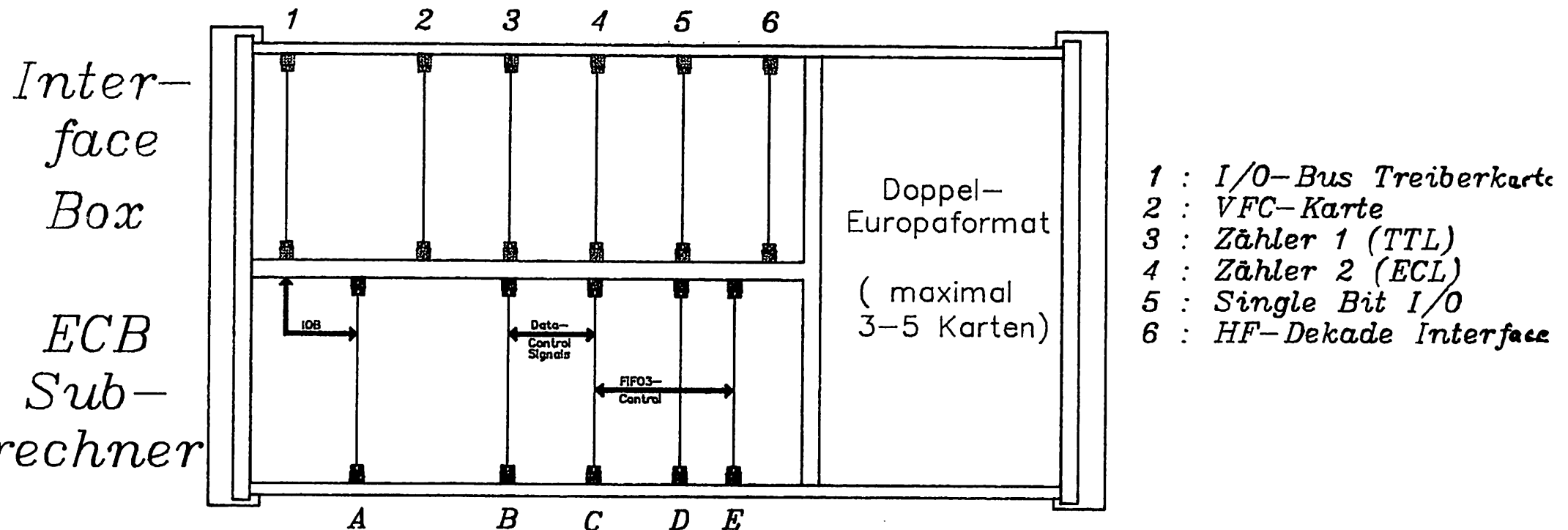


EXPERIMENT



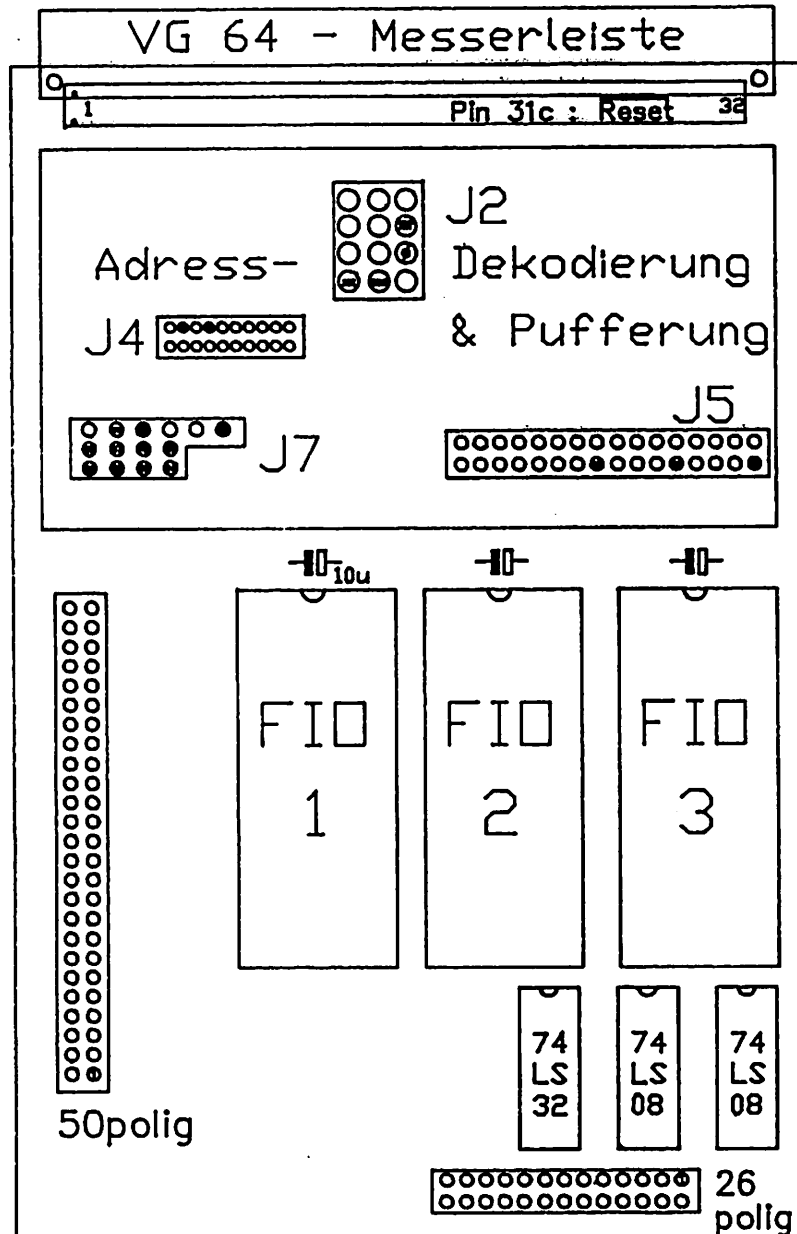


19''-Einschubsystem:



- A : PIO Wrap-Karte (4xZ80PIO)
- B : FIFO-Treiberkarte (Line Drivers, Receiv.)
- C : FIFO Wrap-Karte (3xZ8038FIO)
- D : Timer Wrap-Karte (4xZ80CTC)
- E : CPU IV - Karte (Z80, SRAM, EPROM, 2xPIO, CTC)

zur
FIFO-
Treiber
Karte



FIFO-Karte:

3 x Z8038 FIO

Bus-IN: 16 Bit

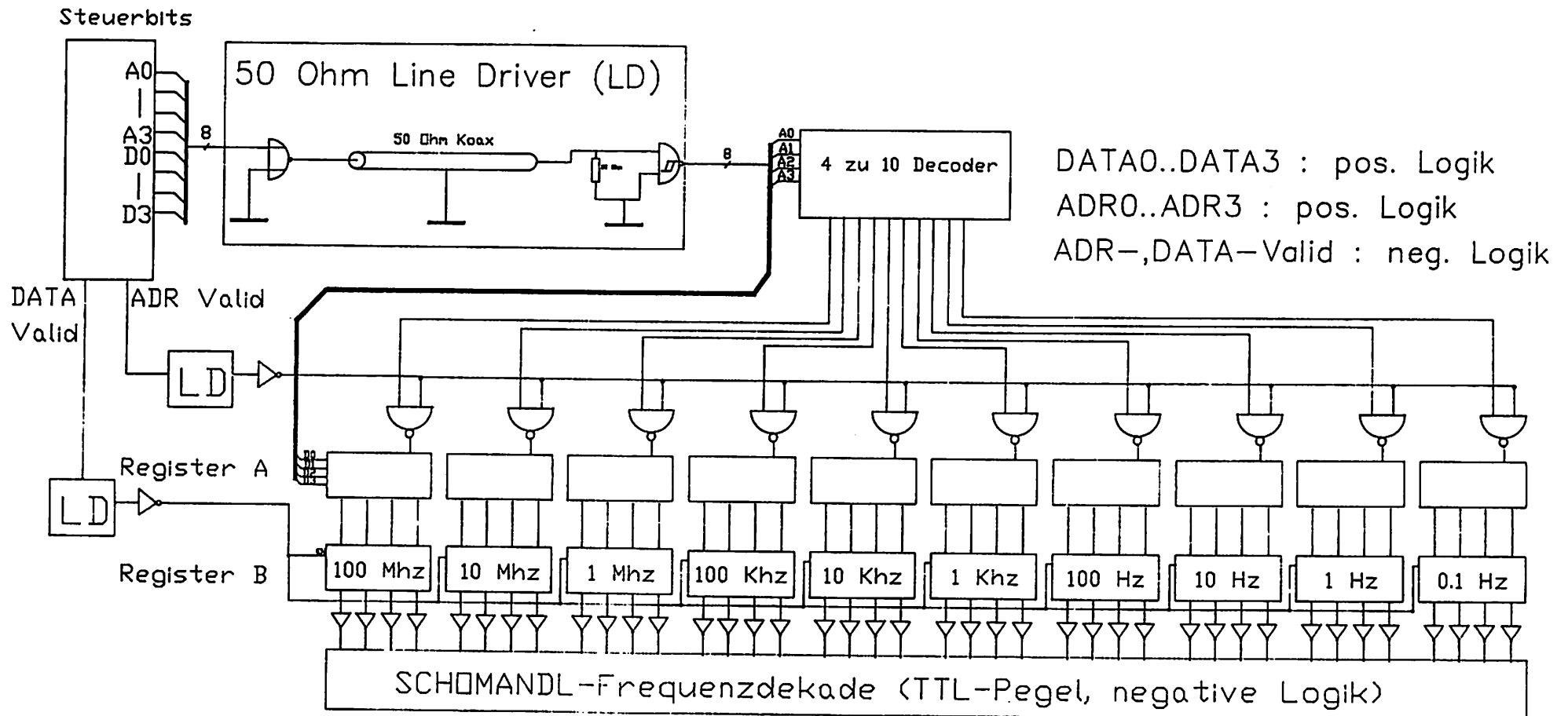
Bus-OUT: 8 Bit

Tiefe: 128 Bit

Bestückungsplan

zur CPU-PI01

Frequenz-Dekaden Treiber-Interface



PC-I/O Wrapkarte:

Bestueckungsplan

PPI = 8255

Timer = 8253/54

W_n = Widerstandsarray

R_n = Receiver SN75173

D_n = Driver SN75172

