

# Final Project Proposal

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## 1 Project Description

For this project, we are interested in considering an amplifier design that is able to drive a large output currents. Possible routes we will consider and attempt based on how far we get are allowing for rail-to-rail input voltages through a pMOS differential pair along with the nMOS differential pair, adding multiple stages for gain, and frequency compensation to account for phase shifts in the output.

A schematic for the base amplifier circuit we will use is shown below,

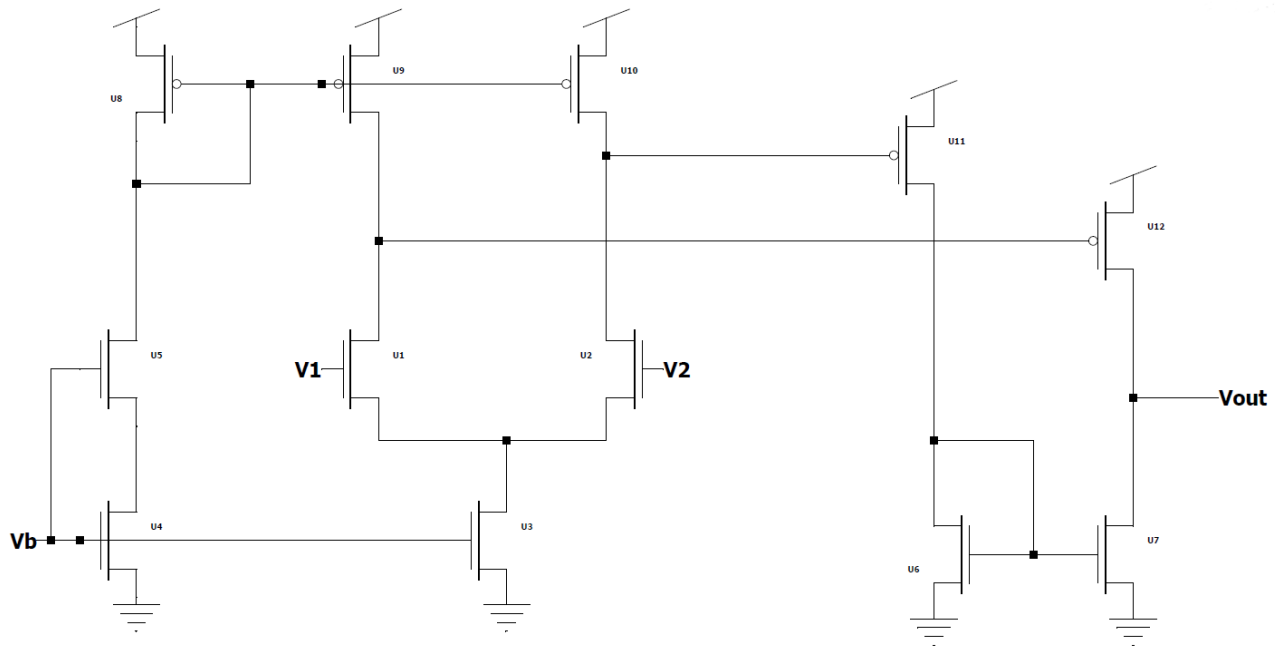


Figure 1: Schematic

## 2 References

We had a conversation with Brad about different amplifiers.