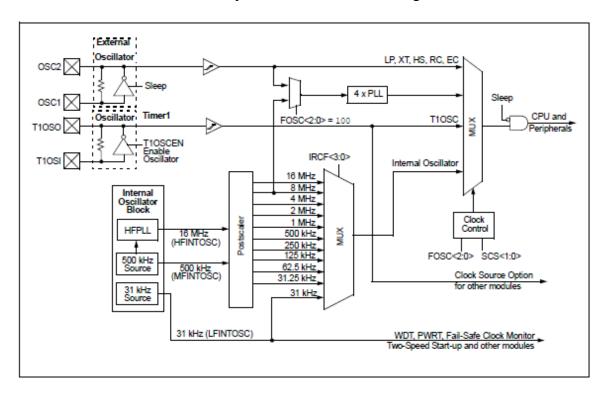
16.317: Microprocessor Systems Design I

Spring 2015

Lecture 28: Key Questions April 13, 2015

1. Describe the basic functionality of the PIC16F1829 clock generation module below:



2. Explain the operation of the programs used to rotate the LEDs using an instruction count-based delay loop (rotate.asm and rotate.c).

3. Explain the features of a typical microcontroller timer module.

4. Explain the operation of the programs used to rotate the LEDs using a timer-based delay loop (timer0.asm and timer0.c).

5. Explain how interrupts can be set up and managed in the PIC microcontrollers.

6. Explain the operation of the programs used to rotate the LEDs using interrupts (interrupt.asm and interrupt.c).

```
***********************
; Lesson 3 - "Rotate"
 This lesson will introduce shifting instructions as well as bit-oriented skip operations to
; move the LED display.
; LEDs rotate from right to left at a rate of 1.5s
; PIC: 16F1829
; Assembler: MPASM v5.43
; IDE: MPLABX v1.10
 Board: PICkit 3 Low Pin Count Demo Board
; Date: 6.1.2012
 *************************
 * See Low Pin Count Demo Board User's Guide for Lesson Information*
 ***********************************
#include <p16F1829.inc>
     _CONFIG _CONFIG1, (_FOSC_INTOSC & _WDTE_OFF & _PWRTE_OFF & _MCLRE_OFF & _CP_OFF & _CPD_OFF &
   _BOREN_ON & _CLKOUTEN_OFF & _IESO_OFF & _FCMEN_OFF);
    __CONFIG _CONFIG2, (_WRT_OFF & _PLLEN_OFF & _STVREN_OFF & _LVP_OFF);
                                     ;supress the 'not in bank0' warning
   errorlevel -302
   cblock 0x70
                                     ;shared memory location that is accessible from all banks
Delay1
                                     ;define two file registers for the delay loop in shared memory
Delay2
    endc
    ; -----LATC-----
   ; Bit#: -7---6---5---4---3---2---1---0---
   ; LED: -----|DS4|DS3|DS2|DS1|-
    ; -------
   ORG 0
                                     ;start of code
Start:
    banksel
                  OSCCON
                                     ;bank1
                                     ;set cpu clock speed of 500KHz
    movlw
                  b'00111000'
    movwf
                  OSCCON
                                     ;move contents of the working register into OSCCON
    clrf
                  TRISC
                                     ;make all of PORTC an output
    banksel
                  LATC
                                     ;select the bank where LATC is (bank2)
    movlw
                  b'00001000'
                                     ;start the rotation by setting DS4 ON
    movwf
                  LATC
                                     ;write contents of the working register to the latch
MainLoop:
OndelayLoop:
    decfsz
                  Delay1,f
                                     ;Waste time.
                  OndelayLoop
                                     ;The Inner loop takes 3 instructions per loop * 256 loopss = 768
    goto
   instructions
    decfsz
                  Delay2,f
                                     ;The outer loop takes an additional 3 instructions per lap * 256
   loops
                  OndelayLoop
                                     ;(768+3) * 256 = 197376 instructions / 125K instructions per second ✔
    goto
    = 1.579 \text{ sec.}
Rotate:
   lsrf
                  LATC, F
                                     ; shift the LEDs and turn on the next LED to the right
                  STATUS, C
                                     ;did the bit rotate into the carry (i.e. was DS1 just lit?)
   htfsc
   bsf
                  LATC, 3
                                     ;yes, it did and now start the sequence over again by turning on
   DS4
                  MainLoop
                                     ;repeat this program forever
   goto
   end
                                     ;end code section
```

```
**********************
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 * move the LED display.
* LEDs rotate from right to left at a rate of 1.5s
* PIC: 16F1829
* Compiler: XC8 v1.00
* IDE: MPLABX v1.10
* Board: PICkit 3 Low Pin Count Demo Board
* Date: 6.1.2012
* See Low Pin Count Demo Board User's Guide for Lesson Information*
* ************************************
*/
#include <htc.h>
                                                //PIC hardware mapping
#define _XTAL_FREQ 500000
                                                //Used by the XC8 delay_ms(x) macro
//config bits that are part-specific for the PIC16F1829
CONFIG(FOSC INTOSC & WDTE OFF & PWRTE OFF & MCLRE OFF & CP OFF & CPD OFF & BOREN ON & CLKOUTEN OFF &
   IESO OFF & FCMEN_OFF);
 _CONFIG(WRT_OFF & PLLEN_OFF & STVREN_OFF & LVP_OFF);
   /* -----LATC-----
    * Bit#: -7---6---5---4---3---2---1---0---
    * LED: -----|DS4|DS3|DS2|DS1|-
    *-----
void main(void) {
                                                //all pins are outputs
   TRISC = 0;
   OSCCON = 0b00111000;
                                                //500KHz clock speed
   LATC = 0b0001000;
                                                //start the rotation by setting DS4 ON - rotate
   from the right to left
   while (1) {
           delay_ms(500);
                                                //delay 500ms
          LATC >> = 1;
                                                //shift to the right by 1
                                                //when the last LED is lit, restart the pattern
          if(STATUSbits.C)
             LATCbits.LATC3 = 1;
   }
}
```

```
***********************
; Lesson 9 - Timer0
; Timer0 is a counter implemented in the processor. It may be used to count instruction
; cycles or external events, that occur at or below the instruction cycle rate.
; In the PIC18, Timer0 can be used as either an 8-bit or 16-bit counter, or timer. The
; enhanced mid-range core implements only an 8-bit counter.
; This lesson configures Timer0 to count instruction cycles and to set a flag when it rolls
; over. This frees up the processor to do meaningful work rather than wasting instruction
; cycles in a timing loop.
; Using a counter provides a convenient method of measuring time or delay loops as it
; allows the processor to work on other tasks rather than counting instruction cycles.
; LEDs rotate from right to left, similar to Lesson 3, at a rate of ~.5 seconds.
; PIC: 16F1829
; Assembler: MPASM v5.43
; IDE: MPLABX v1.10
; Board: PICkit 3 Low Pin Count Demo Board
; Date: 6.1.2012
 *************************
 * See Low Pin Count Demo Board User's Guide for Lesson Information*
#include <p16F1829.inc>
      _CONFIG _CONFIG1, (_FOSC_INTOSC & _WDTE_OFF & _PWRTE_OFF & _MCLRE_OFF & _CP_OFF & _CPD_OFF &
    BOREN_ON & _CLKOUTEN_OFF & _IESO_OFF & _FCMEN_OFF);
    __CONFIG _CONFIG2, (_WRT_OFF & _PLLEN_OFF & _STVREN_OFF & _LVP_OFF);
    errorlevel -302
                                     ;surpress the 'not in bank0' warning
    ; -----LATC-----
    ; Bit#: -7---6---5---4---3---2---1---0---
   ; LED: -----|DS4|DS3|DS2|DS1|-
    ; -----
    Org 0
Start:
                                     ;Setup main init
    banksel
                  OSCCON
                                     :bank1
                  b'00111000'
    movlw
                                     ;set cpu clock speed to 500KHz
    movwf
                  OSCCON
                                     ;move contents of the working register into OSCCON
                                     ;Configure the LEDs
    banksel
                  TRISC
                                     ;bank1
    clrf
                  TRTSC
                                     ;make all of PORTC an output
    banksel
                  ΙΔΤΟ
                                     ;bank2
    movlw
                  b'00001000'
                                     ;start with DS4 lit
    movwf
                   LATC
                                     ;Setup Timer0
    banksel
                  OPTION REG
                                     ;bank1
    movlw
                  b'00000111'
                                     ;1:256 prescaler for a delay of: (insruction-cycle * 256-counts)*
   prescaler = ((8uS * 256)*256) = 524mS
    movwf
                  OPTION REG
MainLoon:
   btfss
                  INTCON, TMR0IF
                                     ;did TMR0 roll over yet?
   bra
                   $-1
                                     ;wait until TMR0 overflows and sets TMR0IF
                   INTCON, TMR0IF
   bcf
                                     ;must clear flag in software
                                     ;rotate the LEDs
```

banksel LATC ;bank2
lsrf LATC, f
btfsc STATUS,C ;did the bit rotate into the carry?
bsf LATC,3 ;yes, put light DS4 back up
bra MainLoop ;continue forever
end

```
**********************
 *
   Lesson 9 - "Timer0"
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 * In the PIC18, Timer0 can be used as either an 8-bit or 16-bit counter, or timer. The
 * enhanced mid-range core implements only an 8-bit counter.
* This lesson configures Timer0 to count instruction cycles and to set a flag when it rolls
^{st} over. This frees ar{\text{up}} the processor to do meaningful work rather than wasting instruction
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   Using a counter provides a convenient method of measuring time or delay loops as it
   allows the processor to work on other tasks rather than counting instruction cycles.
 st LEDs rotate from right to left, similar to Lesson 3, at a rate of \sim.5 seconds.
* PIC: 16F1829
   Compiler: XC8 v1.00
 * IDE: MPLABX v1.10
   Board: PICkit 3 Low Pin Count Demo Board
   Date: 6.1.2012
* ************************************
* See Low Pin Count Demo Board User's Guide for Lesson Information*
*/
#include <htc.h>
                                                  //PIC hardware mapping
                                                  //Used by the XC8 delay_ms(x) macro
#define _XTAL_FREQ 500000
//config bits that are part-specific for the PIC16F1829
__CONFIG(FOSC_INTOSC & WDTE_OFF & PWRTE_OFF & MCLRE_OFF & CP_OFF & CPD_OFF & BOREN_ON & CLKOUTEN_OFF &
   IESO_OFF & FCMEN_OFF);
 _CONFIG(WRT_OFF & PLLEN_OFF & STVREN_OFF & LVP_OFF);
   /* -----LATC-----
    * Bit#: -7---6---5---4---3---2---1---0---
    * LED: -----|DS4|DS3|DS2|DS1|-
    *______
    */
void main(void) {
   OSCCON = 0b00111000;
                                                 //500KHz clock speed
   TRISC = 0;
                                                 //all LED pins are outputs
   LATC = 0:
   OPTION REG = 0b00000111;
                                                //1:256 prescaler for a delay of: (insruction-cycle * 

✓
   256-counts)*prescaler = ((8uS * 256)*256) =~ 524mS
   LATCbits.LATC4 = 1;
                                                //start with DS4 lit
   while (1) {
       while (!INTCONbits.TMR0IF) continue;
                                                //you can let the PIC do work here, but for now we will ∠
    wait for the flag
       INTCONbits.T0IF = 0;
                                                //flag MUST be cleared in software
       LATC >> = 1;
                                                //rotate the LEDs
       if (STATUSbits.C)
                                                //when the last LED is lit, restart the pattern
           LATCbits.LATC3 = 1;
   }
```

}

```
***********************
; Lesson 10 - Interrupts and Pull-ups
; This lesson will introduce interrupts and how they are useful. It will
; also introduce internal weak pull-ups that are available on most PICs.
; It should be noted that this lesson is more efficent than the last
; one, "Timer0". Notice how the processor is no longer waiting for
; Timer0 to roll over. Instead, we let the hardware modules do the work,
; freeing the CPU to do other things in the main loop
; The switch is no longer continuously polled for a button press. Instead,
; an interrupt will occur which will automically place the program counter
; inside of the ISR where we can change directions outisde of normal code execution
; LEDs rotate at a constant speed and the switch reverses their direction
; PIC: 16F1829
; Assembler: MPASM v5.43
; IDE: MPLABX v1.10
; Board: PICkit 3 Low Pin Count Demo Board
; Date: 6.1.2012
* See Low Pin Count Demo Board User's Guide for Lesson Information*
#include <p16F1829.inc>
      _CONFIG _CONFIG1, (_FOSC_INTOSC & _WDTE_OFF & _PWRTE_OFF & _MCLRE_OFF & _CP OFF & _CPD OFF &
   _BOREN_ON & _CLKOUTEN_OFF & _IESO_OFF & _FCMEN_OFF);
    __CONFIG _CONFIG2, (_WRT_OFF & _PLLEN_OFF & _STVREN_OFF & _LVP_OFF);
   errorlevel -302
                                     ;surpress the 'not in bank0' warning
#define
           SWITCH PORTA, 2
                                     ;pin where SW1 is connected..NOTE: always READ from the PORT and
   WRITE to the LATCH
#define
           PULL UPS
                                     ;if this is uncommented, JP5 can be pulled out
#define
           LED_RIGHT
                      0xFF
                                        ;keep track of LED direction
#define
          LED LEFT
                      0x00
   cblock 0x70
                                     ;shared memory location that is accessible from all banks
Direction
Delay1
    endc
   ; ------LATC-----
   ; Bit#: -7---6---5---4---3---2---1---0---
   ; LED: -----|DS4|DS3|DS2|DS1|-
    Org 0x0
                                     ;Reset Vector starts at 0x0000
    bra
                   Start
                                     ;main code execution
    Org 0x0004
                                     ;Interrupt Vector starts at address 0x0004
                  ISR
    goto
Start:
    ;Setup main init
    hanksel .
                   OSCCON
                                     ;bank1
                                     ;set cpu clock speed FO 500KHz
    movlw
                   b'00111000'
    movwf
                   OSCCON
                                     ;move contents of the working register into OSCCON
                   TRISA, RA2
    bsf
                                     ;switch as input
                   ANSELA
    hanksel.
                                     ;bank3
```

```
ANSELA, RA2
     bcf
                                       ;digital
                                       ;can reference pins by their position in the PORT (2) or name (RA2)
                                       ;Configure the LEDs
     banksel
                   TRISC
                                       ;bank1
     clrf
                                       ;make all of PORTC an output
                   TRTSC
     hankse1
                   ΙΔΤΟ
                                       ;bank2
    movlw
                   b'00001000'
                                       ;start with DS4 lit
                                       ;Setup Timer0 as the delay
    banksel
                   OPTION_REG
                                       ;bank1
                                       movlw
                   b'00000111'
   prescaler = ((8uS * 256)*256) =~ 524mS
                   OPTION REG
    movwf
    bsf
                   INTCON, TMR0IE
                                       ;enable the rollover interrupt to occur
                                       ;Setup interrupt-on-change for the switch
    bsf
                   INTCON, IOCIE
                                       ;must set this global enable flag to allow any interrupt-on-change ∠
   flags to cause an interrupt
    banksel
                   IOCAN
                                       :bank7
    bsf
                   IOCAN, IOCAN2
                                       ;when SW1 is pressed, enter the ISR (Note, this is set when a
   FALLING EDGE is detected)
    bsf
                   INTCON, GIE
                                       ; must set this global to allow any interrupt to bring the program
   into the ISR
                                       ;if this is not set, the interrupt flags will still get set, but
   the ISR will never be entered
#ifdef PULL UPS
                                       ;enter here if this is defined (not commented out)
   banksel WPUA
                               ;bank4
   hsf
                   WPUA, 2
                                       ;enable the weak pull-up for the switch
   banksel OPTION_REG
                                ;bank1
   bcf
                   OPTION_REG, NOT_WPUEN ; enable the global weak pull-up bit
               ;this bit is active HIGH, meaning it must be cleared for it to be enabled
#endif
    movlw
                   LED_RIGHT
                                       ;start with LEDs shifting to the right
    movwf
                   Direction
     ;Clear the RAM
     clrf
                   Delay1
MainLoop:
   bra
                   MainLoop
                                       ; can spend rest of time doing something critical here
Debounce:
                                       ;delay for approximatly 5ms
   movlw
                   d'209'
                                       (1/(500KHz/4))*209*3 = 5.016mS
   movwf
                   Delay1
DebounceLoop:
                                       ;1 instruction to decrement,unless if branching (ie Delay1 = 0)
   decfsz
                   Delay1, f
   hra
                   DebounceLoop
                                       ;2 instructions to branch
   return
RotateRight:
                   LATC, f
                                       ;logical shift right
    lsrf
                   STATUS, C
     htfsc
                                       ;did the bit rotate into the carry?
     bsf
                   LATC,3
                                       ;yes, put it into bit 3.
    retfie
RotateLeft:
                   LATC, f
                                       ;logical shift left
    lslf
                   LATC, 4
                                       ;did it rotate out of the LED display?
     htfsc
     bsf
                   LATC, 0
                                       ;yes, put in bit 0
     retfie
                                       ;Enter here if an interrupt has occured
                                       ; First, check what caused the interrupt by checking the ISR flags
```

end

;This lesson only has 2 flags to check ISR: banksel IOCAF ;bank7 btfsc IOCAF, 2 ; check the interrupt-on-change flag bra Service_SW1 ;switch was pressed ;Timer0 overflowed Service_TMR0 bra Service_SW1: ; In order to ensure that no detected edge is lost while clearing flags, ;the following 3 lines mask out only the known changed bits and don ≰ 't ;interfere with the others. A simple clrf would work, but this current ;method is good practice 0xFF movlw IOCAF, w xorwf andwf IOCAF, f ;MUST ALWAYS clear this in software or else stuck in the ISR forever ;clearing this will clear the INTCON, IOCIF call ;delay for 5ms and then check the switch again Debounce banksel **PORTA** ;bank0 btfsc **SWITCH** ;is it still held down? retfie ;nope, exit the ISR back to the main code 0xFF movlw ;toggle the direction state and save it back xorwf Direction, f retfie ;return to main code Service_TMR0: bcf INTCON, TOIF ;MUST ALWAYS clear this in software or else stuck in the ISR forever banksel LATC ;change to bank2 movlw LED_RIGHT ;check what direction currently in ;be sure to save in wreg so as to not corrupt 'Direction' subwf Direction, w btfsc STATUS, Z bra RotateRight bra RotateLeft

;end code generation

```
************************
 *
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 * also introduce internal weak pull-ups that are available on most PICs.
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 * one, "Timer0". Notice how the processor is no longer waiting for
 * Timer0 to roll over. Instead, we let the hardware modules do the work,
 ^{st} freeing the CPU to do other things in the main loop
   The switch is no longer continuously polled for a button press. Instead,
   an interrupt will occur which will automically place the program counter
   inside of the ISR where we can change directions outisde of normal code execution
* LEDs rotate at a constant speed and the switch reverses their direction
* PIC: 16F1829
* Compiler: XC8 v1.00
* IDE: MPLABX v1.10
* Board: PICkit 3 Low Pin Count Demo Board
   Date: 6.1.2012
 * See Low Pin Count Demo Board User's Guide for Lesson Information*
* ************************************
*/
                                                //PIC hardware mapping
#include <htc.h>
#define _XTAL_FREQ 500000
                                                //Used by the XC8 delay_ms(x) macro
#define DOWN
                         0
#define UP
#define SWITCH
                        PORTAbits.RA2
#define LED RIGHT
#define LED_LEFT
#define PULL UPS
                                               //if this is uncommented, the trace under JP5 can be
   cut
                                               //with no affect on the output
//config bits that are part-specific for the PIC16F1829
__CONFIG(FOSC_INTOSC & WDTE_OFF & PWRTE_OFF & MCLRE_OFF & CP_OFF & CPD_OFF & BOREN_ON & CLKOUTEN_OFF &
   IESO OFF & FCMEN OFF);
 CONFIG(WRT_OFF & PLLEN_OFF & STVREN_OFF & LVP_OFF);
   /* -----LATC-----
    * Bit#: -7---6---5---4---3---2---1---0---
    * LED: -----|DS4|DS3|DS2|DS1|-
unsigned char _direction;
                                           //a global variable
void main(void) {
                                           //general init
   OSCCON = 0b00111000;
                                           //500KHz clock speed
   TRISC = 0;
                                            //all LED pins are outputs
   LATC = 0;
                                            //init LEDs in OFF state
   LATCbits.LATC3 = 1;
                                            //DS4 is lit
   _direction = LED_RIGHT;
                                            //start with LEDs rotating from right to left
                                            //setup switch (SW1)
```

```
TRISAbits.TRISA2 = 1;
                                                //switch as input
    ANSELAbits.ANSA2 = 0;
                                                //digital switch
                                                //by using the internal resistors, you can save cost by
    eleminating an external pull-up/down resistor
#ifdef PULL_UPS
                                                //enable the weak pull-up for the switch
    WPUA2 = 1;
    nWPUEN = 0:
                                                //enable the global weak pull-up bit
#endif
                                                //setup TIMER0 as the delay
                                                //1:256 prescaler for a delay of: (insruction-cycle * 256- ✔
    counts)*prescaler = ((8uS * 256)*256) =~ 524mS
    OPTION REG = 0b00000111;
                                                //setup TIMER0
    INTCONbits.TMR0IE = 1;
                                                //enable the TMR0 rollover interrupt
                                                //setup interrupt on change for the switch
    INTCONbits.IOCIE = 1;
                                                //enable interrupt on change global
    IOCANbits.IOCAN2 = 1;
                                                //when SW1 is pressed, enter the ISR
    GIE = 1;
                                                //enable global interupts
    while (1) {
        continue;
                                                //can spend rest of time doing something critical here
}
void interrupt ISR(void) {
    if (IOCAF) {
                                                //SW1 was just pressed
        IOCAF = 0;
                                                //must clear the flag in software
                                                //debounce by waiting and seeing if still held down
         __delay_ms(5);
        if (SWITCH == DOWN) {
            _direction ^= 1;
                                                //change directions
        }
    }
    if (INTCONbits.T0IF) {
        INTCONbits.T0IF = 0;
        if (_direction == LED_RIGHT) {
            LATC >> = 1;
                                                //rotate right
            if (STATUSbits.C == 1)
                                                //when the last LED is lit, restart the pattern
                LATCbits.LATC3 = 1;
        } else{
            LATC << = 1;
                                                //rotate left
            if (LATCbits.LATC4 == 1)
                                                //when the last LED is lit, restart the pattern
                LATCbits.LATC0 = 1;
        }
    }
```

}