16.317: Microprocessor Systems Design I

Spring 2014

Lecture 7: Key Questions
February 7, 2014

1. Explain the operation of the XCHG instruction. What restrictions are placed on this instruction?

2. Explain the operation of the LEA instruction.

3. Explain the operation of the instructions used for loading a full address pointer (LDS, LSS, LES, LFS, LGS).

4. Show the results of running the following program if DATA_SEG_ADDR = 1200H, assuming the memory contents shown:

DATA_SEG_ADDR:0000		
DATA_SEG_ADDR:INIT_TABLE	11	22
	33	44
	55	66
	77	88
	99	AA
	BB	CC
	DD	EE
	FF	16
	03	17

```
MOV  AX, DATA_SEG_ADDR
MOV  DS,AX
MOV  SI,[INIT_TABLE]
LES  DI,[INIT_TABLE+02H]
MOV  AX,[INIT_TABLE+06H]
MOV  SS,AX
MOV  AX,[INIT_TABLE+08H]
MOV  BX,[INIT_TABLE+0AH]
MOV  CX,[INIT_TABLE+0CH]
MOV  DX,[INIT_TABLE+0EH]
```

5. Describe the x86 flags.

6. Describe the operation of the ADD, ADC, and INC instructions.

7. Describe the operation of the SUB, SBB, DEC, and NEG instructions.