16.317: Microprocessor Systems Design I

Spring 2012

Lecture 22: Key Questions
March 23, 2012

1. Describe the reasons for using virtual memory.

2. Describe the virtual memory space on the 80386 and the general address translation mechanism used.

3. Describe how virtual addresses are translated to physical addresses in the 80386
    segmented memory model.

4. Describe how virtual addresses are translated to physical addresses in the 80386
    paged memory model.

5. Describe the purpose and basic operation of a translation lookaside buffer (TLB).