16.317: Microprocessor Systems Design I

Summer 2012

Homework 1

Due **Wednesday, 7/18/12**

<u>NOTE:</u> No late assignments will be accepted, as the solution will be posted after Wednesday's class to allow you time to study it before the first exam.

1. (20 points) Name and describe each of the four general types of operations implemented by most microprocessors. Provide a specific example of an operation in each class.

2. (25 points) Given each of the binary or hexadecimal number below, determine what the decimal value is if the number is (i) an unsigned integer, and (ii) a signed integer.

a. 01011100_2
b. 10100111_2
c. E3H (or 0xE3—recall that, in x86 assembly notation, the "H" at the end of a number signifies that the previous value is in hexadecimal)
d. 7136H
e. BEEFH

3. (30 points) Assume the state of the 80386DX registers are as follows:

   - (DS) = 5722H
   - (ES) = AAAAH
   - (SS) = FE00H
   - (ESI) = 00004007H
   - (EDI) = 0000312CH
   - (EBX) = 00001AB2H
   - (EBP) = 00001010H

   Given each of the logical addresses listed below, answer the following questions:
   - What physical address corresponds to the given logical address?
   - If the processor accesses a word at that address, is the access aligned?
   - If the processor accesses a double word at that address, is the access aligned?

a. DS:DI
b. DS:SI
c. SS:BP
d. ES:BX
e. ES:103EH

4. (25 points) Assume the state of the 80386DX registers are as follows:

   - (DS) = 1234H
   - (ES) = C200H
   - (ESI) = 00001002H
   - (EDI) = 0000DDE0H
   - (EBX) = 00008080H

For each of the instructions below, determine the physical address for the memory operand in each instruction. Recall that memory operands are indicated by square brackets [ ], and that either the source or destination may be a memory operand.

a. MOV AX, [SI]

b. MOV ES:[8888H], BX

c. MOV [DI+4004H], CX

d. MOV ES:[BX+DI], DX

e. MOV AX, 24H[BX][SI]