

16.317: Microprocessor Systems Design I

Spring 2012

Lecture 21: Key Questions
March 21, 2012

1. (Review) Show how selectors and the GDTR are used to access global memory.

2. Show the process used for local memory accesses on the 80386.

3. Describe the interrupt descriptor table and its purpose.
4. Describe the process of task switching and the structures used in task switching on the 80386.