## 16.482 / 16.561: Computer Architecture and Design Fall 2013

## Lecture 2: Key Questions September 16, 2013

1.	Describe each of the addressing modes listed below:
•	Immediate
•	Register direct
•	Register indirect
•	Memory indirect
•	Base + displacement
•	PC-relative
2.	What are the benefits of having few instruction formats? What are the benefits of having many formats?

	.482/16.561: Computer Architecture & Design	Instructor: M. Geiger Lecture 2: Key Questions
3.	What are the pros and cons of fixed vs. variable length instructions	?
4.	Assume a MIPS instruction is represented by the hexadecimal values the values for each instruction field, assuming that the instruction	
•	An R-type instruction	
•	An I-type instruction	

• A J-type instruction

16.482/16.561:	Computer Architecture	& Design
Fall 2013	_	_

5. Describe the different mnemonics used for MIPS integer registers.

6. Describe the MIPS data transfer instructions.

7. Describe what it means for data to be aligned.

8. Compare and contrast big-endian and little-endian data.

9. Say memory holds the word 0xABCD1234, starting at address 0x1000, \$t0 holds the value 0x1000, and \$s0 holds 0xDEADBEEF. What are the results of the following instructions?

• lh \$t1, 2(\$t0)

• lb \$t2, 1(\$t0)

• lbu \$t3, 0(\$t0)

• sh \$s0, 0(\$t0)

• sb \$s0, 3(\$t0)

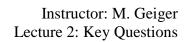
16.482/16.561: Computer Architecture & Design
Fall 2013

Instructor: M. Geiger
Lecture 2: Key Questions

10. Describe the MIPS arithmetic and logical instructions.

- 11. Say t0 = 0x00000001, t1 = 0x00000004, t2 = 0xFFFFFFF. What are the results of the following instructions?
- sub \$t3, \$t1, \$t0
- addi \$t4, \$t1, 0xFFFF
- andi \$t5, \$t2, 0xFFFF
- sll \$t6, \$t0, 5
- slt \$t7, \$t0, \$t1
- lui \$t8, 0x1234

16.482/16.561: Computer Architecture & Design
Fall 2013



12. Describe the different classes of MIPS branch instructions.

13. Explain the use of pseudoinstructions in MIPS assembly.

14. Describe the different jump instructions in MIPS.

15. Describe how if statements are compiled to MIPS assembly, using the example provided in lecture.

16. Describe how loops are compiled to MIPS assembly, using the example provided in lecture.

16.482/16.561: Computer Architecture & Design
Fall 2013

Instructor: M. Geiger
Lecture 2: Key Questions

17. Describe the general semantics of function calls in MIPS assembly.