

EECE.3170: Microprocessor Systems Design I

Spring 2016

Homework 2 Solution

Assume the state of an x86 processor's registers and memory are:

	Address	Lo		Hi	
<i>EAX: EECE3170h</i>	20100h	10	00	08	00
<i>EBX: 00000001h</i>	20104h	10	10	FF	FF
<i>ECX: 00000002h</i>	20108h	08	00	19	91
<i>EDX: 00000004h</i>	2010Ch	20	40	60	80
<i>ESI: 00020100h</i>	20110h	02	00	AB	0F
<i>EDI: 00020110h</i>	20114h	30	99	11	55
	20118h	40	AA	7C	EE
	2011Ch	FF	BB	42	D2
	20120h	30	CC	30	90

*What is the result of each of the instructions listed below? Assume that the instructions execute in sequence—in other words, the result of each instruction may depend on the results of earlier instructions. Correctly evaluating each instruction will earn you **10 points**.*

Note that you may assume any constant values shown using less than 32 bits are zero-extended to 32 bits if necessary (for example, 000Fh = 0000000Fh).

MOV DL, FEh

Solution: DL = FEh

MOV DH, AL

Solution: DH = AL = **70h** (EDX now = 000070FEh)

MOVSX BX, BYTE PTR [ESI+000Fh]

Solution: BX = sign-extended byte at address ESI+000Fh = 00020100h + 000Fh = 0002010Fh

→ BX = 80h sign-extended = **FF80h**

MOV [EDI+ECX], EBX

Solution: Double-word at address EDI+ECX = EBX

EDI+ECX = 00020110h + 00000002h = 00020112h

→ (20112h) = EBX = **0000FF80h** (bytes ordered as 80h, FFh, 00h, 00h)

*MOV [ESI+4*ECX], AX*

Solution: Word at address $ESI+4*ECX = AX$

$$ESI + 4*ECX = 20100h + 4 * 2 = 20108h$$

→ (20108h) = **3170h** (bytes ordered as 70h, 31h)

XCHG CL, [ESI]

Solution: Swap byte values in CL, address 20110h → CL = **10h**, (20110h) = **02h**

MOVZX EAX, WORD PTR [EDI+ECX]

Solution: EAX = zero-extended word at address $EDI+ECX = 20110h + 00000010h = 20120h$

→ EAX = **0000CC30h** (original word underlined)

MOV DX, [EDI+FFFFFFFFAh]

Solution: DX = word at address $EDI+FFFFFFFFAh = 20110h + (-6) = 2010Ah$

→ DX = **9119h**

LEA ECX, [ESI+EBX+0017h]

Solution: $ECX = ESI + EBX + 0017h = 20100h + 0000FF80h + 0017h = \mathbf{30097h}$

MOVSX EBX, BYTE PTR [ESI+4]

Solution: EBX = sign-extended byte at address 20104h = **00000010h** (original byte underlined)