

16.317: Microprocessor Systems Design I

Fall 2013

Lecture 22: Key Questions

October 28, 2013

1. (Review) Show how selectors and the GDTR are used to access global memory.

2. Describe how the LDTR is used to determine the location of the current LDT. Where are the values in the LDTR loaded from? At what point does the LDTR change? And what is the purpose of the LDTR cache?

3. Show the process used for local memory accesses on x86 processors.

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