

16.317: Microprocessor Systems Design I

Spring 2013

Lecture 17: Key Questions

March 6, 2013

1. (Review) Show how selectors and the GDTR are used to access global memory.

2. Describe how the LDTR is used to determine the location of the current LDT. Where are the values in the LDTR loaded from? At what point does the LDTR change? And what is the purpose of the LDTR cache?

3. Show the process used for local memory accesses on the 80386.

4. Describe the interrupt descriptor table and its purpose.
5. Describe the process of task switching and the structures used in task switching on the 80386.