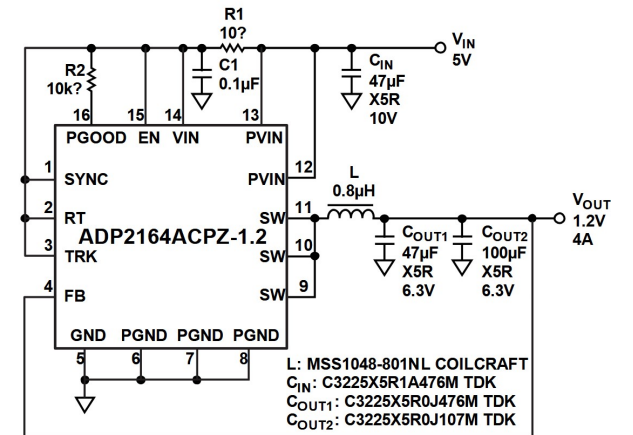
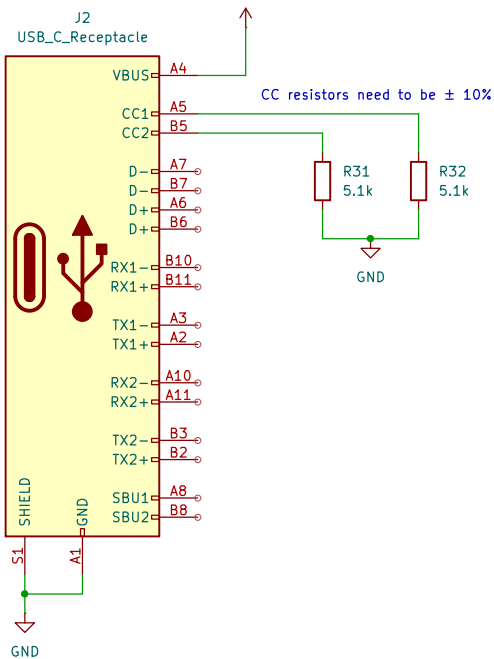


Table 6. Recommended L and C<sub>OUT</sub> Values at f<sub>s</sub> = 600 kHz

V <sub>IN</sub> (V)	V <sub>OUT</sub> (V)	L (μH)	C <sub>OUT</sub> (μF)
3.3	1.0	1	100 + 100
3.3	1.2	1	100 + 100
3.3	1.5	1	100 + 47
3.3	1.8	1	100 + 47
3.3	2.5	1	100
5	1.0	1	100 + 100
5	1.2	1.5	100 + 100
5	1.5	1.5	100 + 47
5	1.8	1.5	100 + 47
5	2.5	1.5	100
5	3.3	1.5	100



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Sheet: /3V3 Power/  
File: power.kicad\_sch

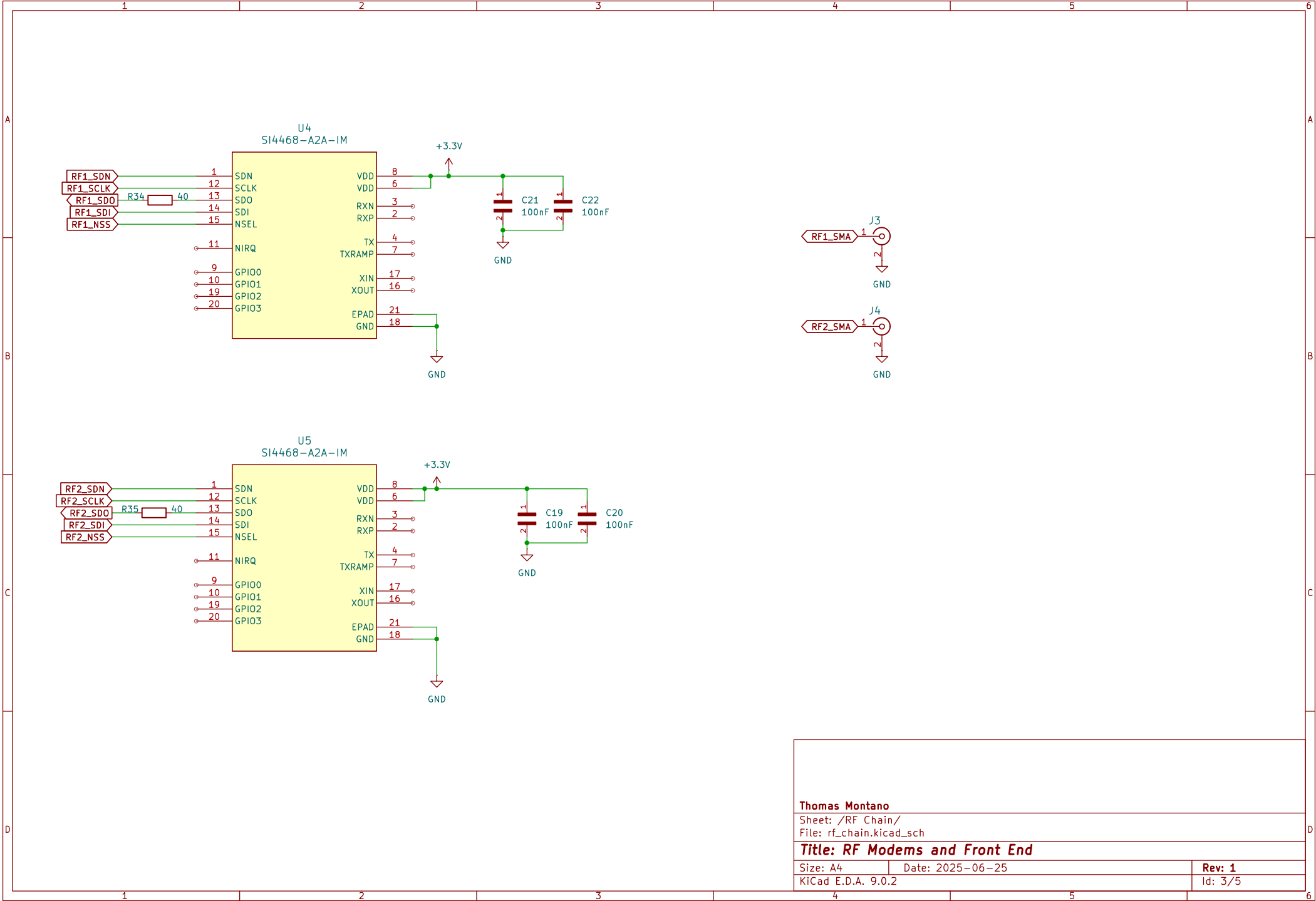
Title: 3.3V Power Block

Size: A4 Date: 2025-06-22

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Rev: 1

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Bias Resistor Connection: A 4.87-k $\Omega$   $\pm$ 1% resistor must be connected from RBIAS to GND.

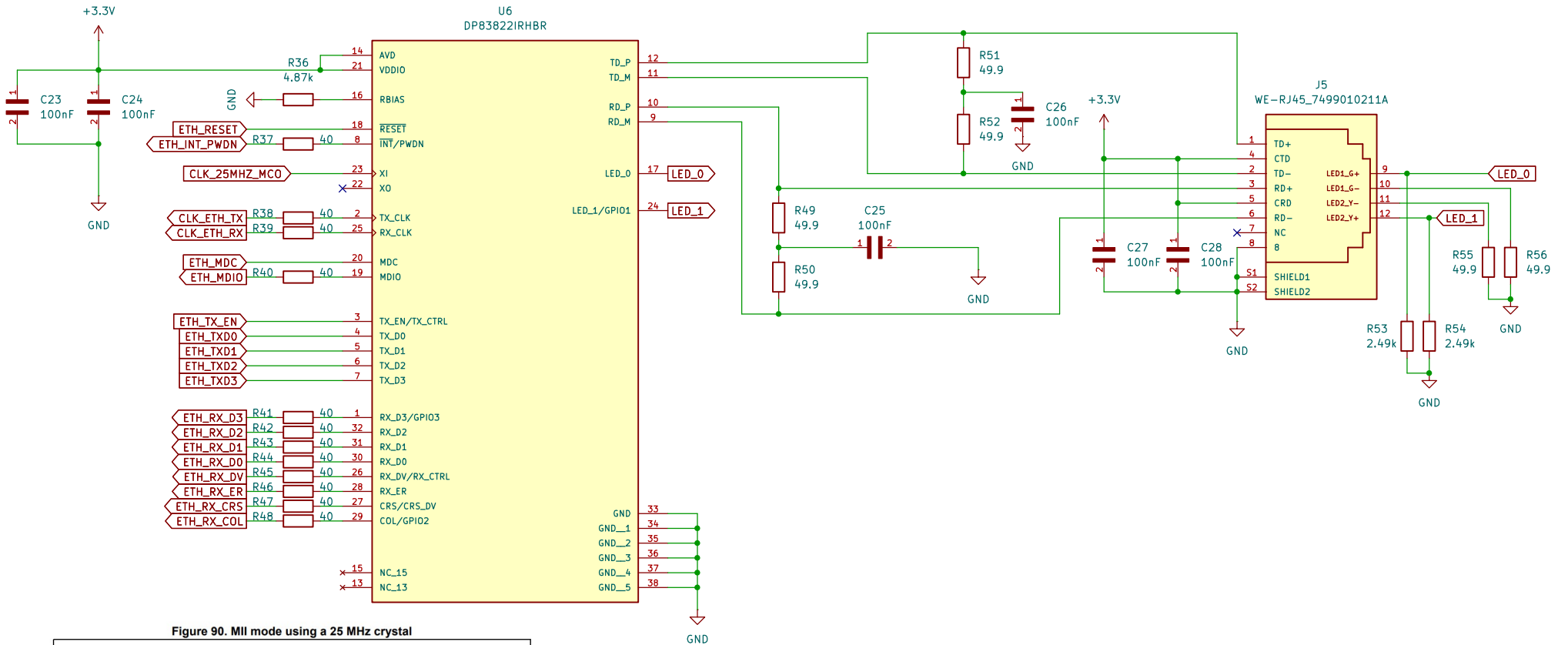
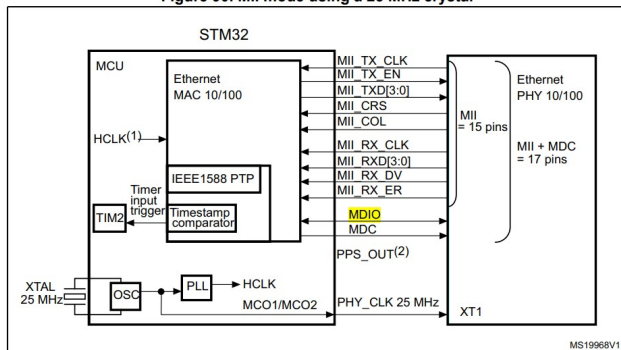


Figure 90. MII mode using a 25 MHz crystal



1.  $f_{HCLK}$  must be greater than 25 MHz.
2. Pulse per second when using IEEE1588 PTP optional signal.

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Sheet: /Ethernet PHY/  
File: ethernet\_phy.kicad\_sch

### Title: Ethernet PHY Interface

Size: A4 Date: 2025-06-25

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