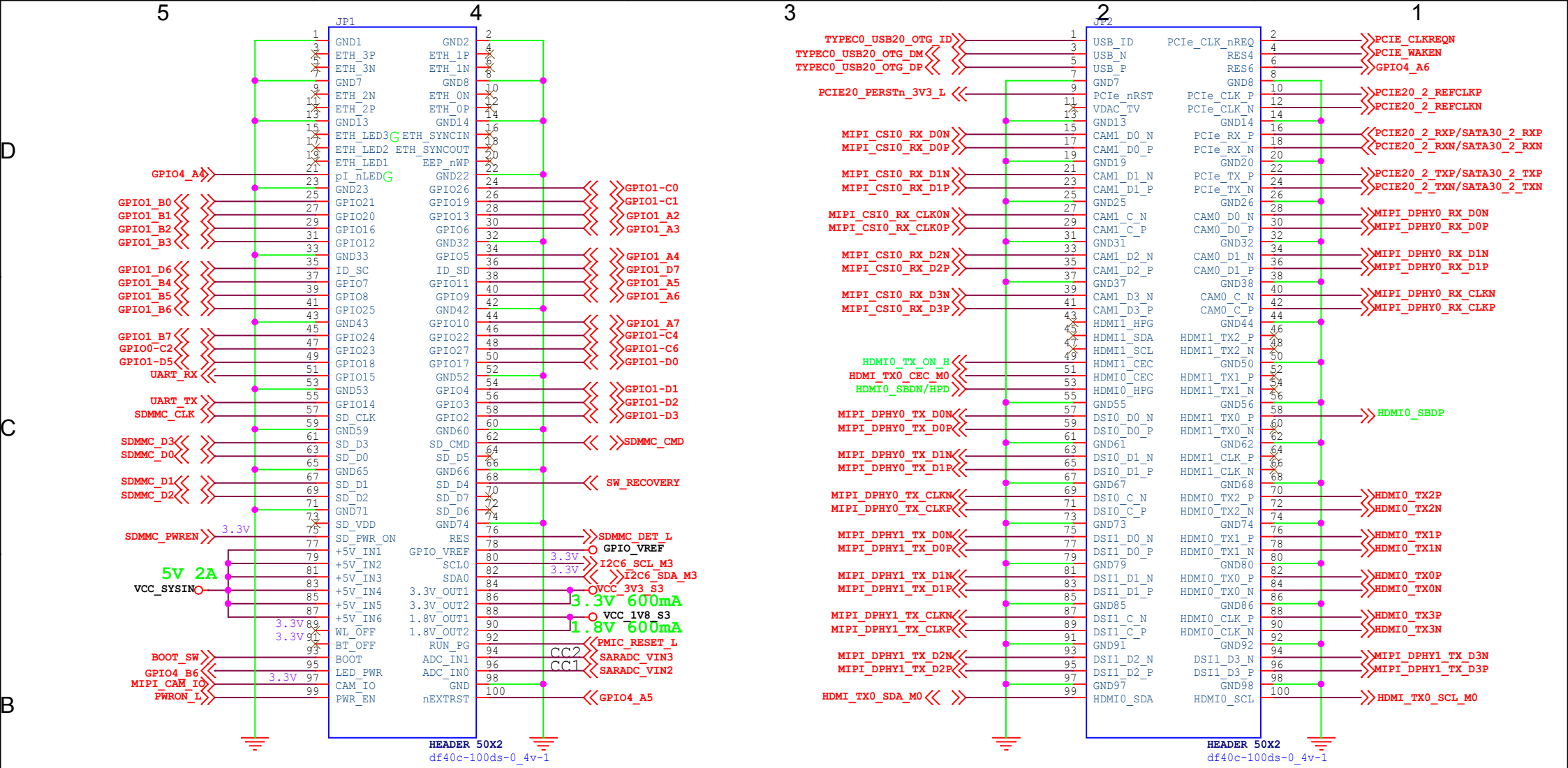
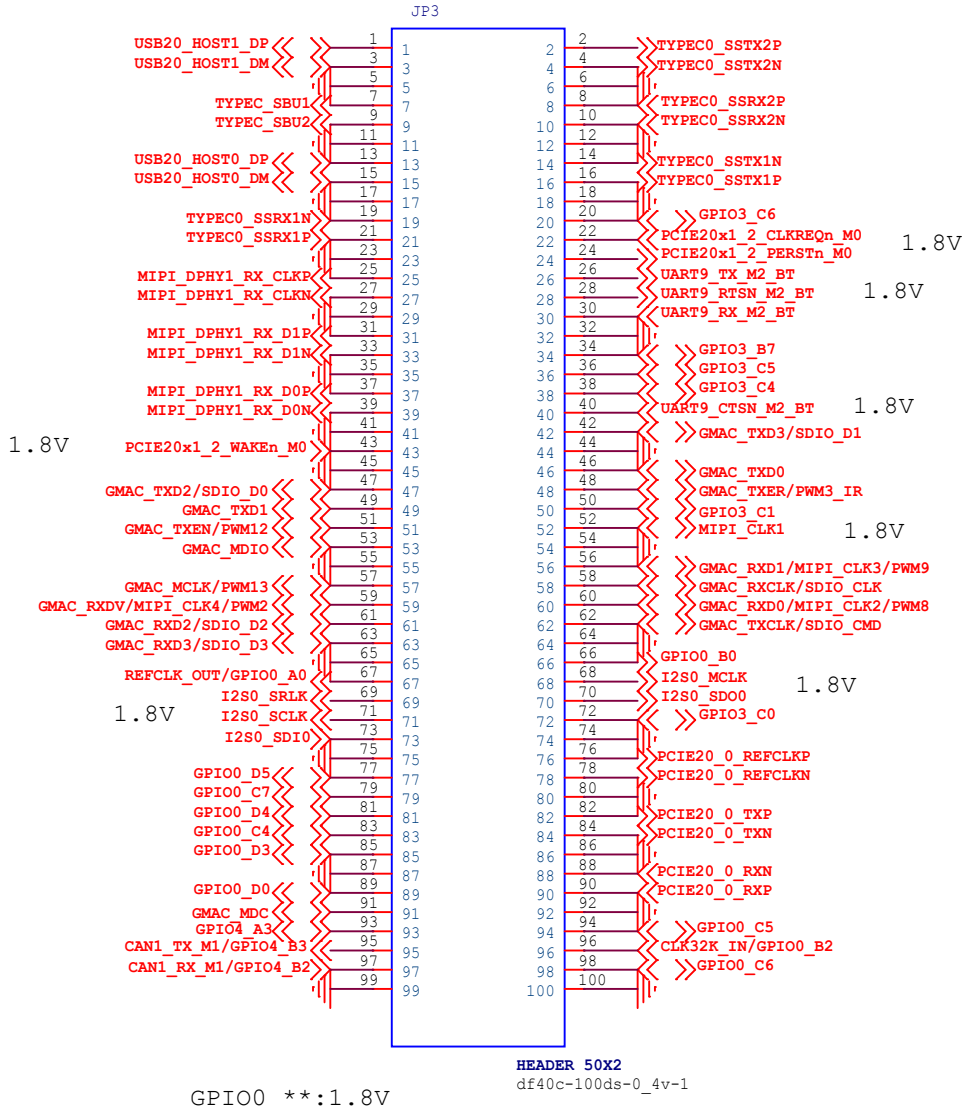
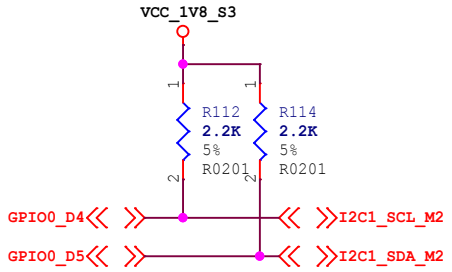
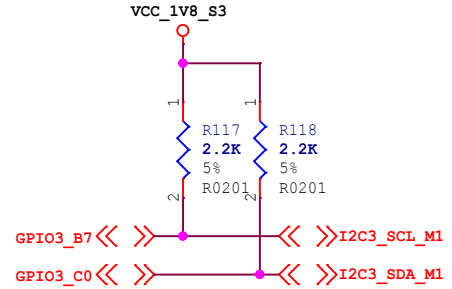


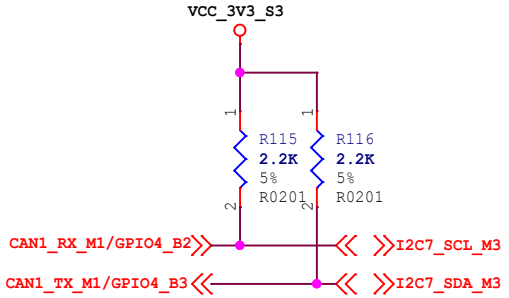
Shenzhen Xunlong Software Co., Ltd	
Project:	CM5_BASE-Tablet
File:	01.Con_JP1_JP2
Date:	Monday, January 13, 2025
Rev:	V1.1
Size:	A3
Sheet:	1
Of:	16



GPIO0_**:1.8V
GPIO1_C0-C7、D0-D5:1.8V
GPIO3_**:1.8V
GPIO4_**:3.3V
GMAC:1.8V
I2S0:1.8V

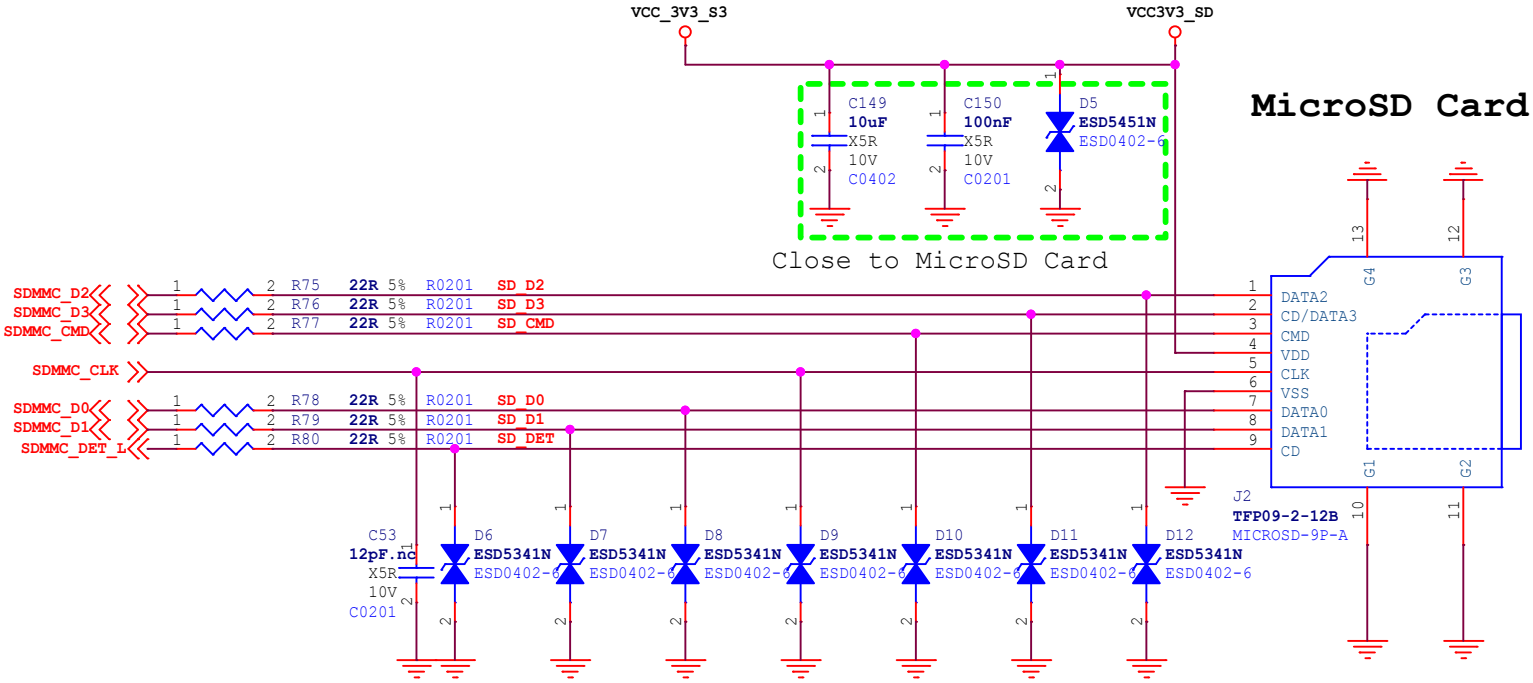






Shenzhen Xunlong Software Co., Ltd				
Project:	CM5_BASE-Tablet			
File:	02.CON_JP3			
Date:	Monday, January 13, 2025		Rev:	V1.1
Size:	A3	Sheet: 2	Of:	16

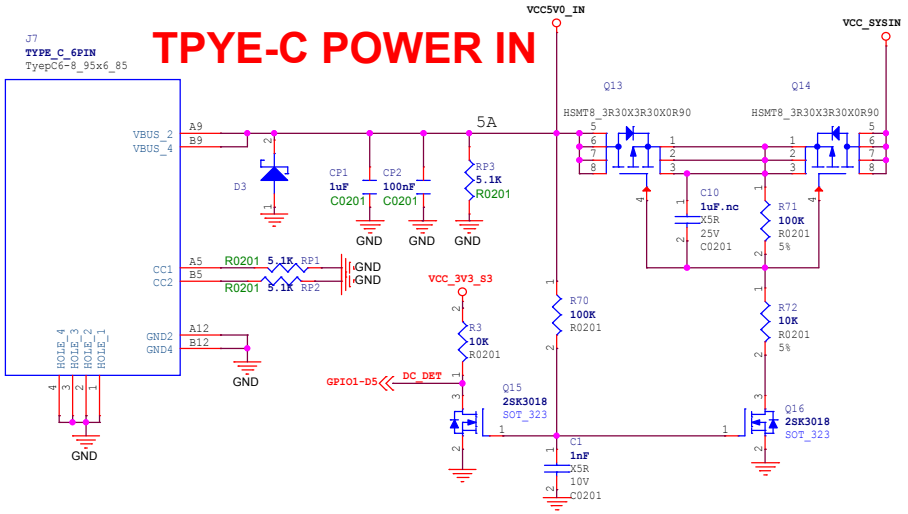
TF CARD



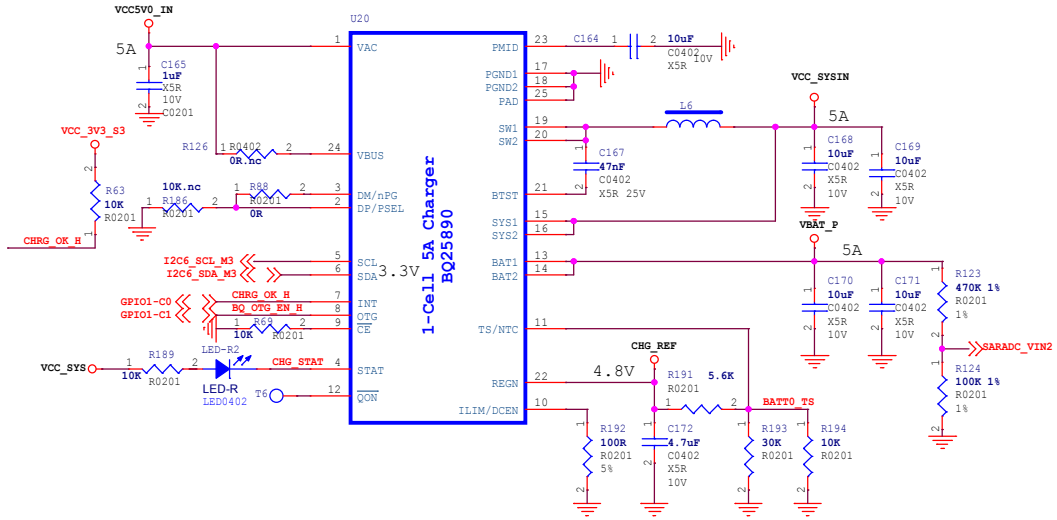
Rockchip Confidential

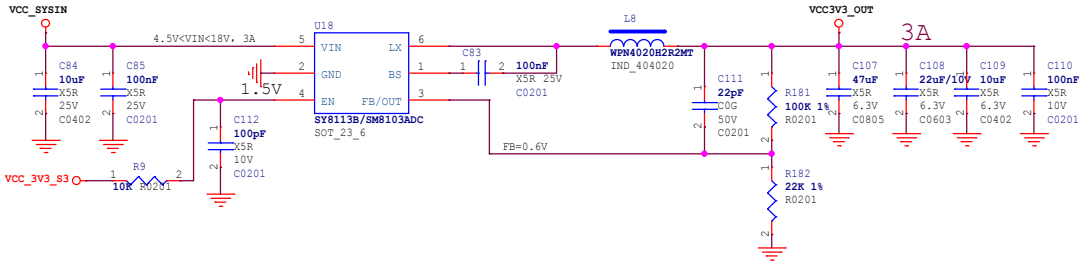
Shenzhen Xunlong Software Co., Ltd					
Project:	CM5_BASE-Tablet				
File:	03.Flash-TF Card				
Date:	Monday, January 13, 2025			Rev:	V1.1
Size:	A3	Sheet:	3	Of:	16

TPYE-C POWER IN

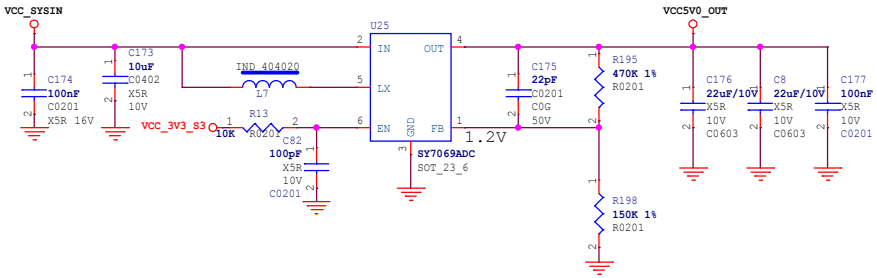


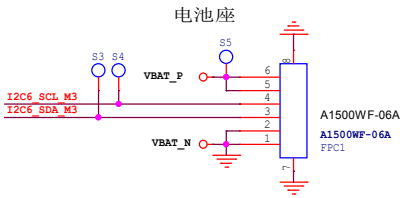
Charger IC





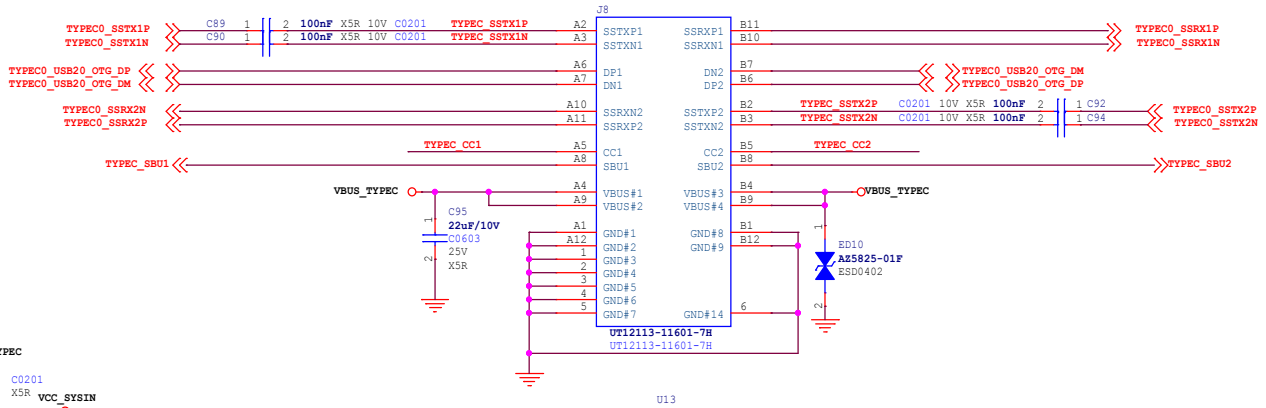
VCC_5V0



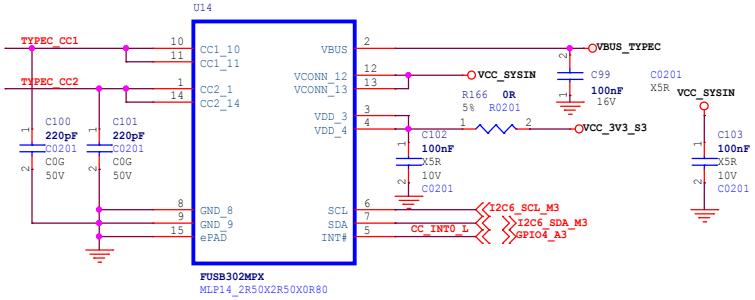


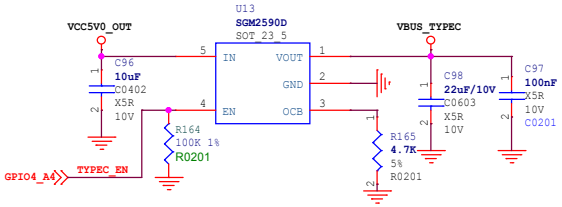
Shenzhen Xunlong Software Co., Ltd					
Project:	CM5_BASE-Tablet				
File:	04.CHARGEAR/EXT_PWR				
Date:	Monday, January 13, 2025			Rev:	V1.1
Size:	A3	Sheet:	4	Of:	16

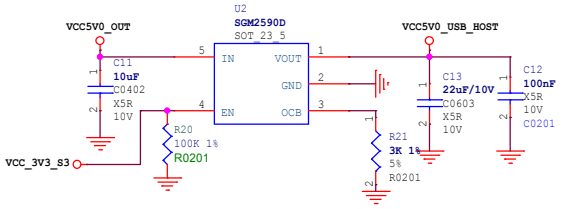
USB Type-C Port

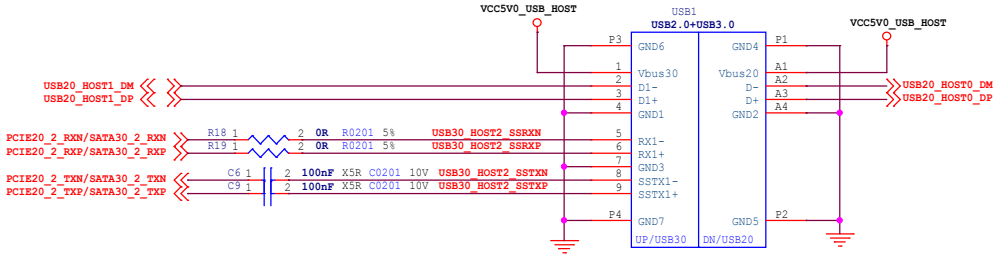


USB Type-C CC CTRL



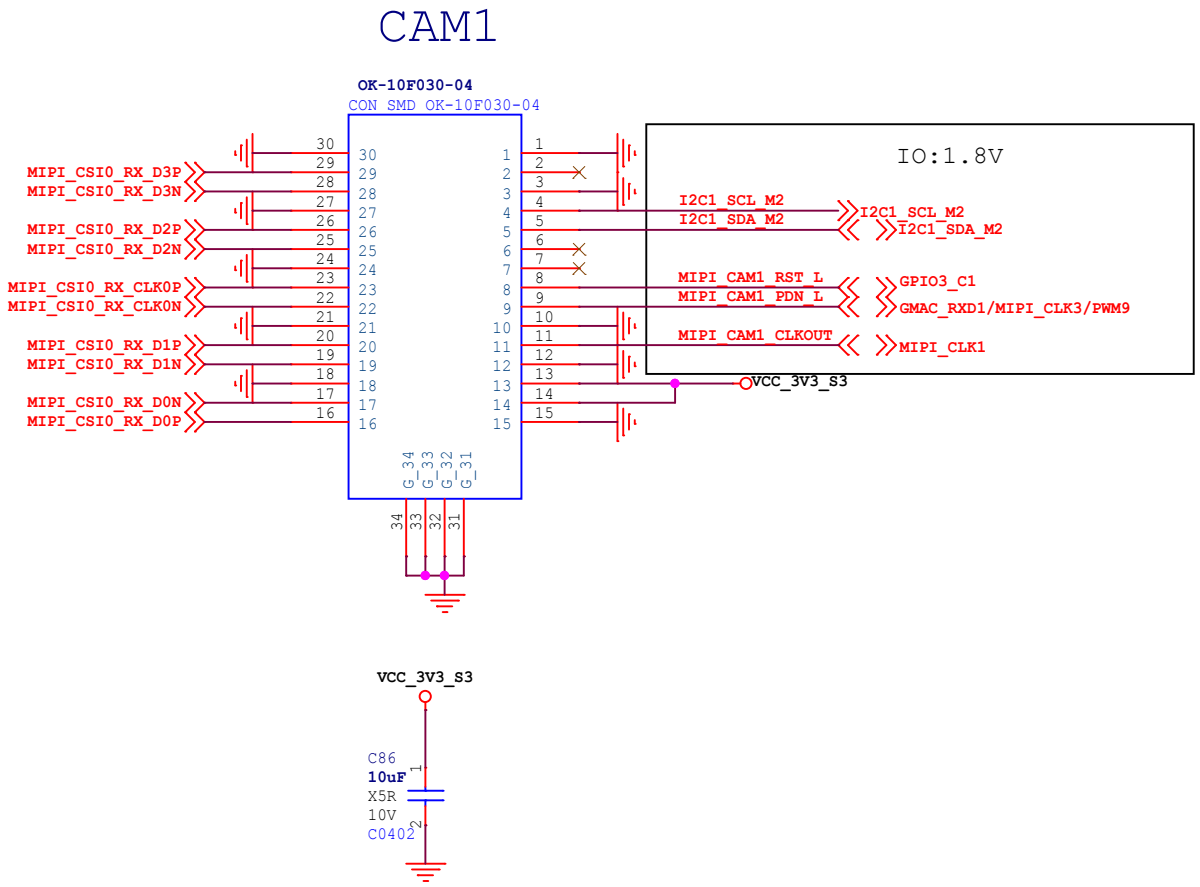






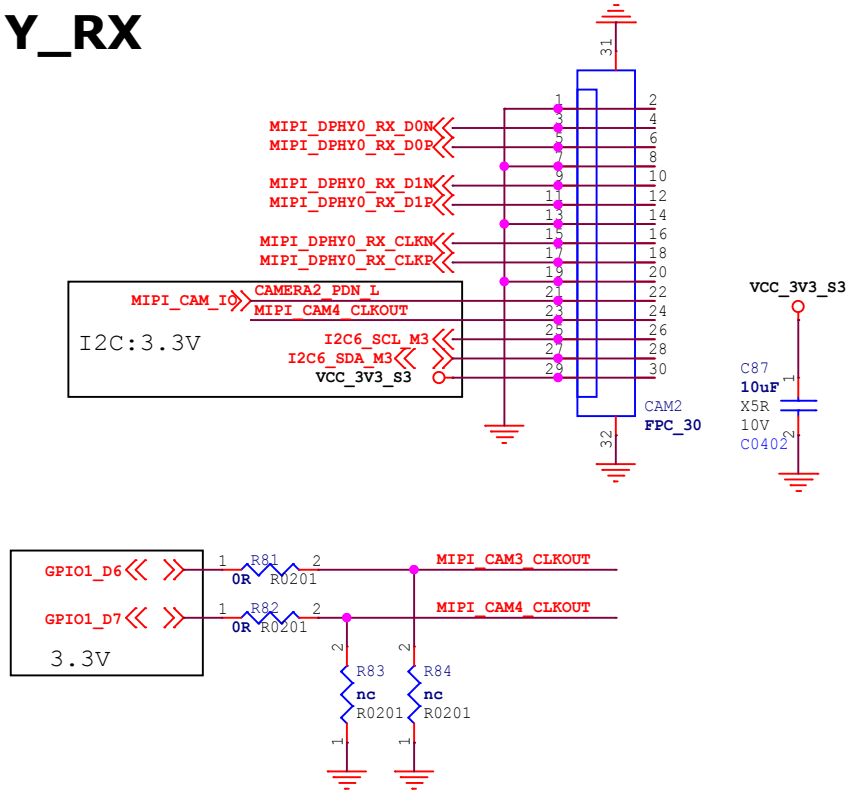
Shenzhen Xunlong Software Co., Ltd					
Project:	CM5_BASE-Tablet				
File:	05.Type-C /USB				
Date:	Monday, January 13, 2025			Rev:	V1.1
Size:	A3	Sheet:	5	Of:	16

MIPI-CSI0_RX

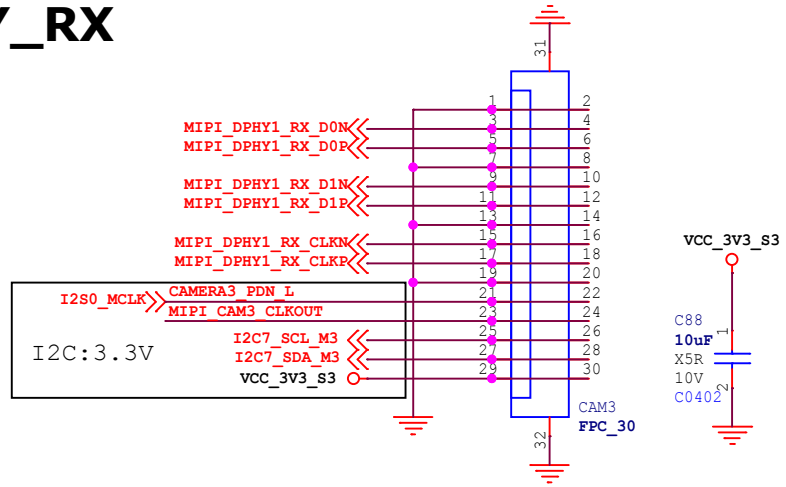


Shenzhen Xunlong Software Co., Ltd					
Project:	CM5_BASE-Tablet				
File:	06.VI-Cam MIPI CSI0-RX				
Date:	Monday, January 13, 2025			Rev:	V1.1
Size:	A3	Sheet:	6	Of:	16

VI-Camera DPHY_RX

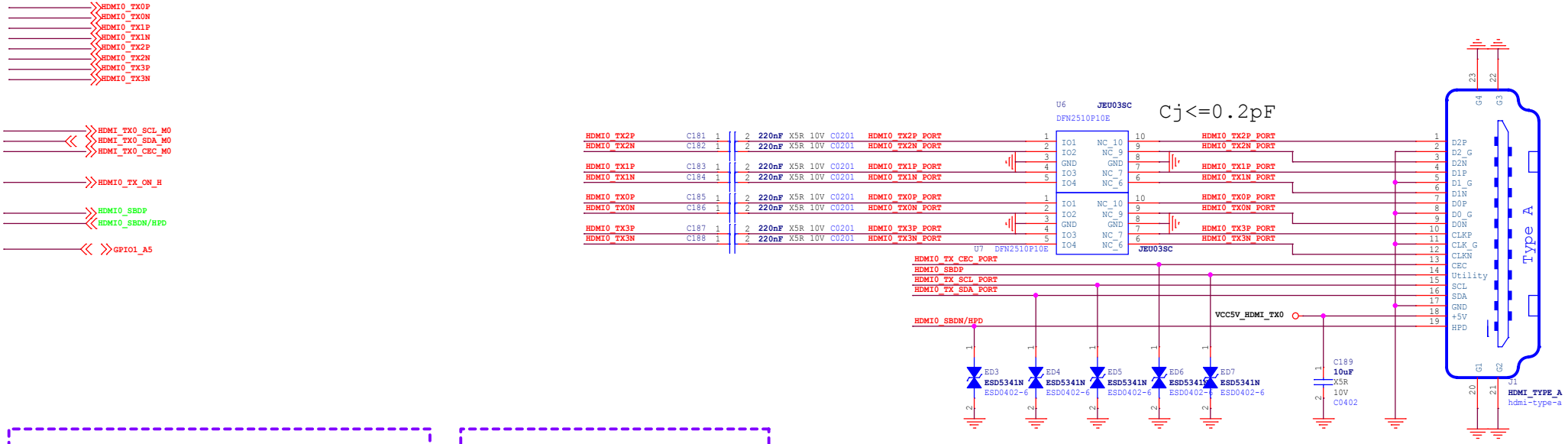


VI-Camera DPHY_RX

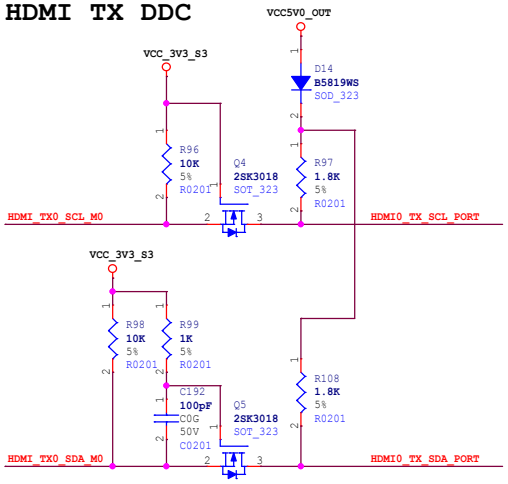


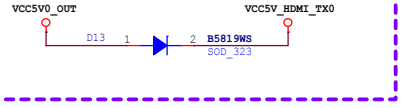
Shenzhen Xunlong Software Co., Ltd					
Project:	CM5_BASE-Tablet				
File:	07.VI-Cam MIPI-DPHY-RX				
Date:	Monday, January 13, 2025			Rev:	V1.1
Size:	A3	Sheet:	7	Of:	16

HDMI2.1 TX0

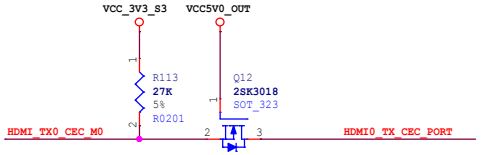


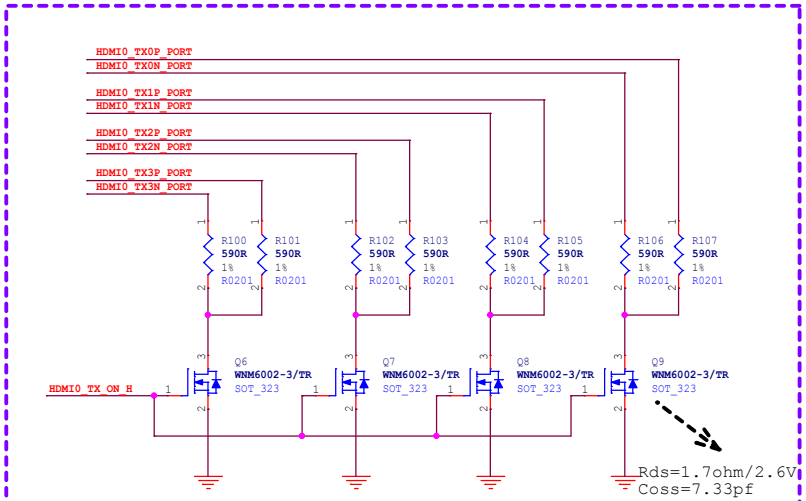
HDMI TX DDC





HDMI TX CEC

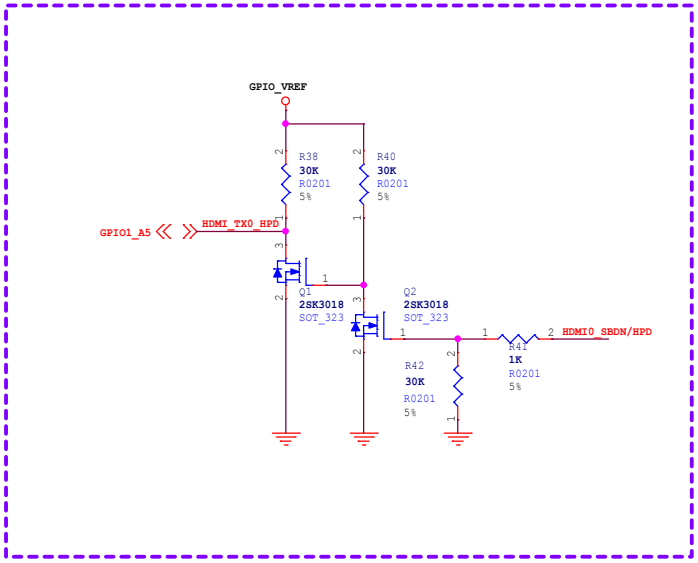




Note:

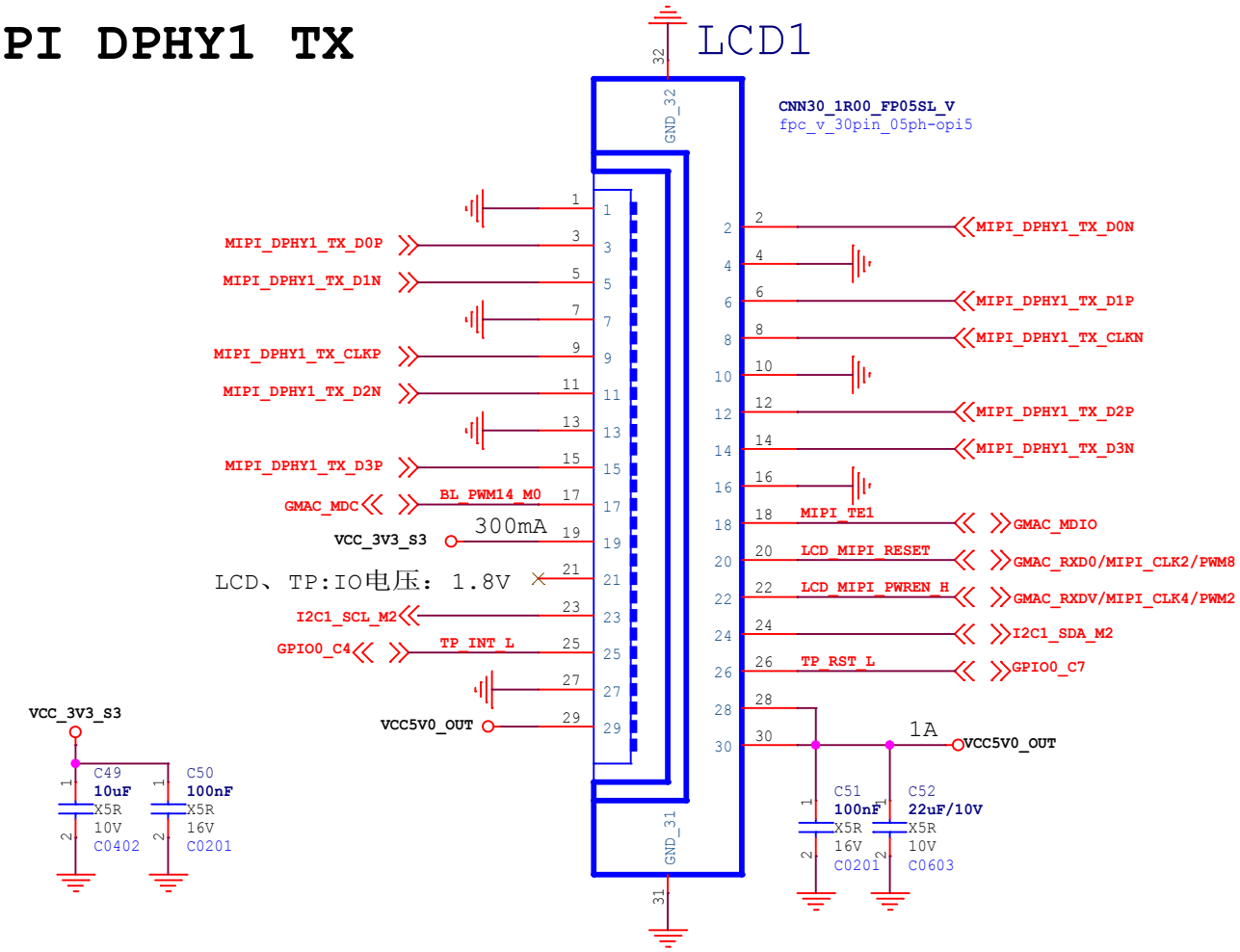
The controller only support AC coupled link In order to backward compatibility or to meet HDMI2.0 (1.4b) DC common mode spec and Voff, need do R based level-shift.

Switch on in HDMI2.0(TMDS) mode
Switch off in HDMI2.1(FRL) mode.



Shenzhen Xunlong Software Co., Ltd				
Project:	CM5_BASE-Tablet			
File:	08.VO-HDMI2.1 TX			
Date:	Monday, January 13, 2025		Rev:	V1.1
Size:	A3	Sheet: 8	Of:	16

MIPI DPHY1 TX



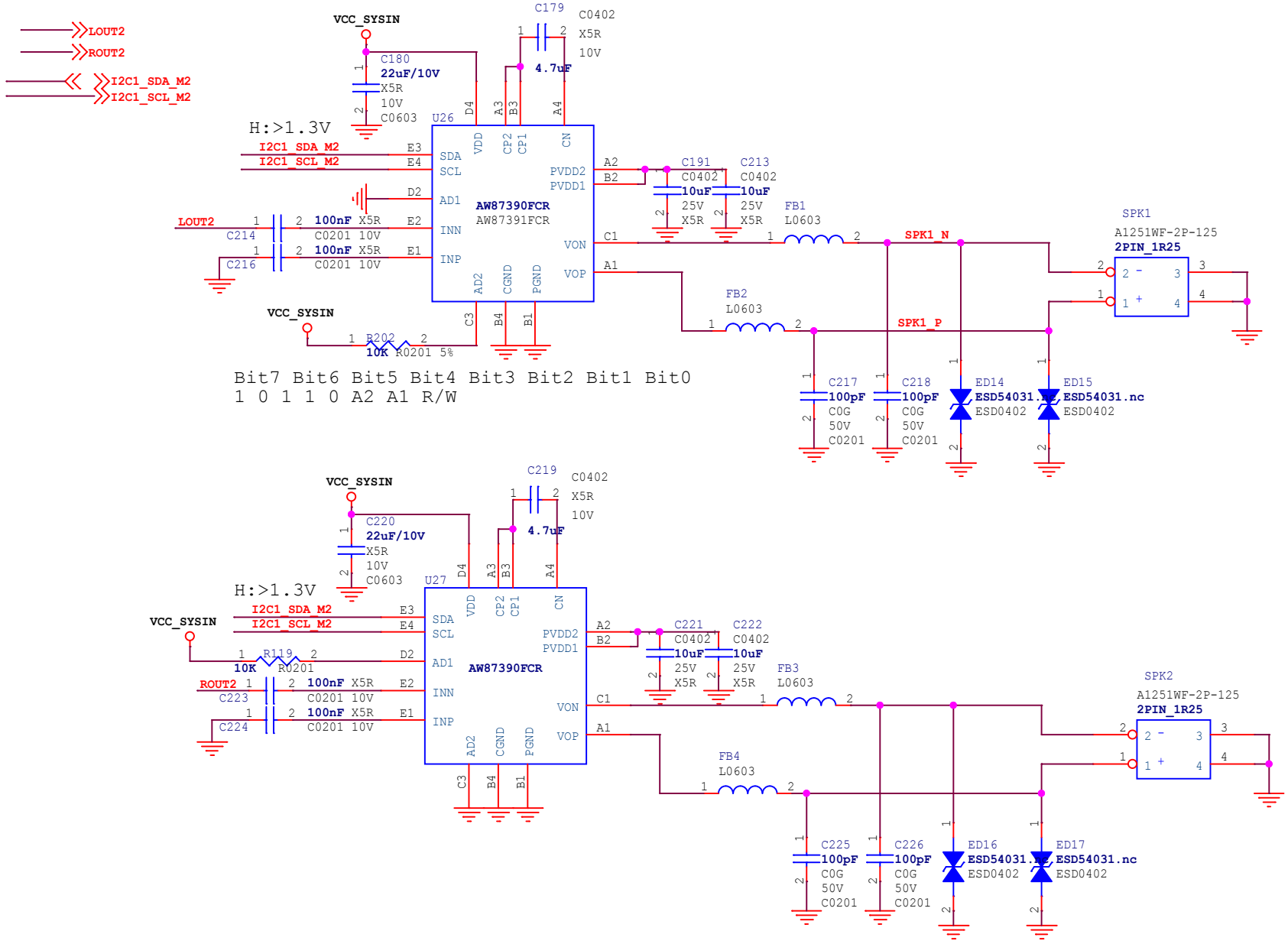
FPC Pin List

Pin1 :GND
Pin2 :D0N
Pin3 :D0P
Pin4 :GND
Pin5 :D1N
Pin6 :D1P
Pin7 :GND
Pin8 :CLKN/AUXN
Pin9 :CLKP/AUXP
Pin10:GND
Pin11:D2N
Pin12:D2P
Pin13:GND
Pin14:D3N
Pin15:D3P
Pin16:GND
Pin17:LCD_PWM_BL
Pin18:LCD_TE
Pin19:VCC3V3_LCD
Pin20:LCD_RST
Pin21:LCD_ID
Pin22:LCD_PWREN
Pin23:TP_I2C_SCL
Pin24:TP_I2C_SDA
Pin25:TP_INT
Pin26:TP_RST
Pin27:GND
Pin28:5V0
Pin29:5V0
Pin30:5V0

LCD、TP:IO电压：1.8V

Shenzhen Xunlong Software Co., Ltd					
Project:	CM5_BASE-Tablet				
File:	09.VO-MIPI_LCD				
Date:	Monday, January 13, 2025			Rev:	V1.1
Size:	A3	Sheet:	9	Of:	16

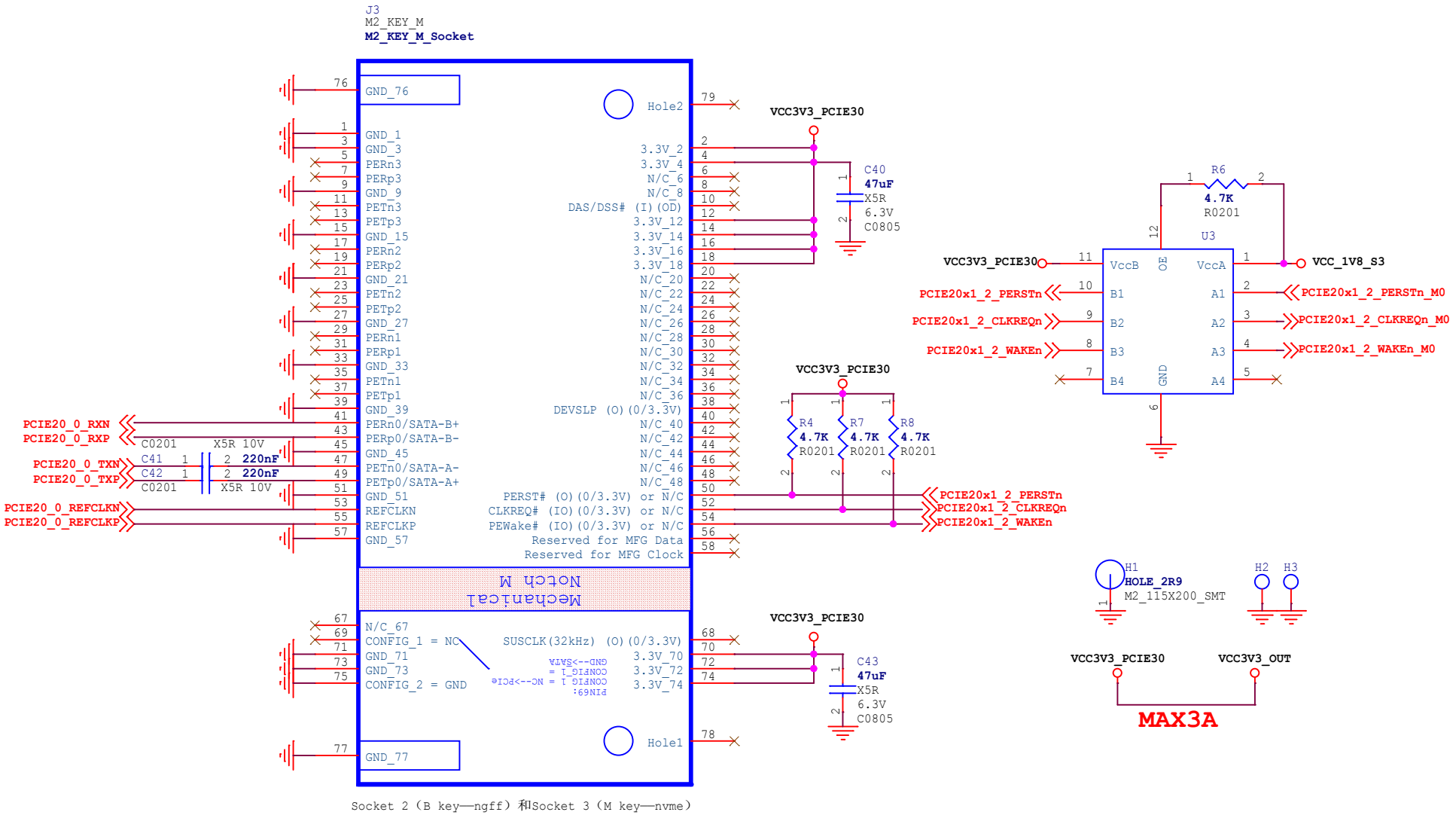
SPK PA



Shenzhen Xunlong Software Co., Ltd					
Project:	CM5_BASE-Tablet				
File:	10.SPK PA				
Date:	Monday, January 13, 2025			Rev:	V1.1
Size:	A3	Sheet:	10	Of:	16

M.2_PCIe/SATA_ Interface

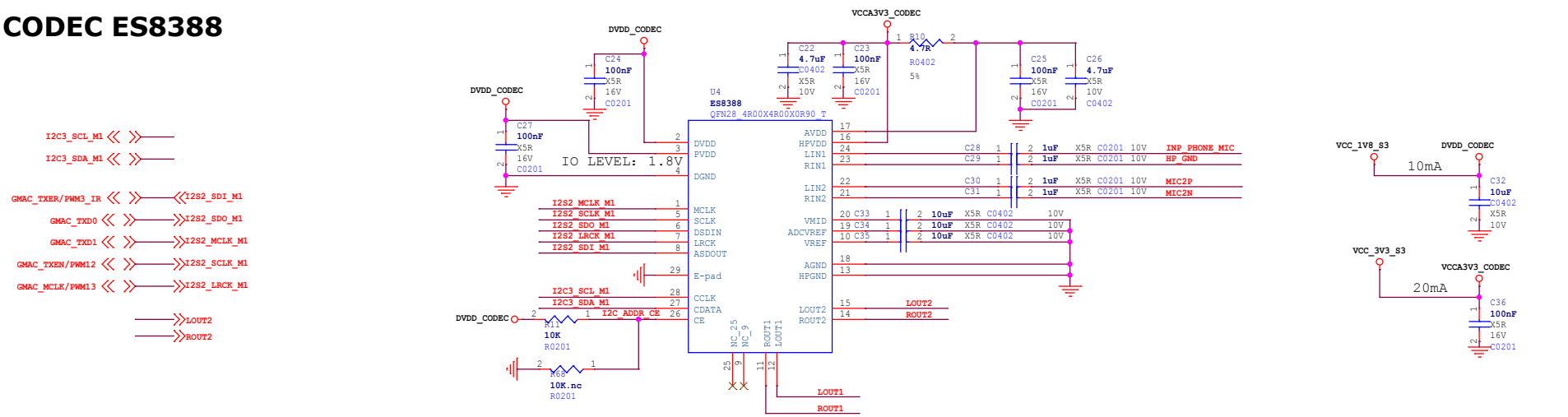
M.2 NVME



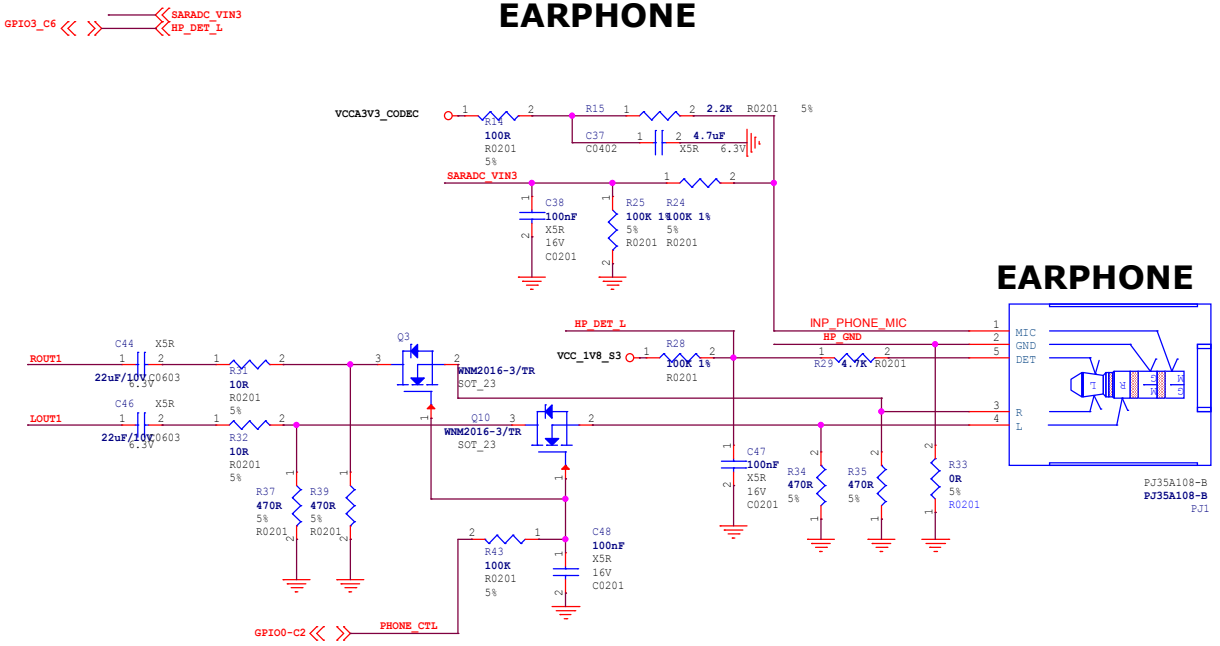
Socket 2 (B key—ngff) 和Socket 3 (M key—nvme)

Shenzhen Xunlong Software Co., Ltd					
Project:	CM5_BASE-Tablet				
File:	11.M.2_PCIe_SATA				
Date:	Monday, January 13, 2025			Rev:	V1.1
Size:	A3	Sheet:	11	Of:	16

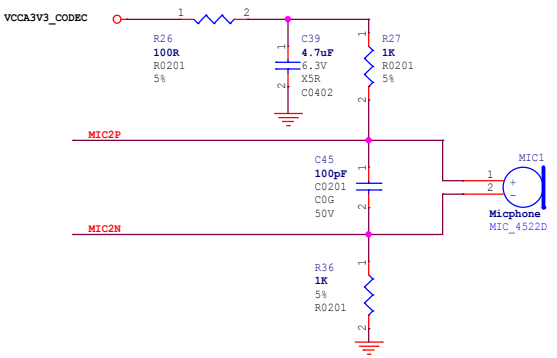
CODEC ES8388



EARPHONE

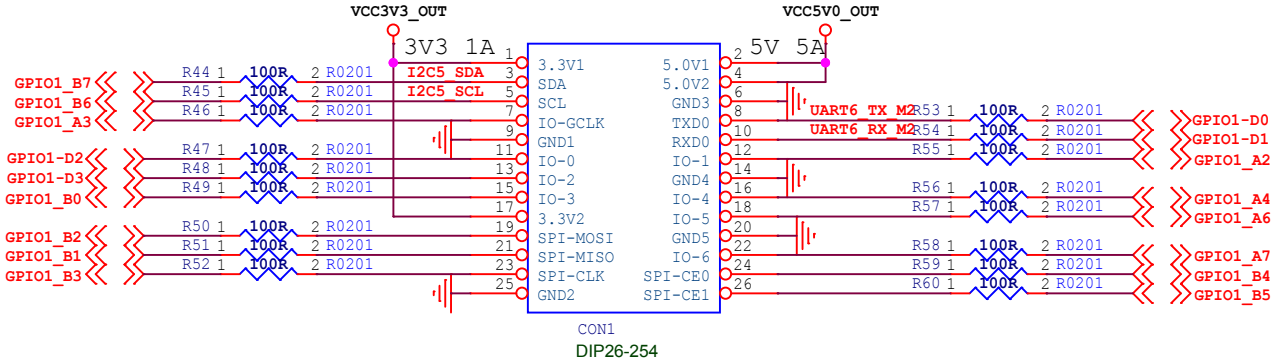


Analog MIC

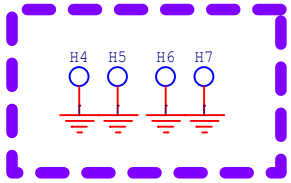


Shenzhen Xunlong Software Co., Ltd					
Project:	CM5_BASE-Tablet				
File:	12.Audio Codec(ES8388)				
Date:	Monday, January 13, 2025			Rev:	V1.1
Size:	A3	Sheet:	12	Of:	16

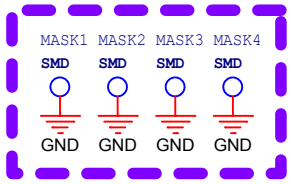
EXT I/O



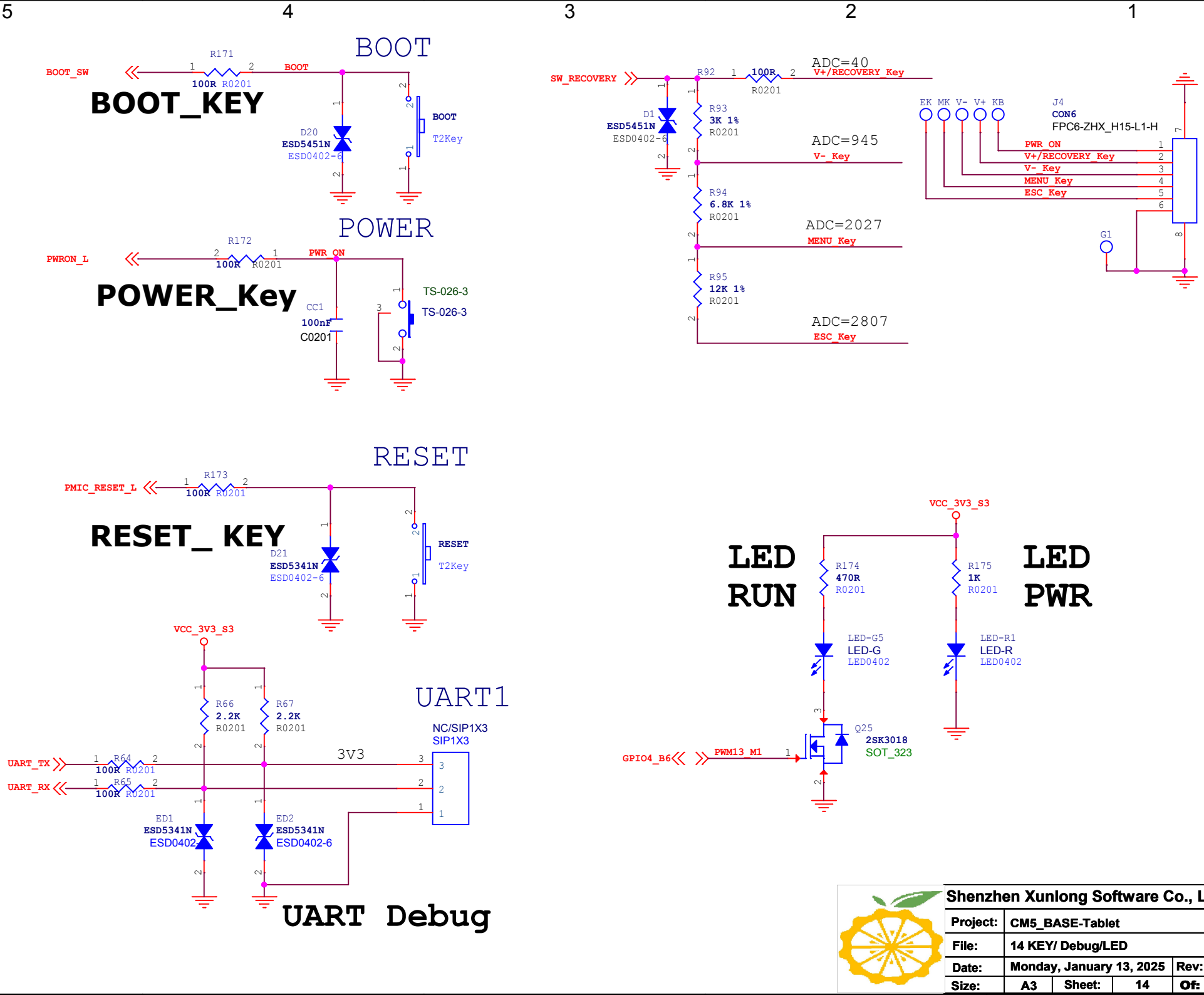
底板固定孔



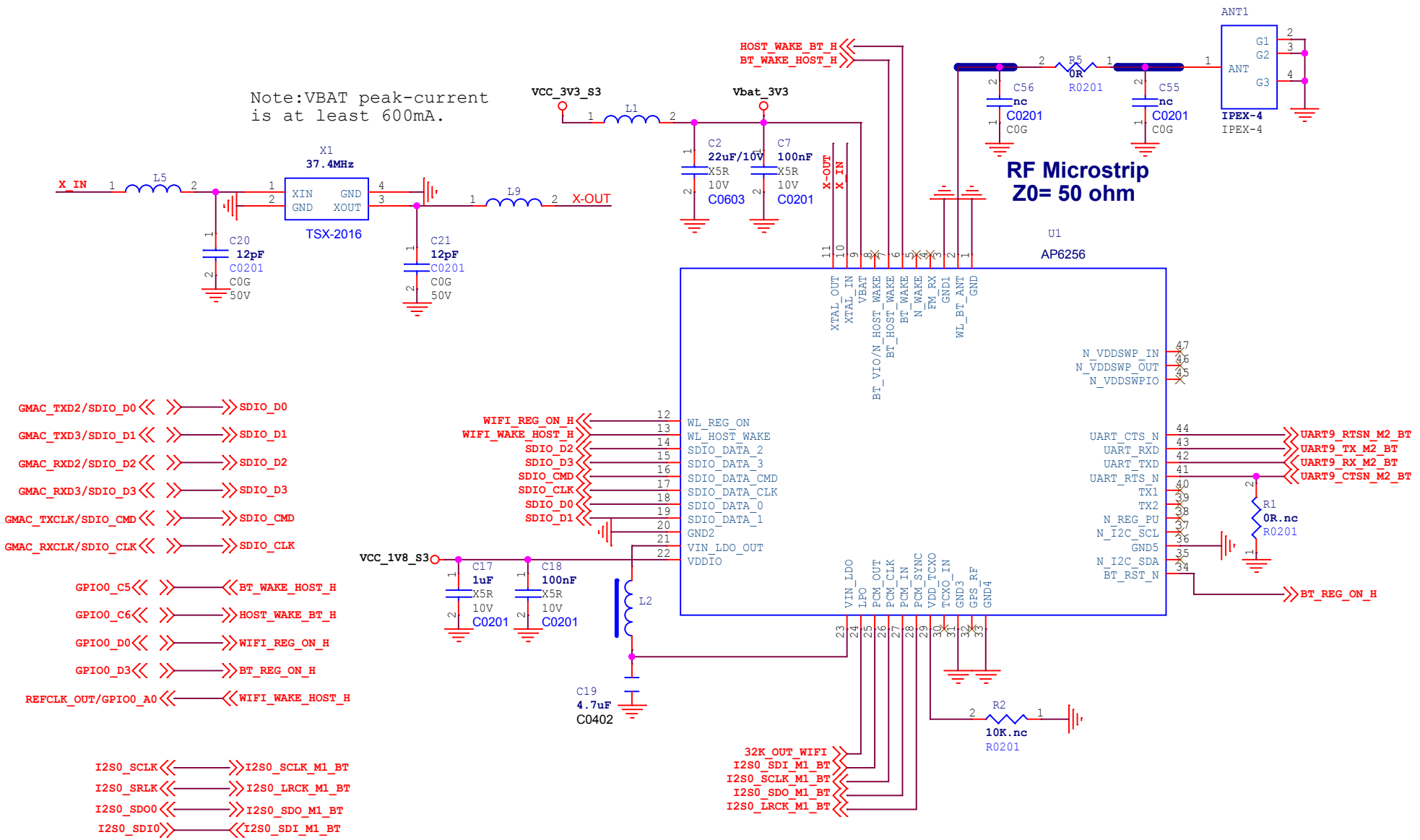
核心板固定孔



Shenzhen Xunlong Software Co., Ltd					
Project:	CM5_BASE-Tablet				
File:	13.EXT I/O				
Date:	Monday, January 13, 2025			Rev:	V1.1
Size:	A3	Sheet:	13	Of:	16

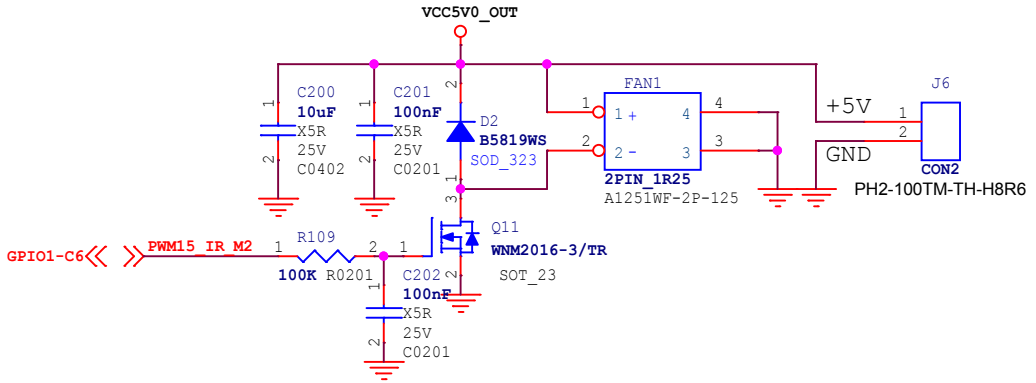


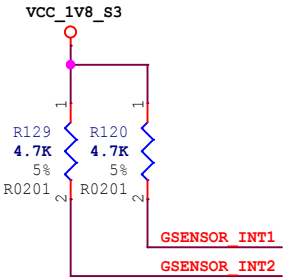
Shenzhen Xunlong Software Co., Ltd					
Project:	CM5_BASE-Tablet				
File:	14 KEY/ Debug/LED				
Date:	Monday, January 13, 2025			Rev:	V1.1
Size:	A3	Sheet:	14	Of:	16

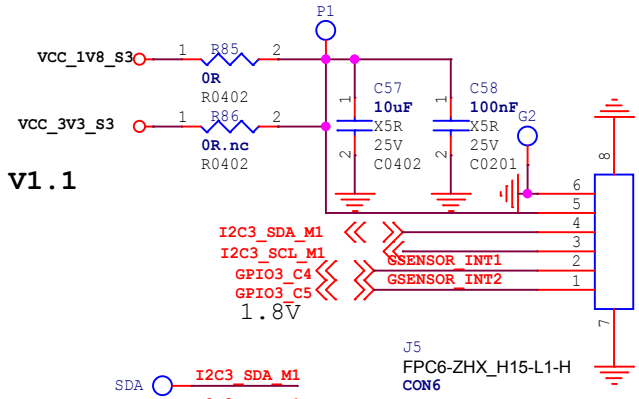


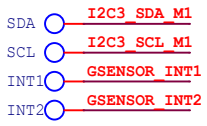
Shenzhen Xunlong Software Co., Ltd	
Project:	CM5_BASE-Tablet
File:	15.WIFI/BT-SDIO
Date:	Monday, January 13, 2025
Rev:	V1.1
Size:	A3
Sheet:	15
Of:	16

FAN_POWER

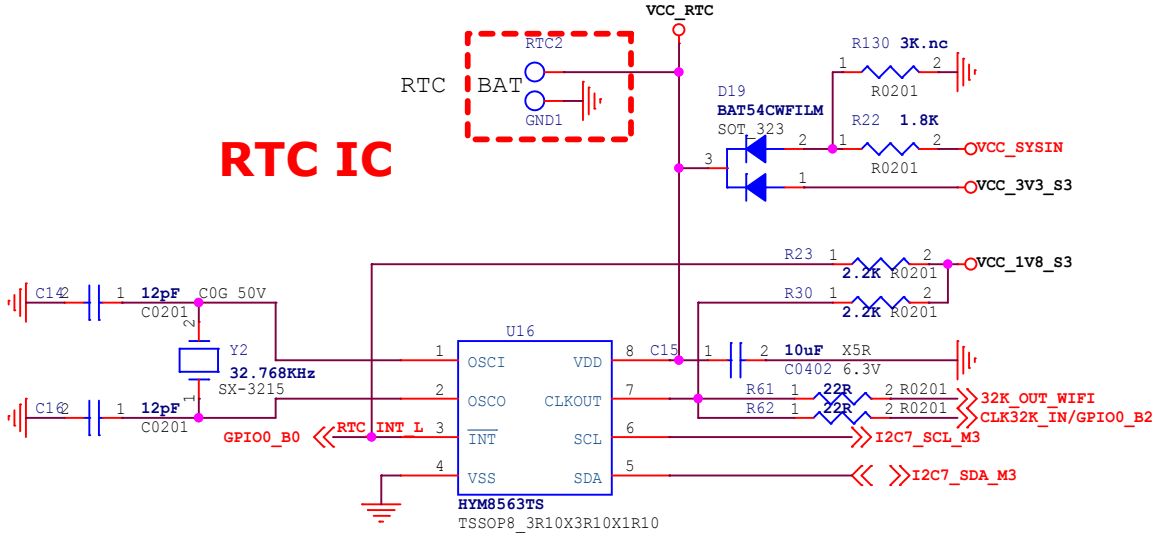






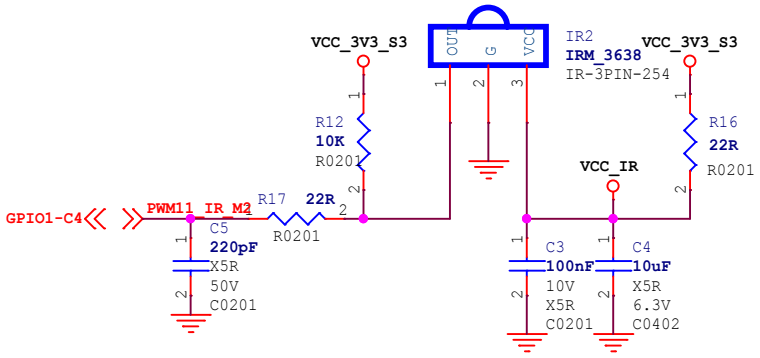


RTC IC



Address:Read A3H,Write A2H

IR Receiver



Shenzhen Xunlong Software Co., Ltd					
Project:	CM5_BASE-Tablet				
File:	16.Sensor/FAN/RTC/IR				
Date:	Monday, January 13, 2025			Rev:	V1.1
Size:	A3	Sheet:	16	Of:	16