

MCU1C	
<del>PC13</del>	<del>PD8</del>
<del>PC14-OSC32_IN</del>	<del>PD9</del>
<del>PC15-OSC32_OUT</del>	<del>PD10</del>
<del>PC0</del>	<del>PD11</del>
<del>PC1</del>	<del>PD12</del>
<del>PC2_C</del>	<del>PD13</del>
<del>PC3_C</del>	<del>PD14</del>
<del>PC4</del>	<del>PD15</del>
<del>PC5</del>	<del>PD0</del>
<del>PC6</del>	<del>PD1</del>
<del>PC7</del>	<del>PD2</del>
<del>PC8</del>	<del>PD3</del>
<del>PC9</del>	<del>PD4</del>
<del>PC10</del>	<del>PD5</del>
<del>PC11</del>	<del>PD6</del>
<del>PC12</del>	<del>PD7</del>

MC3D		
<del>PE0</del>	PF0	OCCTOSMPM P2 C
<del>PE1</del>	PF1	OCCTOSMPM P2 C
<del>PE2</del>	PF2	OCCTOSMPM P2 C
<del>PE3</del>	PF3	OCCTOSMPM P2 C
<del>PE4</del>	PF4	OCCTOSMPM P2 C
<del>PE5</del>	PF5	
<del>PE6</del>	PF6	
<del>PE7</del>	PF7	SPU SCR
<del>PE8</del>	PF8	SPU SD
<del>PE9</del>	PF9	SPU SD
<del>PE10</del>	PF10	
<del>PE11</del>	PF11	
<del>PE12</del>	PF12	
<del>PE13</del>	PF13	
<del>PE14</del>	PF14	
<del>PE15</del>	PF15	

Pin configuration diagram for the STM32H723ZG16 microcontroller. The diagram shows a 48-pin package with pins labeled P0 to P15. Pins P0, P1, P2, P3, P4, and P5 are marked with a red 'X' and a red circle, indicating they are not connected. Pins P6, P7, P8, P9, P10, P11, P12, P13, P14, and P15 are connected to various signals: P6 to P0B0, P7 to P0B1, P8 to P0B2, P9 to P0B3, P10 to P0B4, P11 to P0B5, P12 to P0B6, P13 to P0B7, P14 to P0B8, and P15 to P0B9. The diagram also shows the connection of the P0B0 pin to the P0B0 pin of the OCTOSPIM P2 N3.

[illegible]

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