

Computer Arithmetic

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Disclaimer

This document follows the slides and lessons given by Mr.OUDJIDA, written in a mathematical textbook format, freedoms are taken when writing the course.

To separate the contents of the course to actual additions or out of context information, a black band will be added by its side like the globing this comment.

USE IT AT YOUR OWN RISK!

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Chapter 1

Classical logic

The most basic part of executing a computation on a machine is to describe the most basic information, which is true/false, and then compose them into a statement or a proposition.

Informally, given a sentence, it is said to be a *statement* if

- it is declarative, either affirmative or negative.
- its possible truth values are true or false.
- it is verifiable in reality.

On those statements, we have some rules to give them truth values

- Law of identity: $A = A$ is a true statement.
- Law of non-contradiction: $\neg(A \wedge \neg A)$ is false statement.
- Law of excluded middle: either $\neg A$ or A is true statement.

Now we will formalize calculations on boolean variables which is known as Boolean algebra, it will help us analyze and create circuits later on, also simplifying them to have less components.

1.1. Boolean Algebra

Let $\mathbb{B} := \{0, 1\}$ denote the set of boolean values, which can be represented too with true/false. Any variable ta

Definition 1.1.1 (Boolean Variable): Let x be a variable, x is said to be a boolean variable if it can assume values in \mathbb{B} . Let $f : \mathbb{B}^n \rightarrow \mathbb{B}$ a map, f is called a boolean function.

Boolean functions will be the main study of Boolean algebra, how they can be written, expressed and modified without altering its values, the following operations will be useful for operating on boolean variables and construct functions.

Definition 1.1.2 (Boolean Operations): Let x, y be two boolean variables, we define the operations $+, \cdot, \neg$ to be the logical or, and, not respectively, which have the following truth tables.

y	x	\bar{x}	$x + y$	$x \cdot y$
0	0	1	0	0
0	1	1	1	0
1	0	1	1	0
1	1	1	1	1

there are some other operations that are as follows

- $x \mid y = \bar{x} \cdot \bar{y}$.
- $x \otimes y = x \cdot \bar{y} + \bar{x} \cdot y$.
- $x \Rightarrow y = \bar{x} + y$.

Proposition 1.1.3 (Boolean Identities):

$\bar{\bar{x}} = x$	
$x + x = x$	$x \cdot x = x$
$x + 0 = x$	$x \cdot 1 = x$
$x + 1 = 1$	$x \cdot 0 = 0$
$x + y = y + x$	$x \cdot y = y \cdot x$
$x + (y + z) = (x + y) + z$	$x \cdot (y \cdot z) = (x \cdot y) \cdot z$
$x(y + z) = xz + yz$	$x + yz = (x + y) \cdot (x + z)$
$\bar{x + y} = \bar{x} \cdot \bar{y}$	$\bar{x \cdot y} = \bar{x} + \bar{y}$
$x + \bar{x} = 1$	$x \cdot \bar{x} = 0$

Definition 1.1.4 (Duality): Let $f : \mathbb{B}^n \rightarrow \mathbb{B}$ be a boolean function, we define the dual of f as the map $(x_1, \dots, x_n) \mapsto \overline{f(\bar{x}_1, \dots, \bar{x}_n)}$. We can obtain the dual of a function f by swapping $+$ with $\cdot, 0$ with 1 and keep the variables unchanged.

Definition 1.1.5 (Literal/Minterm/Maxterm): Let x_1, \dots, x_n be boolean variables and y_1, \dots, y_n such that $\forall i \in [1, n], y_i = x_i \vee y_i = \bar{x}_i$.

- A literal is a proposition in the form of x or \bar{x} with x a boolean variable.
- A minterm of x_1, \dots, x_n is the product $y_1 \cdot y_2 \cdots y_n$.
- A maxterm of x_1, \dots, x_n is the sum $y_1 + y_2 \cdots + y_n$.

Definition 1.1.6 (Conjunctive/Disjunctive Normal Form): Let $f : \mathbb{B}^n \rightarrow \mathbb{B}$ be a boolean function, x_1, \dots, x_n boolean variables.

- DNF: $f(x_1, \dots, x_n) = \sum_{i=1}^k X_i$ where X_i are minterms.
- CNF: $f(x_1, \dots, x_n) = \prod_{i=1}^k X_i$ where X_i are maxterms.

Proposition 1.1.7:

1. the dual of a DNF is a CNF and vice versa.
2. Every boolean function can be written with only the defined connectives.
3. Every boolean function can be expressed only using one of those sets $\{+, -\}, \{\cdot, -\}, \{| \}$, we call them a complete set of connectives.
4. Any boolean function can be written in the CNF or DNF.

Proof.

1. A minterm is of the form $y_1 \dots y_n$ then its dual is $y_1 + \dots + y_n$ which is a maxterm, now if f is in a DNF then it is the sum of minterms, the dual will become a product of maxterms which is a CNF, its easy to verify the rest.
2. Let $f : \mathbb{B}^n \rightarrow \mathbb{B}$ a boolean function, we define $g : \mathbb{B}^n \rightarrow \mathbb{B}$ as follows $(x_1, \dots, x_n) \mapsto \sum_{i=1}^{2^n} \sigma_i(x_1, \dots, x_n) \cdot f(x_1, \dots, x_n)$ where $\sigma_i(x_1, \dots, x_n)$ is defined as if we take the i in base 2, $i = i_n i_{n-1} \dots i_1 i_0$ then $\sigma_i(x_1, \dots, x_n) = y_1 \dots y_n$ and if $i_j = 1$ then $y_j = x_j$ otherwise $y_j = \bar{x}_j$. Notice that $\sigma_i(x_1, \dots, x_n) = 1$ if and only if $\overline{x_n \dots x_1}^2 = i$. So we have for $(x_1, \dots, x_n) \in \mathbb{B}^n, g(x_1, \dots, x_n) = f(x_1, \dots, x_n)$ thus f can be written only with the connectives.
3. To prove this statement, we just need to use the fact that any function can be written using only $+, \cdot, -$.

- We have by De Morgans laws that $x \cdot y = \overline{\bar{x} + \bar{y}}$ thus $+, -$ is enough to express every function.
- Same can be used to express $x + y = \overline{\bar{x} \cdot \bar{y}}$.
- Now we can use the NAND to write everything, notice that $x|x = \bar{x}$ and $(x|y)|(x|y) = x \cdot y$ thus we use the previous statement and we get that $\{| \}$ is a complete set of connectives.

4. Notice that in the first statement we proved that any boolean function can be written in the DNF, using the same function σ_i we can construct a DNF, it will be of the form $g : (x_1, \dots, x_n) \mapsto \prod_{i=1}^{2^n} \overline{f(x_1, \dots, x_n)} \cdot \sigma_i(x_1, \dots, x_n)$.

□

1.2. Logic Gates & Digital Circuits

To be able to use and/or/not in circuits, we introduce the most basic circuit components called logic gates, as the table below shows

Not	And	Or					
A	S	A	B	S	A	B	C
0	1	0	0	0	0	0	0
1	0	0	1	1	0	1	0
		1	0	1	1	0	0
		1	1	1	1	1	1

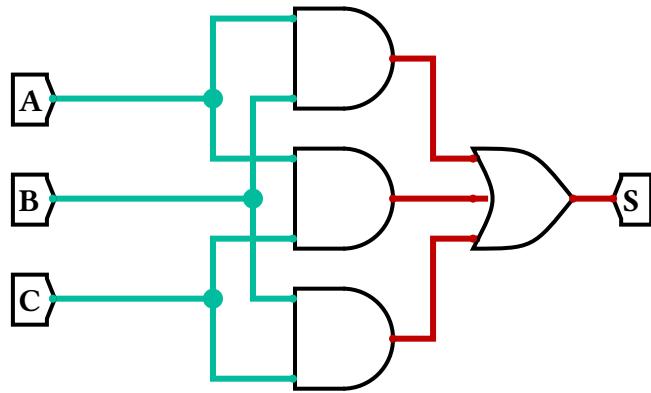
Thus we can use these to represent some circuits that behave as logical circuits called digital circuits.

Example:

- A committee of n individuals decide issues for an organization. Each individual votes either yes or no for each proposal that arises. The proposal is passed if it receives at least p votes. Its easy to notice that the solution is equal to

$$y = \sum_{1 \leq i_1 < \dots < i_p \leq n} x_{i_1} x_{i_2} \dots x_{i_p}$$

where x_1, \dots, x_n represent the votes of the individuals and y the proposal passing. In case, $n = 3$ and $p = 2$ we get



1.3. Transistors

One of the biggest advancements in our modern world is the creation of a transistor, in principle the idea is simple, a transistor is simply an electrically controlled switch. We use this to materialize the logic gates to be able to use them directly.

1.3.1. Circuit Simplification & Reduction Methods

1. Karnaugh Maps
2. Quine-McCluskey Method

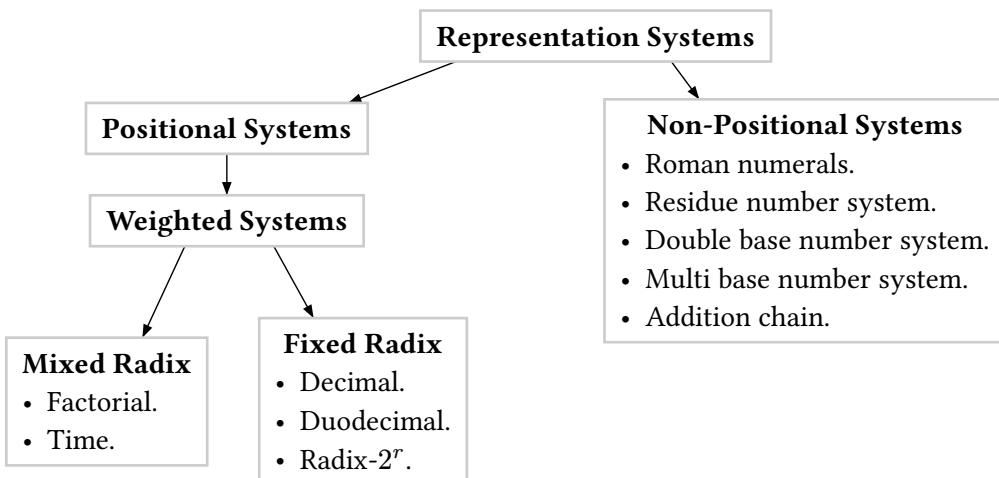
Chapter 2

Arithmetics

Computer arithmetic is the process of using algorithms for doing basic operations on numbers like addition multiplication... etc. To be able to do those operations, a representation of the numbers is needed, which will be the first part of the course.

2.1. Positive Integer Representations

Multiple numeral systems were developed throughout history, the roman numerals were the oldest that are still in use, but not in any complicated systems due to the difficulty of doing arithmetic operations on them.



2.1.1. Weighted Representation System

Let $I = \llbracket 0, n - 1 \rrbracket$, and $\{D_i\}_{i \in I}$ be sets of digits, the digit vector $x = (x_{n-1}, \dots, x_0) \in D_{n-1} \times \dots \times D_0$ and let w a weight vector $w = (w_n, \dots, w_0) \in \mathbb{Z}^n$, we have that the integer mapping of x in this weighted system is $w^t x =$

$\sum_{i=0}^{n-1} x_i w_i$. The number of combinations possible is $\prod_{i=0}^{n-1} \# D_i$ but notice that there representations are not necessarily unique.

2.1.2. Mixed Radix Representation System

The mixed radix representation is the same as weighted but has a different way to get the weight vector, we consider a radix vector $r = (r_{n-1}, \dots, r_0)$, $w_0 = 1$ and $w_i = w_{i-1} \cdot r_{i-1}$ and by using the representation from the weighted system we get $w^t x = \sum_{i=0}^{n-1} x_i \prod_{j=0}^{i-1} r_j$.

- The factorial system is an example of such a system, where we take the radix vector to be $r = (n, n - 1, \dots, 2, 1)$ then we get that the weight vector would be $w = (n!, (n - 1)!, \dots, 2!, 1!)$ and we take $D_i = \{0, 1, \dots, i\}$, thus we get a unique representation for each integer, and using this representation and the previous formula we get that the number of permutations is $\prod_{i=0}^{n-1} \# D_i = \sum_{i=0}^{n-1} i \cdot (i - 1)! = (n + 1)! - 1$.

2.1.3. Fixed Radix Representation System

The fixed radix representation is just taking the mixed radix representation with the vector to be all the same constant r and $D_i = D$ and thus we get that the representation is $\prod_{i=0}^{n-1} x_i r^i$.

- For $r = 10$, $D = \{0, \dots, 9\}$, it is the decimal system.
- For $r = 2$, $D = \{0, 1\}$, it is the binary system.

We get a redundant representation since we can represent a number with multiple representations. Let r, s be the indices of two unbalanced positional number systems, n_r and n_s represent the number of digits required in radices r and s , then we get that $n_r / n_s = \log(s) / \log(r)$.

2.2. Integer Representations In Binary

We went through the way we represent positive integers in multiple systems. Now we will consider the binary system and the goal is to represent negative integers too, for that we will see 3 typical ways to represent them. Consider w the number of bits we will use for the representations and $x = (x_{w-1}, \dots, x_0) \in \mathbb{B}^w$.

Definition 2.2.1 (Sign & Magnitude):

- Value: $X = (-1)^{x_{w-1}} \cdot \sum_{i=0}^{w-2} x_i 2^i$
 - x_{w-1} is called the sign bit
 - (x_{w-2}, \dots, x_0) is the magnitude.
- Range: $\llbracket -2^{w-1} + 1; 2^{w-1} + 1 \rrbracket$.

Advantages	Disadvantages
<ul style="list-style-type: none"> Simple conceptually. Easy to negate the values by flipping the sign bit. Range is balanced evenly around 0 like the unsigned integers with one less bit. 	<ul style="list-style-type: none"> Duplicate value for zero. Arithmetic operations are done with different circuits. Overflow detection is more complicated because of the duplicate zero and sign bit.

Definition 2.2.2 (One's Complement):

- Value: $X = -x_{w-1} \cdot (2^{w-1} - 1) + \sum_{i=0}^{w-2} x_i 2^i$.
- Range: $\llbracket -2^{w-1} + 1; 2^{w-1} + 1 \rrbracket$.

Advantages	Disadvantages
<ul style="list-style-type: none"> Better for arithmetic with similar circuits to unsigned integers. Easy to negate the values by inverting all the bits. 	<ul style="list-style-type: none"> Duplicate value for zero. The range of the representation is asymmetric.

Definition 2.2.3 (Two's Complement):

- Value: $X = -x_{w-1} \cdot 2^{w-1} + \sum_{i=0}^{w-2} x_i 2^i$.
- Range: $\llbracket -2^{w-1}; 2^{w-1} - 1 \rrbracket$.

Advantages	Disadvantages
<ul style="list-style-type: none"> Single zero representation. Arithmetic circuits use the exact same as unsigned integers. 	<ul style="list-style-type: none"> The range is asymmetric. The negation requires extra circuitry.

An overflow may happen in all representations, we will give the rules for detecting overflow in addition. Notice that if we add two numbers of different signs then there is no possible overflow. Thus, we will verify it only for numbers with the same sign.

Proposition (Properties Of Two's Complement):

Let $A = a_n a_{n-1} \dots a_0$ and $B = b_n b_{n-1} \dots b_0$ two numbers represented in the two's complement representation.

- $A + B = c_n c_{n-1} \dots c_0$ overflows if and only if $a_n = b_n$ and $c_n \neq a_n$.
- A can be represented in $n + d$ bits in two's complement with the representation $A = \underbrace{a_n \dots a_n}_d a_{n-1} \dots a_0$.

Proof.

1.

- Consider the value of $A' = a_{n+d} a_{n+d-1} \dots a_{n+1} a_n \dots a_0$, $\forall i \in \llbracket 1, d \rrbracket$, $a_{n+i} = a_n$

$$\begin{aligned}
 A' &= -a_{n+d} \cdot 2^{n+d} + \sum_{i=0}^{n+d-1} a_i \cdot 2^i \\
 &= -a_n \cdot 2^{n+d} + a_n \sum_{i=0}^{d-1} 2^{n+i} + \sum_{i=0}^{n-1} a_i \cdot 2^i = -a_n \cdot \sum_{i=0}^d 2^{n+i} + \sum_{i=0}^{n-1} a_i \cdot 2^i \\
 &= -a_n 2^n \cdot \frac{1 - 2^{d+1}}{1 - 2} + \sum_{i=0}^{n-1} a_i \cdot 2^i = -a_n 2^n \cdot (2^{d+1} - 1) + \sum_{i=0}^{n-1} a_i \cdot 2^i
 \end{aligned}$$

by truncating the first n elements we get $-a_n 2^n \cdot (2 - 1) + \sum_{i=0}^{n-1} a_i \cdot 2^i = A$. \square

2.3. Decimal Representations In Binary

After representing integers with binary, we will represent numbers with decimal digits. So we consider the following representations, we take

Definition 2.3.4 (Fixed Point Representation):

Let $x = x_{n-1}x_{n-2}\dots x_0x_{-1}\dots x_{-m} \in \mathbb{B}^{n+m}$ with $n, m \in \mathbb{N}$.

- Value: $X = -x_{n-1} \cdot 2^{n-1} + \sum_{i=-m}^{n-2} x_i \cdot 2^i$
- Range: $\llbracket -2^{n-1}; 2^{n-1} - 1 \rrbracket / 2^m$.

We denote the values represented in this fixed point system as $Q_{n,m}$ where n is the number of bits for integers and m for the decimal part.

Advantages	Disadvantages
<ul style="list-style-type: none"> • Simple to implement. • Deterministic precision. • Equally distributed range. 	<ul style="list-style-type: none"> • Approximation errors for fractional parts and limited precision. • Overflows occur easily.

Definition 2.3.5 (Floating Point Representation):

Let $x = x_{w-1}x_{w-2}\dots x_0 \in \mathbb{B}^{1+e+m}$ with $m, e \in \mathbb{N}$.

- Value: $X = (-1)^{x_{w-1}} \cdot M \cdot 2^E$ with
 - $E = x_{m+e}\dots x_m - E_{\text{bias}}$ and $E_{\text{bias}} = 2^{e-1} - 1$.
 - $M = x_{m-1}\dots x_0$.
- Range: $\llbracket -2^{2^{E-1}}; 2^{2^{E-1}} \rrbracket$ (not the exact set, many gaps are in this range).

The first bit is called the sign bit, the e bits after it are called the exponent bits and the m are called the mantissa.

Advantages	Disadvantages
<ul style="list-style-type: none"> • Large dynamic range. • Support for special numbers. • Standardized (IEEE-754). 	<ul style="list-style-type: none"> • Non-uniform distribution of presentable values. • Complex for implementation.

Floating point representation extra special numbers depending on the exponent.

sign	exponent	mantissa	represent
any	00...0	00...0	both 0 and -0
+	11...1	00...0	$+\infty$
-	11...1	00...0	$-\infty$
any	11...1	non-zero value	NaN (Not a Number)

Chapter 3

Coding Theory

Hello, so this is a warning more than anything, if you arrived here and you are still with Oudjida, he will become like the so-called-now mythical Berrachedi. Consider this warning seriously, you will maybe lose it from now on if you care, or you will just not care. Try to not care, best advice...

3.1. Radix-2^r Encoding

We start by considering a number $K = k_{l-1} \dots k_0 \in \mathbb{B}^l$, we take $k_j = 0$ if $j \notin [0, l-1]$ and $w \in \mathbb{N}$ a window size, we can write K in the following way

$$K = \sum_{i=0}^{\lceil \frac{l+1}{w} \rceil - 1} Q_i \cdot 2^{w \cdot i}$$

with $Q_i = -2^{w-1}k_{w \cdot i + w - 1} + \sum_{j=0}^{w-2} 2^j \cdot k_{w \cdot i + j} + k_{w \cdot i - 1}$. We can decompose Q_i into the following form $Q_i = (-1)^{s_i} \cdot 2^{n_i} \cdot m_i$, $s_i = k_{w \cdot i + w - 1}$ and using the following procedure we get m_i and n_i .

```

1 - input: Q.
2 - output: m, n such that Q = m*2^n.
3 -
4 - m = Q
5 - n = 0
6 - while m % 2 = 0 do
7 -   n = n + 1
8 -   m = m / 2
9 - end
10 -
11 - return m, n

```

By using this decomposition, we get this writing

$$K = \sum_{i=0}^{\lceil \frac{l+1}{w} \rceil - 1} (-1)^{s_i} \cdot m_i \cdot 2^{w \cdot i + n_i}$$

and thus we can represent it in the Radix-2^w as follows

$$K = \left(\underbrace{00 \dots r_n}_{w \text{ digits}} \underbrace{0 \dots 0}_{n_n} \dots \underbrace{00 \dots r_2}_{w \text{ digits}} \underbrace{0 \dots 000}_{n_2} \dots \underbrace{r_1 0 \dots 0}_{w \text{ digits}} \right)_{2^w}$$

with

$$n = \left\lceil \frac{l+1}{2} \right\rceil - 1 \quad r_i = \begin{cases} m_i & \text{if } s_i = 0 \\ \overline{|m_i|} & \text{if } s_i = 1 \end{cases}$$

We take the following example $K = \overline{1011001110101101101101}^2$, we have that $l = 23$ and $w = 4$, we decompose it into Q_i as follows

$$\begin{array}{c} 0010110011101011011011010 \\ \downarrow \\ S_5 = 00101, S_4 = 10011, S_3 = 11101, S_2 = 10110, S_1 = 01101, S_0 = 11010 \end{array}$$

to calculate the Q_i , for example Q_0 , we take the last digit of S_0 and add it to the first 4 digits, so $Q_0 = 1101 + 0 = 1101$ which is -3 in two's complement, by doing thing for each one of them we get

$$Q_5 = 3, Q_4 = -6, Q_3 = -1, Q_2 = -5, Q_1 = 7, Q_0 = -3$$

by decomposing into m_i and n_i we get

$$\begin{pmatrix} s_5 = 0 \\ m_5 = 3 \\ n_5 = 0 \end{pmatrix}, \begin{pmatrix} s_4 = 1 \\ m_4 = 3 \\ n_4 = 1 \end{pmatrix}, \begin{pmatrix} s_3 = 1 \\ m_3 = 1 \\ n_3 = 0 \end{pmatrix}, \begin{pmatrix} s_2 = 1 \\ m_2 = 5 \\ n_2 = 0 \end{pmatrix}, \begin{pmatrix} s_1 = 0 \\ m_1 = 7 \\ n_1 = 0 \end{pmatrix}, \begin{pmatrix} s_0 = 1 \\ m_0 = 3 \\ n_0 = 0 \end{pmatrix}$$

and then we get the encoding as

$$K = (000300\bar{3}0000\bar{1}000\bar{5}0007000\bar{3})_{2^4}$$

3.2. SSP Encoding

The process is significantly easier than Radix- 2^r . We start by considering a number $K = k_{l-1} \dots k_0 \in \mathbb{B}^l$ and $w \in \mathbb{N}$ a window size. We start with smallest i such that $k_i \neq 0$, we consider the block $k_{i+w-1} \dots k_i$, we take its value in the two's complement representation as d and replace the block $k_{i+w-1} \dots k_i$ with $00 \dots 0d$, if the sign of d is negative then add 1 to the number $k_{l-1} \dots k(i+w)$.

For (I suppose, I hope, I honestly don't know) making the algorithm quicker, we define a look-up table, that takes a number and returns its two's complement representation, we define it as $\text{LUT}(K[w-1, \dots, 0])$ and returns $d = -k_{w-1}2^{w-1} + \sum_{i=0}^{w-2} k_i 2^i$. I would like to take in the algorithms the notation $K[i]$ to mean k_i and $K[j, \dots, i]$ to mean $k_j k_{j-1} \dots k_{i+1} k_i$.

```
1 - input:
2 -   - K: number
3 -   - l: length of K
4 -   - w: window
5 - output: SSP representation of K with window w
6 -
7 - i = 0
8 - while i < l do
9 -   while do K[i] == 0 and i < l do
10 -    i++
11 -  end
12 -
13 - s = K[i+w-1]
14 - d = LUT(K[i+w-1, ..., i])
15 - K[i+w-1, ..., i] = [0, 0, ..., 0, d]
16 - i = i + r
17 -
18 - if s == 1 then
19 -   j = i
20 -   while K[j] == 1 do
21 -     K[j] = 0
22 -     j = j + 1
23 -   end
24 -   K[j] = 1
25 - end
26 - end
27 - return K
```