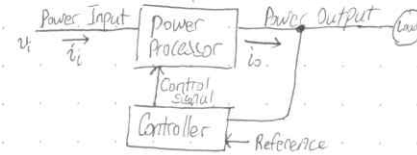


Chapter 1 power problems.

1-1 In the power processor of Fig 1-1, the energy efficiency is 95%. The output to the three-phase load is as follows: 200 V line-to-line (RMS) sinusoidal voltages at 52 Hz and line current of 10 A at a power factor of 0.8 (lagging). The input to the power processor is a single phase utility voltage at 60 Hz. The input power is drawn at unity power factor. Calculate the input current and the input power.

First we start by depicting the mentioned diagram.

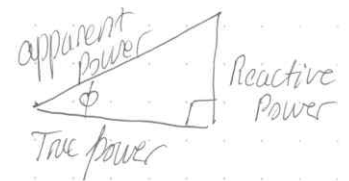


A power factor of 1 implies there are no energy losses. $p.f. = 1$ means all the power that is consumed, is consumed by the resistive element R .

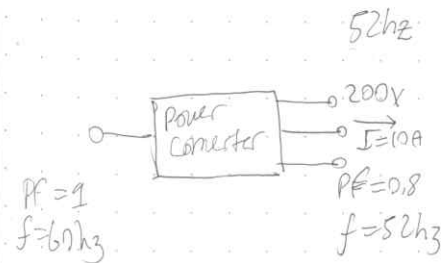
The power factor (p.f.) is the ratio of the True power to the apparent power. We can represent this power factor as $\cos \phi$.

$$P.F. = \frac{\text{True power}}{\text{apparent power}} = \cos \phi$$

It can also be represented as the Resistance R over the impedance Z .



The apparent power is the square root of the sum of the True power squared, and the reactive power squared. Refer to the triangle.



$$\cos \phi = \frac{\text{True power}}{\text{apparent power}}$$

Output: $V_{LL} = 200V$ (3 phase, Rms) at 52Hz
 $I_o = 10A$ at 0.8 lagging pf.

efficiency is at $\eta = 95\%$

The output power can be described as $\sqrt{3} V_{LL} I_o \cdot pf$.

$$\Rightarrow P_{out} = \sqrt{3} V_{LL} I_o (\text{Power factor}) = \sqrt{3} (200V) (10A) (0.8)$$

$$\Rightarrow P_{out} = 2.77 \text{ kW} = 2771.28 \text{ watts.}$$

from the output power, we can find the input power since we were given the efficiency.

$$P_{out} = \eta \cdot P_{in} \Rightarrow P_{in} = P_{out} \left(\frac{1}{\eta} \right) = \frac{2771.28 \text{ Watts}}{(0.95)}$$

This gives us a result of the input power being
 2917 Watts

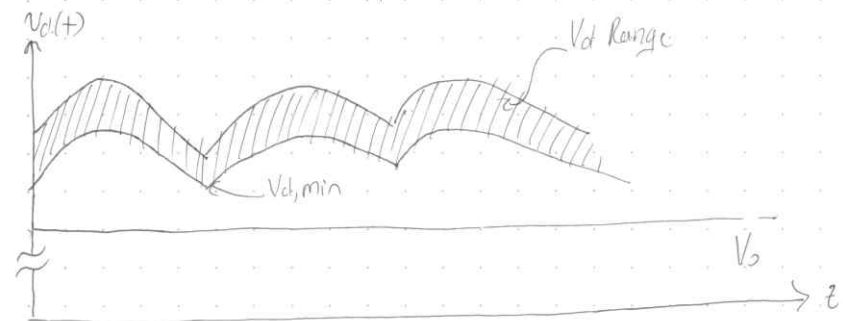
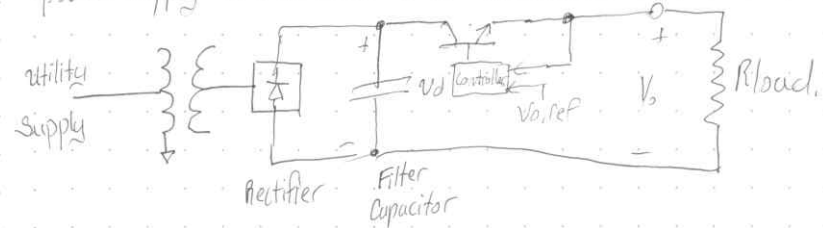
Input	$\eta = 0.95$	Output
Power: 2917W	\rightarrow	Power: 2771
PF = 1		PF = 0.8 lagging
$V_{in} = 230V$		$V_{LL} = 200V$ (3 phases, Rms)
$f = 60Hz$		$f = 52Hz$

The Square Root three is attained by the out of phase connections of the Δ or Y connection.

We know the input power to be $P_{in} = I_{in} V_{in} (P.F.)$
 We have all but the current, so we can solve the current simply by dividing the power by the voltage.

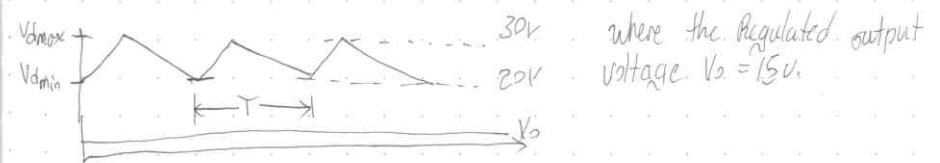
$$I_{in} = \frac{P_{in}}{V_{in} (P.F.)} = \frac{P_{in}}{V_{in} \cdot 1} = \frac{2917 \text{ watt}}{230V} = 12.68 \text{ Amps (Rms)}$$

1-2 Consider a linear regulated dc power supply (Fig. 1-2a). The instantaneous input voltage corresponds to the waveform in fig 1-2b, where $V_{d,min} = 20V$ and $V_{d,max} = 30V$. Approximate this waveform by a triangular wave consisting of two linear segments between the above two values. Let $V_o = 15V$ and assume the output load is constant. Calculate the energy efficiency in this part of the power supply due to losses in the transistor.



Linear dc power supply

Lets look at the lower bound of the input voltage, and for simplicity, we will assume it to be triangular.



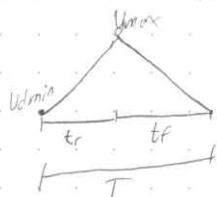
where one waveform may be represented as a triangle of two linear segments.



The average waveform for the voltage over one period is

$$V_{avg}(t) = \frac{1}{T} \int_0^T v_{in}(t) \cdot dt$$

where we will now try and create the waveform

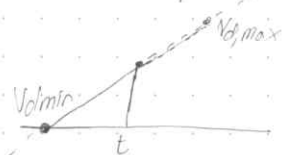


where t_r and t_f are the rise and fall times

$$T = t_r + t_f \Rightarrow t_f = T - t_r$$

where
$$v(t) = \begin{cases} \frac{(V_{d,max} - V_{d,min})}{t_r} \cdot t + V_{d,min} & 0 < t < t_r \\ \frac{(V_{d,min} - V_{d,max})}{t_f} \cdot t + V_{d,max} & t_r < t < T \end{cases}$$

I have all ready made a mistake, and need to express each line segment parametrically with time.



The voltage of the current position at time t can be represented parametrically as the slope and Δt product plus the initial condition.

$$v(t) = V_{d,min} + \frac{\Delta V_d}{\Delta t} \cdot t \text{ which doing so continuously}$$

gives
$$v_{rise}(t) = \frac{dV_d}{dt} \cdot t + V_{d,min} \quad 0 < t < t_r$$

likewise
$$v_{fall}(t) = \frac{dV_d}{dt} \cdot t + V_{d,max} \quad t_r < t < T$$

$$\Rightarrow v(t) = \begin{cases} \frac{(V_{d,max} - V_{d,min})}{t_r} \cdot t + V_{d,min} & 0 < t < t_r \\ \frac{(V_{d,min} - V_{d,max})}{T - t_r} \cdot t + V_{d,max} & t_r < t < T \end{cases}$$

where t_r is the rise time, and T is the period

To take the average voltage
$$v_{avg} = \frac{1}{T} \int_0^T v(t) dt$$

however, we can simplify the integral by splitting up the portions with each corresponding portion.

$$\Rightarrow v_{avg} = \frac{1}{T} \int_0^T v(t) dt = \frac{1}{T} \left[\int_0^{t_r} \left(\frac{V_{d,max} - V_{d,min}}{t_r} \cdot t + V_{d,min} \right) dt + \int_{t_r}^T \left(\frac{V_{d,min} - V_{d,max}}{T - t_r} \cdot t + V_{d,max} \right) dt \right]$$

Evaluating the first integral:

$$\int_0^{t_r} \left(\frac{V_{d,max} - V_{d,min}}{t_r} \cdot t + V_{d,min} \right) dt \Rightarrow \left. \frac{1}{2} \left(\frac{V_{d,max} - V_{d,min}}{t_r} \right) t^2 + V_{d,min} t \right|_0^{t_r}$$

Simplifying, we get the first integral to be $\frac{1}{2} \left(\frac{V_{d,max} - V_{d,min}}{t_r} \right) t_r^2 + V_{d,min} t_r$

$$\Rightarrow \left[\frac{1}{2} (V_{d,max} - V_{d,min}) t_r + V_{d,min} t_r \right] = \frac{1}{2} V_{d,max} t_r + \frac{1}{2} V_{d,min} t_r$$

$$\Rightarrow \frac{1}{2} t_r (V_{d,max} + V_{d,min})$$

Evaluating the falling integral, we get:

$$\int_{t_r}^T \left(\frac{V_{d,min} - V_{d,max}}{T - t_r} \cdot t + V_{d,max} \right) dt = \left. \left(\frac{V_{d,min} - V_{d,max}}{T - t_r} \right) \cdot \frac{1}{2} t^2 + V_{d,max} t \right|_{t_r}^T$$

$$\Rightarrow \left(\frac{V_{d,min} - V_{d,max}}{T - t_r} \right) \cdot \frac{1}{2} T^2 + V_{d,max} \cdot T - \left[\left(\frac{V_{d,min} - V_{d,max}}{T - t_r} \right) \cdot \frac{1}{2} t_r^2 + V_{d,max} t_r \right]$$

That's where

$$\Rightarrow \left(\frac{V_{d,min} - V_{d,max}}{T - t_r} \right) \frac{1}{2} (T^2 - t_r^2) + V_{d,max} (T - t_r)$$

1) the product of difference is the difference of squares
 $(T^2 - t_r^2) = (T + t_r)(T - t_r)$

$$\Rightarrow \frac{1}{2} (V_{d,min} - V_{d,max}) (T + t_r) + V_{d,max} (T - t_r)$$

expanding and simplifying we get:

$$\Rightarrow \frac{1}{2} V_{d,min} T - \frac{1}{2} V_{d,max} T - V_{d,max} t_r + \frac{1}{2} V_{d,min} T - \frac{1}{2} V_{d,max} T + V_{d,max} T$$

$$\Rightarrow \frac{1}{2} t_r (V_{d,min} - 3V_{d,max}) + \frac{1}{2} (V_{d,min} + V_{d,max}) T$$

By combining all the information we derived, we get

$$V_{avg} = \frac{1}{T} \int_0^T V(t) dt = \frac{1}{T} \left[\frac{1}{2} (V_{max} + V_{min}) t_r + \frac{1}{2} (V_{max} + V_{min}) T + \dots \right. \\ \left. \dots + \frac{1}{2} (V_{min} - 3V_{max}) t_r \right]$$

Combining the t_r terms, we get:

$$\frac{1}{2} t_r (V_{max} - 3V_{max} + 2V_{min}) = \frac{1}{2} (-2V_{max} + 2V_{min}) t_r \\ \Rightarrow (V_{min} - V_{max}) t_r$$

So $V_{avg} = \frac{1}{T} \left[(V_{min} - V_{max}) t_r + \frac{1}{2} (V_{max} + V_{min}) T \right]$

$$\Rightarrow V_{avg} = (V_{min} - V_{max}) \frac{t_r}{T} + \frac{1}{2} (V_{max} + V_{min})$$

$$\Rightarrow V_{avg} = (V_{min} - V_{max}) \frac{t_r}{T} + \frac{1}{2} V_{max} + \frac{1}{2} V_{min}$$

$$\Rightarrow V_{avg} = V_{min} \frac{t_r}{T} - V_{max} \frac{t_r}{T} + \frac{1}{2} V_{max} + \frac{1}{2} V_{min}$$

$$\Rightarrow V_{avg} = V_{min} \left(\frac{1}{2} + \frac{t_r}{T} \right) + V_{max} \left(\frac{1}{2} - \frac{t_r}{T} \right)$$

If we assume the case where the triangle is symmetric, so that $t_r = t_f = \frac{T}{2}$ we end up getting $V_{avg} = V_{min}$.

Does this make sense? No, we should get $\frac{V_{max} + V_{min}}{2}$

Let's look at the second integral

$$\frac{1}{2} \left(\frac{V_{min} - V_{max}}{T - \frac{T}{2}} \right) \cdot t^2 + V_{max} \cdot t \Big|_{\frac{T}{2}}^T \Rightarrow \frac{1}{2} \frac{(V_{min} - V_{max})}{\frac{T}{2}} \cdot \frac{T^2}{2} + V_{max} \cdot T$$

$$\Rightarrow \frac{(V_{min} - V_{max})}{T} \left(\frac{T^2}{2} \right) + V_{max} \cdot T + \frac{(V_{min} - V_{max})}{T} \frac{T^2}{2} + V_{max} \frac{T}{2}$$

$$\Rightarrow (V_{min} - V_{max}) T + V_{max} T = \frac{T}{2} \left(\frac{(V_{min} - V_{max})}{T} + V_{max} \right)$$

$$\Rightarrow V_{min} T - \frac{1}{2} V_{min} T + \frac{1}{2} V_{max} T \Rightarrow \frac{1}{2} V_{min} T + \frac{1}{2} V_{max} T$$

adding the first integral, we get

$$\frac{1}{2} \left(\frac{T}{2} \right) (V_{min} + V_{max}) + \frac{T}{2} (V_{min} + V_{max}) = (V_{min} + V_{max}) T \left(\frac{1}{2} + \frac{1}{2} \right)$$

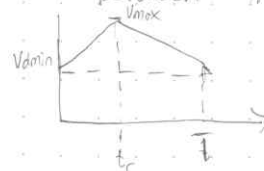
and we remember we divide by T so $V_{avg} \Big|_{t_r = \frac{T}{2}} = \frac{3}{2} (V_{min} + V_{max})$

which is still wrong. The only conclusion is a mistake.

Reevaluating the first, we look at when we split the integral

$$V_{avg} = \frac{1}{T} \int_0^T V(t) dt = \frac{1}{T} \left[\int_0^{T/2} \left(\frac{V_{max} - V_{min}}{T/2} \right) t dt + \int_{T/2}^T V_{min} dt \right] \\ + \frac{1}{T} \left[\int_0^{T/2} V_{max} dt + \int_{T/2}^T \left(\frac{V_{min} - V_{max}}{T/2} \right) (T - t) dt \right]$$

we realize that when we split the integral the average splits between the two so we need to



looking at the waveform the area under the curve. I realize I forgot to account for the offset!!

The area of the rectangle $V_{min} \cdot T$ and the area of the triangle is $\frac{1}{2} b \cdot h = \frac{1}{2} T (V_{max} - V_{min})$ and we normalize the duration T . So this tells us that the average is independent of the rise time.

$\Rightarrow V_{max}$

 $Area = \int_0^T V(t) dt = [V_{d,min} \cdot T + \frac{1}{2} T (V_{d,max} - V_{d,min})]$

The average is $V_{avg}(t) = \frac{1}{T} [V_{d,min} \cdot T + \frac{1}{2} T (V_{d,max} - V_{d,min})]$

$\Rightarrow V_{avg}(t) = V_{d,min} + \frac{1}{2} (V_{d,max} - V_{d,min})$
 $\Rightarrow V_{avg}(t) = \frac{(V_{d,min} + V_{d,max})}{2}$

On average the input power would be $P_{in} = I_o V_{avg}$.

$\Rightarrow P_{in} = I_o \frac{1}{2} [V_{d,min} + V_{d,max}]$

We were given $V_{d,min} = 20V$, $V_{d,max} = 30V$

$P_{in} = \frac{(30+20)}{2} I_o = \frac{50}{2} I_o = 25 I_o$

The energy efficiency ideally for this triangular case

$\eta = \frac{P_{out}}{P_{in}} = \frac{V_o I_o}{P_{in}} = \frac{15V I_o}{25V I_o} = 0.6$

$\Rightarrow \eta = 0.6$

The average voltage as I have been attempting earlier

$V_{avg} = \frac{1}{T} \int_0^T V(t) dt = \frac{(V_{d,min} + V_{d,max})}{2}$

Let's try $V_{avg} = \frac{1}{T} [a \int_0^{tr} V(t) dt + b \int_{tr}^T V(t) dt]$

where we know $a \int_0^{tr} V(t) dt = \frac{1}{2} tr [V_{max} - V_{min}] + V_{min} tr$

$b \int_{tr}^T V(t) dt = \frac{1}{2} (T - tr) [V_{max} - V_{min}] + V_{min} (T - tr)$

Let's evaluate $a \int_0^{tr} V(t) dt = \frac{1}{2} tr [V_{max} - V_{min}]$

$a \int_0^{tr} V(t) dt$ where $0 < t < tr$



where before, I wanted to $\frac{(V_{d,max} - V_{d,min}) \cdot tr}{tr} + V_{d,min}$ $0 < t < tr$

$V(t) = (V_{max} - V_{min}) \frac{t}{tr} + V_{min}$ $0 < t < tr$

Validating, $V(0) = V_{min}$, $V(tr) = V_{max} - V_{min} + V_{min} = V_{max}$

So then Let's evaluate the integral

$a \int_0^{tr} V(t) dt = a \int_0^{tr} [V_{max} - V_{min}] \frac{t}{tr} + V_{min} dt$

$= a \left[\frac{1}{2} (V_{max} - V_{min}) \frac{t^2}{tr} + V_{min} t \right]_0^{tr} \Rightarrow a \left[\frac{1}{2} (V_{max} - V_{min}) tr + V_{min} tr \right]$

$\Rightarrow a [tr (\frac{1}{2} V_{max} + \frac{1}{2} V_{min})] = a \frac{1}{2} (V_{max} + V_{min}) \cdot tr$

But comparing the two results we get So a and b are 1.

$a \cdot \frac{1}{2} tr (V_{max} + V_{min}) = \frac{1}{2} tr (V_{max} - V_{min})$

$\Rightarrow a (V_{max} + V_{min}) = V_{max} - V_{min}$ as $a + b$

$(a + b) (V_{max} + V_{min}) = V_{max} - V_{min}$
 $a V_{max} + a V_{min} = V_{max} - V_{min}$

So... we find we got the correct result.

$\int_0^{tr} V(t) dt = tr \cdot \frac{1}{2} (V_{max} - V_{min}) + V_{min} \cdot tr$

$\int_{tr}^T V(t) dt = \frac{1}{2} (T - tr) (V_{max} - V_{min}) + V_{min} (T - tr)$

$V_{avg} = \frac{1}{T} \left[\int_0^{tr} V(t) dt + \int_{tr}^T V(t) dt \right] = \frac{1}{T} \left[tr \cdot \frac{1}{2} (V_{max} - V_{min}) + V_{min} \cdot tr + \frac{1}{2} (T - tr) (V_{max} - V_{min}) + V_{min} (T - tr) \right]$

$\Rightarrow V_{avg} = \frac{1}{2T} (V_{max} - V_{min}) + V_{min} \cdot \frac{T}{T} = \frac{1}{2} (V_{max} - V_{min}) + V_{min}$

So I am finding my integrals correct now.

1-3 Consider a switch mode dc-power supply represented by the circuit in Fig. 1-4a. The input dc voltage $V_d = 20V$ and the switch duty ratio $D = 0.75$. Calculate the Fourier components of v_{oi} using the description of Fourier analysis.

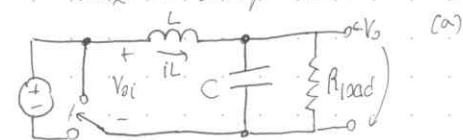
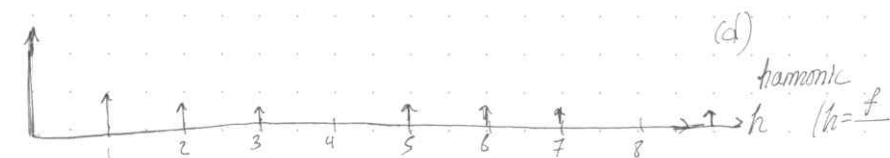
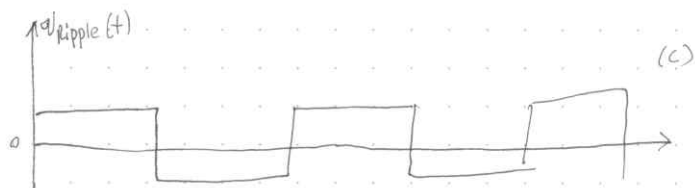
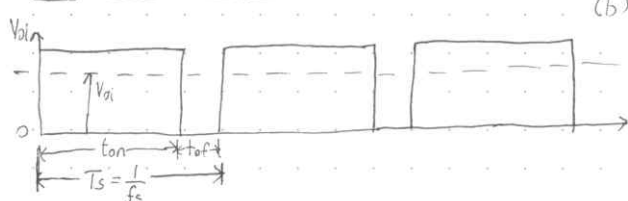


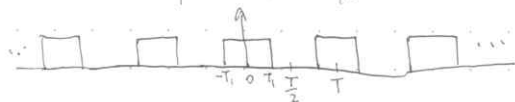
Figure 1-4 Equivalent circuit, waveforms and frequency spectrum of the supply in Fig 1-3.



Let's review our signals. We have a rectangular function $\Pi(t) \rightarrow$

$$\Pi(t) = \begin{cases} 1, & |t| < T/2 \\ 0, & |t| > T/2 \end{cases} \rightarrow$$

For a periodic, rectangular function



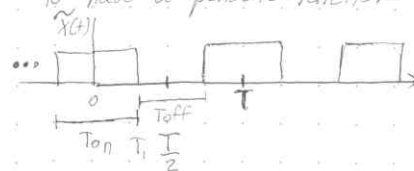
where the Fourier series coefficients a_k are found using

$$a_k = \frac{1}{T} \int_T x(t) e^{-jk\omega_0 t} dt = \frac{1}{T} \int_T x(t) e^{-jk(2\pi/T)t} dt \quad (3.39)$$

likewise, we have previously

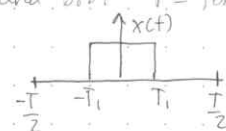
$$x(t) = \sum_{k=-\infty}^{\infty} a_k e^{jk\omega_0 t} = \sum_{k=-\infty}^{\infty} a_k e^{jk(2\pi/T)t} \quad (3.38)$$

Let's carefully define our variable function $x(t)$ to have a periodic variable duty cycle.



where the period T is the sum of durations when the duty is on and off. $T = T_{on} + T_{off}$

$$x(t) = \begin{cases} 1, & |t| < T_1 \\ 0, & T_1 < |t| < T/2 \end{cases}$$



Evaluating the DC Fourier component $a_k = \frac{1}{T} \int_T x(t) e^{-jk(2\pi/T)t} dt$ ($k=0$) (average)

So, we have $a_0 = \frac{1}{T} \int_T x(t) e^0 dt$

We defined the limits of the square interval as $\pm T_1$

$$a_0 = \frac{1}{T} \int_{-T_1}^{T_1} 1 dt = \frac{1}{T} (T_1 - (-T_1)) = \frac{2T_1}{T}$$

$$a_k \text{ for } k \neq 0 \quad a_k = \frac{1}{T} \int_{-T_1}^{T_1} e^{-jk\omega_0 t} dt = \frac{1}{jk\omega_0 T} e^{-jk\omega_0 t} \Big|_{-T_1}^{T_1}$$

Evaluating the bounds of the integral, we get the Fourier series components for when $k \neq 0$

$$a_k = \begin{cases} \frac{2T_1}{T} & k=0 \\ \frac{2}{k\omega_0 T} \left[\frac{e^{jk\omega_0 T_1} - e^{-jk\omega_0 T_1}}{2j} \right] & k \neq 0 \end{cases}$$

and we realize that the duty cycle is only modified by the bounds

using Euler's identity, we can simplify the case for this non-variable $x(t)$

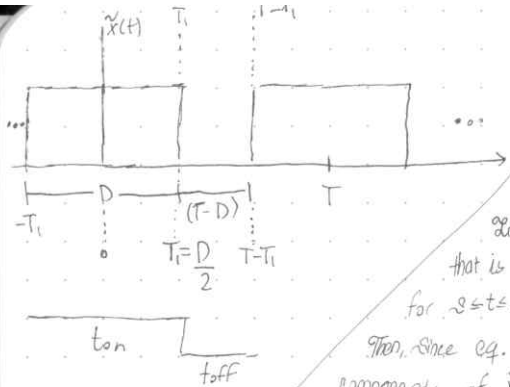
$$\Rightarrow a_k = \frac{2 \sin(k\omega_0 T_1)}{k\omega_0 T} = \frac{\sin(k\omega_0 T_1)}{kT}, \quad k \neq 0$$

where we have used the fact that $\omega_0 T = 2\pi$.

and we can interpret these as samples of an envelope function

$$\Rightarrow T a_k = \frac{2 \sin \omega T_1}{\omega} \Big|_{\omega=k\omega_0} \quad \tilde{x}(t) = \sum_{k=-\infty}^{\infty} a_k e^{jk\omega_0 t}$$

and this gives us a good review of oppehheim



The periodic function $\tilde{x}(t)$ variable with duty D .

$$a_k = \frac{1}{T} \int_{-\infty}^{\infty} \tilde{x}(t) e^{-jk\omega_0 t} dt$$

Looking at it simpler, $x(t)$ is a finite duration signal that is equal to $\tilde{x}(t)$ over exactly one period — say, for $s \leq t \leq s+T$ for some value s — and zero otherwise.

Then, since eq. (3.39) allows us to compute the Fourier components of $\tilde{x}(t)$ by integrating over only one period, we can write:

$$a_k = \frac{1}{T} \int_{-\infty}^{\infty} x(t) e^{-jk\omega_0 t} dt$$

$$a_k = \frac{1}{T} \int_s^{s+T} \tilde{x}(t) e^{-jk\omega_0 t} dt = \frac{1}{T} \int_s^{s+T} x(t) e^{-jk\omega_0 t} dt$$

Since $x(t)$ is zero outside the range $s \leq t \leq s+T$

$$\Rightarrow a_k = \frac{1}{T} \int_{-\infty}^{\infty} x(t) e^{-jk\omega_0 t} dt \quad \text{Comparing to the Fourier transform,}$$

$$X(j\omega) = \int_{-\infty}^{\infty} x(t) e^{-j\omega t} dt \quad \text{we conclude } a_k = \frac{1}{T} X(j\omega) \Big|_{\omega = k\omega_0}$$

where $X(j\omega)$ is the Fourier Transform of $x(t)$. This states that the Fourier coefficients of $\tilde{x}(t)$ are proportional to samples of the Fourier transform of one period of $\tilde{x}(t)$. This alludes to the concept of duality.

Solving for the dc component, $a_0 = \frac{1}{T} \int_{-D/2}^{D/2} x(t) e^{0} dt$. This is an average. We essentially are adding up where the square pulse exists, as 1.

$$\Rightarrow a_0 = \frac{t}{T} \Big|_{-D/2}^{D/2} = \frac{1}{T} \left(\frac{D}{2} - \left(-\frac{D}{2} \right) \right) = \frac{D}{T} \Rightarrow a_0 = \frac{D}{T}, \quad k=0$$

$$a_k = \frac{1}{T} \int_{-D/2}^{D/2} e^{-jk\omega_0 t} dt = \frac{1}{j k \omega_0 T} e^{-jk\omega_0 t} \Big|_{-D/2}^{D/2} = \frac{1}{j k \omega_0 T} \left[e^{-jk\omega_0 (D/2)} - e^{-jk\omega_0 (-D/2)} \right]$$

Multiplying by one in the form $\frac{2}{2}$ leads us to having $\frac{-2}{j k \omega_0 T} \left[\dots \right]$

$$\Rightarrow a_k = \frac{-2}{j k \omega_0 T} \frac{e^{jk\omega_0 (D/2)} - e^{-jk\omega_0 (D/2)}}{2} \quad \text{converting, with Euler's identity, back to the sinusoidal form}$$

$$\Rightarrow a_k = \frac{-2}{j k \omega_0 T} \sin(k\omega_0 D/2)$$

$$a_k = \begin{cases} \frac{D}{T}, & k=0 \\ \frac{-2}{k\omega_0 T} \sin(k\omega_0 D/2), & k \neq 0 \end{cases}$$

If we were to reset the periodic function $\tilde{x}(t)$, we would end up with the same Fourier components. This is because we only care about the integration of one period, for our purpose, whether it is offset or not, the same amount gets covered in a period. To account for the change in voltage, we can simply scale these constants by an amplitude.

So returning to our example, the average voltage will be

$$V_i = \frac{1}{T} \cdot D \cdot V_d \quad \text{where we were given } V_d = 20V \text{ and } D = 0.75$$

Now if we look at our argument D is the portion of the period we have on. If we had a full duty, where $D=1$, we would have a dc of $V_d = 20V$, and we know the average is linear with D . So we can use D to represent the fraction of time the period is filled over one period.

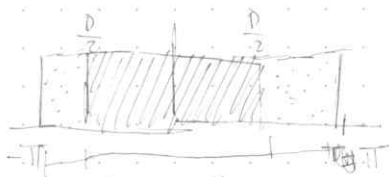
$$\Rightarrow T=1, V_{avg} = D \cdot V_d = 20 \cdot 0.75 = 15V$$

using the same methodology we can calculate the Fourier components.

$$a_k = \frac{-2}{k\omega_0 T} \sin(k\omega_0 D/2) \quad \text{where we recognize } \omega_0 T \text{ as } 2\pi$$

$$\Rightarrow a_k = \frac{-2}{2\pi k} \sin(2\pi k D/2) \Rightarrow a_k = \frac{-1}{\pi k} \sin(\pi k D)$$

So, for the voltage $a_k = \frac{V_d}{\pi k} \sin(\pi k D)$. This essentially means that the period T is over a value of π , the half a natural cycle, however π time is unable to satisfy the duration.



We want the
integration over the full range $0-T$
including the empty space

In our integration we missed this fact by forgetting

$$\omega_s T = 2\pi$$

$$\Rightarrow a_k = \frac{1}{T} \int_0^T x(t) e^{-jk\omega_s t} dt = \frac{1}{T} \int_0^T x(t) e^{-jk(2\pi/T)t} dt$$

So, reworking the integral $\Rightarrow a_k = \frac{V_d}{T} \int_{-D/2}^{D/2} e^{-jk(2\pi/T)t} dt$

$$\Rightarrow a_k = \frac{V_d}{T} \left[\frac{-T}{jk2\pi} e^{-jk(2\pi/T)t} \right]_{-D/2}^{D/2}$$

$$\Rightarrow a_k = \frac{-V_d}{\pi} \frac{e^{jk\pi D/T} - e^{-jk\pi D/T}}{2j} = \frac{V_d}{\pi} \frac{e^{jk\pi D/T} - e^{-jk\pi D/T}}{2j} = \frac{V_d}{\pi} \sin\left(\frac{k\pi D}{T}\right)$$

But we see that for negative frequencies, we can rectify these frequencies by choosing only the positive values of k components and doubling the results, so instead of having

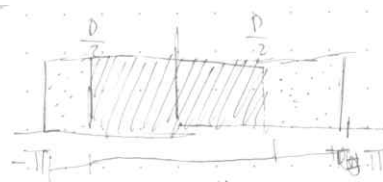


So by choosing we symmetrize the result

$$a_k = \frac{2V_d}{\pi k} \sin(\pi k D) \quad , \quad k \neq 0 \quad | \quad a_0 = DV_d \quad k=0$$

$$\therefore a_k = 15V, 8V, 6.37V, 3V, 0V, 1.8V, \dots \text{ for } k=0 \dots 5$$

This is because the area under is the same as so the height gets doubled.



We want the integration over the full range $0-\pi$ including the empty space

In our integration we missed this fact by forgetting

$$\omega_s T = 2\pi$$

$$\Rightarrow a_k = \frac{1}{T} \int_0^T x(t) e^{-jk\omega_s t} dt = \frac{1}{T} \int_0^T x(t) e^{-jk(2\pi/T)t} dt$$

So, reworking the integral $\Rightarrow a_k = \frac{V_d}{T} \int_{-D/2}^{D/2} e^{-jk(2\pi/T)t} dt$

$$\Rightarrow a_k = \frac{V_d}{T} \cdot \frac{-T}{jk2\pi} e^{-jk(2\pi/T)t} \Big|_{-D/2}^{D/2}$$

$$\Rightarrow a_k = \frac{-V_d}{\pi} \frac{e^{jk\pi D/T} - e^{-jk\pi D/T}}{2j} = \frac{V_d}{\pi} \frac{e^{jk\pi D/T} - e^{-jk\pi D/T}}{2j} = \frac{V_d}{\pi} \sin\left(\frac{k\pi D}{T}\right)$$

But we saw before that for more frequency, we can rectify these frequencies by choosing only the positive values of k components and doubling the results, so instead of having



So by choosing an symmetry this way

$$a_k = \frac{2V_d}{\pi k} \sin(\pi k D) \quad , \quad k \neq 0 \quad | \quad a_0 = DV_d \quad k=0$$

$$\therefore a_k = 15V, 8V, 6.37V, 3V, 0V, 1.8V, \dots \text{ for } k=0 \dots 5$$

This is because the area under is the same as so the height gets doubled.

Mikhail Traubharis

continuing with the last problems book, 1-3.

We find that the Fourier components are

$$a_k = 1.8V, 0V, 3V, 6.37V, 9V, 15V$$

$$\text{for } k = \dots, -5, -4, -3, -2, -1, 0, 1, 2, 3, 4, 5, \dots$$

$$a_k = 0.9V, 0V, 1.5V, 3.185V, 4.5V, 15V, 4.5V, 3.185V, 1.5V, 0V, 0.9V$$

$$\text{for } -\infty < k < \infty$$

$$\text{which was found derived from } a_k = \begin{cases} \frac{V_d}{\pi k} \sin(\pi k D) & k \neq 0 \\ V_d \cdot D & k = 0 \end{cases}$$

This is for the bilateral Fourier components.

For the unilateral Fourier components we simply double the non-dc, average component as we fold the negative k components with the positive.

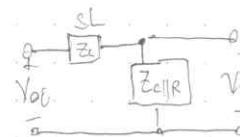
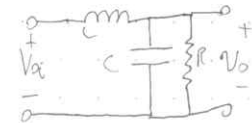
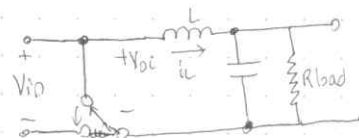
$$\Rightarrow a_{k, \text{unilateral}} = \begin{cases} \frac{2 \cdot V_d \sin(\pi k D)}{\pi k} & k > 0 \\ V_d \cdot D & k = 0 \end{cases}$$

for duty $D = 0.75$, and $V_d = 20V$.

$$\Rightarrow |a_k| = 15V, 9.0V, 6.37V, 3.0V, 0.0V, 1.8V, 2.12V$$

1-4

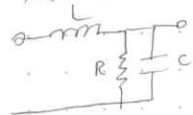
In problem 1-3, the switching frequency $f_s = 300 \text{ kHz}$ and the resistive load draws 240 W . The filter components corresponding to Fig. 1-4a are $L = 1.3 \mu\text{H}$ and $C = 50 \mu\text{F}$. Calculate the attenuation in decibels of the ripple voltage in v_{oi} at various harmonic frequencies. (Hint: To calculate the load resistance, assume the output voltage to be constant DC, without any ripple.)



$$\Rightarrow V_{oi} \rightarrow [H(s)] \rightarrow V_o \quad \text{where } H(s) \text{ is Voltage circuit division}$$

$$\Rightarrow H(s) = \frac{V_o(s)}{V_{oi}(s)} = \frac{R \parallel \left(\frac{1}{Cs}\right)}{sL + [R \parallel \left(\frac{1}{Cs}\right)]}$$

Design we have the transfer function $H(s)$ of the circuit



$V_o = 20V$, $D = 0.75$, $f_s = 300kHz$, DP
 $L = 1.3mH$, $C = 50nF$

where $v_{oi}(t)$ was our duty cycled $V_d \cdot \tilde{x}(t)$ from before.
 Calculating $H(s) = \frac{R \parallel \frac{1}{sC}}{sL + [R \parallel \frac{1}{sC}]}$ where $R \parallel \frac{1}{sC} = \frac{R}{R + \frac{1}{sC}}$

$$R \parallel \frac{1}{sC} = \frac{R}{sC} \left(\frac{1}{R + \frac{1}{sC}} \right) = \frac{R}{sC} \left(\frac{sC}{RCs + 1} \right) = \frac{R}{RCs + 1}$$

$$\Rightarrow H(s) = \frac{\frac{R}{sC} \cdot \frac{1}{R + \frac{1}{sC}}}{sL + \left[R \parallel \left(\frac{1}{sC} \right) \right]} = \frac{R}{RCs + 1} \cdot \frac{1}{sL + \frac{R}{RCs + 1}} = \frac{R}{sL(RCs + 1) + R}$$

$$H(s) = \frac{R}{RCs + 1} = \frac{R}{sL(RCs + 1) + R} = \frac{R}{(RLC)s^2 + Ls + R}$$

For our harmonic components, as before, we sample at a period T_s and since we are assuming a steady state σ is $0 \rightarrow z = j\omega = jk2\pi f$

To calculate the attenuation then for each component we take $20\log_{10}|H(s)|$

Let's find the Resistance R . P_{out} is assumed constant.

$P_{out} = \frac{V_o^2}{R} \Rightarrow R = \frac{V_o^2}{P_o}$ The average output dc voltage we calculated to be $15V$, $\rightarrow V_{o,avg} = V_o = 15V$

we were given the power draw to be a constant $240W$

$$R = \frac{V_o^2}{P_{out}} = \frac{15^2}{240} = 0.9375 \Omega$$

$$\therefore H(s) = \frac{V_o(s)}{V_{oi}(s)} = \frac{R}{(RLC)s^2 + Ls + R} = \frac{0.9375 \Omega}{6.099e-11s^2 + 1.3e-6s + 0.9375}$$

The first sampling component frequency gives our ~~fourier~~ harmonic attenuations at the specific frequencies

$S = j\omega_s = j(2\pi \cdot f_s)$ where our frequency was $300kHz$

$$H(s) \approx R \left(\frac{1}{-RLC(2\pi \cdot h \cdot f_s)^2 + 2\pi \cdot h \cdot f_s \cdot L + R} \right)$$

Tabulating the numbers in a computer. ~~The~~ Refer to problem 1-4, p4

As a result we have our harmonic components.

1 $20\log_{10}|H(s)|$

0	0
1	-47.1333db
2	-59.252db
3	-66.31813654db
4	-71.32574325 db
5	-75.20792118 db
6	-78.37891582 db
7	-81.05941002 db
8	-83.38102631 db
9	-85.42861796 db

at $f_s = 300kHz$

where $H(\omega_s, h) = \frac{R}{-RLC \cdot h^2 \omega_s^2 + R + \omega_s \cdot L \cdot h}$

Adding the numbers, we get

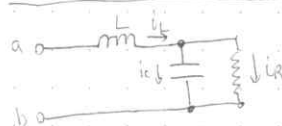
$$H(f_s, h) = \frac{0.9375}{-2.4057e-29 f^2 h^2 + 8.168e-06 f \cdot h + 0.9375}$$

	db
1	-47.2
2	-59.3
3	-66.4
4	-71.4
5	-75.2

minor correction
I forgot the i component.

1-5 In problem 1-4, assume the output voltage to be a pure dc = $15V$. Calculate and draw the voltage and current associated with the filter inductor L , and the current through C . using the capacitor current obtained above, estimate the p-p voltage across C which we previously assumed to be zero.

(Hint: Note that under steady state conditions, the average value of the current through C is zero.)



using Kirchhoff's current law, we assume $i_c = i_s$ (if we assume the output voltage to be constant, we find that $V_o = 15 = I \cdot R$ and we calculate the power sunk in the resistor.

$P_R = \frac{240W}{15V} = 16A$

This essentially enforces the current in the Resistor to be constant.

$$I_R = 16A, V_R = 15, R = 0.9375 \Omega$$

So any ripple current and voltage will be exchanged only through the inductor L and the capacitor C .

This also means the average capacitor current will be zero, and the average inductor current will be

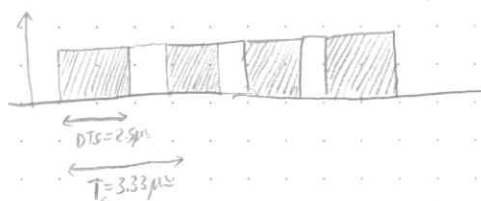
i.e. \therefore On average $i_L = I_L = I_R = i_R \Rightarrow I_C = 0$
where I_R is fixed at 16 amps due to the problem's assertion.

The switching frequency was given as $f_s = 300kHz$.

The period is thus $T = \frac{1}{f_s} = 3.33 \mu s$

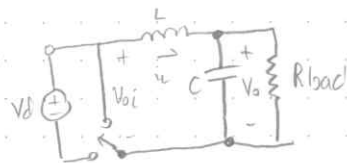
The duty period with a duty of $D = 0.75$ is calculated as

$$D \cdot T = 0.75 \cdot 3.33 \mu s = 2.5 \mu s$$

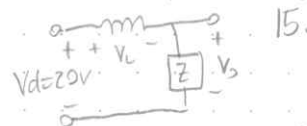


During $0 < t < 2.5 \mu s$
we have an active
duty cycle
and during $2.5 \mu s < t < T$
we are off for one period
 $0 < t_{on} < 2.5 < t_{off} < T$

Redrawing the circuit,



When the switch is on $V_d = 20$, and is the same voltage as V_{di} . The output voltage was asserted by the problem to be



Using Kirchhoff voltage law,

$$-V_d + V_L + V_0 = 0 \Rightarrow V_d - V_0 = V_L$$

$$\therefore V_L = V_d - V_0 = 20 - 15V = 5V$$

a constant 5 volts is asserted in the On state.

I am assuming 5 volts always across the inductor in the on state. Since by definition of an ideal voltage source, the voltage is asserted by this source.

when in the on state $V_L = 5V$.

when in the off state, no power is flowing and our superior ideal Resistor takes the current of the capacitor making the ~~current~~ voltage of the capacitor 0.

However in Reality, we will have a transient

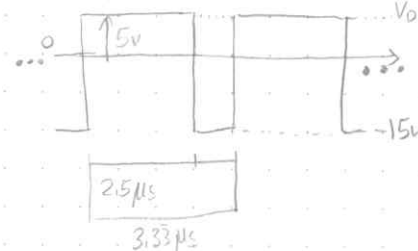
$$\text{So } V_L = -C \frac{dV}{dt} \cdot t + 15$$

Notice how we spike up to 15V instantly ~~early~~ when the switch is thrown, shorting the voltage source.

This is still somewhat of an idealization though. The circuit app dampens the transient as it prevents it from changing instantly.

However, we can continue this logic and assert that power is always drawn from the power supply even when switched off. This idealization with strong assertions is better comforted with the fact that these are very small periods of time, and another wave will soon fill its place.

\therefore The ideal switching inductor voltage is represented below.



In reality we will have more of a ~~smooth~~ transient due to the ~~very~~ very small capacitor.

We can calculate the inductor current in the inductor

$$V_L = L \frac{di}{dt} + iR, \quad i_L = i_C + i_R = \frac{1}{L} \int_0^{t_{on}} V_L(\tau) d\tau + i_C$$



If we assume a ~~smooth~~ smooth ~~transient~~ transient in the ~~reality~~ reality of this ideal case or

$V_L = L \frac{di(t)}{dt} + V_{L0}$ gives us the voltage with changing current. We know the inductor voltage, let us integrate it to get the current for the case of this inductor.

$$\Rightarrow \frac{di_L}{dt}(t) = \frac{V_L}{L} - V_{L0} \text{ integrating both sides by } t$$

$$i_L = \int \frac{V_L}{L} dt + V_{L0} = \frac{1}{L} \int V_L dt + V_{L0} dt$$

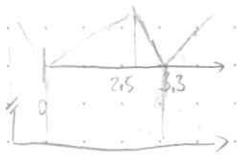


We have our interval as from $0 \rightarrow 2.5\mu s$ the duty cycle we are on.

evaluating the starting current at $t=0$

$$\frac{di_L}{dt}(t) = \frac{V_L(t) - V_{L0}}{L}$$

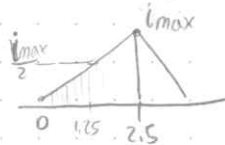
we have a constant 5 volts across the inductor when the inductor is turned on.



we already know the average inductor current will be $\frac{V_{L0}}{R} = 16 \text{amps}$

If we assume the inductor current ripple as a triangle and a square we remember we derived the average voltage as $V_{avg} = \frac{V_{min} + V_{max}}{2}$

Just calculating the Δ component, we assert the initial condition current being 0.



The p-p voltage is gotten by integrating the voltage with respect to time

$$i_L = \int_0^{2.5e-6} V_L \cdot dt = \frac{5 \cdot 2.5e-6}{1.3e-6}$$

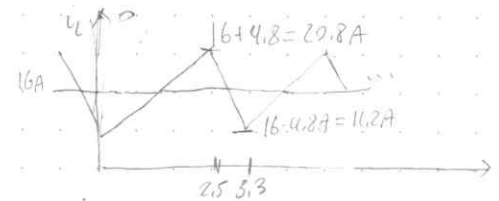
$$i_L = 9.6A$$

The intuition is this: If the voltage in an inductor is constant, the current is constantly changing.

$$V_L = L \frac{di(t)}{dt}$$

So the current spanned during this on time is 9.6A.

We already know the average so the ripple, centered at 16 amps swings 4.8 Amps.



Since we know that over the inductor current is not zero, and we have had the Resistor current affected constant. The Capacitor current must change in proportion to the inductor current.

$$\Rightarrow i_L(t) = i_C(t) + I_R \Rightarrow i_C(t) = i_L(t) - I_R$$

We know the average capacitor current as follows



we know the current of the capacitor will be

$$i_C = C \frac{dv}{dt}$$

So we have a maximum magnitude of current running through the capacitor to be 4.8A.

the voltage spanned by the changing voltage ends up being proportional to the area under the triangle, with a base

$$\therefore \Delta V_C = \frac{1}{C} \cdot \frac{\text{Integral}}{\text{Current}} \rightarrow \frac{1}{C} \int_0^T i_C \cdot dt \cdot \frac{I}{2}$$

$$= \frac{1}{C} \cdot \frac{1}{2} \cdot \frac{T}{2} \cdot 4.8A = \frac{1}{50e-6} \cdot \frac{1}{4} \cdot 4.8 \cdot 3.3e-6$$

$$\Rightarrow \Delta V_C = 0.0792V \approx 80mV$$

Problem 1-6

Considering only the switching frequency component in V_o in problems 1-3 and 1-4, calculate the peak-to-peak ripple in the output voltage across C_o . Compare the result with that obtained in problem 1-5.

The switching frequency of a square wave is the first frequency component of its Fourier series, excluding the ripple.

The Fourier component has been calculated to have a voltage of 9V.

So the first component of the input voltage $V_{oi} = 9.023V$

Next, we calculated the attenuation at the first harmonic to be -47.13db.

$$\Rightarrow 20 \log_{10} \left| \frac{V_o(s)}{V_{oi}(s)} \right| = -47.13 \text{db} = 20 \log_{10} |H(s)|$$

However, I can do better, since I already know the transfer function at the first component as

$$H_1(f = 300 \text{kHz}) = -0.004348 \angle 4.9426 \text{e-}05^\circ$$

The magnitude is calculated to be 0.004348

So the voltage S_{on} is the first component

$$(\hat{V}_{oi}) = 0.004348 \cdot (\hat{V}_i) = 39 \text{mV}$$

To get the p-p voltage of the C_{op} we double this number. Since we also have the segment which goes negative.

$$\Rightarrow \text{Therefore } \Delta V_{c \text{ p-p}} = 2 \cdot H_1(s) \cdot (V_{oi}) = 0.07830076V$$

The S_{on} is 78.3mV

Comparing to the previous result of 0.0792, we are very close. It is only a 0.1% difference!!!

17. Reference 4 refers to a U.S. Department of Energy Report that estimated that over 100 billion kWh/year can be saved in the United States by various energy conservation techniques applied to the pump driven systems. Calculate how many 1000 MW generating plants running constantly supply this wasted energy, which could be saved, and (b) the savings in dollars if the cost of electricity is 0.1 \$/kWh.

[4] N. Mohan and R.J. Ferraro, "Techniques for energy conservation in AC Motor driven Systems," EPRI Final Report EM-2037 project 1201-1213, September 1981

(a) There is 100 billion kWh saved in a year.
 $\Rightarrow 100 \cdot 10^9 \cdot 10^3 \text{ W-hr}$

a year has 8760 hours in a year

$$\frac{100 \text{ billion kWh}}{8760 \text{ h}} = 11.4 \text{ million watts}$$

This is about the same as 4-5 power plants.
 $\frac{1 \text{ kWh}}{1000 \text{ Wh}}$

The savings off this 100 billion kWh is simple

$$\text{Savings} = 100 \text{ billion kWh} \cdot \frac{0.1 \$}{\text{kWh}} = 10 \cdot 10^9 \$$$

which is 10 billion dollars.

Chapter 2 Problems

2-1 The data sheets of a switcher specify the following switching times corresponding to the linearized characteristics shown in Fig. 2-6b for clamped-inductive switchings:

$$t_{ri} = 100 \text{ ns} \quad t_{fv} = 50 \text{ ns} \quad t_{rv} = 100 \text{ ns} \quad t_{ri} = 200 \text{ ns}$$

Calculate and plot the switching power loss as a function of frequency in a range 25-100 kHz, assuming $V_d = 300 \text{ V}$ and $I_o = 4 \text{ A}$ in the circuit.

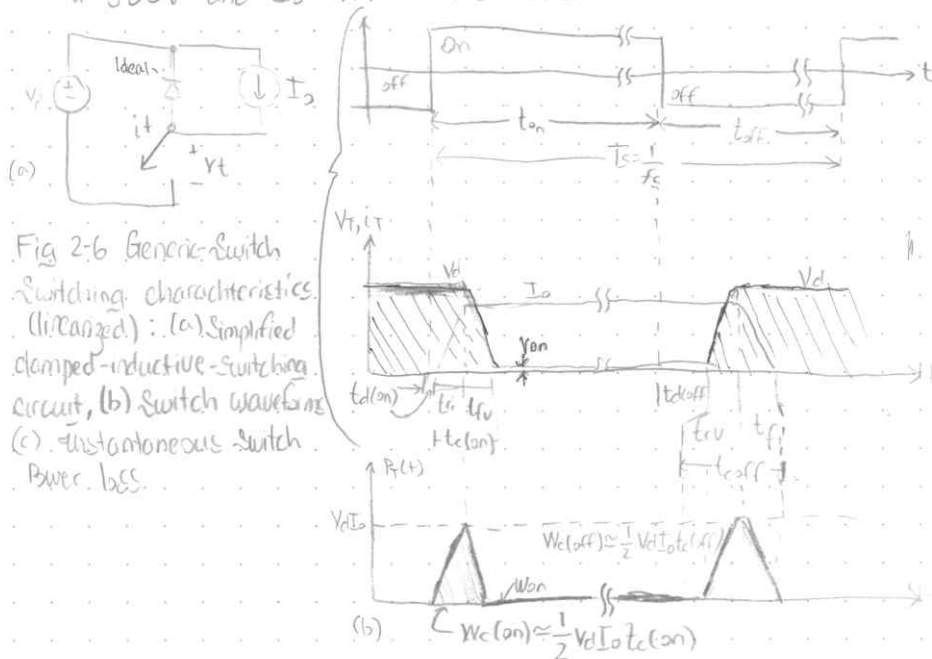


Fig 2-6 Generic Switch Switching characteristics. (linearized): (a) Simplified clamped-inductive-switching circuit, (b) Switch waveforms (c) Instantaneous switch power loss.

In order to consider power dissipation in a semiconductor device, a controllable switch is connected in the simple circuit shown in Fig. 2-6A. This circuit models a very commonly encountered situation in power electronics: The current flowing through a switch must also flow through some series inductance(s). This circuit is similar to the circuit of Fig. 1-3b, which was used to introduce switch mode power electronic circuits. The dc current source approximates the current that would actually flow due to inductive energy storage. The diode is assumed to be ideal because our focus is on the switch characteristics, though in practice the diode reverse recovery current can significantly affect the stresses on the switch.

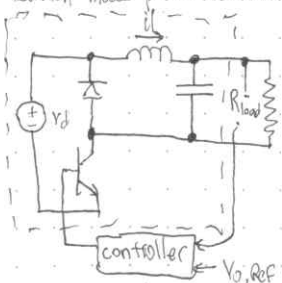


Fig 1-3 Switch mode dc power supply

When the switch is on, the entire current I_o flows through the switch and the diode is reverse biased. When the switch is turned off, I_o flows through the diode and a voltage equal to the input voltage appears across the open switch, assuming zero voltage drop across the ideal diode. Figure 2-6b shows the waveforms for the current through the switch and across the voltage across the switch when it is being operated at a repetition rate or switching frequency of $f_s = 1/T_s$, with T_s being the switching time period. The switching waveforms are represented by linear approximations to the actual waveforms in order to simplify the discussion.

When the switch has been off for a while, it is turned on by applying a positive control signal to the switch, as is shown in Fig 2-6-B shows the waveforms for the current through the switch and the voltage across the switch when it is being operated at a repetition rate or switching frequency of $f_s = 1/T_s$, with T_s being the switching time period. The switching waveforms are represented by linear approximations to the actual waveforms in order to simplify the discussion.

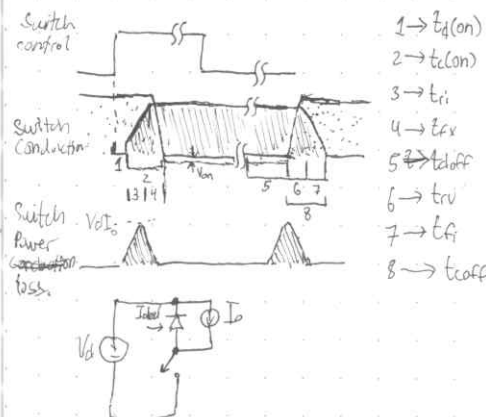
When the switch has been off for a while during the turn-on transition of this generic switch, the current build up consists of a short delay time $t_{d(on)}$ followed by the current rise time t_{ri} . Only after the current I_o flows entirely through the switch can the diode become reverse biased and the switch voltage fall to a small on state value of V_{on} with a voltage fall time of t_{fv} . The waveforms in Fig 2-6b indicate that large values of switch voltage and current are present simultaneously during the crossover interval $t_{c(on)}$, where:

$$t_{c(on)} = t_{ri} + t_{fv} \quad (2-1)$$

The energy dissipated in the device during this turn-on transition can be approximated from Fig. 2-6c as:

$$W_{c(on)} = \frac{1}{2} V_d I_o t_{c(on)}$$

where it is recognized that no energy dissipation occurs during the turn-on delay interval $t_{d(on)}$.



Once the switch is fully on, the on-state voltage V_{on} will be an order of magnitude a volt or a volt or so depending on the semiconductor device, and it will be conducting a current I_o . The switch remains in conduction during the interval t_{on} , which in general is much longer than the turn-on and off transition times. The energy dissipation W_{on} in the switch during this on-state interval can be approximated as

$$W_{on} = V_{on} I_o t_{on} \quad (2-3)$$

where $t_{on} \gg t_{d(on)}, t_{c(off)}$.

In order to turn the switch off, a negative control signal is applied to the control signal on the switch. During the turn-off transition period of the switch, generic switch, the voltage build up consists of a turn-off delay time $t_{d(off)}$ and a voltage rise time t_r . Once the voltage reaches its final value of V_d . (See Fig. 2-6a), the diode can become forward biased and begin to conduct current. The current in the switch falls to zero with a current fall time t_f as the current I_o commutates from the switch to the diode. Large values of switch voltage and switch current occur simultaneously during the crossover interval $t_{c(off)}$, where:

$$t_{c(off)} = t_r + t_f \quad (2-4)$$

The energy dissipated in the switch during this turn-off transition can be written, using Fig. 2-6c, as:

$$W_{c(off)} = \frac{1}{2} V_d I_o t_{c(off)} \quad (2-5)$$

where any energy dissipation during the turn-off delay interval $t_{d(off)}$ is ignored. Since it is compared to $W_{c(off)}$.

The instantaneous power dissipation $p_T(t) = V_{rit}$ plotted in Fig. 2-6c makes it clear that a large instantaneous power dissipation occurs in the switch during the turn-on and turn-off intervals. There are f_s such turn-on and turn-off transitions per second. Hence the average switching power loss P_s in the switch due to these transitions can be approximated from Eqs. 2-2 and 2-5 as:

$$P_s = \frac{1}{2} V_d I_o f_s (t_{c(on)} + t_{c(off)}) \quad (2-6)$$

This is an important result because it shows that the switching power loss in a semiconductor switch varies linearly with the switching frequency and the switching times. Therefore, if devices with short switching times are available, it is possible to operate at high switching frequencies in order to reduce filtering requirements and at the same time keep the switching power loss in the device from being excessive.

The other major contribution to the power loss in the switch is the average power dissipated during the on-state P_{on} , which varies in proportion to the on-state voltage. From Eq. 2-3, P_{on} is given by:

$$P_{on} = V_{on} I_o t_{on} / T_s \quad (2-7)$$

which shows that the on-state voltage in a switch should be as small as possible.

The leakage current during the off state (switch open) of controllable switches is negligibly small, and therefore the power loss during the off state can be neglected in practice. Therefore, the total average power dissipation P_T in a switch is the sum of P_s and P_{on} .

From the considerations discussed in the preceding paragraphs, and following characteristics in a controllable switch, are desirable:

- (1) Small leakage current in the off state.
- (2) Small on-state voltage V_{on} to minimize on-state power loss.
- (3) Short turn-on and turn-off times. This permits the usage at high frequencies.
- (4) High on-state current rating. In high-current applications, this would minimize the need to connect several devices in parallel, thereby avoiding the problem of current sharing.
- (5) Large forward- and reverse-voltage-blocking capability. This will need to minimize the need for series connection of several devices, which complicates the control and protection on the switches. Moreover, most of the device types have a minimum on-state voltage regardless of their blocking voltage rating. A series connection of several such devices would lead to a higher total on-state voltage and hence higher conduction losses. In most (but not all) converter circuits, a diode is placed across the controllable switch to allow the current to flow in the reverse direction. In those circuits, controllable switches are not required to have any significant reverse-voltage blocking capability.
- (6) Positive temperature coefficient of on-state resistance. This ensures that paralleled devices will share the total current equally.
- (7) Small control power required to switch the device. This will simplify the control circuit design.
- (8) Capability to withstand rated voltage and rated current simultaneously while switching. This will eliminate the need for external protection (snubber) circuits across the device.
- (9) Large $\frac{dv}{dt}$ and $\frac{di}{dt}$ ratings. This will minimize the need for external circuits otherwise needed to limit dv/dt and di/dt in the device so that it is not damaged.

We should note that the clamped-inductive-switching circuit of Fig. 2-6a results in higher switching power loss and puts higher stresses on the switch in comparison to the resistive switching circuit shown in problem 2-2 (Fig. P2-2).

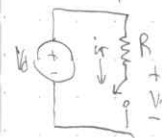


Figure P2-2

Now we will continue with the problem after that lengthy review of considerations.

Given:

- $t_{ri} = 100\text{ns}$
- $t_{fv} = 50\text{ns}$
- $t_{rv} = 100\text{ns}$
- $t_{fi} = 200\text{ns}$

The portion of time when the switch is consuming power is given by $t_{c(on)}$. The time of rising current is given by t_{ri} , and the time of falling voltage, t_{fv} . When these dynamics are in play, before the switch has settled, a larger amount of power is drawn for this duration. The time during this power spike is trivially calculated as:

$$t_{c(on)} = t_{ri} + t_{fv} = 100\text{ns} + 50\text{ns} = 150\text{ns}$$

The time when the switch closes is given by $t_{c(off)}$ and it also consumes power during this switch. The duration is given by ~~$t_{c(off)}$~~ t_{rv} , the time of the rising voltage, and ~~the~~ ^{once} the voltage charge accumulates the current drains in the diode. ~~the~~ t_{fi} is the time of the falling current. Combined these give us $t_{c(off)}$:

$$t_{c(off)} = t_{rv} + t_{fi} = 100\text{ns} + 200\text{ns} = 300\text{ns}$$

Notice how the off switch time is double the on switch time. This shows us how we cannot simply double the on time, and assume the off time is symmetric.

The power can be represented as the area underneath the VI curve, which are triangular. We can approximate these as triangles. The peak power represents the height of both triangles.



The area of the first triangle is

$$P_{s,on} = \frac{1}{2} \underbrace{(V_d I_0)}_{\text{height}} \underbrace{t_{c(on)}}_{\text{base}} \cdot f_s$$

likewise the second triangle

$$P_{s,off} = \frac{1}{2} (V_d I_0) t_{c(off)} f_s$$

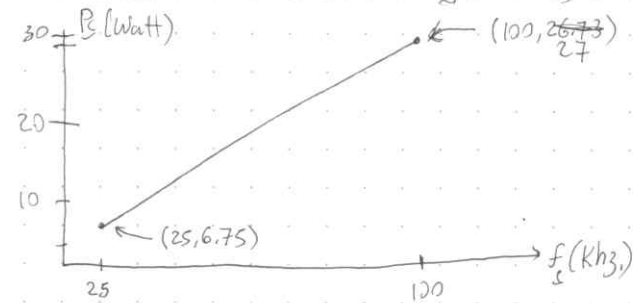
We can combine the common terms and thus get the power consumed during the switch as

$$P_s = \frac{1}{2} V_d I_0 [t_{c(on)} + t_{c(off)}] \cdot f_s \quad (5.1) \text{ Watt}$$

$$\Rightarrow P_s = \frac{1}{2} (500\text{V})(4\text{A}) \cdot [450\text{e-}9] \cdot f_s \text{ Watt}$$

$$\Rightarrow P_s = 2.7 \cdot 10^{-4} \text{ W} \cdot f_s \text{ which is } 270 \mu\text{W per cycle}$$

We were instructed to plot the power, and for our model, the power is consumed linearly with frequency. For every cycle we consume 270 μW a cycle, so in a kHz we will consume 270 μW . We were instructed to use the Range, 25kHz to 100kHz.



at 25kHz we consume about 7 watt,
at 100kHz we consume is ~~27~~ 27 Watt.

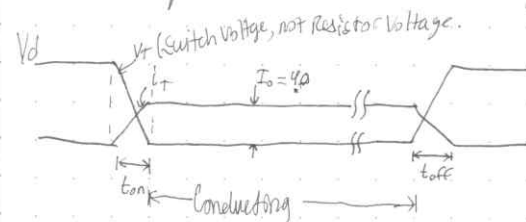
Whether this linear case is what happens or not is not to be determined by this model. This model is highly idealized and simple to fit the pedanticness of the textbook problem.

2-2 Consider the Resistive Switching circuit shown in Fig. p2-2. $V_d = 300\text{V}$, $f_s = 100\text{kHz}$ and $R = 75\Omega$, so that the on state current is the same as in problem 2-1.

Assume the switch turn-on time to be the sum of t_{ri} and t_{fv} in problem 2-1. Similarly, assume the turn off time to be the sum of t_{rv} and t_{fi} .



Assume linear voltage and current switching characteristics, plot the switch voltage and current and the switching power loss as a function of time. Compare the average power loss with that in problem 2-1.



The current consumed by the resistor is simply

$$I_0 = \frac{V_d}{R} = \frac{300\text{V}}{75\Omega} = 4.0 \text{ Amps}$$

this occurs when:

$$t_{on} < t < t_{off}$$

We again assume that

$$t_{on} = t_{ri} + t_{fv} = 150\text{ns}$$

$$t_{off} = t_{rv} + t_{fi} = 300\text{ns}$$

The power consumption during this conducting time is 1.2 kW.

The power consumed by the Resistor when on is 1.2 kW.

The current is assumed to be linear along with the voltage for the transient periods between the conduction and off states.

The current and voltage is being represented linearly in this idealistic model such that

$$i_T = I_0 \left(\frac{t}{t_{on}} \right) \text{ where the fraction bound } \& \text{ are } (\alpha t < t_{on})$$

for the range when the transient enables $[0 < t < t_{on}]$

$$i_T = I_0 \left(1 - \frac{t}{t_{on}}\right), \quad V_T = V_d \left(1 - \frac{t}{t_{on}}\right)$$

The voltage is also represented as a linear drop in this model.

So the power is the VI product of the voltages for this time range.

$$P_{T_{on}} = V_d I_0 \left(1 - \frac{t}{t_{on}}\right) t = (300 \text{ volts})(4 \text{ Amps}) \left(t - \frac{t^2}{t_{on}}\right) \frac{1}{t_{on}}$$

The actual work energy is the integration of powers

$$\text{Work consumed to switch on} = \int_0^{t_{on}} (P_T)_{on} dt$$

$$\Rightarrow W_{on} = \frac{V_d I_0}{t_{on}} \int_0^{t_{on}} \left(t - \frac{t^2}{t_{on}}\right) dt = \frac{V_d I_0}{t_{on}} \left[\frac{t^2}{2} - \frac{t^3}{3 t_{on}} \right]_0^{t_{on}}$$

$$W_{on} = \frac{V_d I_0}{t_{on}} \left[\frac{1}{2} t_{on}^2 - \frac{1}{3} \frac{t_{on}^3}{t_{on}} \right] = \frac{V_d I_0}{t_{on}} \left[\frac{1}{2} t_{on}^2 - \frac{1}{3} t_{on}^2 \right] = \frac{V_d I_0}{t_{on}} \left[\frac{1}{6} t_{on}^2 \right]$$

We remember our chem. rule for integration.

$$W_{on} = \frac{V_d I_0}{t_{on}} \left[\frac{1}{2} t^2 - \frac{1}{3} \frac{t^3}{t_{on}} \right]_0^{t_{on}} = \frac{V_d I_0}{t_{on}} \left[\frac{1}{2} t_{on}^2 - \frac{1}{3} \frac{t_{on}^3}{t_{on}} \right]$$

$$\Rightarrow W_{on} = V_d I_0 \cdot t_{on} \left[\frac{1}{2} - \frac{1}{3} \right] = V_d I_0 \cdot t_{on} \left[\frac{3}{6} - \frac{2}{6} \right] = \frac{V_d I_0 \cdot t_{on}}{6}$$

$$\boxed{W_{on} = \frac{V_d I_0 \cdot t_{on}}{6}}$$

Similarly, for the off switch, we can represent the current linearly as $i_T = I_0 \left(1 - \frac{t}{t_{off}}\right)$, $V_T = V_d \left(\frac{t}{t_{off}}\right)$

$$\text{Resulting in a power } P_{off} = V_d I_0 \left(1 - \frac{t}{t_{off}}\right) \cdot t \cdot \frac{1}{t_{off}}$$

and the work ends up being an analogous result, only t_{on} is replaced with t_{off} .

$$\Rightarrow \boxed{W_{off} = \frac{V_d I_0 \cdot t_{off}}{6}}$$

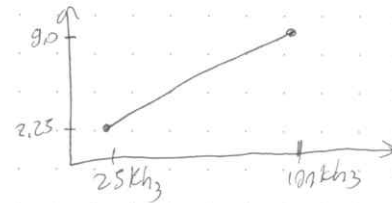
The work consumed by the switch on and switch off factors out the common elements.

$$W_{switch} = W_{on} + W_{off} = \frac{V_d I_0}{6} [t_{off} + t_{on}]$$

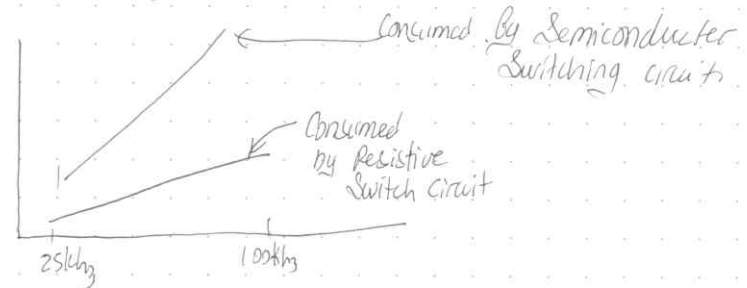
The average power consumed is determined by dividing by the seconds a cycle occurs, or in other words multiplying by frequency. The higher frequency the better averages

So the average switching power loss as a function of switching frequency is simply:

$$P_s(f_s) = f_s \cdot W_s = \frac{V_d I_0}{6} [t_{on} + t_{off}] f_s = \frac{300 \cdot 4}{6} [450e-9] \text{ Watt}$$



The power consumption also varies linearly with frequency.



at 100KHz, the P2-1 circuit gives 27 watt, this resistive switching circuit is 9 watt.

$$\frac{1}{2} V_i \cdot t \cdot f \quad \text{vs} \quad \frac{1}{6} V_i \cdot t \cdot f$$

~~The resistive switch consumes a third of the power. However what I would consider more concerning is that the resistive switch stays on, burning current, while the diode probably has less resistance when it is a steady state on. This is probably~~