## TATU BOGDAN – GR 3.1 – CLOCKED SYNCHRONOUS STATE MACHINES

- 1. Design a modulo 11 cyclic counter which counts either in ascending, or in descending, binary counting order (not both of them). Implement the counter with:
  - a. D flip-flops

		D flip-f	flops - asce	nding bina	ry counting	gorder		
Nr.	Q3	Q2	Q1	Q0	Q3*	Q2*	Q1*	Q0*
0	0	0	0	0	0	0	0	1
1	0	0	0	1	0	0	1	0
2	0	0	1	0	0	0	1	1
3	0	0	1	1	0	1	0	0
4	0	1	0	0	0	1	0	1
5	0	1	0	1	0	1	1	0
6	0	1	1	0	0	1	1	1
7	0	1	1	1	1	0	0	0
8	1	0	0	0	1	0	0	1
9	1	0	0	1	1	0	1	0
10	1	0	1	0	0	0	0	0
11	1	0	1	1	d/0	d/0	d/0	d/0
12	1	1	0	0	d/0	d/0	d/0	d/0
13	1	1	0	1	d/0	d/0	d/0	d/0
14	1	1	1	0	d/0	d/0	d/0	d/0
15	1	1	1	1	d/0	d/0	d/0	d/0

• Karnaugh Maps – minimal risk/cost implementations

			D3			
				Q	3	
						_
	Q3Q2 Q1Q0	00	01	11	10	
	00	0	0	d/0	1	
_	01	0	0	d/0	1	Q0
01	11	0	1	d/0	d/0	Qu
Q1	10	0	0	d/0	0	
			C	(2		
		М	INIMAL CO	ST		
		D3 = Q1'	* Q3 + Q0	* Q1 * Q2		
		M	IINIMAL RI	SK		
	D3	= Q1' * Q2'	* Q3 + Q0	* Q1 * Q2 *	* Q3'	

			D2			
				C	(3	
	Q3Q2 Q1Q0	00	01	11	10	
	00	0	1	d/0	0	
	01	0	1	d/0	0	
01	11	1	0	d/0	d/0	Q0
Q1	10	0	1	d/0	0	
		N 4		(2 .c.t		
	D2 =		INIMAL CO + Q0' * Q2 -		' Q2'	
			INIMAL RIS		~_	
	D2 = Q1' * Q2	2 * Q3' + Q	0' * Q2 * Q	3' + Q0 * Q	1 * Q2' * C	Q3'

			D1			
				Q	(3	
	Q3Q2 Q1Q0	00	01	11	10	
	00	0	0	d/0	0	
	01	1	1	d/0	1	
01	11	0	0	d/0	d/0	Q0
Q1	10	1	1	d/0	0	
						_
				(2		
			INIMAL CO			
			Q1' + Q0'			
			INIMAL RIS			
	D1 = Q0 *	Q1' * Q2' +	+ Q0 *Q1' *	Q3' + Q0'	* Q1 * Q3'	

			D0	_	_	
				C	(3	
	Q3Q2 Q1Q0	00	01	11	10	]
	00	1	1	d/0	1	
	01	0	0	d/0	0	00
01	11	0	0	d/0	d/0	Q0
Q1	10	1	1	d/0	0	
			C	(2		
			INIMAL CO			
		D1 = Q(	)' * Q3' + Q	0' * Q1'		
		M	INIMAL RI	SK		
		D1 = Q0' *	'Q3' + Q0'	* Q1' * Q2'		

## b. T flips-flops (enable)

				T flip-flops	(enable) -	ascending	binary cou	nting order				
Nr.	Q3	Q2	Q1	Q0	Q3*	Q2*	Q1*	Q0*	EN3	EN2	EN1	ENO
0	0	0	0	0	0	0	0	1	0	0	0	1
1	0	0	0	1	0	0	1	0	0	0	1	1
2	0	0	1	0	0	0	1	1	0	0	0	1
3	0	0	1	1	0	1	0	0	0	1	1	1
4	0	1	0	0	0	1	0	1	0	0	0	1
5	0	1	0	1	0	1	1	0	0	0	1	1
6	0	1	1	0	0	1	1	1	0	0	0	1
7	0	1	1	1	1	0	0	0	1	1	1	1
8	1	0	0	0	1	0	0	1	0	0	0	1
9	1	0	0	1	1	0	1	0	0	0	1	1
10	1	0	1	0	0	0	0	0	1	0	1	0
11	1	0	1	1	d/0	d/0	d/0	d/0	d/1	d/0	d/1	d/1
12	1	1	0	0	d/0	d/0	d/0	d/0	d/1	d/1	d/0	d/0
13	1	1	0	1	d/0	d/0	d/0	d/0	d/1	d/1	d/0	d/1
14	1	1	1	0	d/0	d/0	d/0	d/0	d/1	d/1	d/1	d/0
15	1	1	1	1	d/0	d/0	d/0	d/0	d/1	d/1	d/1	d/1
		,										

## • Karnaugh Maps – minimal risk/cost implementations

			EN3				
				Q	3	-	
	Q3Q2 Q1Q0	00	01	11	10	]	
	00	0	0	d/1	0		
	01	0	0	d/1	0		00
01	11	0	1	d/1	d/1		Q0
Q1	10	0	0	d/1	1		
				2			
			INIMAL CO				
			* Q3 + Q0				
			INIMAL RIS				
	EN3	= Q1 * Q3	+ Q0 * Q1	* Q2 + Q2 *	* Q3		

			EN2				
			,	C	(3		
						]	
	Q3Q2 Q1Q0	00	01	11	10		
	00	0	0	d/1	0		
	01	0	0	d/1	0		Q0
Q1	11	1	1	d/1	d/0		Qυ
ŲΙ	10	0	0	d/1	0		
			Q	(2			
		М	INIMAL CO	ST			
		EI	N2 = Q0 * C	Q1			
		M	INIMAL RI	SK			
		EN2 = Q0	* Q1 * Q3'	+ Q2 * Q3			

-						
			EN1			
				C	(3	
	Q3Q2 Q1Q0	00	01	11	10	
	00	0	0	d/0	0	
_	01	1	1	d/0	1	Q0
Q1	11	1	1	d/1	d/1	Qu
QI	10	0	0	d/1	1	
			Q	(2		
		M	INIMAL CO	ST		
		EN1	= Q0 + Q1	* Q3		
		M	INIMAL RIS	SK		
	EN	N1 = Q0 * C	(3' + Q0 * C	Q2' + Q1 * (	Q3	

Q2 00		C	)3	
Q2 00				
	01	11	10	
1	1	d/0	1	
1	1	d/1	1	
1	1	d/1	d/1	Q0
1	1	d/0	0	
	C	(2		
M	INIMAL CO	ST		
ENO:	= Q3' + Q1'	* Q2'		
N	IINIMAL RI	SK		
	1 1 1 M ENO N	1 1 1 1 1 1 1 1 MINIMAL CO ENO = Q3' + Q1' MINIMAL RIS	1 1 d/1 1 d/1	1 1 d/1 1  1 1 d/1 d/1  1 1 d/0 0  Q2  MINIMAL COST  EN0 = Q3' + Q1' * Q2'  MINIMAL RISK

2. Design a zero-counting machine: a clocked synchronous state machine with one input X and one output Z. The output will be 1 if and only if the number of 0's (zero) received at the input X since reset is a multiple of 5, and the output will be 0 otherwise. Use D or T flip-flops for implementing the machine. Use either minimal risk or minimal cost approach.

		X = 1			X = 0									
Z	Q0*	Q1*	Q2*	Q0*	Q1*	Q2*	Q0	Q1	Q2	Nr.				
1	0	0	0	1	0	0	0	0	0	0				
0	1	0	0	0	1	0	1	0	0	1				
0	0	1	0	1	1	0	0	1	0	2				
0	1	1	0	0	0	1	1	1	0	3				
0	0	0	1	0	0	0	0	0	1	4				
d/0	d/0	d/0	d/0	d/0	d/0	d/0	1	0	1	5				
d/0	d/0	d/0	d/0	d/0	d/0	d/0	0	1	1	6				
d/0	d/0	d/0	d/0	d/0	d/0	d/0	1	1	1	7				
	D0	D1	D2	D0	D1	D2								

• Karnaugh Maps – minimal risk/cost implementations

			D2			
				)	<b>(</b>	
						]
	X Q2 Q1Q0	00	01	11	10	
	00	0	0	1	0	
	01	0	d/0	d/0	0	00
Q1	11	1	d/0	d/0	0	Q0
	10	0	d/0	d/0	0	
			Q	(2		
		М	INIMAL CO	ST		
		D2 = X *	* Q2 + X' * (	Q0 * Q1		
			INIMAL RIS			
	D2 = 2	X * Q0' * Q	1' * Q2 + X'	* Q0 * Q1	* Q2'	

			D1				
				X			
						]	
	X Q2 Q1Q0	00	01	11	10		
	00	0	0	0	0		
	01	1	d/0	d/0	0		Q0
Q1	11	0	d/0	d/0	1		Qυ
ŲΙ	10	1	d/0	d/0	1		
		N.4		(2 CT			
	D1		INIMAL CO		01		
	DI		L + X' * Q0 *		ŲΙ		
	D4 V * O4		INIMAL RIS		+ 041 + 00		
	D1 = X * Q1	. * Q2'+Q(	) * Q1 * Q2	Z' + X' * Q0	* Q1' * Q2	-	

				D0			
					X		
		X Q2 Q1Q0	00	01	11	10	
		00	1	0	0	0	
		01	0	d/0	d/0	1	00
Q1		11	0	d/0	d/0	1	Q0
ŲΙ		10	1	d/0	d/0	0	
Q2							
				INIMAL CO			
			D0 = X' *	* Q0' * Q2'	+ X * Q0		
			M	INIMAL RI	SK		
			D0 = X' * Q	0' * Q2' + X	* Q0 * Q2	1	