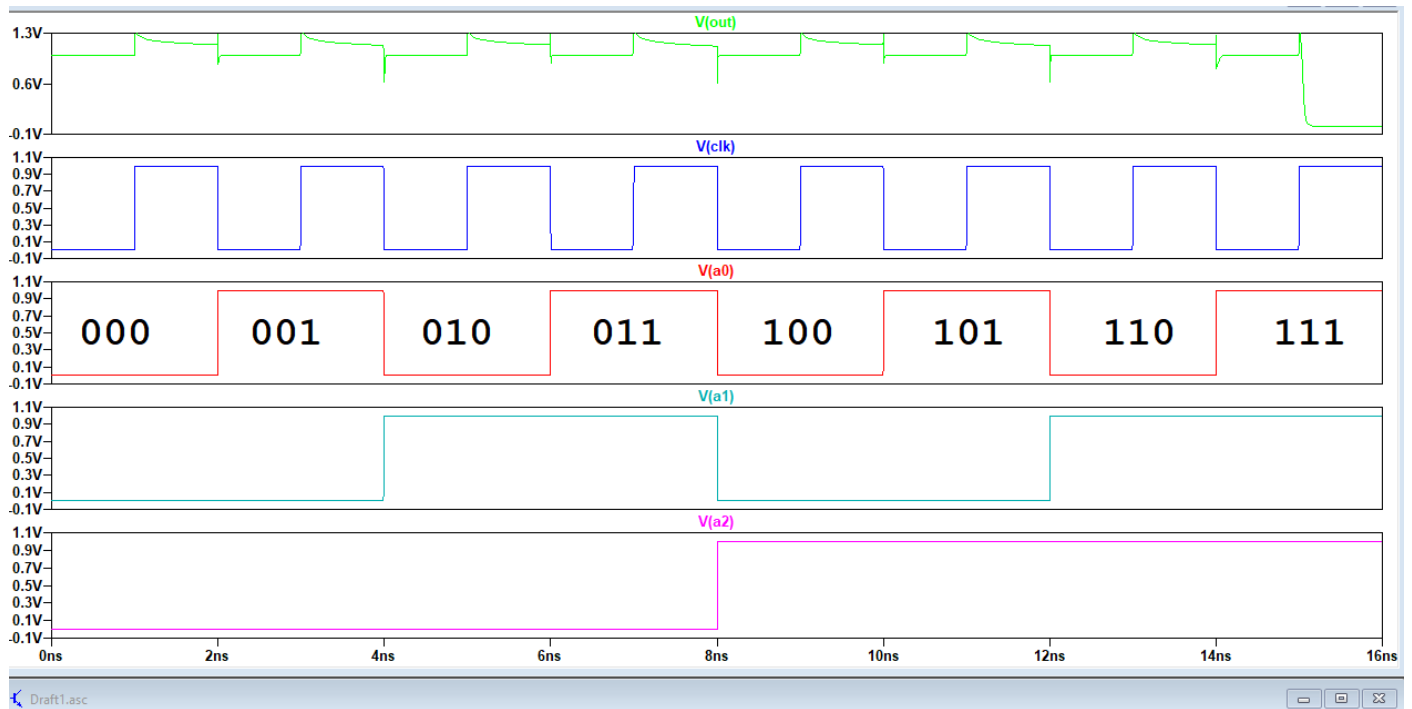


Tatu Bogdan – CTI EN 3.1

PE LOGIC & DOMINO LOGIC



```
VDD VDD 0 DC 1
.include cmosedu_models.txt
.tran 16n
```

