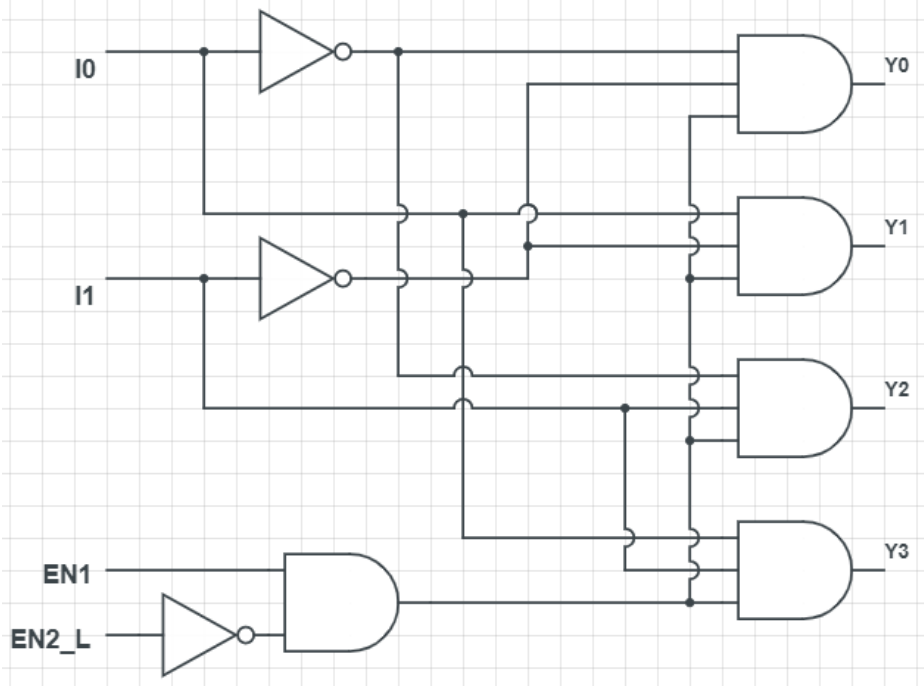


LAB 8 & 9

1. Decoders & Multiplexers

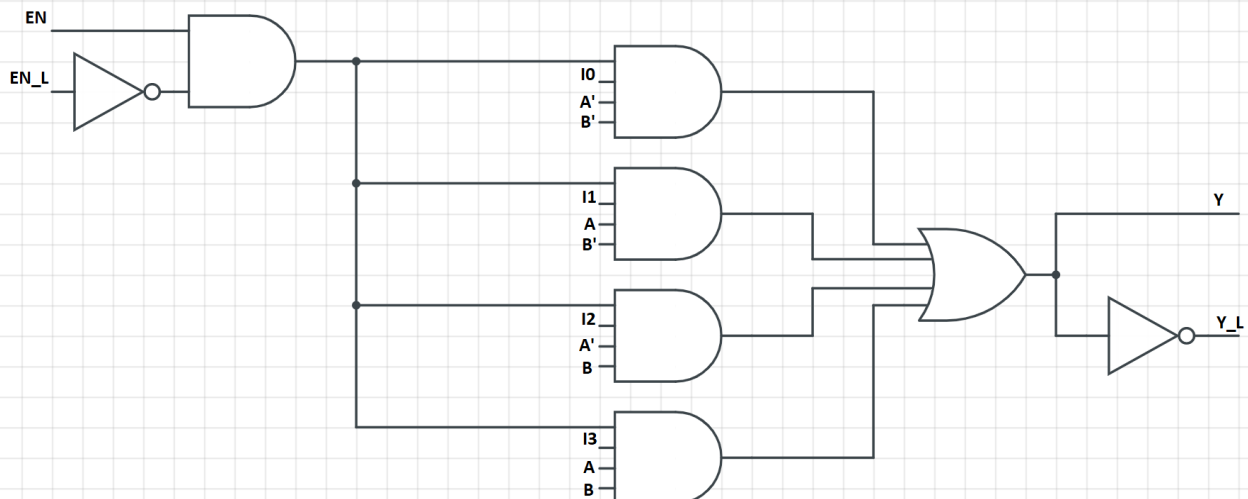
• Ex 1

Inputs				Outputs			
EN1	EN2_L	I1	I0	Y3	Y2	Y1	Y0
0	x	x	x	0	0	0	0
x	1	x	x	0	0	0	0
1	0	0	0	0	0	0	1
1	0	0	1	0	0	1	0
1	0	1	0	0	1	0	0
1	0	1	1	1	0	0	0

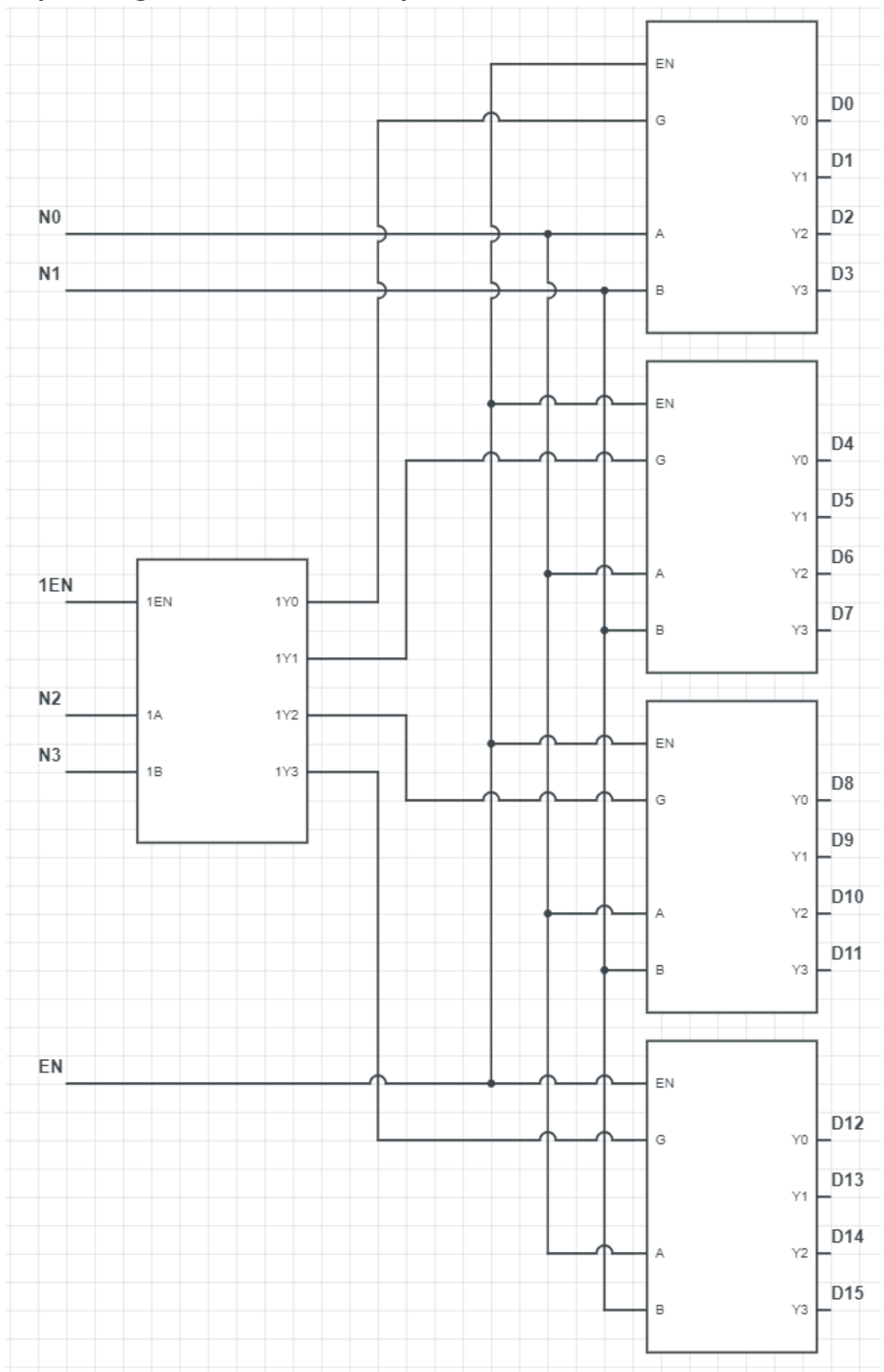


• Ex 7

Inputs				Output	
EN1	EN2_L	B	A	Y	Y_L
0	x	x	x	0	1
x	1	x	x	0	1
1	0	0	0	I0	I0'
1	0	0	1	I1	I1'
1	0	1	0	I2	I2'
1	0	1	1	I3	I3'



2. Expanding Decoders & Multiplexers



3. Implementing Functions with Combinational Circuits

