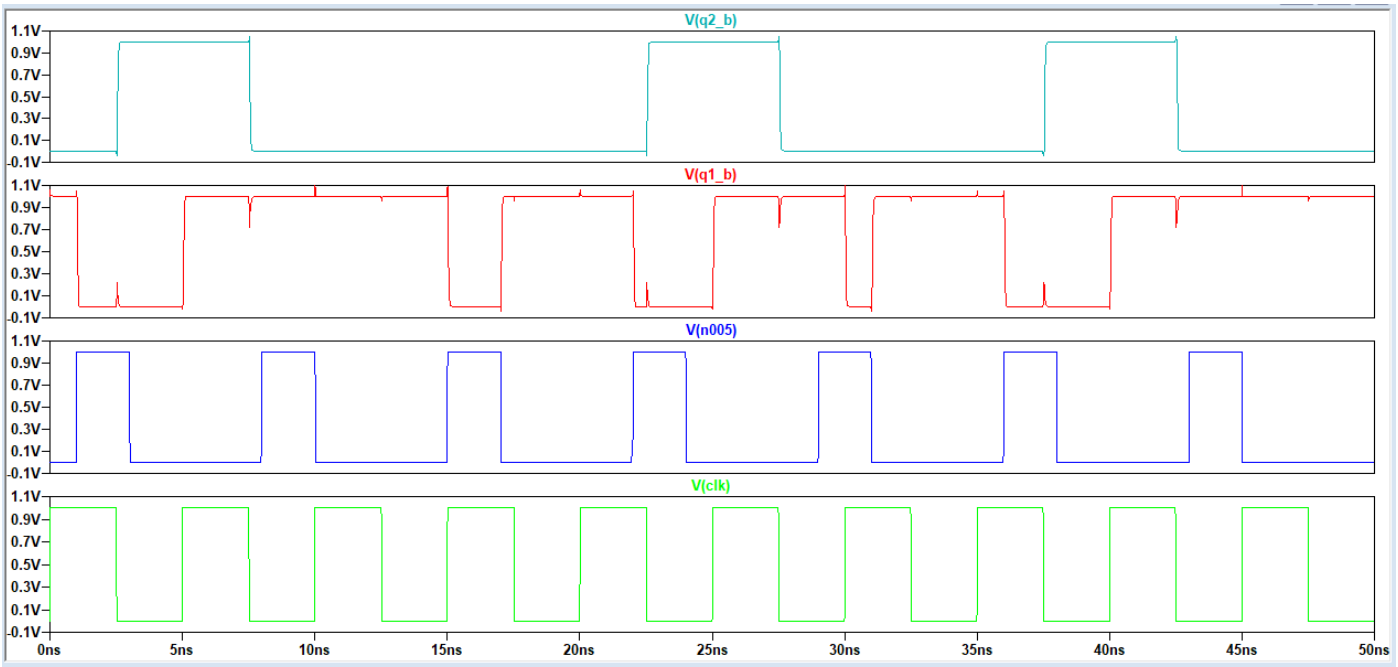
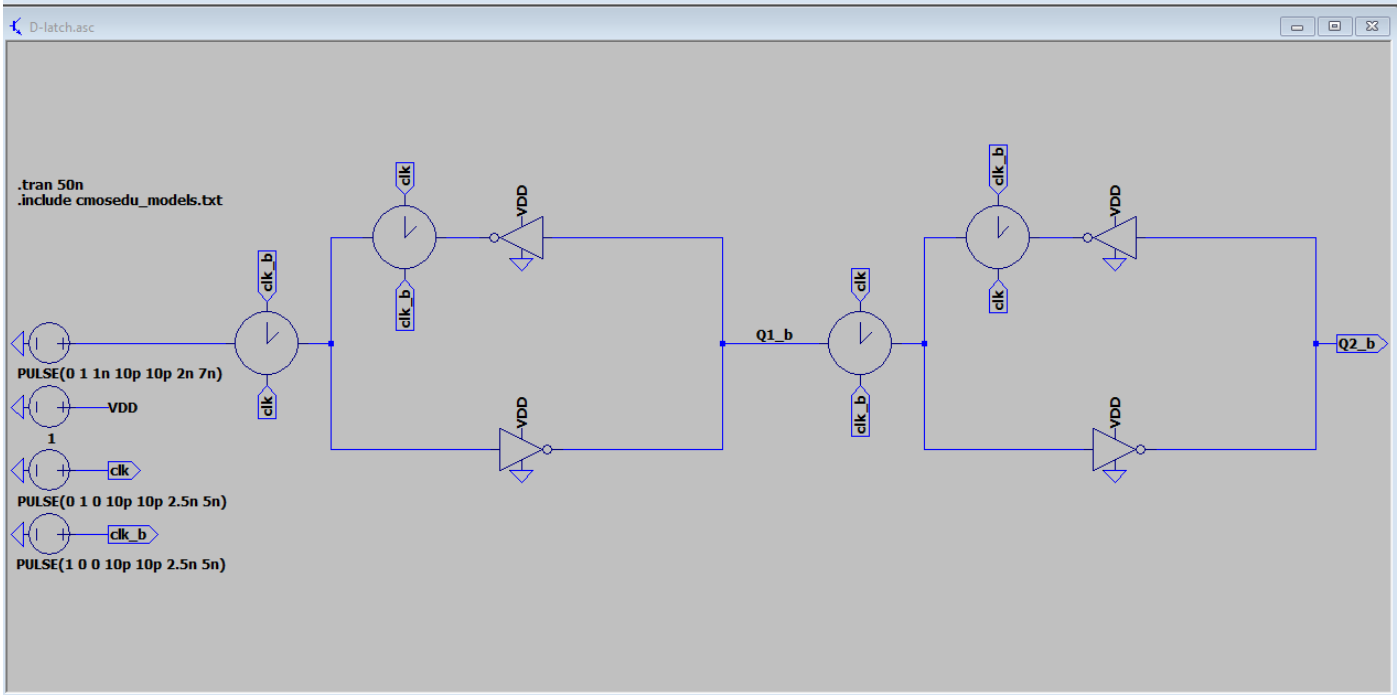
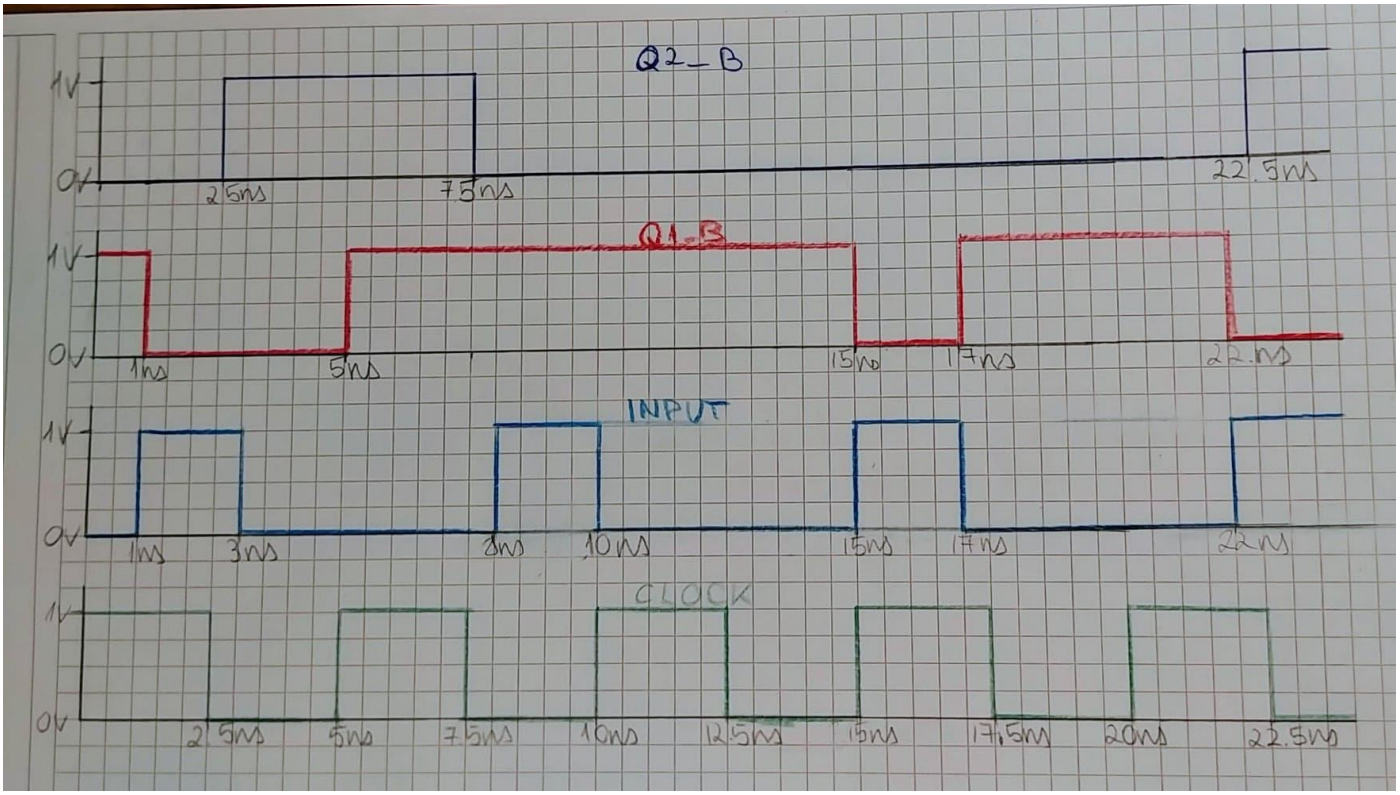
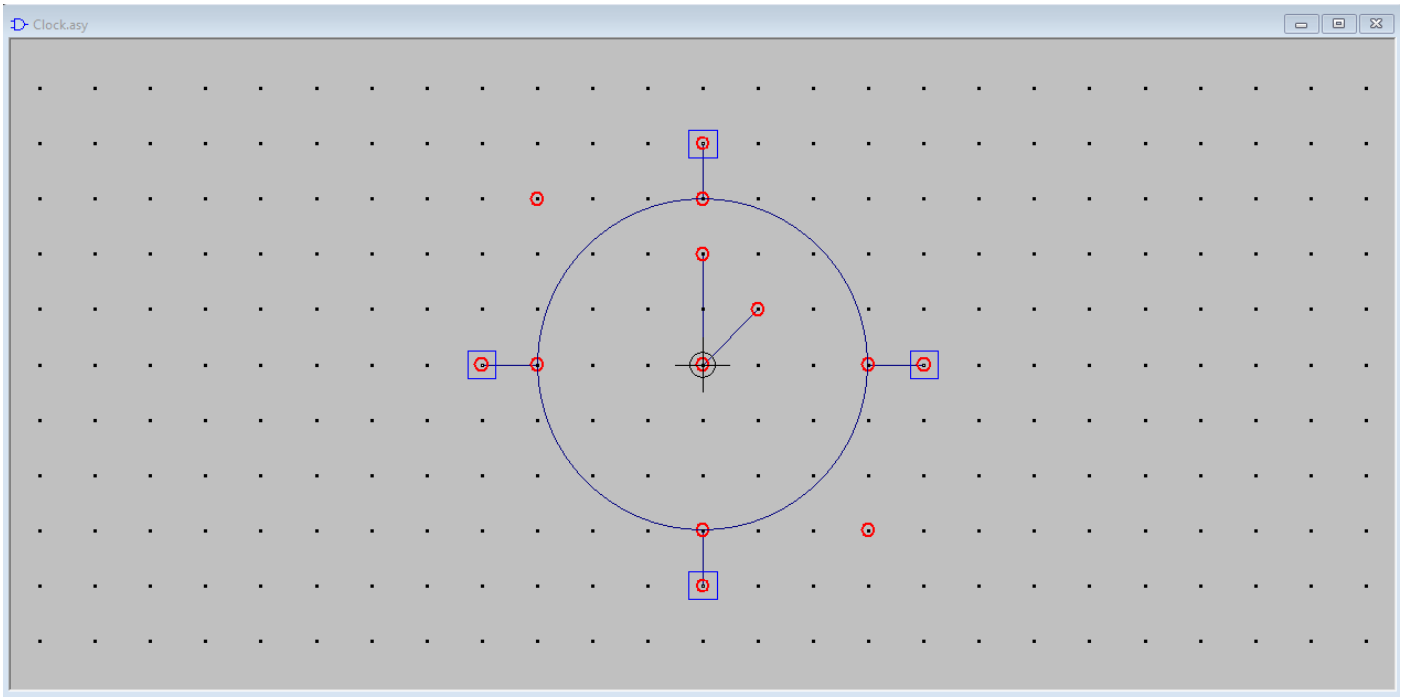
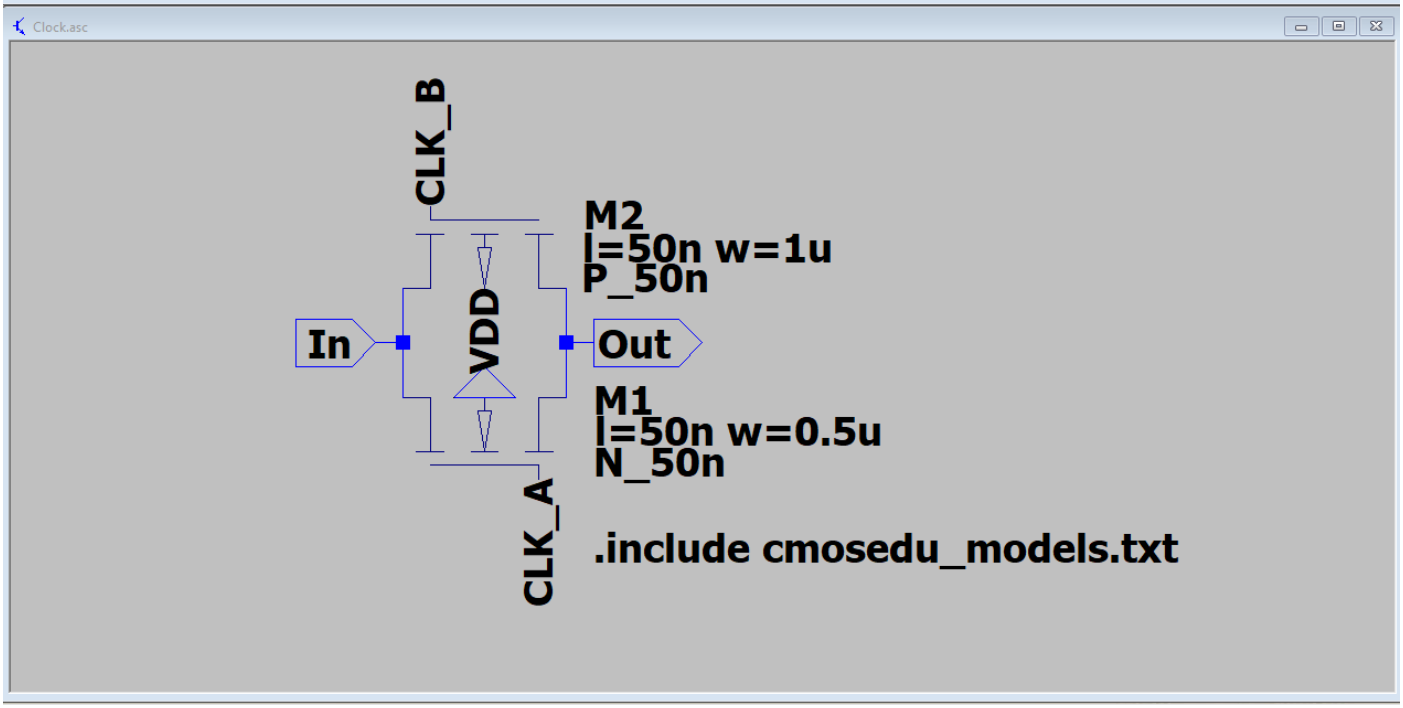
Tatu Bogdan – CTI EN 3.1

EDGE-TRIGGERED D FLIP-FLOP

- Clock: f = 20 MHz
- Input Voltage: T1 = 2ns T = 7ns






Q2_B
1V
0V
2.5ns
7.5ns
22.5ns
Q1_B
1V
0V
1ns
5ns
15ns
17ns
22.ns
INPUT
1V
0V
1ns
3ns
8ns
10ns
15ns
17ns
22ns
CLOCK
1V
0V
2.5ns
5ns
7.5ns
10ns
12.5ns
15ns
17.5ns
20ns
22.5ns

- Clock Component