

$X = -4 \quad Y = +7$

$X = -4 = 1100_{C1}$

$Y = +7 = 0111_{C2}$

$\frac{16}{96}$

$P = 11100100_{C2}$

$= -128 + 1100100$

$= -128 + 6 \times 16 + 4$

$= -128 + 100 = -28$

$P = X \times Y = -4 \times 7 = -28$

A	Q	n	COUNT
0000	11000	0111	00
0000	01100		01
0000	00110		10
0000	00011		11
1001	10011		
1100	10011		
1110	10011		
1111	10011		

11
COUNTS = 21

4.6. Combinational array structures for binary multiplication

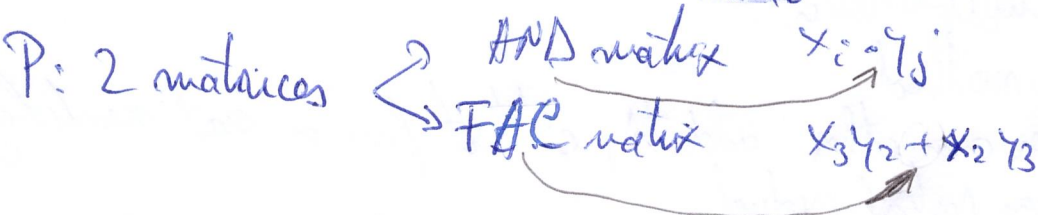
$X, Y = \text{unsigned}, 4 \text{ bits, integers}$

$X = x_3 x_2 x_1 x_0 \quad Y = y_3 y_2 y_1 y_0$

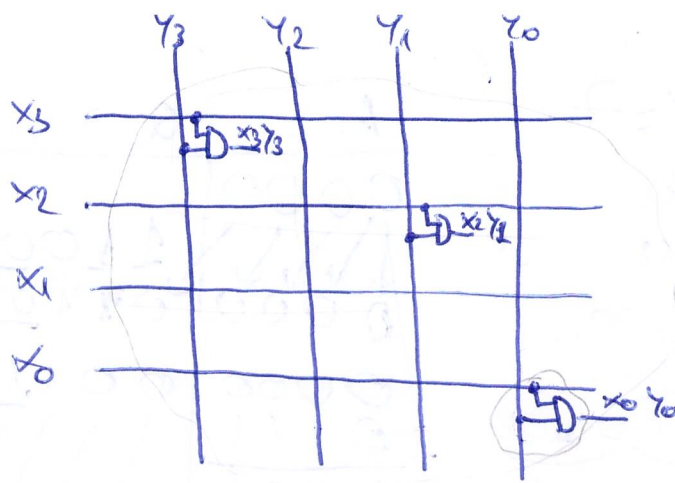
$$P = X \times Y = \left(\sum_{i=0}^3 x_i \cdot 2^i \right) \times \left(\sum_{j=0}^3 y_j \cdot 2^j \right) = \sum_{i=0}^3 2^i \cdot \left(\sum_{j=0}^3 x_i \cdot y_j \cdot 2^j \right)$$

$$P = 2^0 (x_0 y_0 \cdot 2^0 + x_0 y_1 \cdot 2^1 + x_0 y_2 \cdot 2^2 + x_0 y_3 \cdot 2^3) + 2^1 (x_1 y_0 \cdot 2^0 + x_1 y_1 \cdot 2^1 + x_1 y_2 \cdot 2^2 + x_1 y_3 \cdot 2^3) + 2^2 (x_2 y_0 \cdot 2^0 + x_2 y_1 \cdot 2^1 + x_2 y_2 \cdot 2^2 + x_2 y_3 \cdot 2^3) + 2^3 (x_3 y_0 \cdot 2^0 + x_3 y_1 \cdot 2^1 + x_3 y_2 \cdot 2^2 + x_3 y_3 \cdot 2^3)$$

$$P = 2^6 \cdot x_3 y_3 + 2^5 \cdot (x_3 y_2 + x_2 y_3) + 2^4 \cdot (x_3 y_1 + x_2 y_2 + x_1 y_3) + 2^3 \cdot (x_3 y_0 + x_2 y_1 + x_1 y_2 + x_0 y_3) + 2^2 \cdot (x_2 y_0 + x_1 y_1 + x_0 y_2) + 2^1 \cdot (x_1 y_0 + x_0 y_1) + 2^0 \cdot x_0 y_0$$

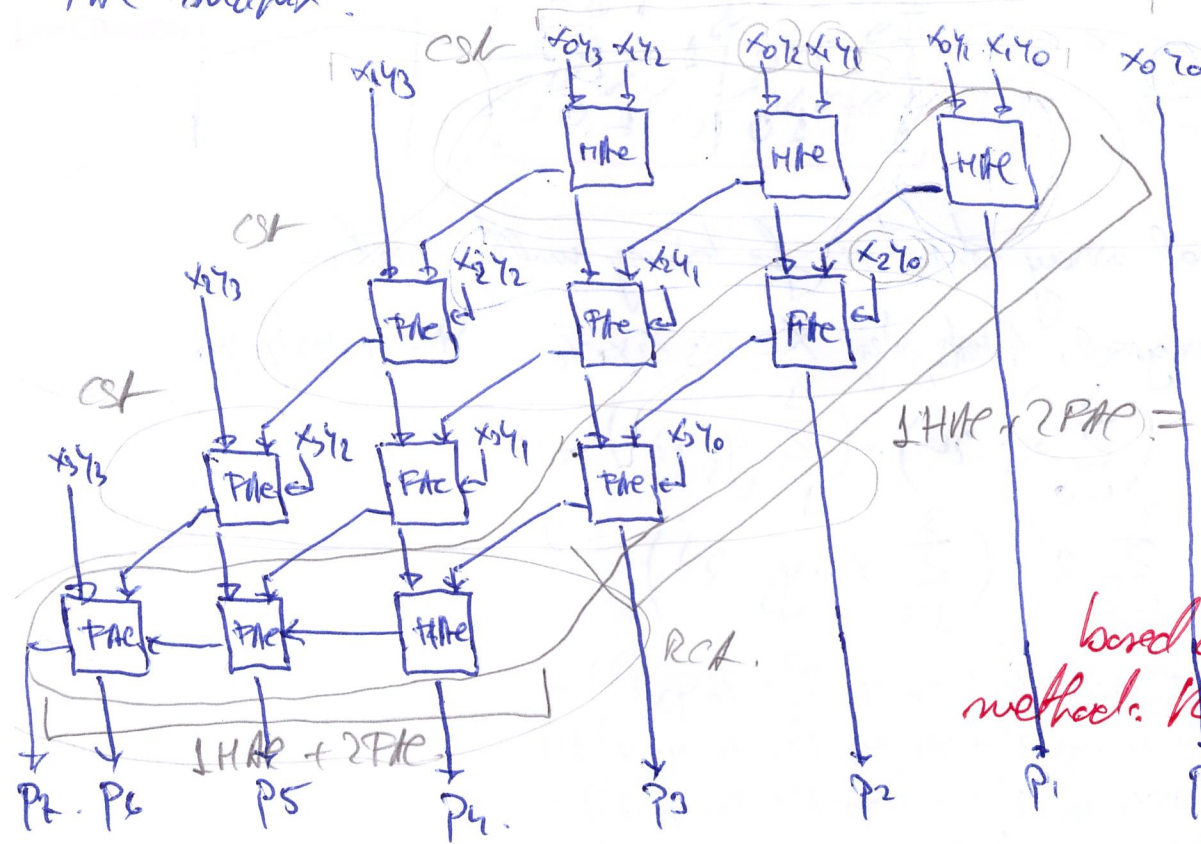


AND matrix:



$n-1$ cells
 \downarrow HME
 $n-2$ PHE
 $((n-2) \cdot 2 + 1)d$
 $(2n-3)d$

FHE matrix:



$n \text{ bit} \rightarrow 2 \cdot n - 3$

$4 \text{ bit} \rightarrow 5d$
 $(2 \cdot 4 - 3)$

$1 \text{ HME} + 2 \text{ PHE} = 1d + 4d = 5d$

the structure is based on the second method. Keeping the partial products fixed

$$\text{Latency } D = D_{\text{AND matrix}} + D_{\text{PHE matrix}} = 1d + 2(2n-3)d = (4n-5)d$$

$$\text{Area } A = A_{\text{AND matrix}} + A_{\text{PHE matrix}} = n^2 \cdot A_{\text{AND gate}} + n(n-1) \cdot A_{\text{PHE/mult}}$$

Not favorable for VLSI implementation

- wire crossing
- lack of regularity

Signed operands multiplication:

- use Booth method
- each layer can either add to, subtract from or not modify the previous partial product
- \rightarrow at the output of each layer, a partial product is delivered.

- input x_i, x_{i-1} pair \rightarrow decide the operation of the layer. (2)
- if $x_i x_{i-1} = 01 \rightarrow$ add $y_i \cdot z$ to the previous p-product
 - if $x_i x_{i-1} = 10 \rightarrow$ subtract $y_i \cdot z$ from u
 - if $x_i x_{i-1} = 00, 11 \rightarrow$ leave the previous p-product unchanged

Design a cell operating like \rightarrow

- only use y_i
- perform either $+$, $-$, or do not modify the input

$\rightarrow +$ and $-$: carry chain adder/subtractor

\rightarrow use carries

- need 2 control lines for selecting one of the 3 operations ($+$, $-$, NOP)
- receive one bit of the previous partial product $\rightarrow z$
- generate one bit of the next partial product

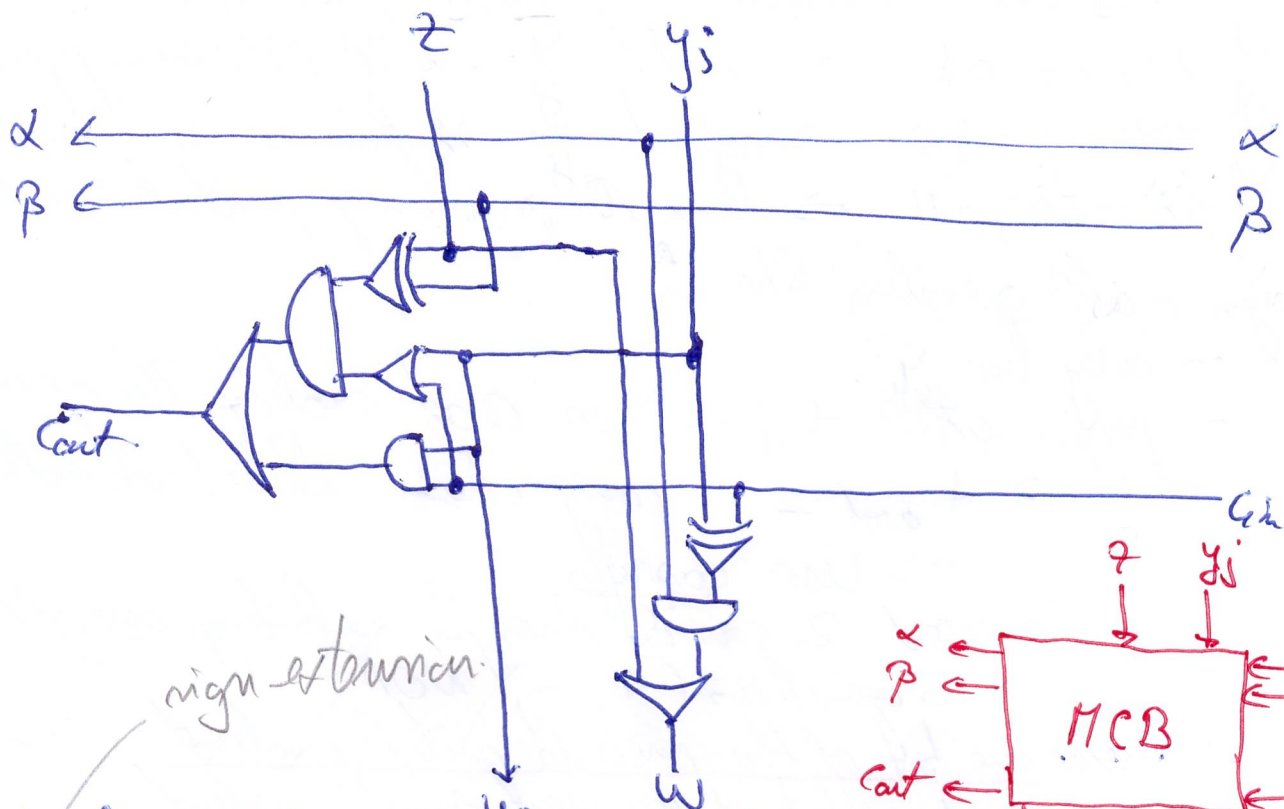
Operator	Outputs	
	w	Carry
Addition	$w = y_i \oplus z \oplus c_{in}$	$\text{Carry} = y_i \cdot z + y_i \cdot c_{in} + z \cdot c_{in}$
Subtraction	$w = y_i \oplus z \oplus c_{in}$	$\text{Carry} = y_i \cdot \bar{z} + y_i \cdot c_{in} + \bar{z} \cdot c_{in}$
NOP	$w = z$	irrelevant

Encode the 3 operations as 2 control variables α, β
 Compute w and Carry , using α, β :

$$w = z \oplus \alpha (y_i \oplus c_{in})$$

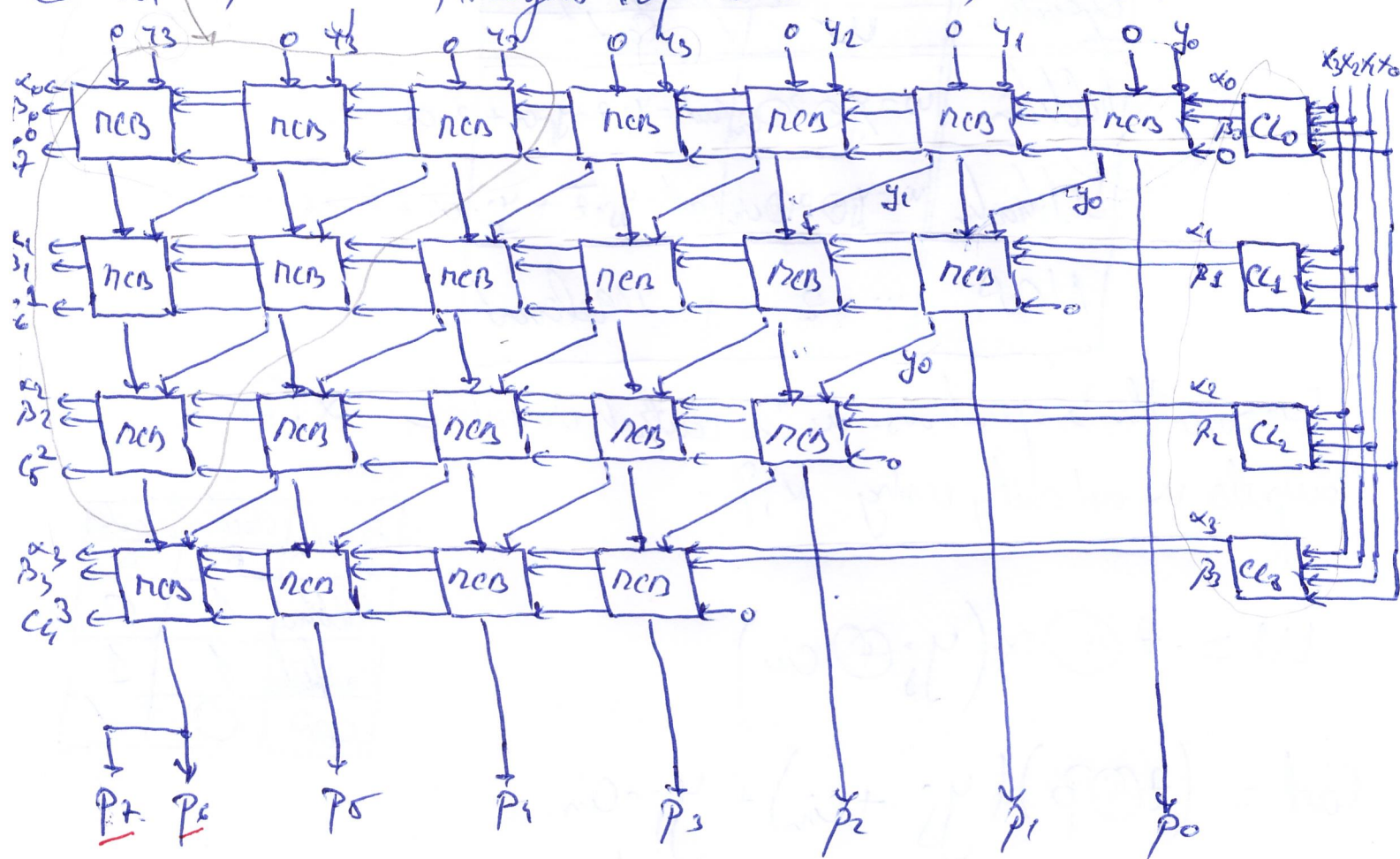
$$\text{Carry} = (z \oplus \beta) (y_i + c_{in}) + y_i \cdot c_{in}$$

Operator	Control variables	
	α	β
Addition	1	0
Subtraction	1	1
NOP	0	0



Carry extension

Architecture for the combinational carry-lookahead multiplier based on Booth's method. Consider 2, 1, integers represented in CL, as 4 bits.



Q2: $X = +5 = x_3x_2x_1x_0 = \boxed{01010}$
 $Y = -6 = \boxed{1010}$

00000000	$P_0 = 0$
11110100	$Y \cdot 2^0$
00001100	$P_1 = P_0 - Y \cdot 2^0$
11101000	$Y \cdot 2^1$
11110100	$P_2 = P_1 + X \cdot 2^1$
11010000	$Y \cdot 2^2$
00100010	$P_3 = P_2 - Y \cdot 2^2$
10100000	$Y \cdot 2^3$
11100010	$P_4 = P_3 + Y \cdot 2^3 = P$

$P = 11100010 = -128 + 1100010 = -128 + 96 + 2 = -30$
 $P = X * Y = +5 * (-6) = -30$

Generating α_i, β_i

NOP	$\alpha_i \beta_i = 00$	for NOP ($x_i x_{i-1} = 00$)
ADD	$\alpha_i \beta_i = 10$	for ADD ($x_i x_{i-1} = 01$)
SUB	$\alpha_i \beta_i = 11$	for SUB ($x_i x_{i-1} = 10$)

Input				Booth recoded				Output							
x_3	x_2	x_1	x_0	x_{3B}	x_{2B}	x_{1B}	x_{0B}	α_3	β_3	α_2	β_2	α_1	β_1	α_0	β_0
0	0	0	0	0	0	0	0	0	d	d	d	d	d	d	d
0	0	0	1	0	0	1	T	0	d	d	d	1	0	1	1
0	0	1	0	0	1	T	0	0	d	1	0	1	1	0	d
0	0	1	1	0	1	0	T	0	d	1	0	0	d	1	1
0	1	0	0	1	T	0	0	1	0	1	1	0	d	0	d
0	1	0	1	1	T	1	T	1	0	1	1	1	0	1	1
1	1	0	0	0	T	0	0	0	d	1	1	0	d	0	d
1	1	0	1	0	T	1	T	0	d	1	1	1	0	1	1
1	1	1	0	0	0	T	0	0	d	0	d	1	1	0	d
1	1	1	1	0	0	0	T	0	d	0	d	0	d	1	1

After minimization:

$\alpha_0 = x_0$
 $\beta_0 = 1$
 $\alpha_1 = x_1 \oplus x_0$
 $\beta_1 = x_1$
 $\alpha_2 = x_2 \oplus x_1$
 $\beta_2 = x_2$
 $\alpha_3 = x_3 \oplus x_2$
 $\beta_3 = x_3$