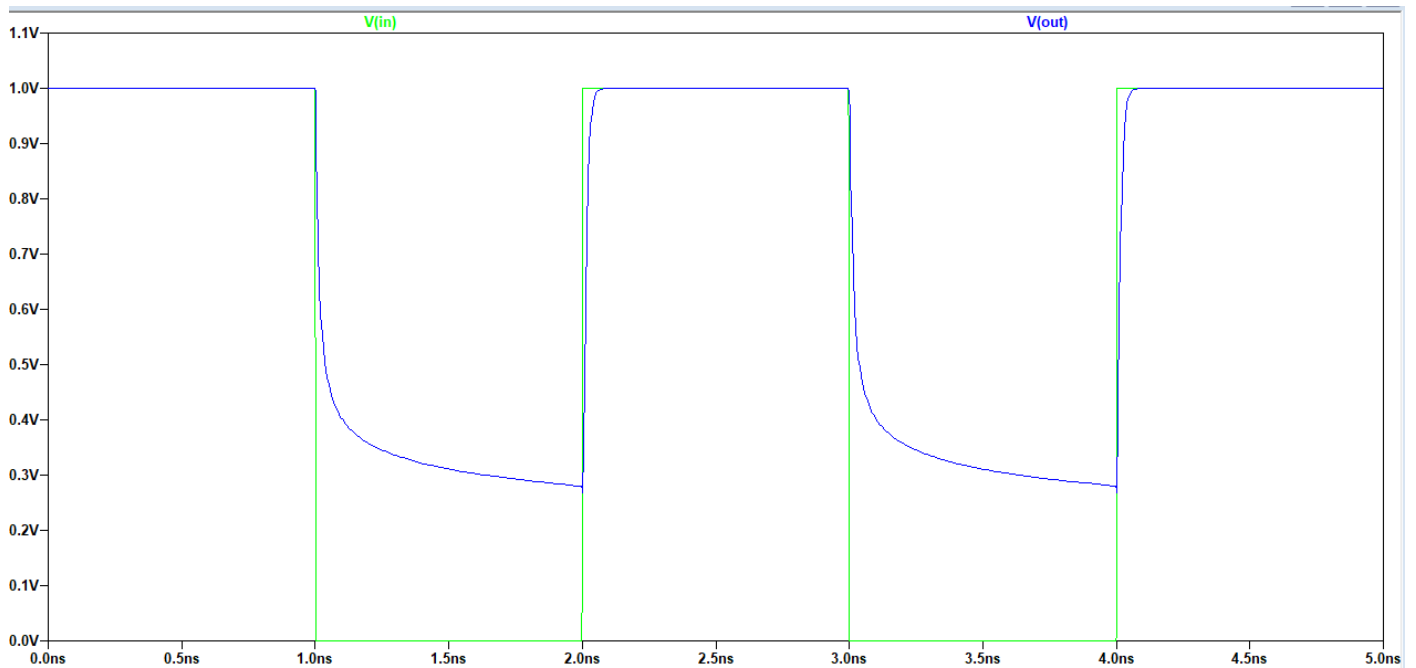


MOSFET PASS GATES



Draft1.asc

```
.include cmosedu_models.txt
.tran 5n
VDD VDD 0 DC 1
```

