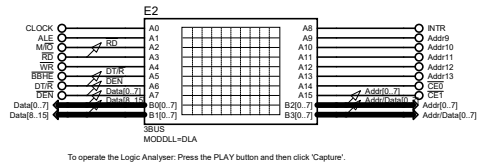
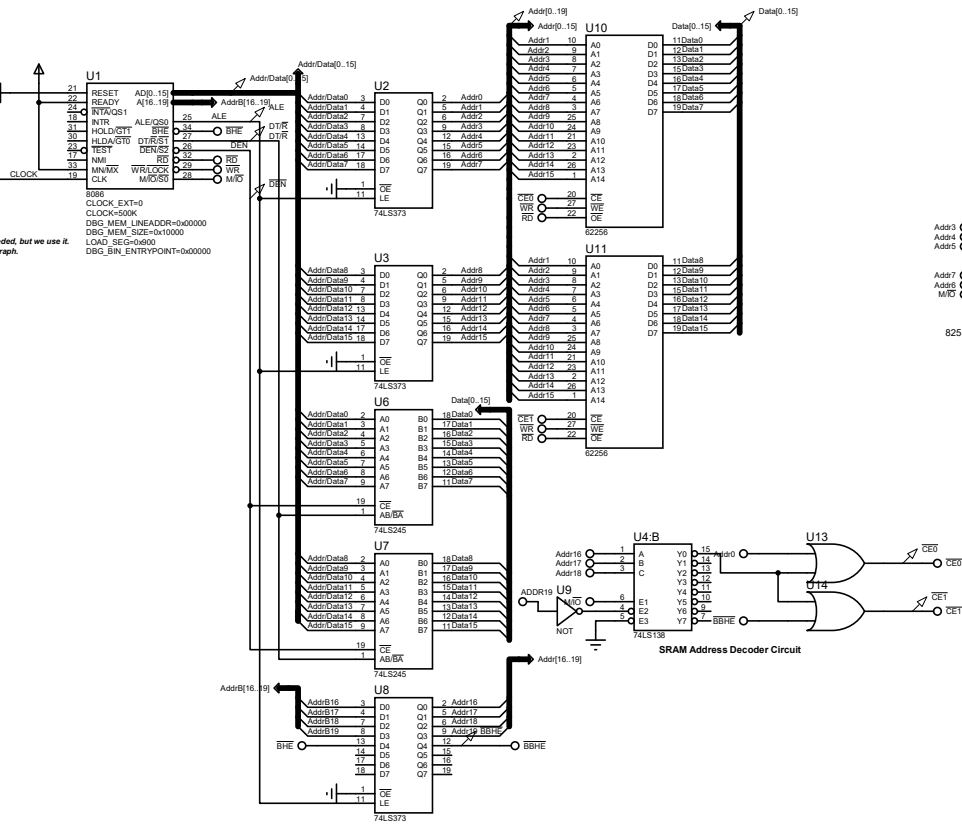
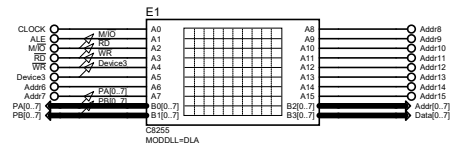
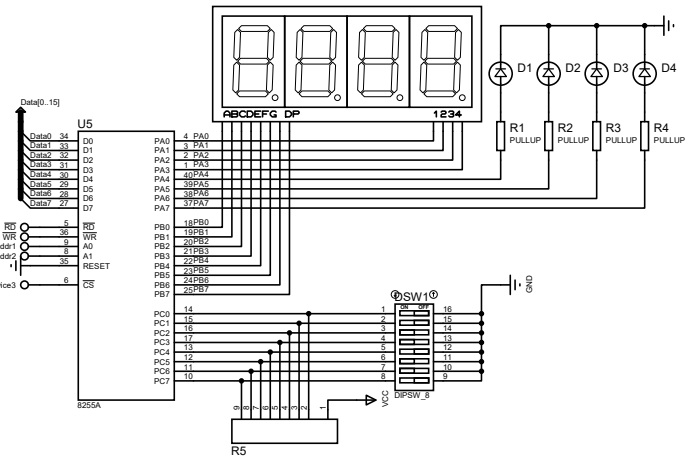


The Clock generator is not needed, but we use it as a timing reference on the graph.



To operate the Logic Analyser: Press the PLAY button and then click 'Capture'.

8255 Chip I/O Port Address Decoder Circuit



To operate the Logic Analyser: Press the PLAY button and then click 'Capture'.

