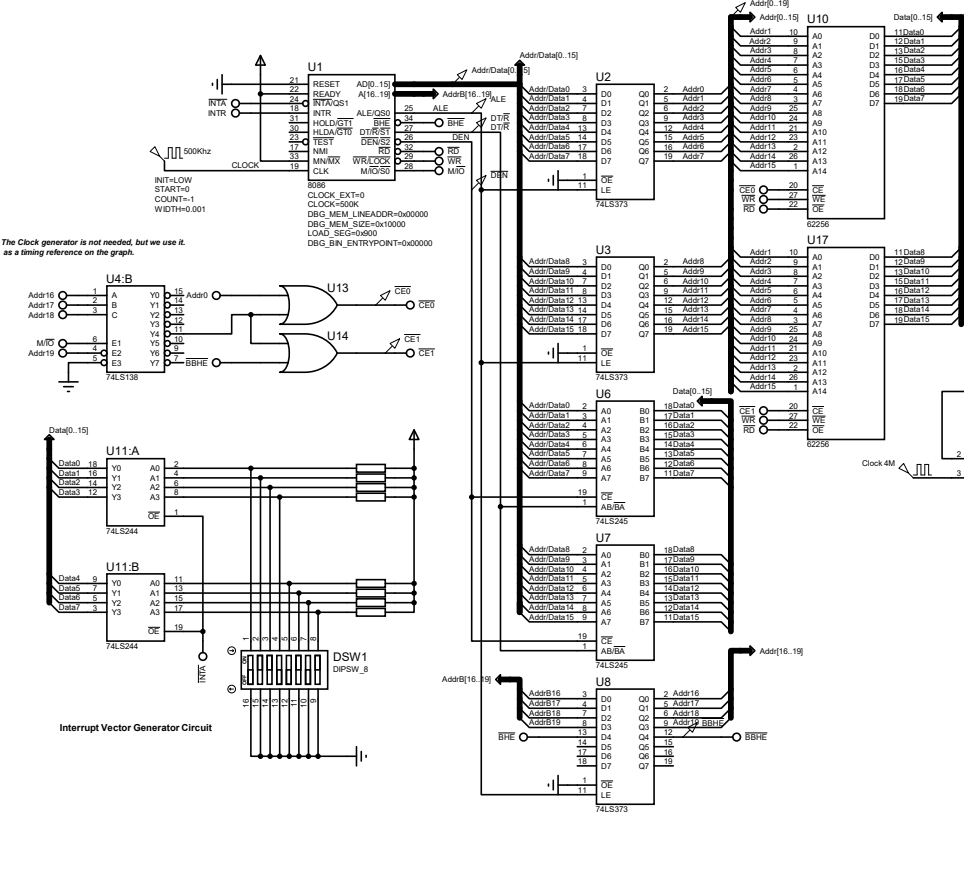
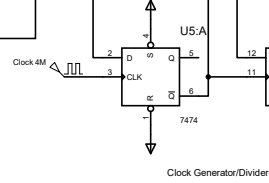


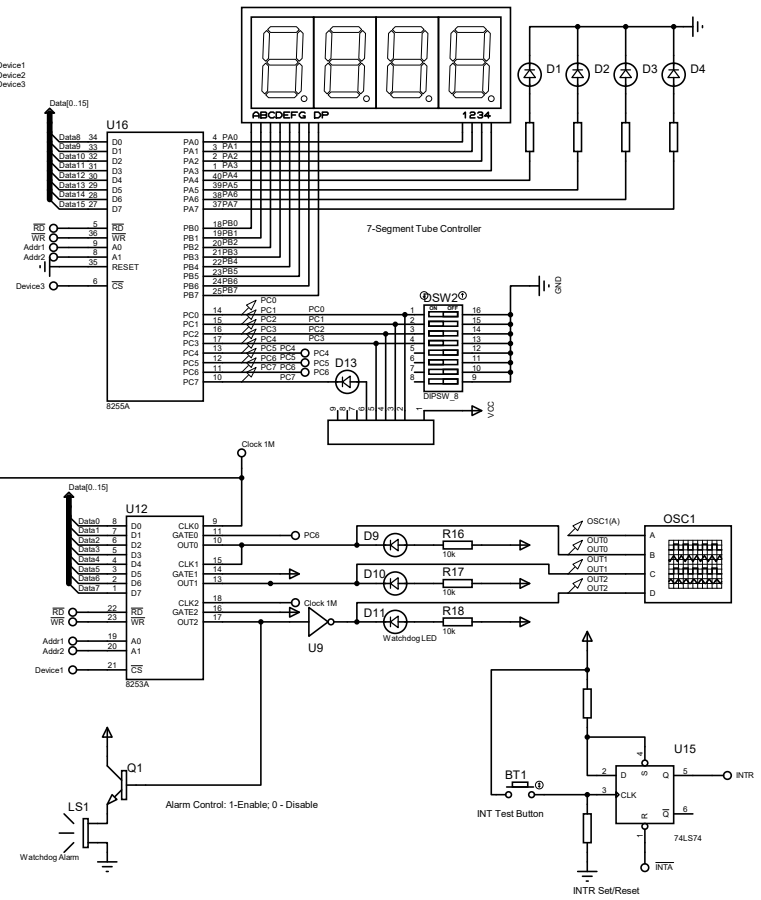
The Clock generator is not needed, but we use it as a timing reference on the graph.



I/O Space Port Address Decoder Circuit



Clock Generator/Divider



## Debugging facilities

The equipment here help you debug the system software and hardware cooperation.

The Digital Graph illustrates the signal timing for your reference.

