

Series LC Circuit Worksheet

Introduction

Understanding the behavior of reactive components such as capacitors and inductors based on their complex impedances is a critical skill in electrical engineering. In this lesson we will analyze the behavior of one such circuit using Laplace transforms.

Discussion Overview

As discussed in the lecture, LC circuits tend to oscillate at certain frequencies determined by the combination of the capacitor's and inductor's impedances. To better understand the frequency response of such circuits, we will examine the following circuit.

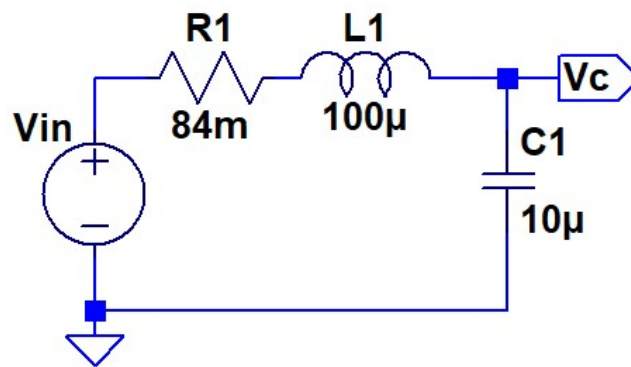


Figure 1 - Series LC Circuit

In order to determine the voltage V_C across the capacitor, we apply the voltage divider equation using the impedances of R_1 , L_1 and C_1 . Note that resistor R_1 represents the series resistance of the inductor; and L_1 and C_1 are treated as ideal inductor and capacitor respectively.

Recall that the impedance of an inductor is given as $Z_L = sL$, and that of a capacitor is given as $Z_C = \frac{1}{sC}$. Therefore,

$$V_C = V_{in} \frac{Z_C}{Z_C + R + Z_L} = V_{in} \frac{\frac{1}{sC}}{\frac{1}{sC} + R + sL} = V_{in} \frac{1}{1 + sCR + s^2CL}$$

To examine the behavior of the circuit in frequency domain, we let $s = j\omega$:

$$V_C = V_{in} \frac{1}{1 + j\omega CR - \omega^2 CL} = V_{in} \frac{1}{1 - \omega^2 CL + j\omega CR}$$

The magnitude of the voltage across the capacitor is, therefore, given by

$$|V_C| = \left| V_{in} \frac{1}{1 - \omega^2 CL + j\omega CR} \right| = |V_{in}| \frac{1}{\sqrt{(1 - \omega^2 CL)^2 + (\omega CR)^2}} \quad \text{Eq. 1}$$

As seen in Eq. 1, V_C is a function of frequency ω ($= 2\pi f$). We will now examine how V_C changes as a function of frequency. This is called the frequency response of the circuit.

Frequency Response

We will examine Eq. 1 for three different regions in frequency domain; low frequencies, high frequencies and the so called “corner frequency”.

Low Frequencies

First, we note that for lower frequencies close to zero, the denominator in Eq. 1 is approximately one.

$$\sqrt{(1 - \omega^2 CL)^2 + (\omega CR)^2} \Big|_{\omega \rightarrow 0} \approx 1$$

Therefore, for lower frequencies, the voltage across the capacitor is approximately the same as the input voltage:

$$V_C|_{\omega \rightarrow 0} \approx V_{in}$$

We note that this behavior is in line with our expectations for very low frequency signals including DC. For low frequencies, an inductor acts like a short while a capacitor acts like an open circuit which would lead to $V_C = V_{in}$.

High Frequencies

The second region of interest is for very large frequencies. For large frequencies, the denominator in Eq. 1 is dominated by ω , and as ω gets larger, the fraction goes to zero.

$$\sqrt{(1 - \omega^2 CL)^2 + (\omega CR)^2} \Big|_{\omega \rightarrow \infty} \approx \sqrt{(\omega^2 CL)^2 + (\omega CR)^2} \rightarrow \infty \Rightarrow \frac{1}{\sqrt{(1 - \omega^2 CL)^2 + (\omega CR)^2}} \Big|_{\omega \rightarrow \infty} \approx 0$$

Therefore, for large frequencies, the voltage across the capacitor tends towards zero:

$$V_C|_{\omega \rightarrow \infty} \approx 0$$

This behavior, again, is in line with our expectations. For large frequencies, an inductor acts like an open circuit while a capacitor acts like a short leading to $V_C = 0$.

Resonance Frequency

The final region of interest is around the frequency $\omega = \frac{1}{\sqrt{LC}}$. For $\omega = \frac{1}{\sqrt{LC}}$, the term $(1 - \omega^2 CL)$ is equal to zero, and assuming that R is fairly small, this would lead to a very large value for the fraction in Eq. 1. We can find the exact value of V_C by plugging $\omega = \frac{1}{\sqrt{LC}}$ in Eq. 1:

$$V_C|_{\omega=\frac{1}{\sqrt{LC}}} = V_{in} \frac{1}{R} \sqrt{\frac{L}{C}}$$

which for $R \rightarrow 0$ would result in a large V_C .

As the name suggests, the circuit starts oscillating around the resonance frequency $\omega = \frac{1}{\sqrt{LC}}$.

Frequency Response Plot

The plot in Figure 2 shows the frequency response of the circuit in Figure 1 assuming $V_{in} = 1$. For the values of R , L and C given in Eq. 1, the resonance frequency is

$$f = \frac{\omega}{2\pi} = \frac{1}{2\pi\sqrt{LC}} = 5.03\text{KHz}$$

Eq. 2

and

$$V_C|_{\omega=\frac{1}{\sqrt{LC}}} = V_{in} \frac{1}{R} \sqrt{\frac{L}{C}} \approx 31.5\text{dB}$$

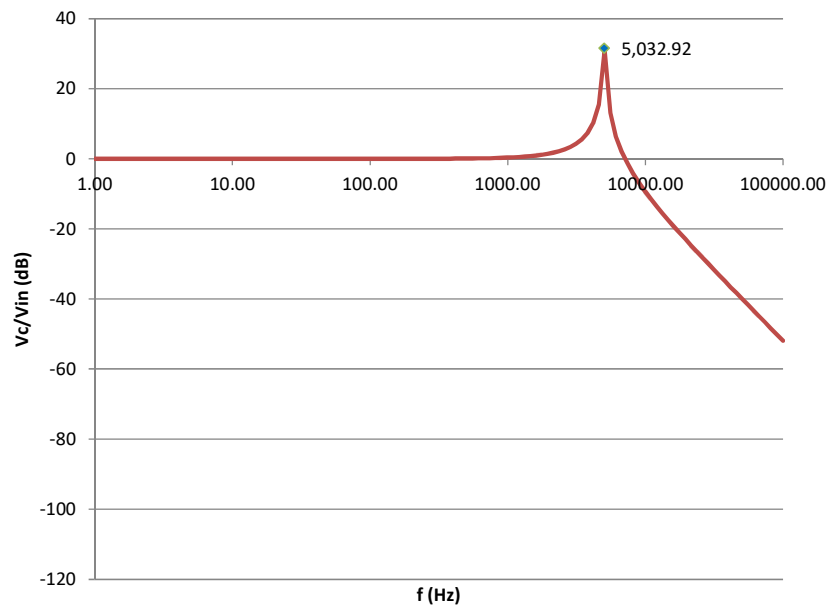


Figure 2 - Frequency Response of an LC Circuit

Procedure

In this section, you are asked to create a SPICE model and also build the circuit shown in Figure 3. Note that the series resistance of the inductor will be specified as part of the inductor's parameters.

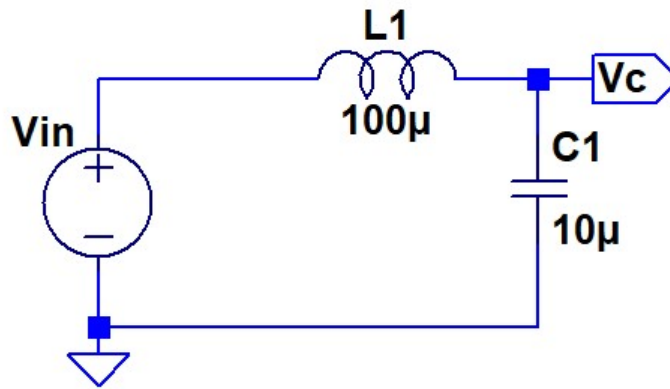


Figure 3 - LTSpice LC Circuit

LTSpice Model

- Capture the circuit in Figure 3 in LTSpice.
- When selecting a value for L1, chose a part with 100μH of inductance and a series resistance of $R_{ser} \approx 0.083\Omega$ as shown below.

Select Stock Inductor				
<div>Quit and Edit Database</div> <div>List All Inductors in Database</div> <div>OK</div> <div>Cancel</div>				
L[μH]	Mfg.	Part No.	Ipk[A]	Rser[Ω]
100.0	Coilcraft	RFS1412-104	2.600	0.083
100.0	Würth Elektronik	7447221101 WE-TIF 1016	2.600	0.080
100.0	Coilcraft	PCH-45X-104	2.800	0.074
100.0	Würth Elektronik	7447231101 WE-TIF 1018	2.800	0.072
100.0	Gowanda	059AT1003V	3.000	0.050
100.0	Gowanda	894AT1003V	3.000	0.030
100.0	Coilcraft	PCV-1-104-03	3.400	0.058
100.0	Gowanda	GT10-108	3.500	0.040

Note that selecting this part, sets the inductor's series resistance to 83mΩ

- Set your voltage source to a "PULSE" source with the following parameters
 - Initial voltage = 0
 - Von = 1V

Name: _____

- c. Delay = 0
- d. Rise time = $1\mu\text{s}$
- e. Fall time = $1\mu\text{s}$
- f. On time = 0.888ms
- g. Period = 1.78ms

Independent Voltage Source - Vin

Functions

- ☐ (none)
- ☒ PULSE(V1 V2 Tdelay Trise Tfall Ton Period Ncycles)
- ☐ SINE(Voffset Vamp Freq Td Theta Phi Ncycles)
- ☐ EXP(V1 V2 Td1 Tau1 Td2 Tau2)
- ☐ SFFM(Voff Vamp Fcar MDI Fsig)
- ☐ PWL(t1 v1 t2 v2...)
- ☐ PWL FILE: Browse

DC Value

DC value:

Make this information visible on schematic: ☐

Small signal AC analysis (AC)

AC Amplitude:

AC Phase:

Make this information visible on schematic: ☒

Parasitic Properties

Series Resistance[Ω]:

Parallel Capacitance[F]:

Make this information visible on schematic: ☐

Vinitial[V]:

Von[V]:

Tdelay[s]:

Trise[s]:

Tfall[s]:

Ton[s]:

Tperiod[s]:

Ncycles:

Additional PWL Points

Make this information visible on schematic: ☐

Cancel OK

- D. Setup the model to run a “transient” simulation with the following parameters
- a. Starting the print at time 0,
 - b. Ending at time 1.78ms,
 - c. Starting capture at time 0.88,
 - d. With a maximum simulation step size of $1\mu\text{s}$, and
 - e. Setting the external voltage sources to 0 at the “startup”

Below is the syntax for your reference:

`.tran <Tprint> <Tstop> [<Tstart> [<Tmaxstep>]] [<options>]`

- E. Run the simulation and display the waveform for the voltage Vc.
- F. What does the waveform look like?

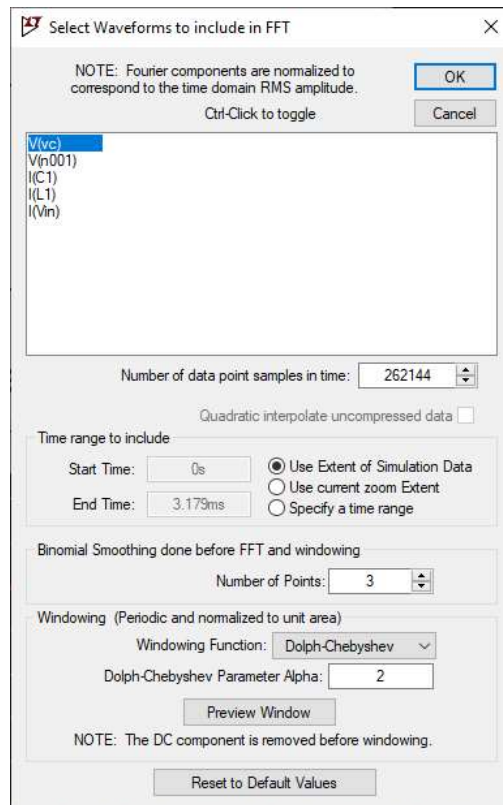
G. Estimate the frequency of the waveform and record it here

$$f = \text{_____} Hz$$

Frequency Response

In this section, we will use the FFT tool of LTSpice to plot the frequency response of the voltage across the capacitor. FFT stands for Fast Fourier Transform, and it is a mathematical algorithm for extracting the various frequency components present in a signal. (The details of the FFT algorithm are beyond the scope of this level.)

- H. Right click on the waveform window and select View → FFT
- I. From the FFT window, select V(vc) to display
- J. Leave all the settings as default except for the following:
 - a. Set the “Windowing Function” to “Dolph-Chebyshev”, and
 - b. Set the “Dolph-Chebyshev Parameter Alpha” to 2.



K. Click on OK to run the FFT.

You should now see a waveform similar to the one given in Figure 2.

Name: _____

L. Measure the frequency at which the response peaks and record it below.

$f =$ _____ Hz

M. How does this value compare with the calculated value in Eq. 2 or the estimated value in step G?

N. Save your schematic

AC Analysis

In this section, we use the AC Analysis of SPICE to simulate the circuit over a range of frequencies and examine the frequency response of the circuit.

O. Save your circuit as a new schematic

P. Change the voltage source to a sine wave function and configure it with the values given below.

- DC offset = 0
- Amplitude = 1
- Frequency = 5K
- AC Amplitude = 1
- AC Phase = 0
- All the other parameters should be left blank

Independent Voltage Source - Vin

Functions

- ☐ (none)
- ☐ PULSE(V1 V2 Tdelay Trise Tfall Ton Period Ncycles)
- ☒ SINE(Voffset Vamp Freq Td Theta Phi Ncycles)
- ☐ EXP(V1 V2 Td1 Tau1 Td2 Tau2)
- ☐ SFFM(Voff Vamp Fcar MDI Fsig)
- ☐ PWL(t1 v1 t2 v2...)
- ☐ PWL FILE: Browse

DC offset[V]:

Amplitude[V]:

Freq[Hz]:

Tdelay[s]:

Theta[1/s]:

Phi[deg]:

Ncycles:

Additional PWL Points

Make this information visible on schematic: ☐

DC Value

DC value:

Make this information visible on schematic: ☐

Small signal AC analysis(AC)

AC Amplitude:

AC Phase:

Make this information visible on schematic: ☐

Parasitic Properties

Series Resistance[Ω]:

Parallel Capacitance[F]:

Make this information visible on schematic: ☒

Cancel OK

Name: _____

Note: For AC Analysis, the parameters under “Small signal AC analysis (.AC)” need to be set.

- Q. Change the Transient directive to the following AC Analysis directive
- Type of sweep: Decade
 - Number of points per decade: 100
 - Start frequency: 1
 - Stop frequency: 100K

Below is the syntax for your reference

`.ac <oct, dec, lin> <Npoints> <StartFreq> <EndFreq>`

Note: Setting the above parameters, you are directing SPICE to run the simulations by sweeping the frequency of your voltage source (sine wave) from 1Hz (start frequency) to 100KHz (stop frequency) with 100 points between each decade.

- R. Run the simulation and probe Vc.
S. Measure the frequency at which the response peaks and record it below.

$f = \text{_____} \text{ Hz}$

- T. How does this value compare with the calculated value in Eq. 2 or the estimated value in step G?

- U. How does the frequency response plot compare with the one given in Figure 2?

Lab Build and Measurements

In this section, you are asked to build the circuit in Figure 3 and make measurements using an oscilloscope.

- A. Note the values of your inductor and capacitor and record them here

$$C = \text{_____} F$$

$$L = \text{_____} H$$

- B. Measure the series resistance of the inductor and record it here

$$R_L = \text{_____} \Omega$$

- C. Build the circuit in Figure 3 on a breadboard.

- D. Set the waveform generator on your oscilloscope to a square wave with the following parameters:

- Amplitude = 0.5V ($V_{pk-pk} = 1V$)
- Offset = 0.5V
- Frequency = 160Hz

- E. Use the waveform generator as the input source to your circuit.

- F. Connect the oscilloscope to observe the voltage across your capacitor. Here are the suggested initial settings:

- Ch. 1 volts/div = 0.5V
- Horizontal sec/div = 2ms
- Trigger set to the leftmost location on the screen (~16ms)

- G. What does the waveform look like?

- H. Estimate the frequency of the waveform and record it here

$$f = \text{_____} Hz$$

- I. Use the “Math” function to plot the FFT of the signal.

Name: _____

J. Measure the frequency at which the response peaks and record it below.

$f = \text{_____} \text{ Hz}$

K. How does this value compare with the calculated value in Eq. 2 or the estimated value in step H above?

L. Change the waveform generator output to a sine wave with the following parameters:

- a. Amplitude = 0.5V ($V_{pk-pk} = 1V$)
- b. Offset = 0V
- c. Frequency = 100Hz

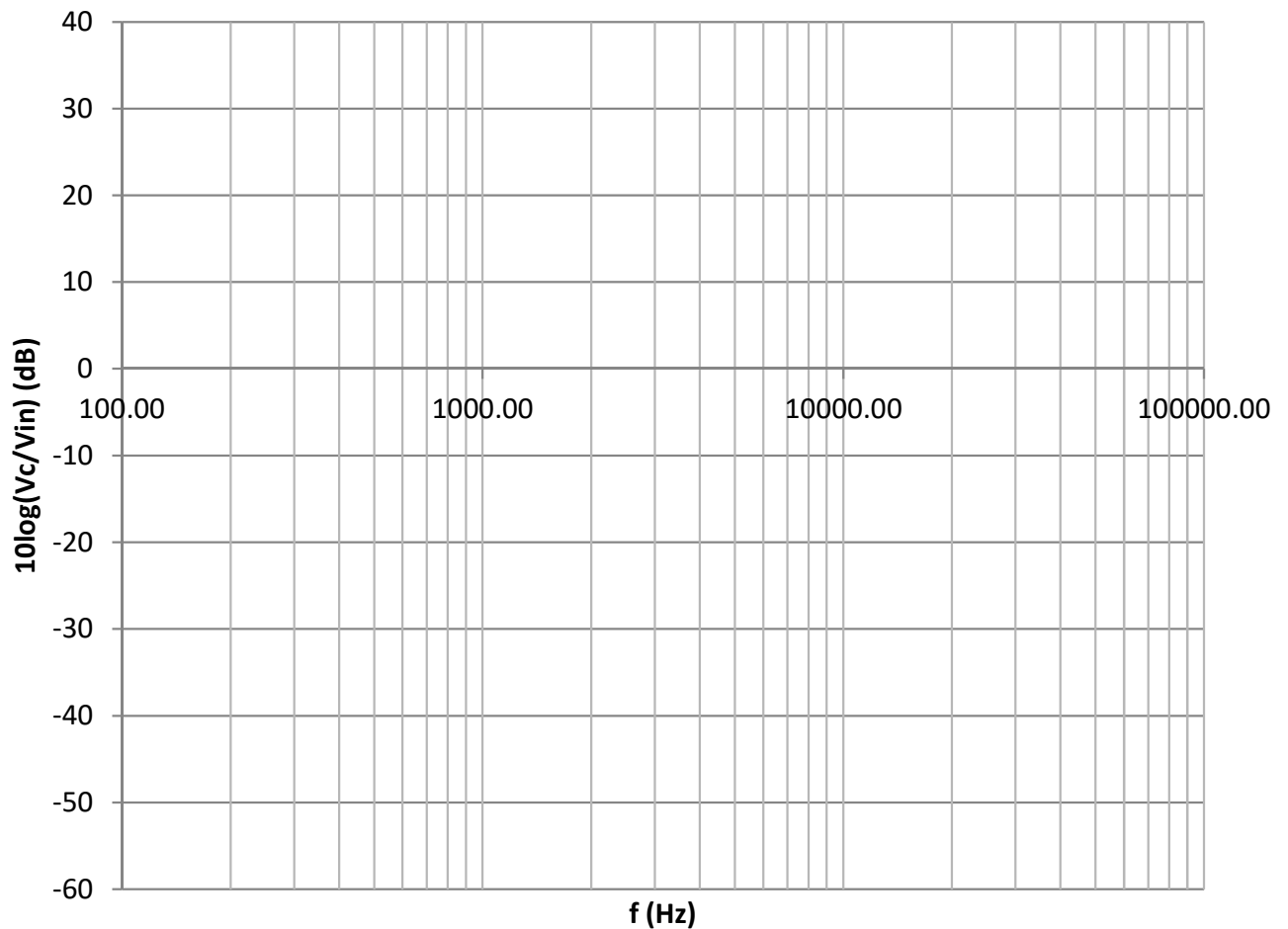
M. Measure the peak to peak voltage across the capacitor and record them in the table below for the frequencies given in each row:

Frequency (Hz)	Amplitude across C1 (V_c)	$\frac{V_c}{V_{in}}$	$10\log\left(\frac{V_c}{V_{in}}\right)$
100			
500			
1.0K			
2.0K			
3.0K			
4.0K			
4.5K			
5.0K			

Name: _____

Frequency (Hz)	Amplitude across C1 (V_c)	$\frac{V_c}{V_{in}}$	$10\log\left(\frac{V_c}{V_{in}}\right)$
5.5K			
6.0K			
7.0K			
8.0K			
10.0K			

N. Plot the values of $10\log\left(\frac{V_c}{V_{in}}\right)$ vs frequency below.



As observed from the measurements above, the signal generator is not able to maintain the supplied voltage at $1V_{pk-pk}$. The reason for this is that the impedance of the entire circuit at the resonant frequency $\omega = \frac{1}{\sqrt{LC}}$, as seen by the voltage source, is nearly zero. That means that the voltage source would need to provide a very large amount of current in order to maintain the configured voltage. However, since our voltage source is not an ideal one, the voltage drops to the level dictated by the max current limit of the source.

To find the impedance of the circuit in Figure 1, we observe that all the components are in series. Therefore,

$$Z_T = R_L + sL + \frac{1}{sC}$$

Letting $s = j\omega$, we have

$$Z_T = R_L + j\omega L + \frac{1}{j\omega C} = R_L + j\left(\omega L - \frac{1}{\omega C}\right)$$

Now, if we let $\omega = \frac{1}{\sqrt{LC}}$, the total impedance at resonant frequency is

$$Z_T|_{\omega=\frac{1}{\sqrt{LC}}} = R_L + j\left(\frac{1}{\sqrt{LC}}L - \frac{1}{\frac{1}{\sqrt{LC}}C}\right) = R_L + j\left(\sqrt{\frac{L}{C}} - \sqrt{\frac{L}{C}}\right) = R_L$$

Since, $R_L = 0.083\Omega$, the required current to maintain a $1V_{pk-pk}$ across the circuit is

$$\frac{1}{R_L} = \frac{1}{0.083} \approx 12A!$$

In the next lab, we will consider a parallel LC circuit that avoids the “zero impedance” problem.