

Parallel Resistor Networks

Introduction

The focus of this exercise is an examination of basic parallel DC circuits with resistors. A key element is Kirchhoff's Current Law which states that the sum of currents entering a node must equal the sum of the currents exiting that node. The current divider rule will also be investigated.

Discussion Overview

A parallel circuit is defined by the fact that all components share two common nodes. The voltage is the same across all components and will equal the applied source voltage. The total supplied current may be found by dividing the voltage source by the equivalent parallel resistance. It may also be found by summing the currents in all of the branches. The current through any resistor branch may be found by dividing the source voltage by the resistor value. Consequently, the currents in a parallel circuit are inversely proportional to the associated resistances. An alternate technique to find a particular current is the current divider rule. For a two resistor circuit this states that the current through one resistor is equal to the total current times the ratio of the other resistor to the total resistance.

Schematics

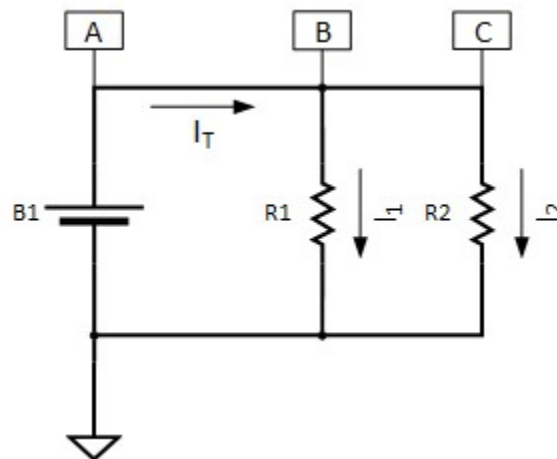


Figure 1 - Parallel Resistor Network

Procedure

- A. Given the circuit shown in Figure 1, determine the equivalent resistance of the circuit for R_1 and R_2 values given in Table 1 below. (Note that for the case where R_2 is a thermistor, you will need to measure the resistance under the conditions given in the table.)

Name: _____

- B. Given $V_{B1} = 9V$, determine the voltages at points A, B and C with respect to ground:

$$V_A = \underline{\hspace{2cm}}$$

$$V_B = \underline{\hspace{2cm}}$$

$$V_C = \underline{\hspace{2cm}}$$

- C. Given the equivalent resistance found in A and using Ohm's law ($I = \frac{V}{R}$), determine the theoretical total current (I_T), the current through R_1 (I_1) and the current through R_2 (I_2). Record the values in Table 1 below.
- D. Construct the resistor network on a breadboard. **Do not connect the battery at this point! Before** connecting the battery, measure the value of each resistor and the equivalent resistance. Record the equivalent resistance value in Table 1 and compare it to the theoretical value determined in step A.
- E. Connect the battery and multimeter to measure the voltages at points A, B and C with respect to ground. (Note that the black lead of the multimeter should be connected to the ground reference point and the red lead should be connected to points A, B and C respectively.) Record the values below and compare to theoretical values in step B.

$$V_A = \underline{\hspace{2cm}}$$

$$V_B = \underline{\hspace{2cm}}$$

$$V_C = \underline{\hspace{2cm}}$$

- F. Using the multimeter, measure the total current (between points A and B), the current through R_1 (between point B and R_1), and the current through R_2 (between point C and R_2). (Note that current is measured at a single point in the circuit. The multimeter will need to be inserted between point B and R_1 , for example, to measure I_1 . The red lead of the multimeter should be connected to where the current is entering the meter and the black lead connected to the point where the current exiting.) Record the values in Table 1 and compare them to the theoretical values determined in step C.

Table 1 - Calculated and Measured Circuit Values

R1 (Ω)	R2 (Ω)	Calculated				Measured		
		Req (Ω)	$I_T (A) = V_{B1} / R_{eq}$	I ₁ (A)	I ₂ (A)	I _T (A)	I ₁ (A)	I ₂ (A)
1K	2K							
100K	Thermistor Ambient Temp =							
100K	Thermistor Iced =							

G. For extra credit, determine an equation for I_2 based on I_T , R_1 and R_2 . (This is known as the **current divider** equation.)

We know that

$$I_2 = \frac{V_{B1}}{R_2} \quad \text{Eq. 1}$$

$$V_{B1} = I_T R_{eq} \quad \text{Eq. 2}$$

Substituting Eq. 2 into Eq. 1, we have

$$I_2 = \frac{(\quad)}{R_2} \quad \text{Eq. 3}$$

We also know that

$$R_{eq} = \frac{R_1 R_2}{R_1 + R_2} \quad \text{Eq. 4}$$

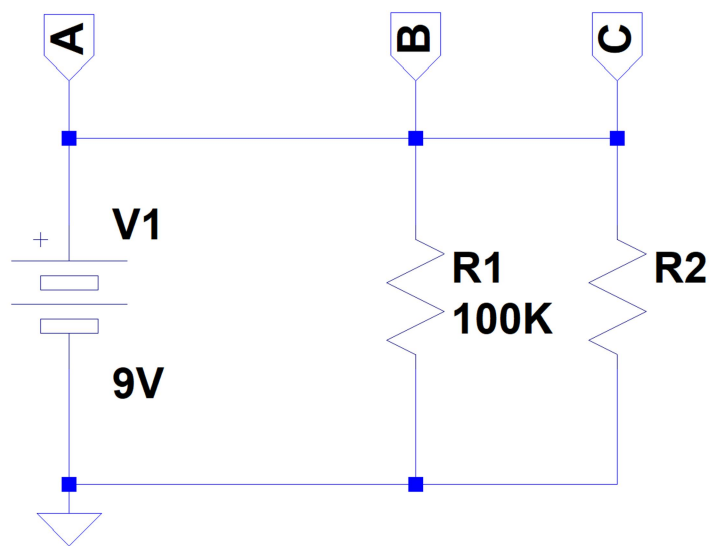
Therefore, substituting Eq. 4 for R_{eq} in Eq. 3, we have

$$I_2 = \frac{I_T}{R_2} (\quad)$$

Or

$$I_2 = I_T \frac{R_1}{(\quad + \quad)}$$

H. Enter below circuit in LTSpice



- Make the value of R2 a parameter.
- Enter a SPICE directive to step the value of R2 from 80K to 480K in steps of 80K.
- Enter another SPICE directive to run a transient (time) simulation from 0 to 1ms stepping at 1us. The initial voltage value should be set to 0. (Recall that this was done by adding the “startup” parameter to the .tran SPICE directive.)
- Run the simulation, and record the values listed in Table 2 below for R2 = 80K and 480K.

Table 2 Parallel Resistor Network SPICE Simulation Results

R2	V _A (V)	V _B (V)	V _C (V)	I _T (A)	I ₁ (A)	I ₂ (A)
80K						
480K						