





# AD9889B vs. AD9889A



- ◆ Added Gamut Metadata Packet (GMP) registers (part of HDMI 1.3)
  - GMP packet registers are contained in a secondary register map.
  - The device address for the secondary register map is programmable via register 0xCF of the primary register map. (default = 0x70)
  - Transmission of the GMP data is enabled by 0x44[1] (primary register map)
  - Refer to AD9889B Programming Guide for details
- ◆ Added AVI Info frame bits at registers 0xCD and 0xCE
  - Refer to AD9889B Programming Guide for details
- ◆ Added circuitry to support 4:2:2 to 4:4:4 conversion for modes using pixel repetition.
- ◆ Available in BGA, LFCSP, and LQFP packages
  - 80MHz and 165MHz speed grades available