

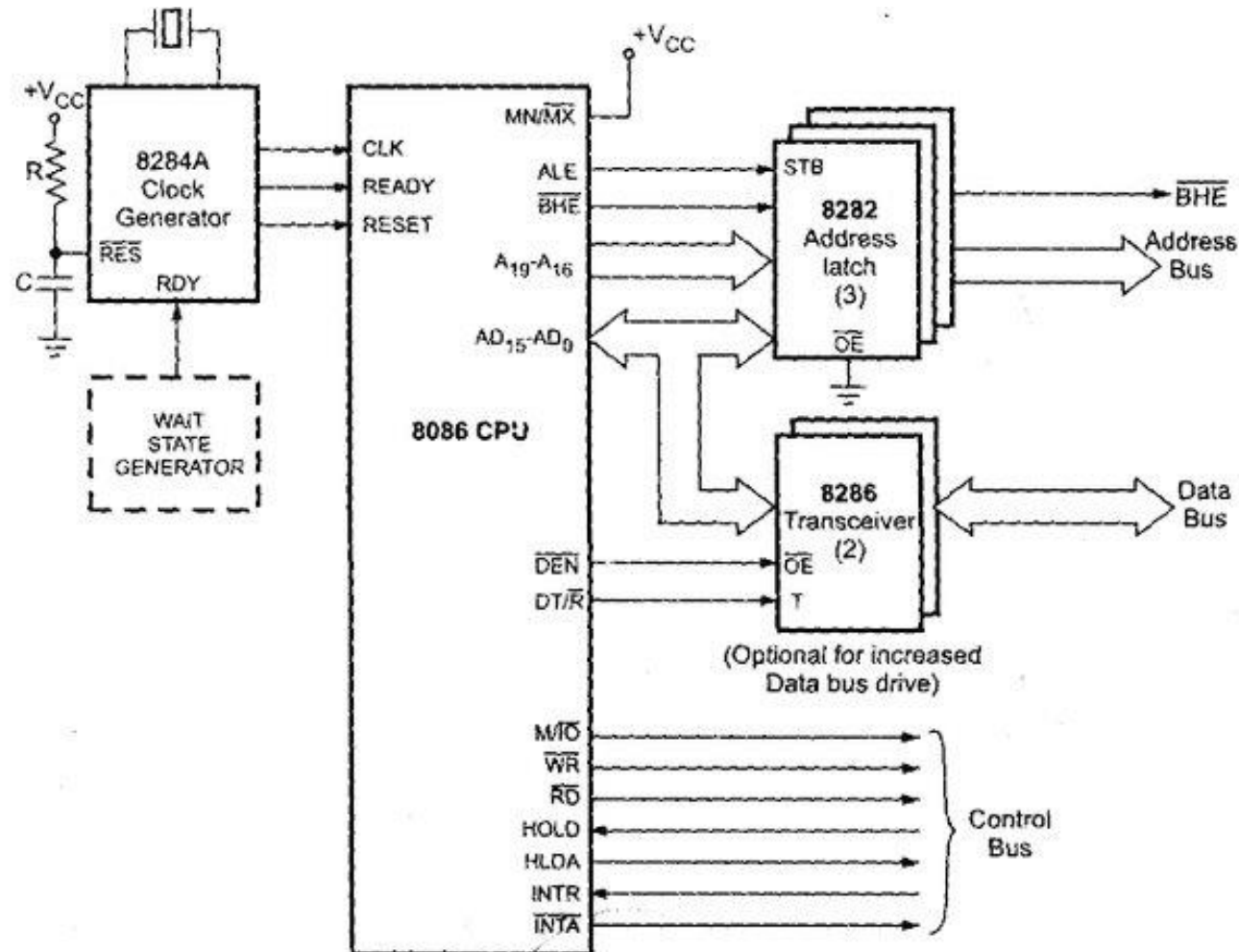


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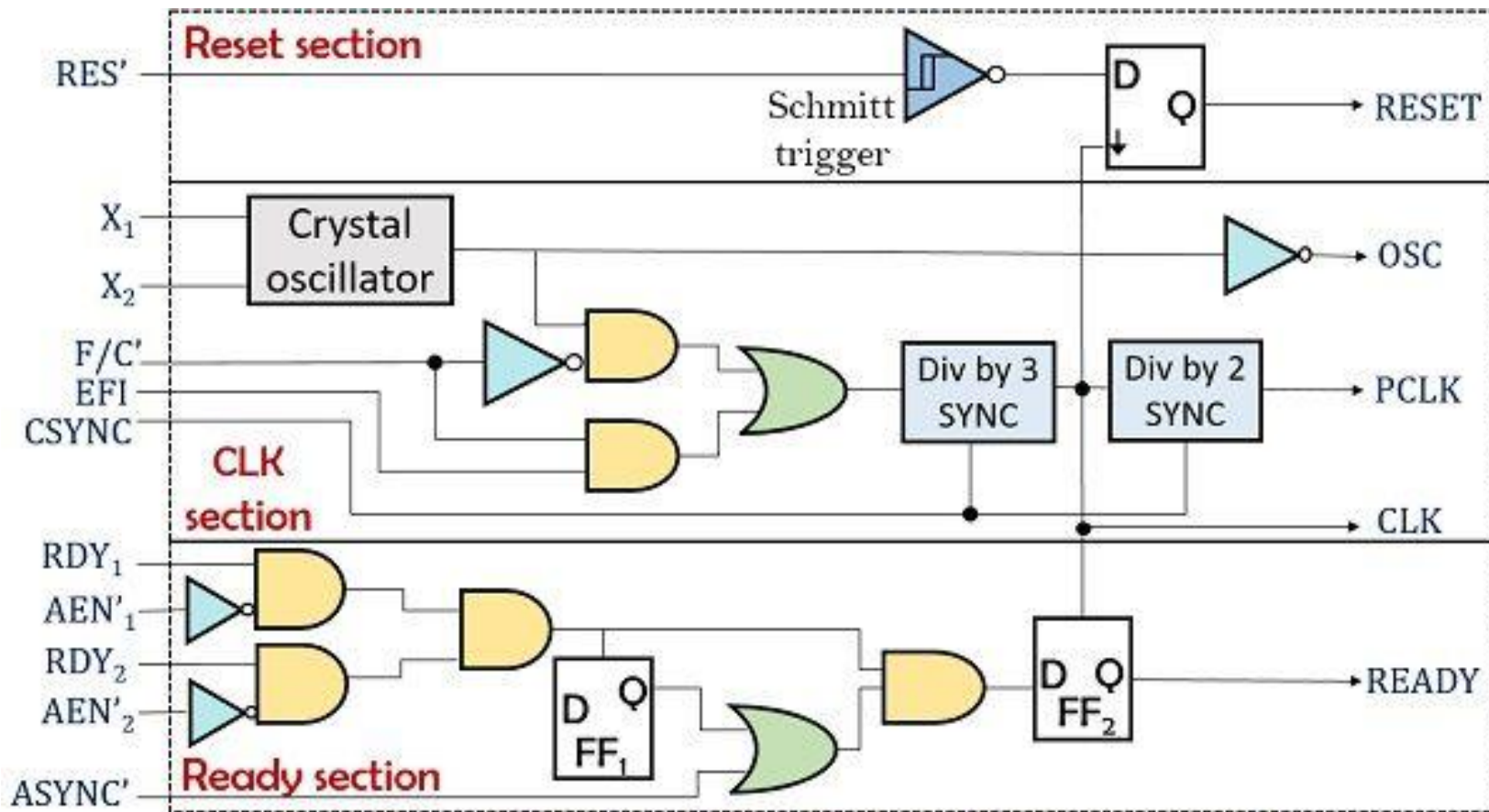
Microprocessors & Interfacing **Pin Diagram**

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Hardware Specifications



Internal Diagram – 8284A

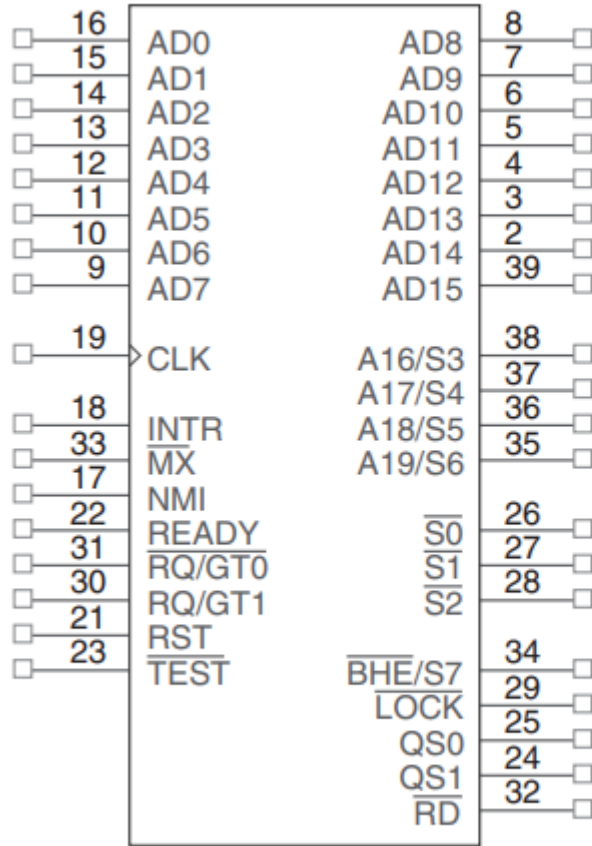


Logic Circuit of 8284 Clock Generator

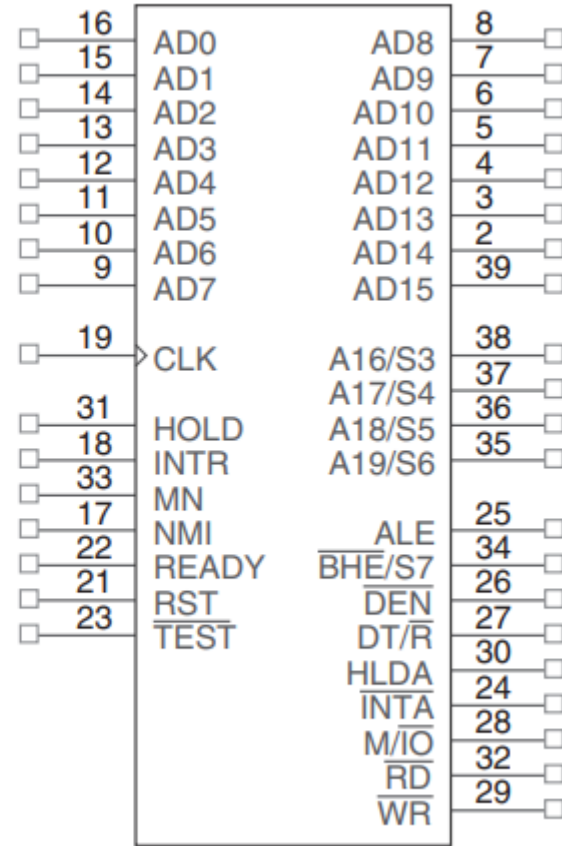
Minimum & Maximum Mode

- 8088/8086 can be configured to work in any of the two modes
- Minimum Mode
 - $\overline{MN}/\overline{MX}'$ – logic 1
 - Single processor in system
 - Smaller systems/ Cheaper
- Maximum Mode
 - $\overline{MN}/\overline{MX}'$ – logic 0
 - Larger systems – more than one processor
 - e.g. – Numeric Data processor (8087) –co-processor

Pin Diagram

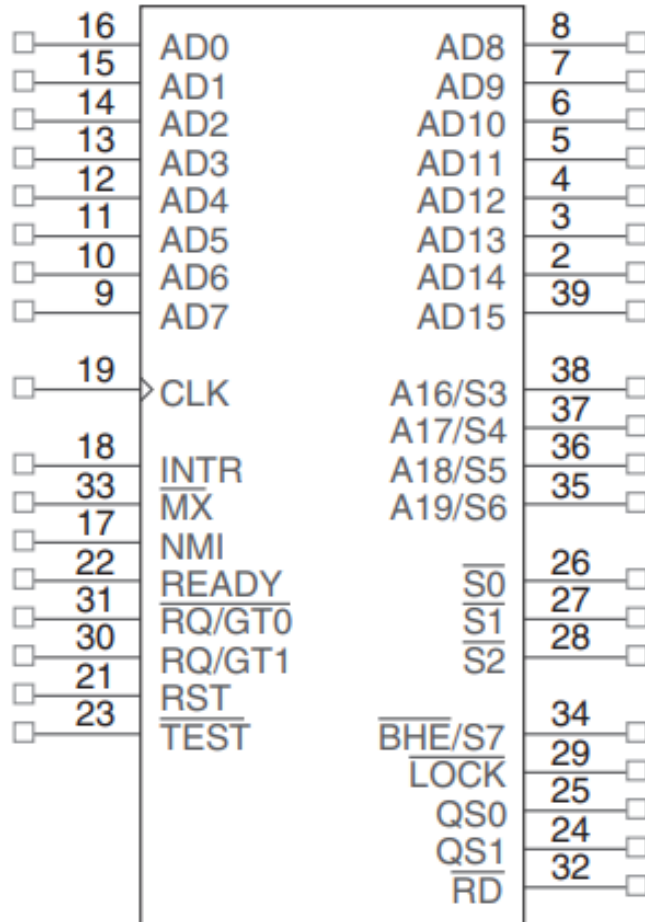


8086MAX



8086MIN

Input/output Characteristics



8086MAX

Input Characteristics

Logic Level	Voltage	Current
0	0.8 V maximum	$\pm 10 \mu\text{A}$ maximum
1	2.0 V minimum	$\pm 10 \mu\text{A}$ maximum

Output Characteristics

Logic Level	Voltage	Current
0	0.45V maximum	2.0 mA maximum
1	2.4 V minimum	$-400 \mu\text{A}$ maximum



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Thank You