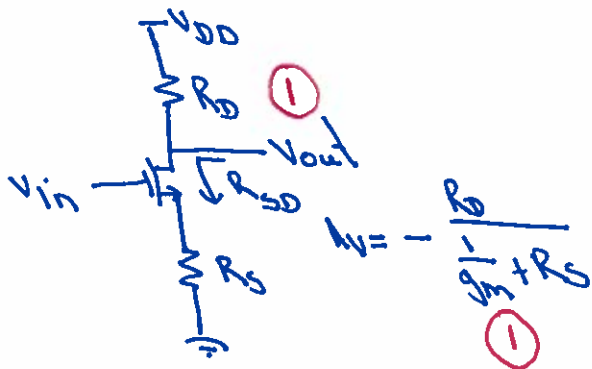


Q1- BOOKWORK/CALCULATION FOR NEW EXAMPLE

- ① (a) Source degeneration is when a circuit element (eg. resistor) is connected between the source terminal (in a MOSFET) and the common node (i.e. GND for NMOS and VDD for PMOS). ①

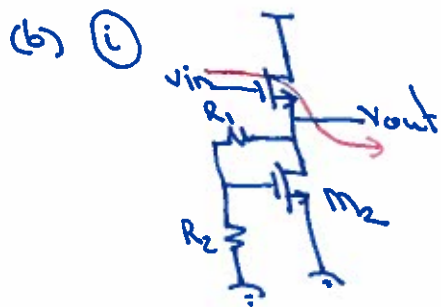


Advantages (compared to CS)

1. output resistance R_{out} is increased from r_{o1} to $(g_{m1}r_{o1}R_S + R_S + r_{o1})$ ①/2
2. Large signal operation is linearised (i.e. larger small signal region). ①/2

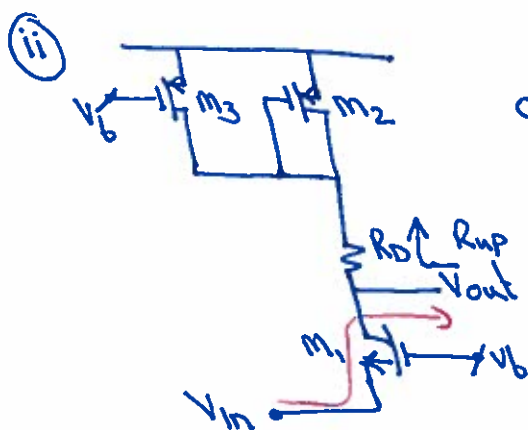
Disadvantages (compared to CS)

1. reduced voltage (or) headroom. ①/2
2. reduced voltage gain. ①/2



Source follower $\therefore A_v = \frac{R_S \parallel r_o}{\frac{1}{g_m} + R_S \parallel r_o}$ ①

$R_S = r_{o2} \parallel (R_1 + R_2) \therefore A_v = \frac{r_{o2} \parallel r_{o1} \parallel (R_1 + R_2)}{\frac{1}{g_{m1}} + r_{o1} \parallel r_{o2} \parallel (R_1 + R_2)}$ ③



C.G. amplifier $\therefore A_v = +g_{m1}(r_{o1} \parallel R_{up})$ ①

$A_v = +g_{m1}(r_{o1} \parallel R_{up})$ ①

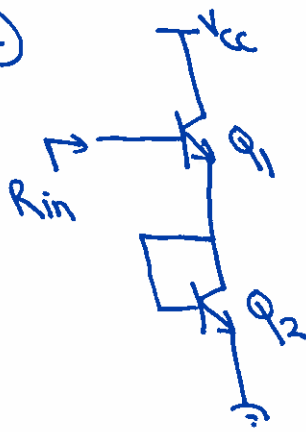
$R_{up} = R_D + \frac{1}{g_{m2}} \parallel r_{o2} \parallel r_{o3}$

$\approx R_D + \frac{1}{g_{m2}} \parallel r_{o3}$

(assuming $\frac{1}{g_{m2}} \ll r_{o2}$) ①

$\therefore A_v = +g_{m1}(r_{o1} \parallel (R_D + \frac{1}{g_{m2}} \parallel r_{o3}))$ ①

(c) (i)



$$(Use R_{in} = r_{\pi 1} + (\beta + 1) R_E) \quad (1)$$

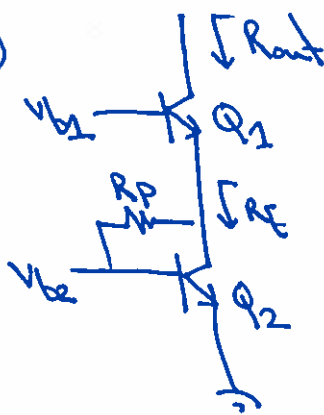
$$\text{where } R_E = \frac{1}{g_{m2}} \parallel r_{\pi 2} \parallel r_{o2} \quad (1)$$

$$\therefore R_{in} = r_{\pi 1} + (\beta + 1) \left(\frac{1}{g_{m2}} \parallel r_{\pi 2} \parallel r_{o2} \right) \quad (1)$$

$$\approx r_{\pi 1} + (\beta + 1) \left(\frac{1}{g_{m2}} \right) \quad (1)$$

$$(\text{assuming } \frac{1}{g_{m2}} \ll r_{\pi 2} \ll r_{o2}) \quad (1)$$

(ii)



emitter degeneration. (1)

$$\therefore R_{out} = g_{m1} r_{o1} (R_E \parallel r_{\pi 1}) + (R_E \parallel r_{\pi 1}) + r_{o1} \quad (1)$$

$$\text{where } R_E = R_p \parallel r_{o2} \quad (1)$$

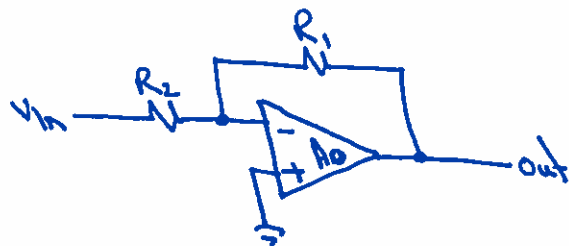
$$\therefore R_{out} = g_{m1} r_{o1} (R_p \parallel r_{o2} \parallel r_{\pi 1}) + (R_p \parallel r_{o2} \parallel r_{\pi 1}) + r_{o1} \quad (2)$$

(d) (i) Output voltage range of a differential amplifier depends on the number of devices that are "stacked" which may limit the headroom. This is because all devices must remain in saturation (i.e. $V_{DS} - V_{TH} \leq V_{DS}$). For example, using cascode devices will limit the o/p range. (3)

(ii) The slew rate of an amplifier gives a measure on how fast the output can change. As this is fundamentally due to the output current charging and discharging the load, the SR is limited by the bias current (or "drivability"). (2)

(e) $A_v(o.l.) = 106 \text{ dB} = 10^{\left(\frac{106}{20}\right)} = 200,000$ (1)

$A_v(c.l.) = 200$

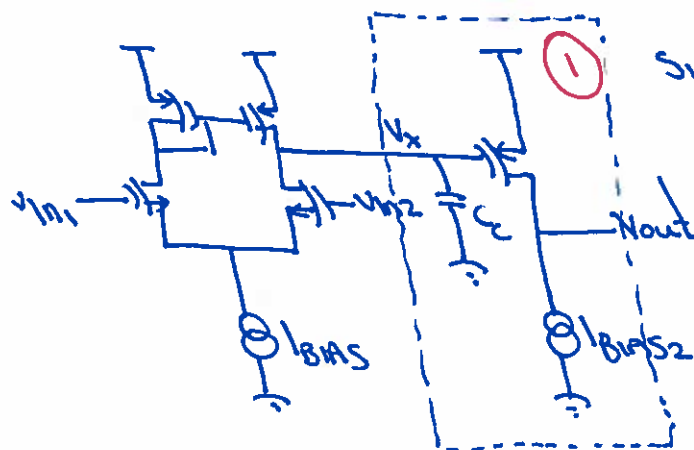


$$A_v(c.l.) = -\frac{R_1}{R_2} \left(1 - \frac{1}{A_o} \left(1 + \frac{R_1}{R_2} \right) \right) \quad (2)$$

$$= -200 \left(1 - \frac{1}{200000} (1 + 200) \right)$$

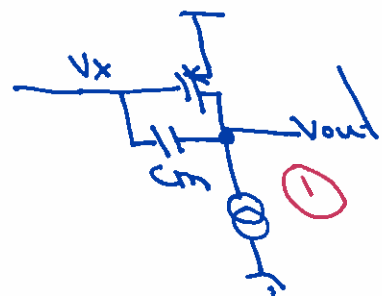
$\therefore \epsilon = 0.1005\%$ (1)

(f) The typical procedure to improve phase margin in op-amp design is to add a compensation capacitance such as to limit the bandwidth so sufficient phase margin is achieved. By exploiting the Miller effect this capacitor value (2)



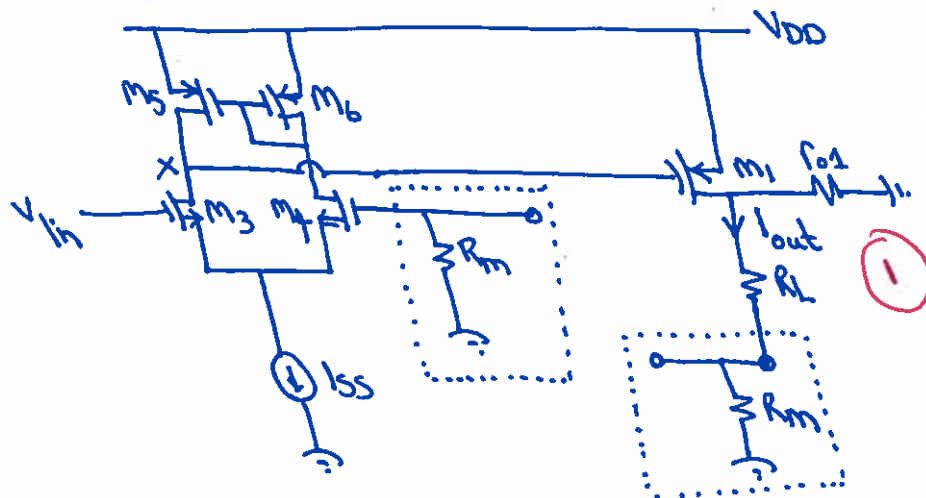
Since $\frac{V_{out}}{V_x} = A_{v2}$ (-ve amplification stage)

a floating capacitor placed across this will appear as:
 $C_c = C_m(1 + |A_{v2}|)$ at the input (1)



Q2 - A NEW THEORETICAL APPLICATION

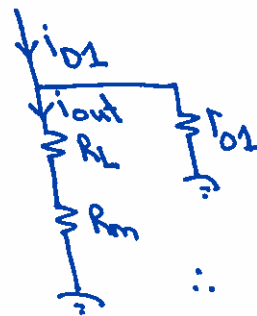
② a (i) open loop gain \rightarrow need to "break" the loop.



$$G_m(\text{open loop gain}) = \frac{I_{out}}{V_{in}} = \frac{V_x}{V_{in}} \cdot \frac{I_{out}}{V_x} \quad (1/2)$$

$$\frac{V_x}{V_{in}} = -g_{m3}(r_{o3} \parallel r_{o5}) \quad (1)$$

$$\frac{I_{out}}{V_x} = \frac{I_{out}}{I_{D1}} \times \left(\frac{I_{D1}}{V_x} \right) \quad (1/2) \quad \text{where } \frac{I_{D1}}{V_x} = g_{m1}$$

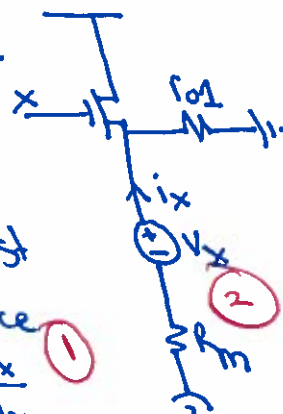


$$\frac{I_{out}}{I_{D1}} = \frac{r_{o1}}{r_{o1} + (R_L + R_m)} \quad (1)$$

$$\therefore G_m = \frac{+g_{m1} g_{m3} r_{o1} (r_{o3} \parallel r_{o5})}{r_{o1} + R_L + R_m} \quad (1)$$

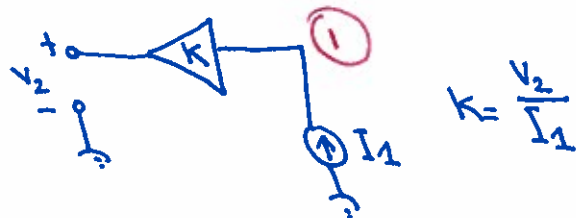
(ii) $R_{out}(oL)$

Need to replace R_L with a test voltage source V_x and $R_{out} = \frac{V_x}{i_x}$ (1)



$$R_{out}(oL) = r_{o1} + R_m \quad (2)$$

⑥ feedback factor K



$$\therefore V_2 = I_{out} \cdot R_m$$

$$\therefore K = \frac{V_2}{I_{out}} = R_m$$

$$\text{Error signal} = V_P - \text{feedback} = V_{in} = R_m I_{out} = \epsilon$$

$$\therefore \epsilon = \frac{V_{in}}{1 + G_m \cdot K} = \frac{V_{in}}{1 + R_m G_m}$$

⑦ Transconductance amplifier \therefore voltage in, current out.

$$\therefore \text{Ideally } R_{in} = \infty$$

$$R_{out} = \infty$$

\therefore -ve feedback will increase both R_{in} and R_{out} .

$$\therefore R_{out} = (1 + \text{loop gain}) R_{out}(OL) \\ = (1 + R_m G_m)(R_m + r_{o1})$$

⑧ Loop gain $= (1 + R_m G_m)$ so either R_m can be increased or $G_m(OL)$

Ideally R_m should be kept small \therefore need to increase $G_m(OL)$

$$G_m(OL) = \frac{g_{m1} g_{m3} r_{o1} (r_{o3} \parallel r_{o5})}{r_{o1} + R_L + R_m}$$

if bias current is increased eg. I_{SS} , this would increase g_{m1} but would also decrease r_{o3}, r_{o5} .

However can increase g_{m3} (and/or g_{m1}) by increasing its (W/L) .

© R_m should ideally be kept small so majority of voltage drop is across the load. (2)

→ Given $V_{DD} = 10V$ and $I_{out} = 5mA$

However R_m also needs to provide sufficient bias voltage to sustain device M_4 in saturation. (1)

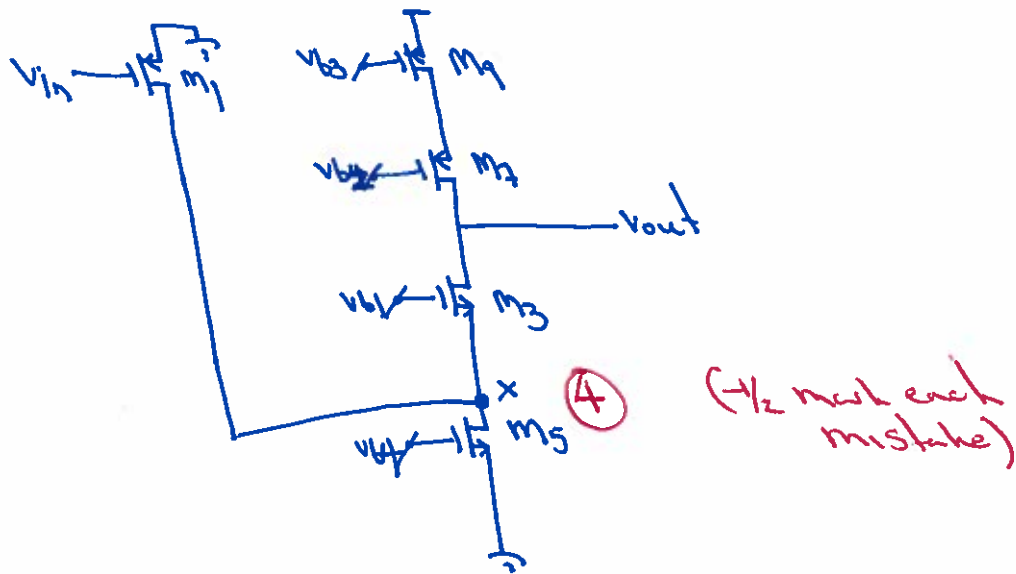
$$\therefore V(R_m) > V_{GS(sat)}$$

→ Lets design for $V(R_m) = 0.5V$ (1)

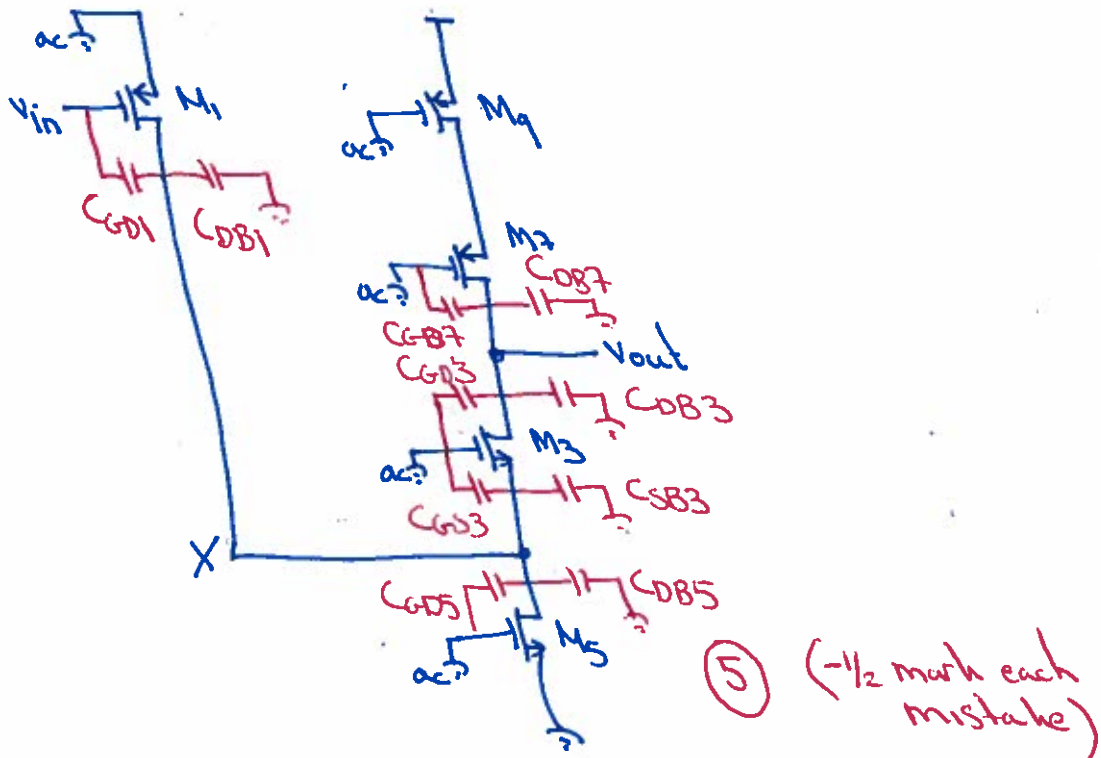
$$\therefore R_m = \frac{0.5V}{5mA} = 100\Omega \quad (1)$$

Q3 - CALCULATION FOR A NEW EXAMPLE

- ③ (a) (i) Assuming perfect symmetry, sources of diff. pair can be connected to AC. ground. ①



- (ii) with parasitics associated with nodes x and V_{out} .



for (i) and (ii) OK as single diagram

⑥ $A_v = -G_m R_{out}$

$R_{out} = R_{up} \parallel R_{down}$

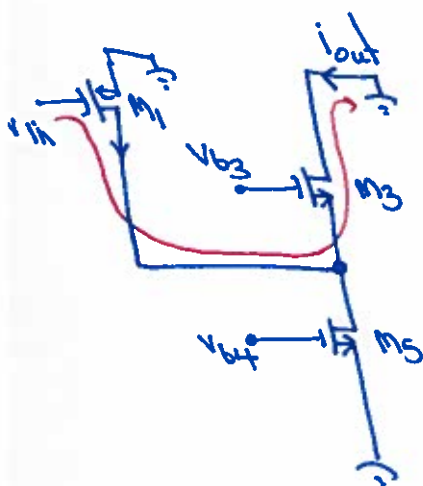
$R_{up} = g_{m7}(r_{o7} r_{o9} + r_{o7} + r_{o9})$

$R_{down} = g_{m3} r_{o3}(r_{o5} \parallel r_{o1}) + r_{o3} + r_{o5} \parallel r_{o1}$

\therefore assuming $g_m r_o \gg 1$ ①

$R_{out} \approx g_{m7} r_{o7} r_{o9} + g_{m3} r_{o3}(r_{o5} \parallel r_{o1})$

①



$i_{out} = I_{d3}$

$I_{d3} + I_{d5} = I_{d1}$ (assuming $\frac{1}{g_{m3}} \ll r_{o5}$, then $I_{d3} \approx I_{d1}$) ①

$I_{d1} = v_{in} g_{m1}$

$\therefore \frac{i_{out}}{v_{in}} = G_m = g_{m1}$ ①

$\therefore A_v = -g_{m1} [g_{m7} r_{o7} r_{o9} + g_{m3} r_{o3}(r_{o1} \parallel r_{o5})]$ ①

⑦

$g_m = \sqrt{2 \mu C_{ox} \frac{W}{L} I_D}$

$r_o = \frac{1}{\lambda I_D}$

$g_{m1} = \sqrt{2(200\mu)(200)200\mu} = 4mS$

$g_{m3} = \sqrt{2(200\mu)(1)250\mu} = 316\mu S$

$g_{m7} = \sqrt{2(100\mu)(\frac{5}{2})250\mu} = 353\mu S$

$r_{o7} = \frac{1}{0.2(250\mu)} = 20k\Omega$

$r_{o1} = \frac{1}{0.2(200\mu)} = 25k\Omega$

③

$r_{o3} = \frac{1}{0.1(250\mu)} = 40k\Omega$

$r_{o5} = \frac{1}{0.1(50\mu)} = 200k\Omega$

$\therefore A_v = -4m(353\mu(20k)(20k) + \frac{316\mu(40k)(25k \parallel 200k)}{0.2(250\mu)}) = -1688 = 64.5dB$ ①

$$d) f_{Px} = \frac{1}{2\pi R_x C_x} \quad (1)$$

$$f_{Pout} = \frac{1}{2\pi R_{out} C_{out}} \quad (1)$$

Since $C_{DB} = C_{SB} = \emptyset$

$$C_x = C_{D1} \left(1 + \frac{g_{m1}}{g_{m3}}\right) + \cancel{C_{DB1}} + C_{D5} + \cancel{C_{DB5}} + \cancel{C_{D3}} + \cancel{C_{SB3}} \quad (1)$$

$$R_x = r_{o1} \parallel r_{o5} \parallel \frac{1}{g_{m3}} \parallel r_{o3} \approx \frac{1}{g_{m3}} \text{ (assuming } g_m \ll r_o) \quad (1)$$

$$C_{out} = C_{D7} + \cancel{C_{DB7}} + C_{D3} + \cancel{C_{DB3}} \quad (1)$$

$$R_{out} = \text{as in (b).}$$

$$e) C_x = \left[(0.2) \times 100 \left(1 + \frac{40}{3}\right) + (0.2) \left(\frac{4}{2}\right) + \frac{2}{3} (12)(2)(2) \right] \text{ ff} \\ = \underline{319 \text{ ff}} \quad (1)$$

$$R_x = \frac{1}{g_{m3}} = \frac{1}{316 \mu\text{S}} = \underline{3.16 \text{ k}\Omega} \quad (1)$$

$$R_{out} = \underline{422 \text{ k}\Omega} \text{ (from c)}$$

$$C_{out} = 5(0.2) + 2(0.2) = \underline{1.4 \text{ ff}} \quad (1)$$

$$\therefore f_{Px} = \frac{1}{2\pi (3.16 \text{ k}) (319 \text{ ff})} = \underline{157.9 \text{ MHz}} \quad (1)$$

$$f_{Pout} = \frac{1}{2\pi (1.4 \text{ ff}) (422 \text{ k})} = \underline{269.4 \text{ MHz}} \quad (1)$$