```
cache line (block) of 128 bytes
struct Register{
                 → 8 bytes
                                                     name1
                                           name1
                                                                loc1
                                                                          loc1
   string name;
   pair<int,int> loc; → 8 bytes
                                           name2
                                                                loc2
                                                                          loc2
                                                     name2
                                                                          loc3
                                           name3
                                                     name3
                                                                loc3
                                                                          loc4
                                           name4
                                                     name4
                                                                loc4
vector<Register> registers;
                                              . . .
                                                        . . .
                                                                  . . .
                                                                           . . .
for(Register r : registers){
                                           name8
                                                     name8
                                                                loc8
                                                                          loc8
   r.loc = make pair(...);
                                               50% unused cache data
```