

Design requirement: In Blue
Routing requirement: In Red
Large current routing: Bold green

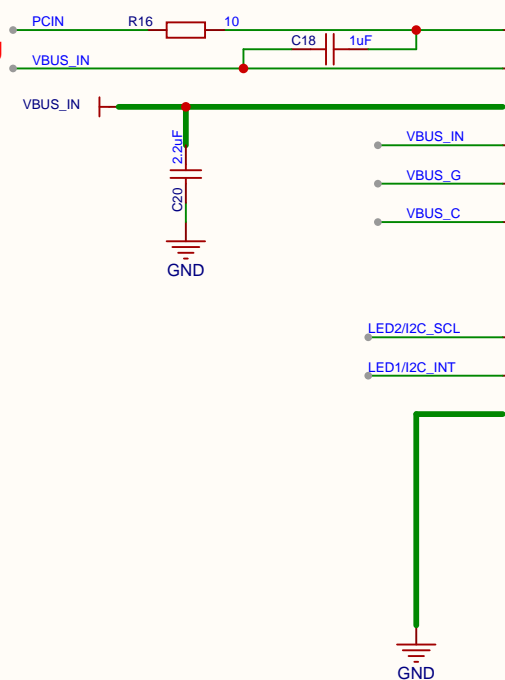


PCIN pin must connect from DRAIN on correspon MOS
DO NOT CONNECT TO CSN1 ON IC SIDE

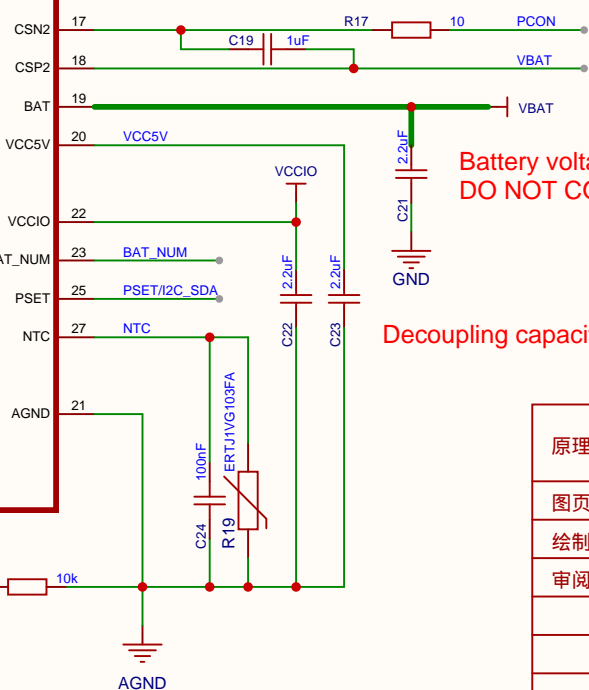
PCON pin must connect from DRAIN on correspon MOS
DO NOT CONNECT TO CSN2 ON IC SIDE

VBUS_IN pin must connect from INPUT
DO NOT CONNECT TO CSP1 ON IC SIDE

Kelvin Routing

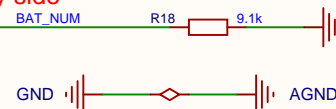



Kelvin Routing



Battery voltage sample must coming from battery side
DO NOT CONNECT TO CSP2 IC SIDE

Decoupling capacitor place close to their pin



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|---|---------|-------------------------------------|----|--------|------------|
| 原理图 | Battery | | | 更新日期 | 2025-12-15 |
| | | | | 创建日期 | 2025-11-16 |
| 图页 | P1 | | | 物料编码 | |
| 绘制 | | BrainStimulator-4Channels-Prototype | | | |
| 审阅 | | | | | |
| | | | | | |
| | | | | | |
| | | 版本 | 尺寸 | 页 | 1 共 1 |
|  | | V1.0 | A4 | 嘉立创EDA | |