

MAPP: The Berkeley Model and Algorithm Prototyping Platform

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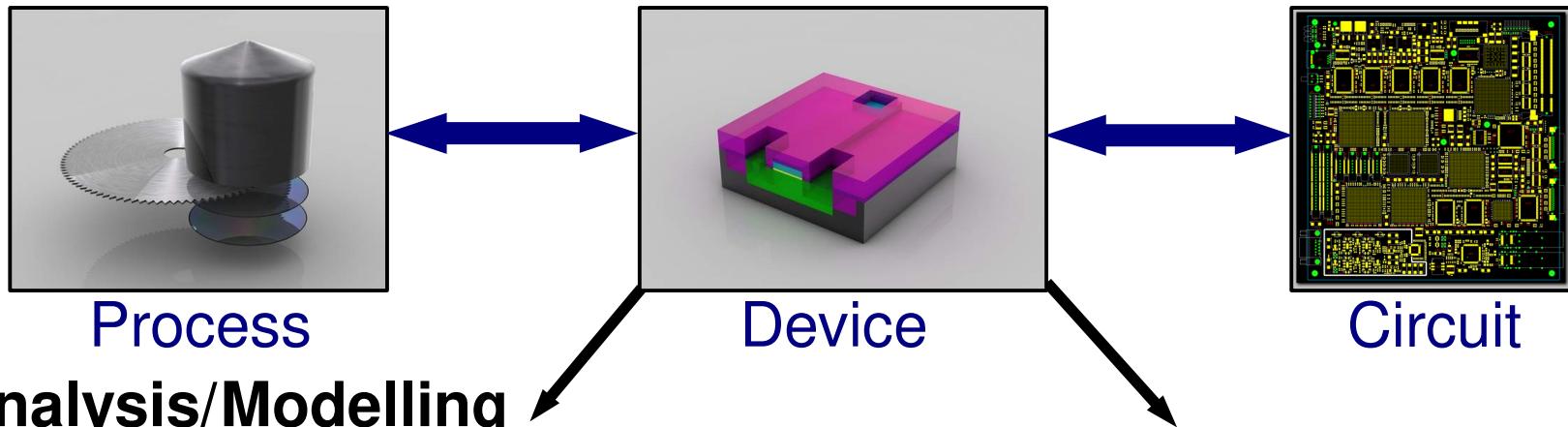
EECS Department
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Albuquerque, NM



Compact Modelling

EDA: Electronic Design Automation



Analysis/Modelling for Individual Devices ("device simulation")

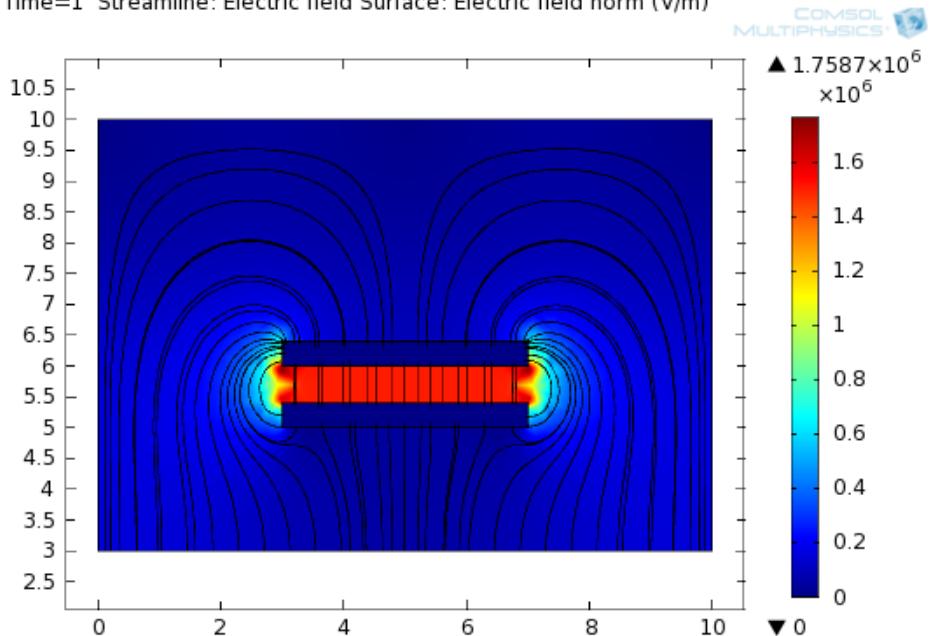
- Provides **detailed** information about device operation & characteristics
- Computationally intensive
 - » EM simulation, drift-diffusion eqns., numerical solution of PDEs, etc.

Compact Model of Device

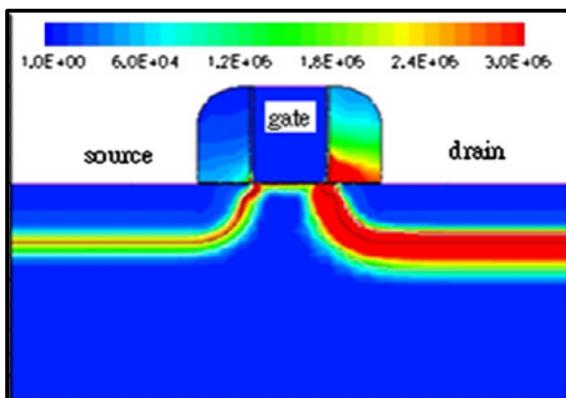
- **Simple** enough to be incorporated in circuit simulators
- **Accurate** enough to have predictive value for circuits
- **Terminal behaviour** important
 - » internal details less important
- Purpose: use in **circuit design**
 - » via circuit simulation

Compact Modelling

Time=1 Streamline: Electric field Surface: Electric field norm (V/m)

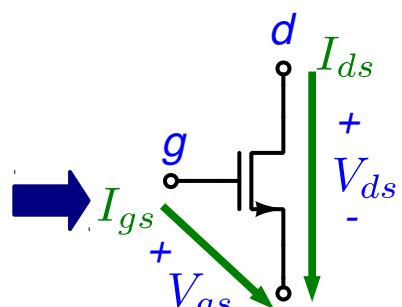


“device-level” simulation of a capacitor
(eg, finite element electrostatic)



S.L. Jang, J.S. Yuan, S.D. Yen, E. Kritchanchai, G.W. Huang “Experimental evaluation of hot electron reliability on differential Clapp-VCO” Microelectronics Reliability, Volume 53, Issue 2, February 2013, Pages 254–258

MOSFET



voltage-current relationships

$$q(v) = Cv$$

$$i(t) = \frac{d}{dt} q(v(t))$$

charge capacitance

electrical parameter

$$C = \frac{\epsilon_r \epsilon_0 L \cdot W}{d}$$

geometrical parameters

or

$$C = \frac{\epsilon_r \epsilon_0 L \cdot W}{d} \left[1 + \frac{d}{\pi W} + \frac{d}{\pi W} \ln \left(\frac{2\pi W}{d} \right) \right] + \dots$$

H. B. Palmer 1927

Compact Model

- Schichman-Hodges (core)

$$I_{ds} = f(V_{gs}, V_{ds})$$

$$= \begin{cases} \beta \left[(V_{gs} - V_T) - \frac{V_{ds}}{2} \right] V_{ds}, & \text{if } V_{ds} < V_{gs} - V_T \\ \frac{\beta}{2} (V_{gs} - V_T)^2, & \text{if } V_{ds} \geq V_{gs} - V_T \\ 0 & \text{if } V_{gs} < V_T \end{cases}$$

$$I_{gs} = 0$$

- BSIM/EKV/PSP/MVS...

Simulation Algorithms

- DC operating point and DC sweep
- small-signal AC
- transient: FE, BE, TRAP, LMS, GEAR, ...
- PSS (periodic steady-state): HB, shooting
- noise analyses
- sensitivity analyses
- distortion analyses
- stochastic and statistical methods
- macro-modelling, MOR, “analog verification”, ...

Modelling and Simulation Today

- motivation for MAPP

develop good
compact models

prototype
simulation algorithms

- many pitfalls:

you will need:

A common, open-source simulation framework
in MATLAB

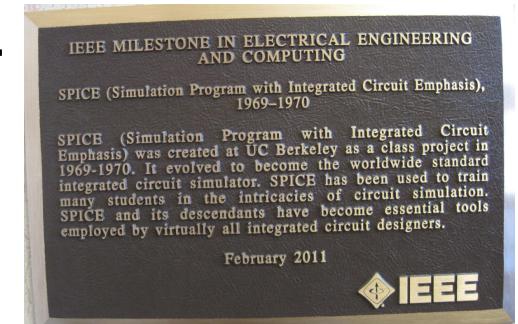
-
- hard to debug or resolve
- parsing, equation formulation,
output, ...

compact model developers
and simulation people blame
each other

huge (waste of) effort of
re-development of
basic capabilities

Why not use SPICE?

- SPICE: the original open-source simulator
 - » de-facto standard
 - » structure: all analyses in all models
 - » prototyping models & algorithms: takes months to years
 - » pain to write (even for those who can)
 - e.g., shooting method (S-SPICE)
- To be useful: **modular, well-structured, flexible**
 - » separated models, algorithms, numerics, I/Os
 - » simple, clean interfaces
 - » short, easy to read, easy to modify



Excerpt from dioload.c (SPICE3)

```
#ifdef SENSDEBUG
    printf("vd = %.7e \n", vd);
#endif /* SENSDEBUG */
    goto next1;
}

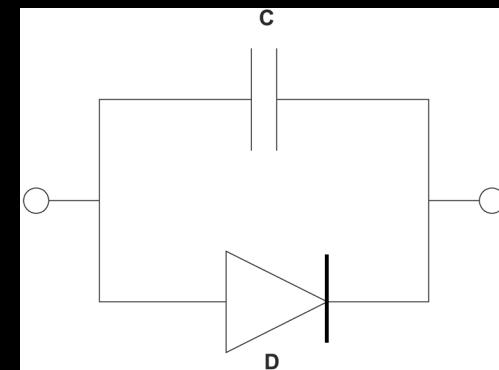
if (ckt->CKTmode & MODEINITSMSIG) {
    vd= * (ckt->CKTstate0 + here->DIOvoltage);
} else if (ckt->CKTmode & MODEINITTRAN) {
    vd= * (ckt->CKTstate1 + here->DIOvoltage);
} else if ( (ckt->CKTmode & MODEINITJCT)
            (ckt->CKTmode & MODETRANOP)
            && (ckt->CKTmode & MODEUIC) ) {
    vd=here->DIOinitCond;
} else if ( (ckt->CKTmode & MODEINITJCT) && here->DIOoff) {
    vd=0;
} else if ( ckt->CKTmode & MODEINITJCT) {
    vd=here->DIOtVcrit;
} else if ( ckt->CKTmode & MODEINITFIX && here->DIOoff) {
    vd=0;
} else {
#ifndef PREDICTOR
    if (ckt->CKTmode & MODEINITPRED) {
```

Sensitivity analysis code
AC analysis code

Transient
analysis
related
code

Glimpse: Diode Model in MAPP

```
1 function MOD = diodeCapacitor_ModSpec_wrapper()
2 % ModSpec description of an ideal diode in parallel with a capacitor
3 MOD = ee_model();
4 MOD = add_to_ee_model(MOD, 'external_nodes', {'p', 'n'});
5 MOD = add_to_ee_model(MOD, 'explicit_outs', {'ipn'});
6 MOD = add_to_ee_model(MOD, 'parms', {'C', 2e-12, 'Is', 1e-12, 'VT', 0.025});
7 MOD = add_to_ee_model(MOD, 'f', @f);
8 MOD = add_to_ee_model(MOD, 'q', @q);
9 end
10
11 function out = f(S)
12     v2struct(S);
13     out = Is*(exp(vpn/VT)-1);
14 end
15
16 function out = q(S)
17     v2struct(S);
18     out = C*vpn;
19 end
"diodeCapacitor_ModSpec_wrapper.m" 19L, 548C written
```



1,1

All

MOD.terminal

MOD.parms

MOD.explicit_outs

MOD.f: function handle

MOD.q: function handle

...

- executable (in Matlab)
- takes 10min to write
- works in all analyses

Glimpse: Shooting Method in MAPP

Shooting Algorithm in MAPP (pseudo-code)

```
shootObj = shoot(DAE); // constructor
```

```
1: shootObj.DAE = DAE;  
2: shootObj.tranObj = LMS(DAE); // transient simulation object  
3: set up member functions: .solve, .g, and .J  
4: return shootObj;
```

```
shootObj.solve (initguess, T):
```

```
1: x0 ← NR(@g, @J, initguess);  
2: shootSols = tranObj.solve(x0, 0, T);  
3: return shootSols;
```

```
shootObj.g (x0):
```

```
1: tranSols = tranObj.solve(x0, 0, T);  
2: return gout = tranSols(:, n) - x0;
```

```
shootObj.J (x0):
```

```
1: tranSols = tranObj.solve(x0, 0, T);  
2: Ci_pre = DAE.dq_dx(x0);  
3: M = eye(n);  
4: for i = 2:n do  
5:   x = tranSols(:, i); u = inputs(:, i);  
6:   Ci = DAE.dq_dx(x); Gi = DAE.df_dx(x, u);  
7:   M = (Ci + (tpts(i) - tpts(i-1)) * Gi) \ Ci_pre * M;  
8:   Ci_pre = Ci;  
9: end for  
10: return Jout = M - eye(n);
```

object-oriented

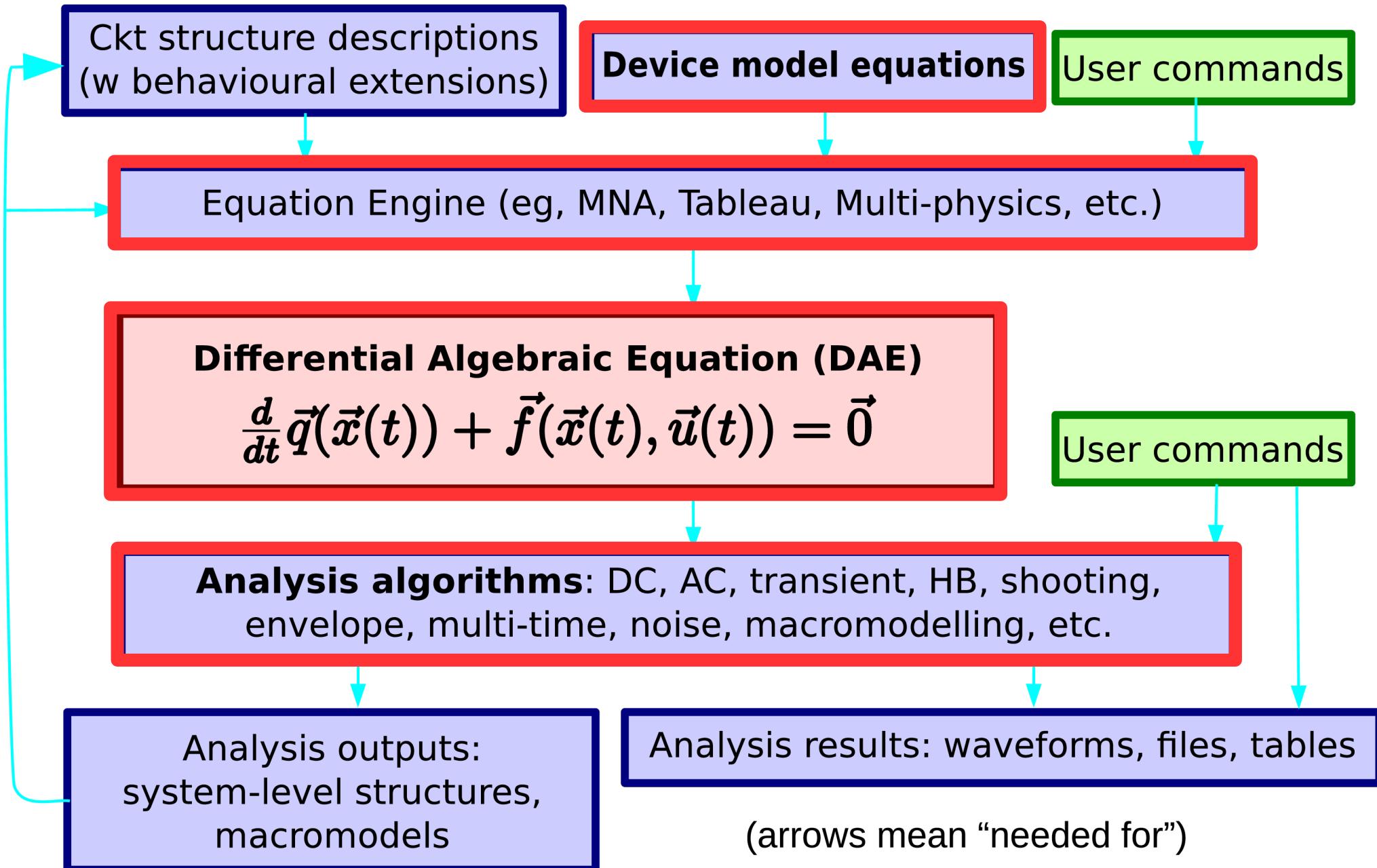
reuses LMS (transient) code

150 lines of code

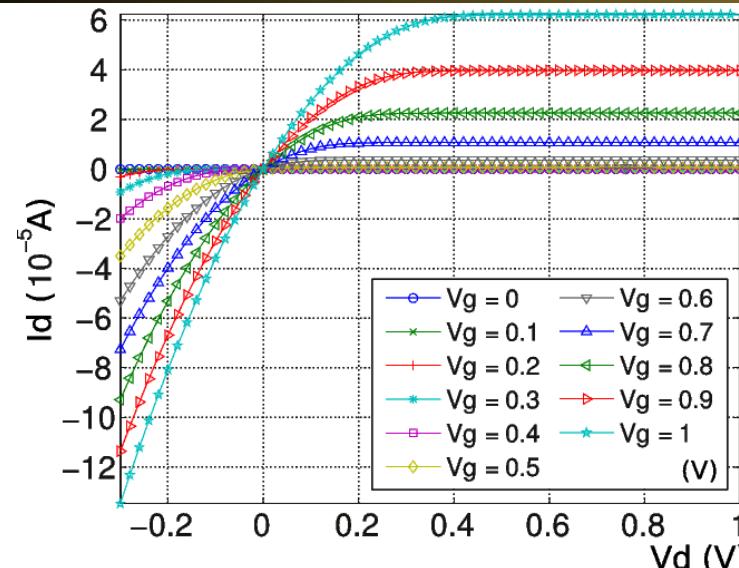
works with all devices,
circuits, domains

a pleasure to write
(you too can do it)

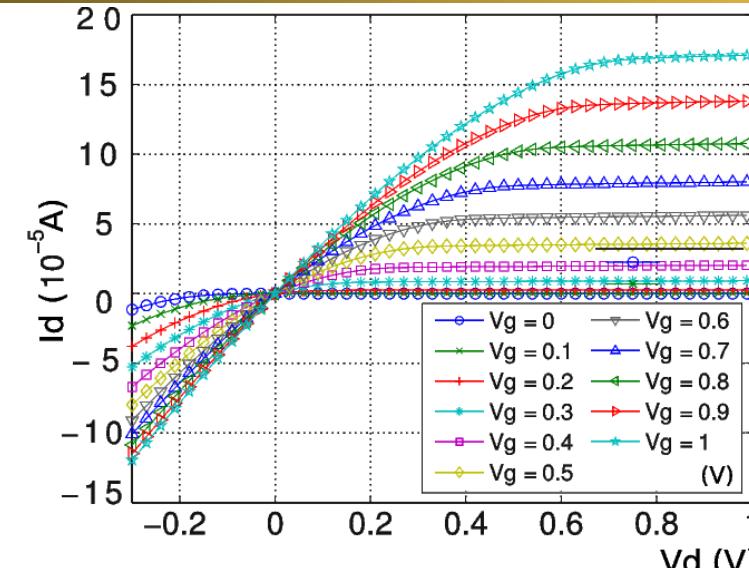
Code Structuring of MAPP



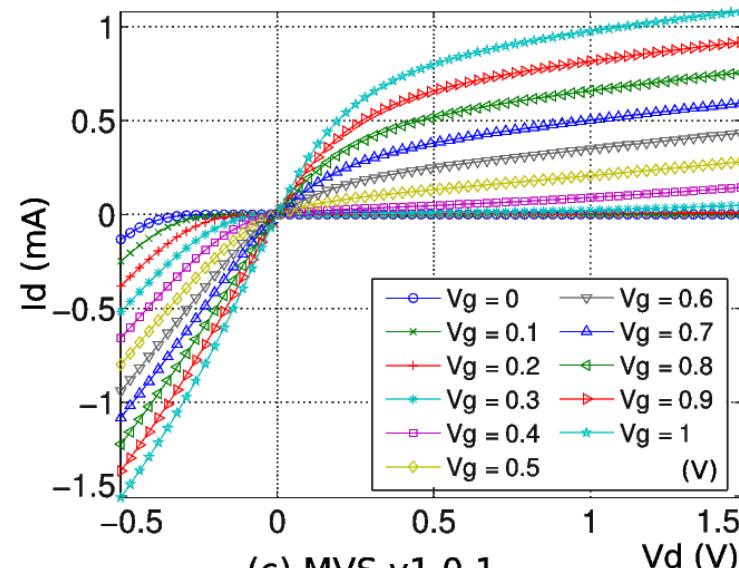
MAPP: Compact Model Prototyping



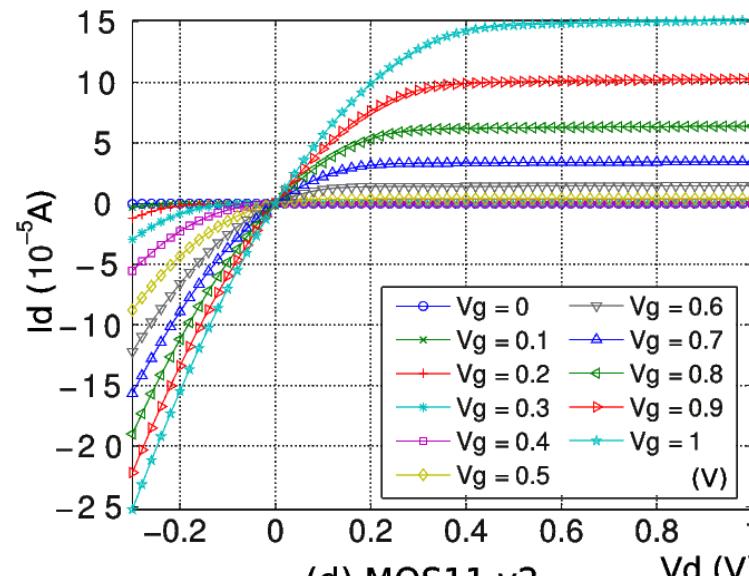
(a) BSIM6.1.0
default: $L=10\mu m$, $W=10\mu m$



(b) PSP Level 103 v3.0
default: $L=10\mu m$, $W=10\mu m$

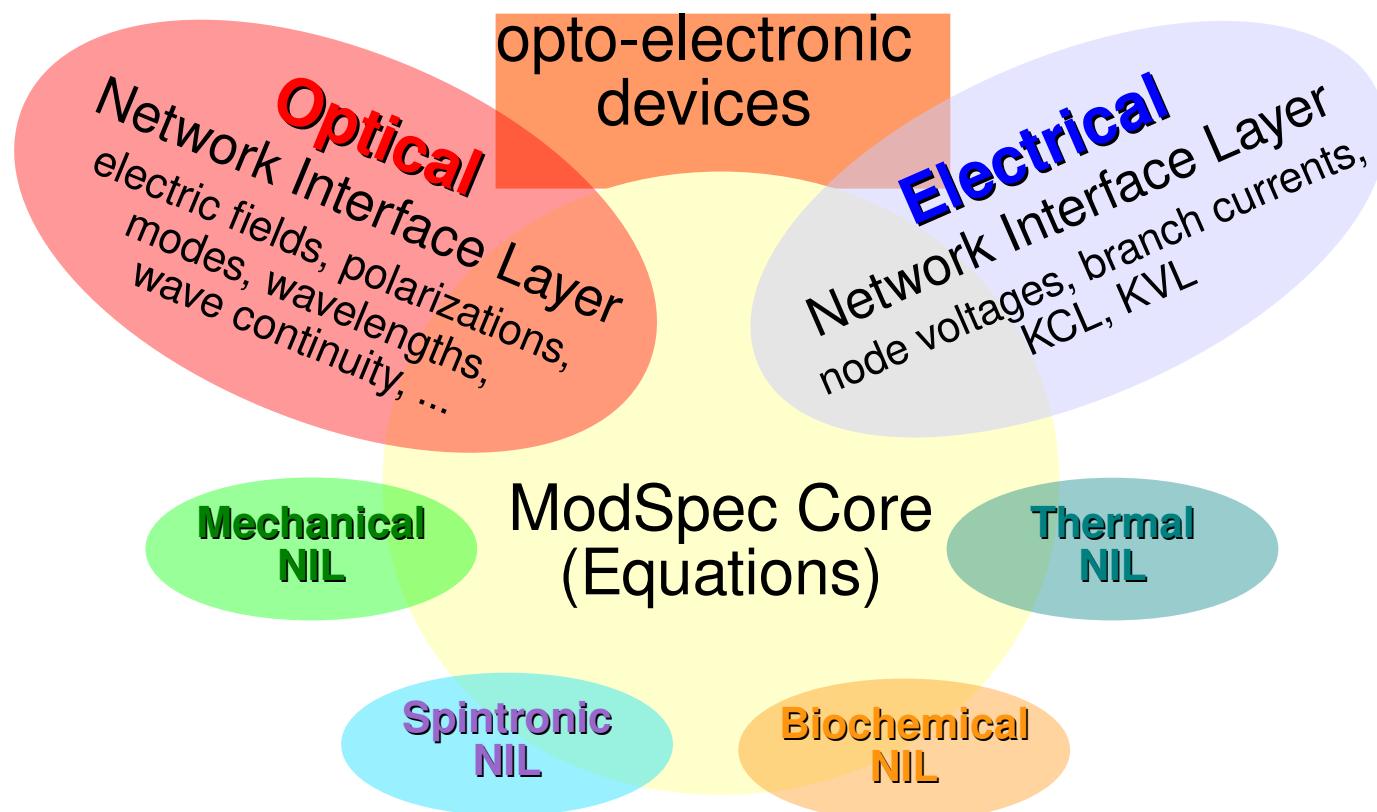


(c) MVS v1.0.1
default: $L=80nm$, $W=1\mu m$

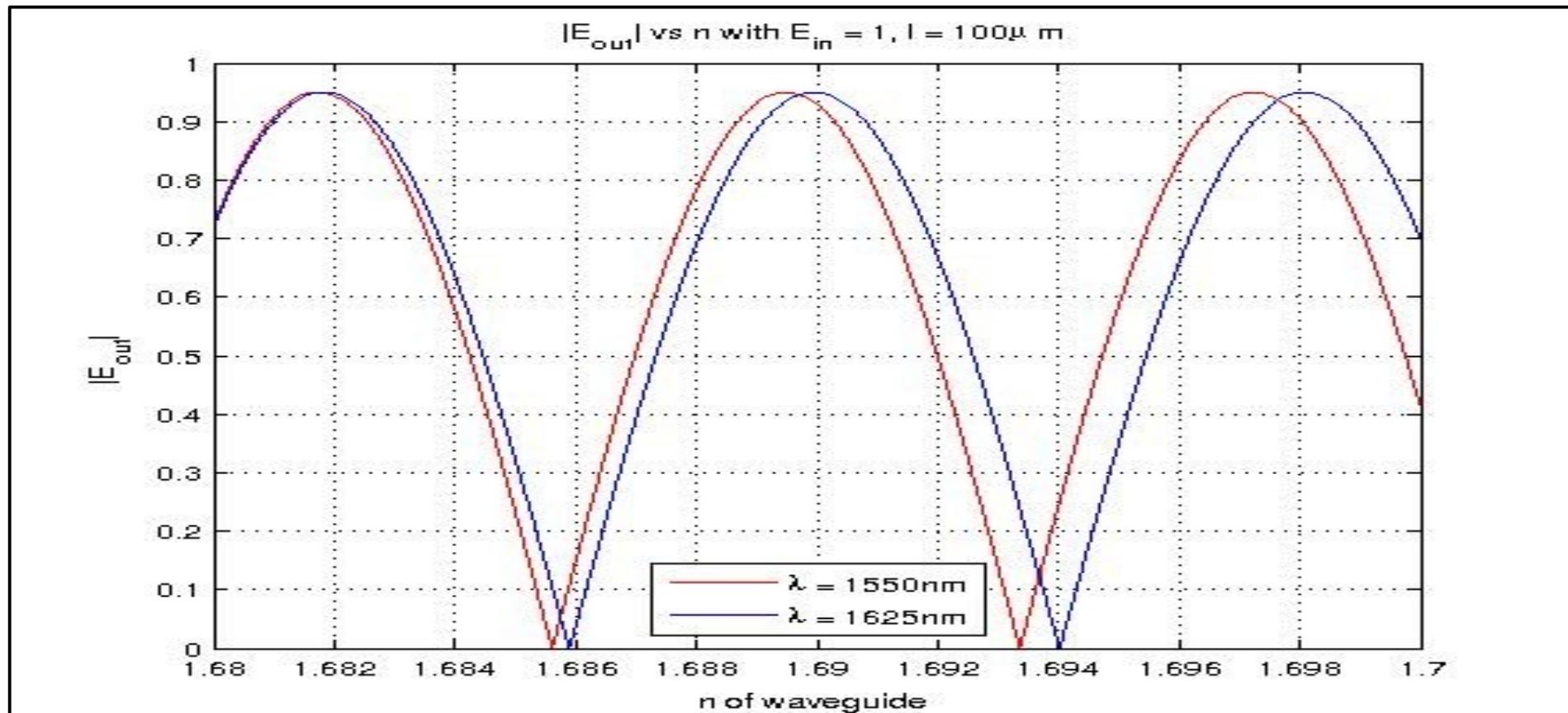
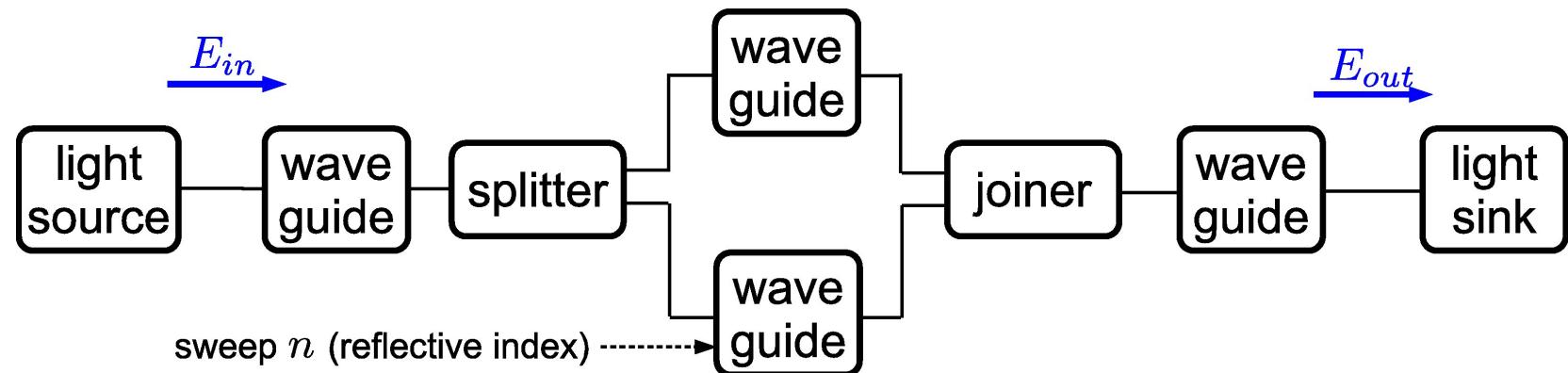


(d) MOS11 v2
default: $L=1\mu m$, $W=1\mu m$

MAPP: Multiphysics Support



Optical System Modelling/Simulation Example



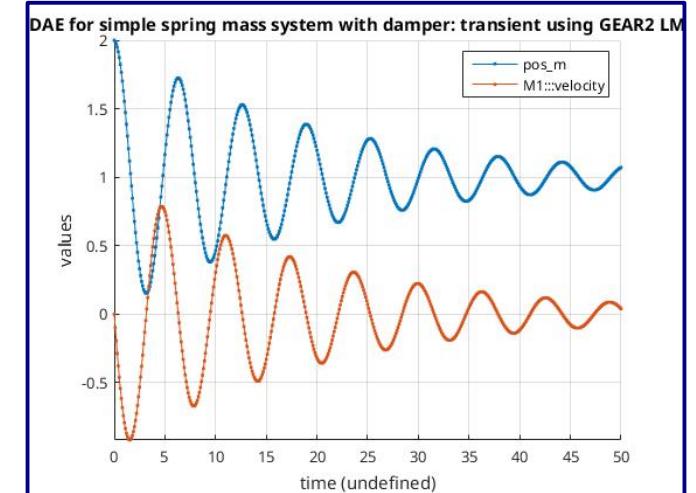
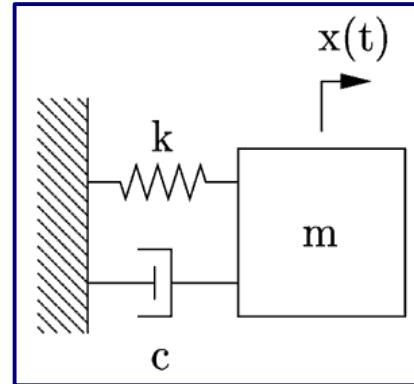
Multiphysics Systems

potential/flow systems:

kinematic NIL:

“flow”: force

“potential”: position



magnetic NIL:

“flow”: magnetic flux

“potential”: magnetomotive force

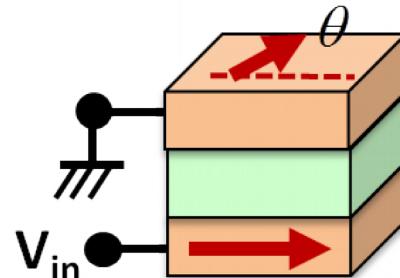
thermal NIL:

“flow”: power flow

“potential”: temperature

Spintronic systems:

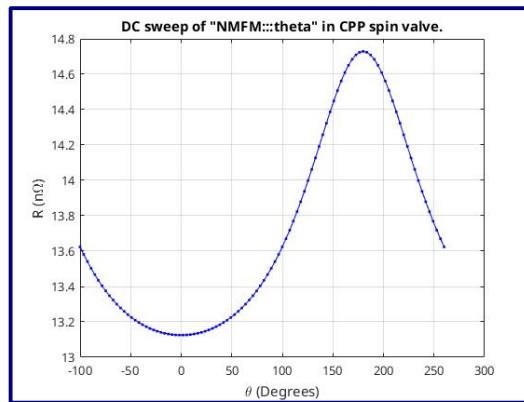
vectorized spin currents
vectorized spin voltages



Chemical reaction networks

rates and concentrations

“KCLs” at nodes have d/dt terms



Kerem Yunus Camsari; Samiran Ganguly;
Supriyo Datta (2013), "Modular Spintronics Library,"
<https://nanohub.org/resources/17831>.

LTI MOR Example in MAPP

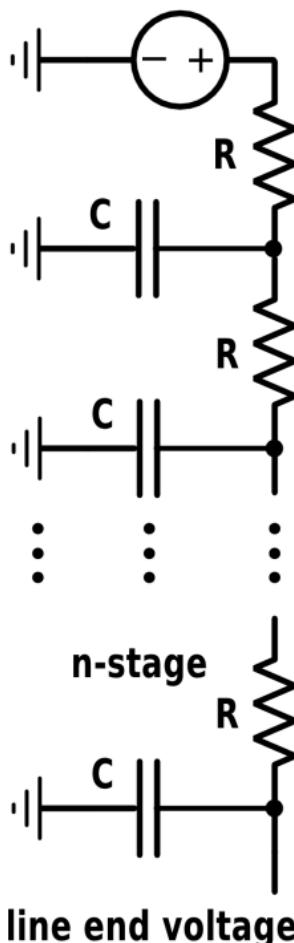
Equation formulation for the RC line circuit

Extract C/G matrices,
Arnoldi MOR

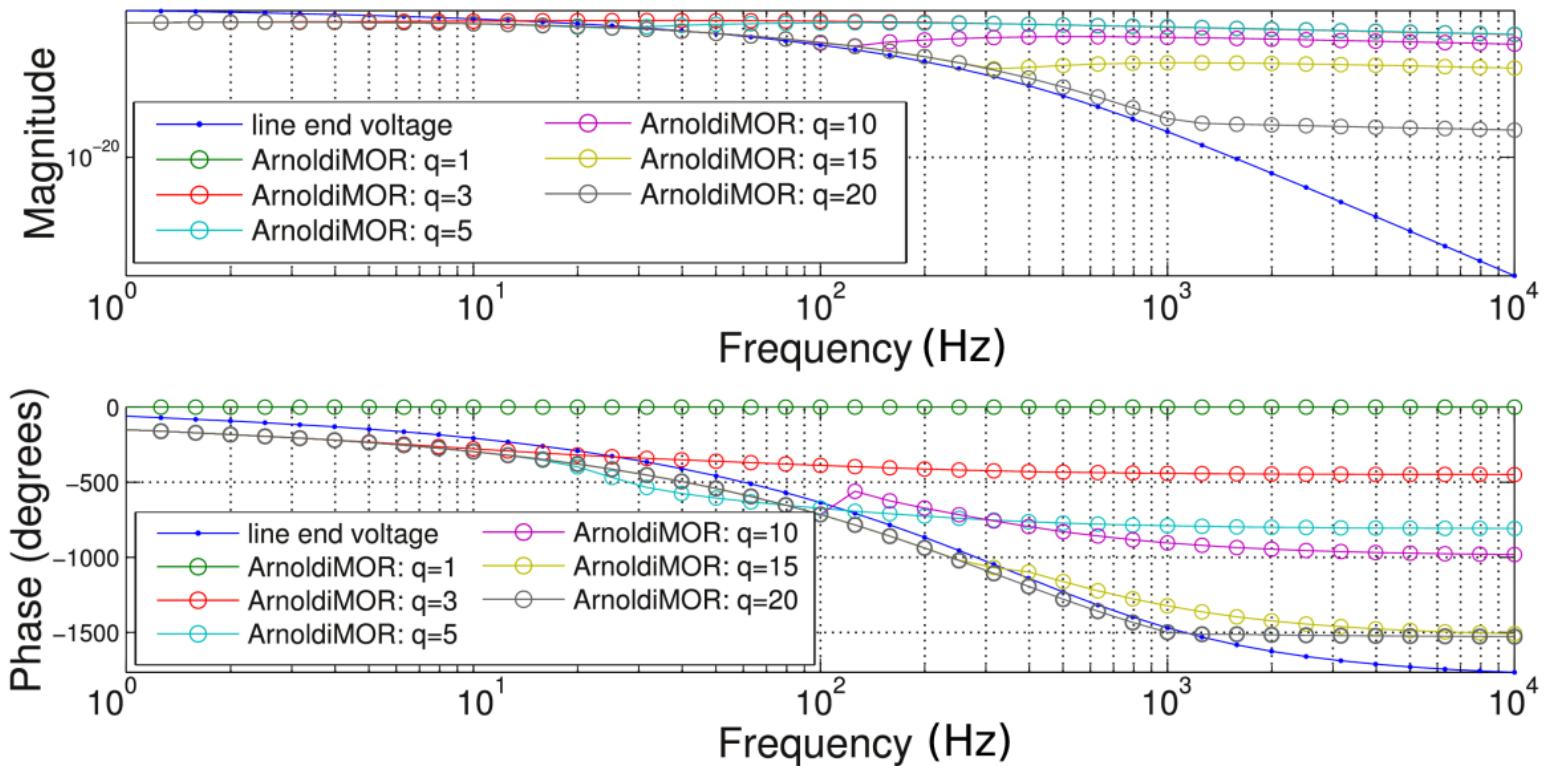
Equation formulation for the reduced-order system

AC simulation

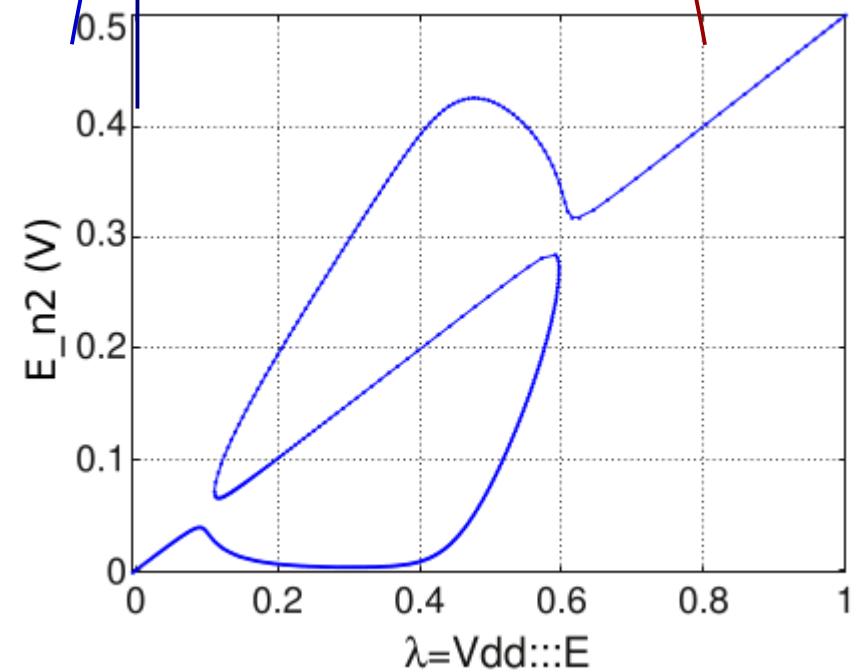
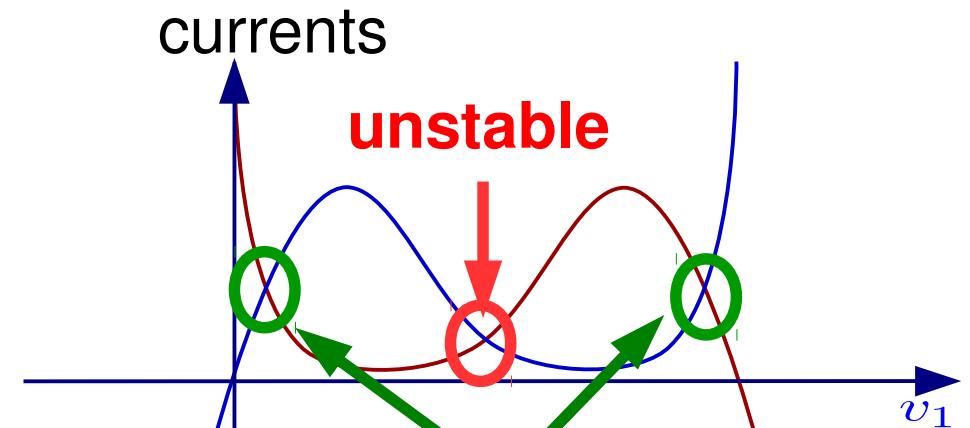
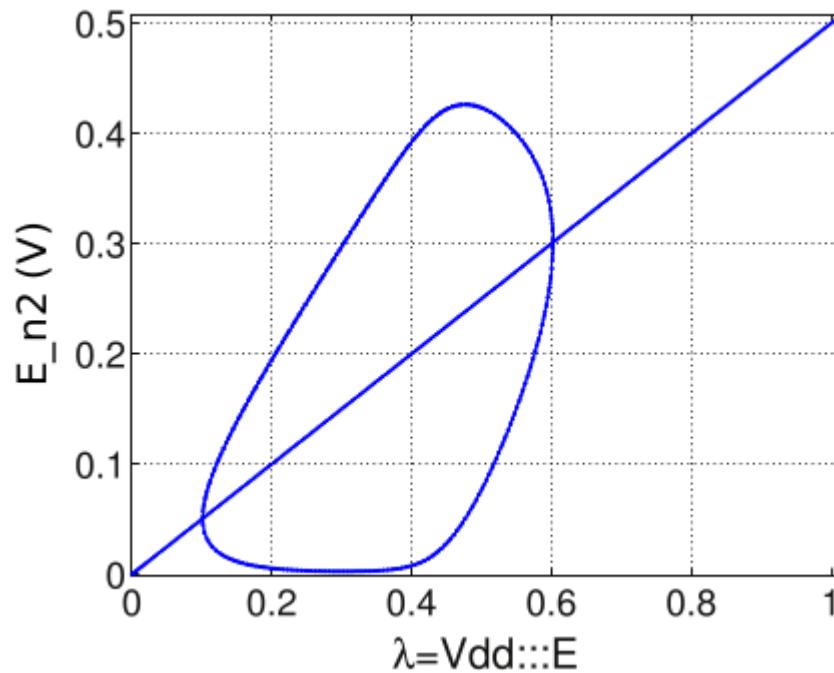
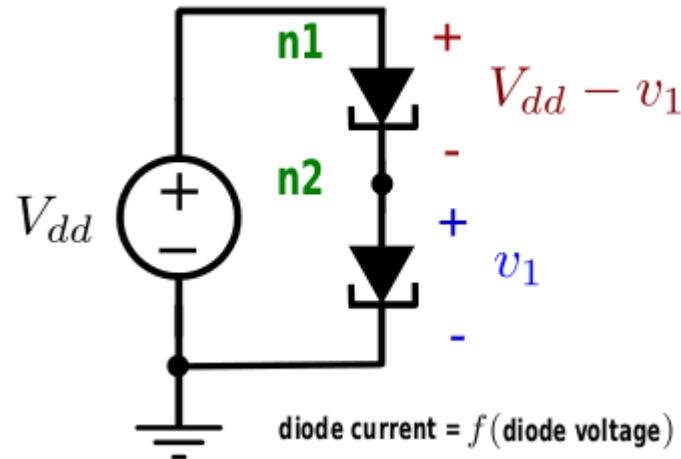
AC simulation



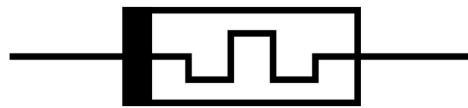
AC analysis: RC line with 20 segments:
line end voltages with and without MOR



Homotopy Analysis on Goto Pair

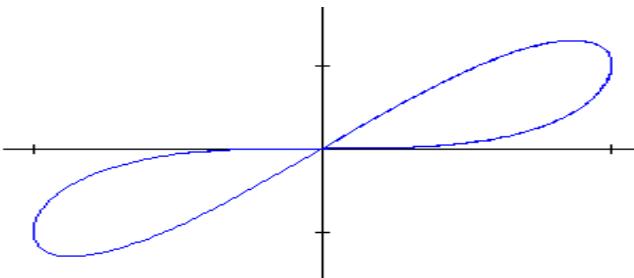


Finding Folds with Homotopy

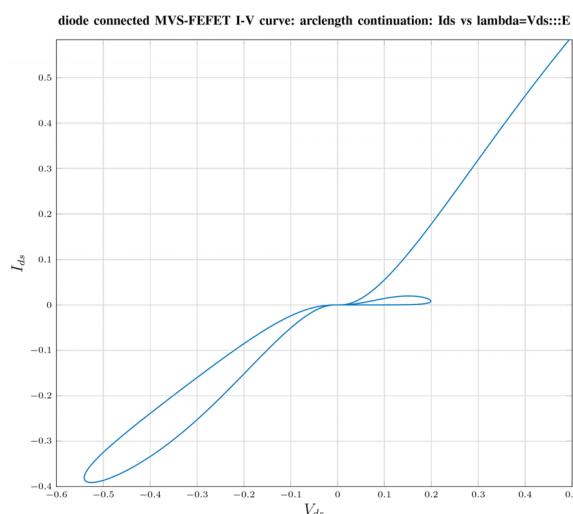


memristor

(RRAM, CBRAM, PCM...)

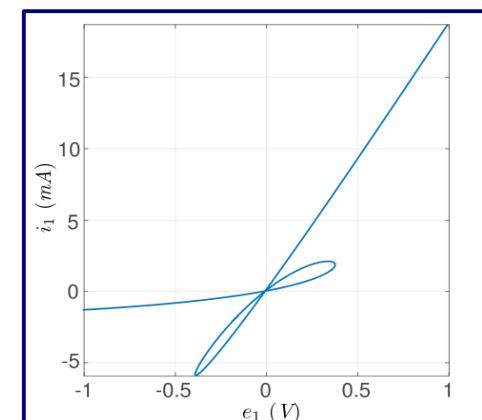
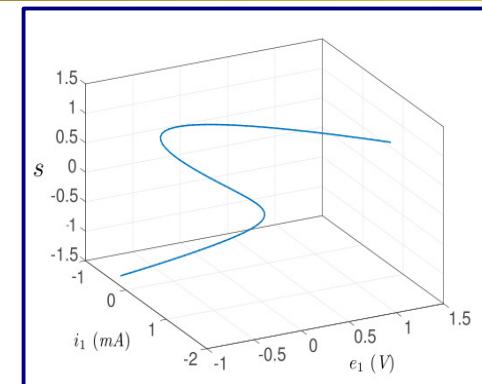
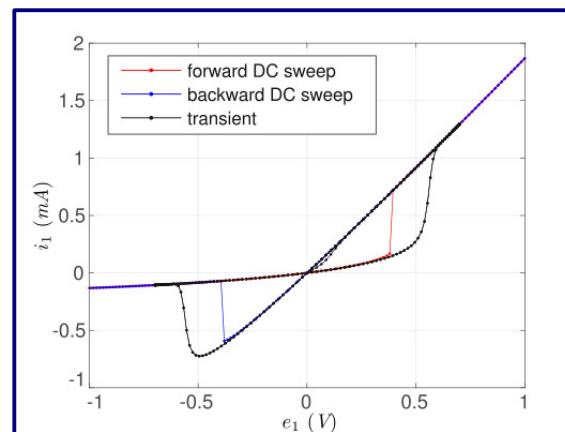


NC-FET

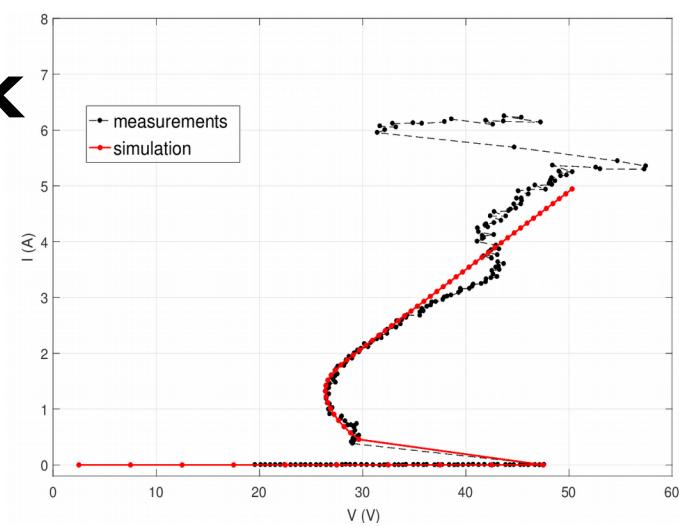


$$\text{ipn} = f_1(\text{vpn}, s)$$

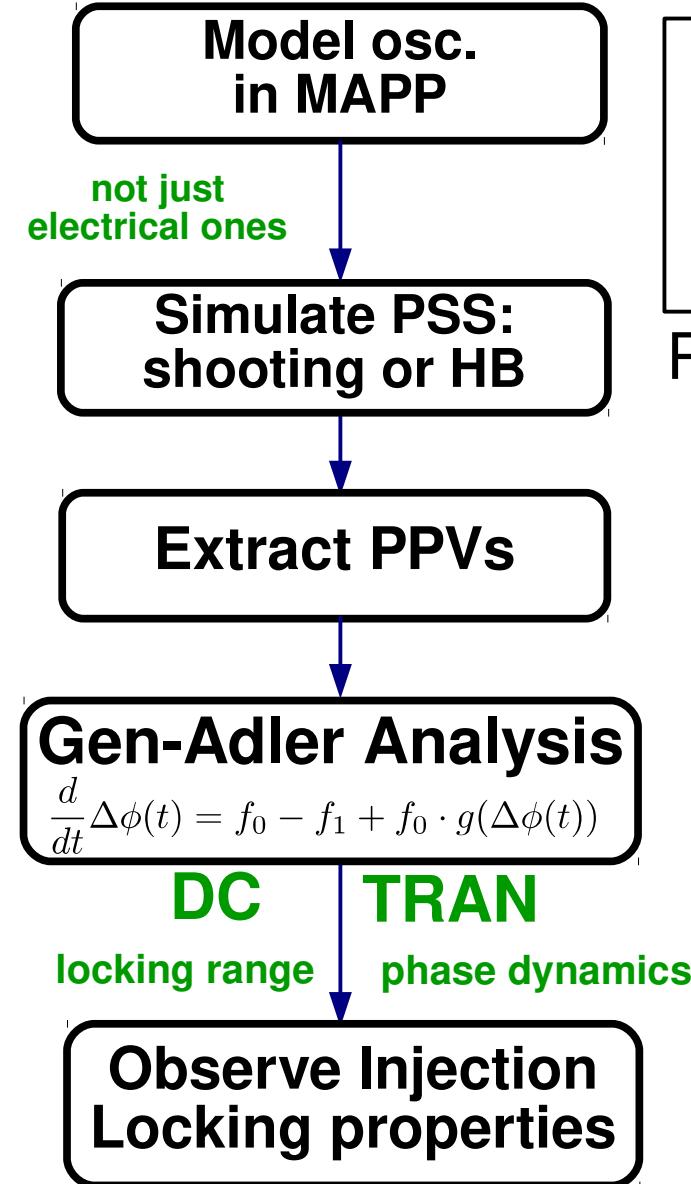
$$\frac{d}{dt}s = f_2(\text{vpn}, s)$$



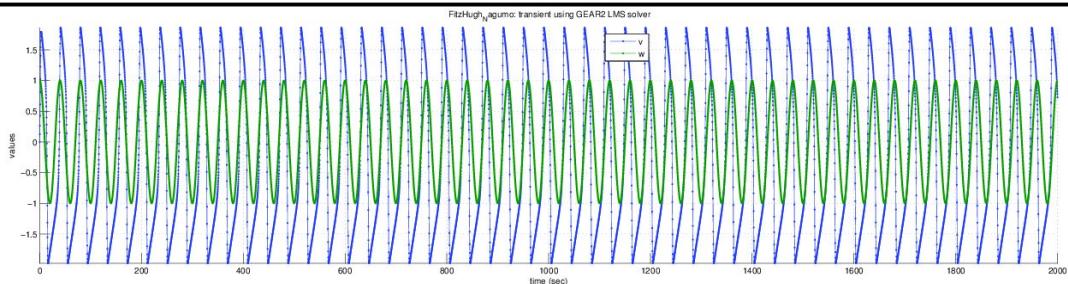
**ESD
snapback**



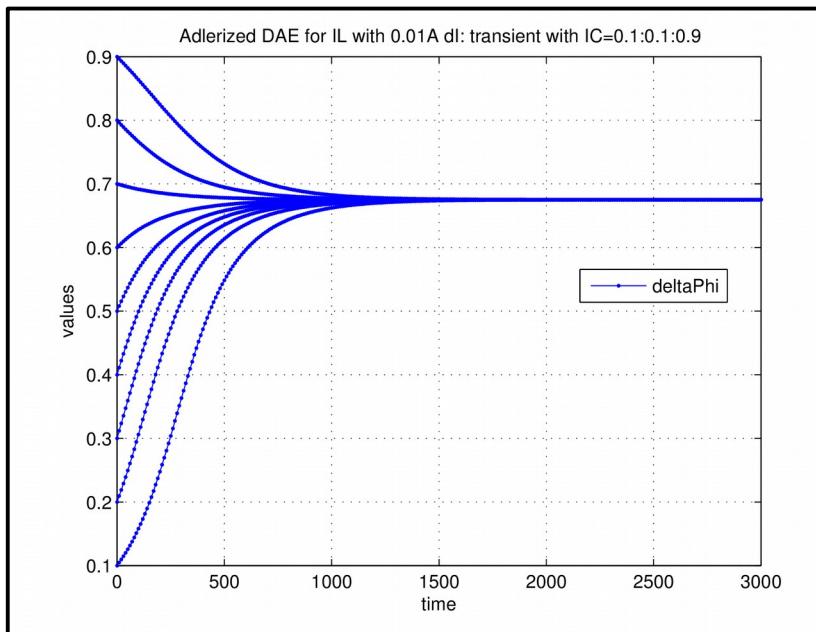
Phase-macromodel Simulation in MAPP



Standard TRAN simulation:



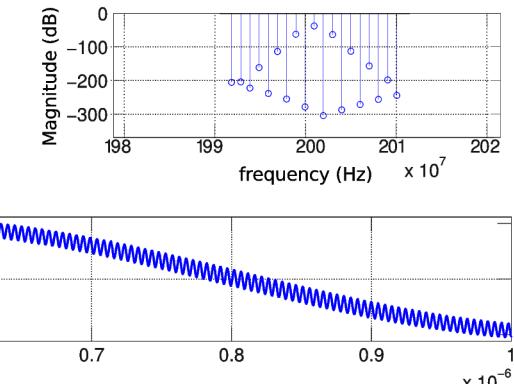
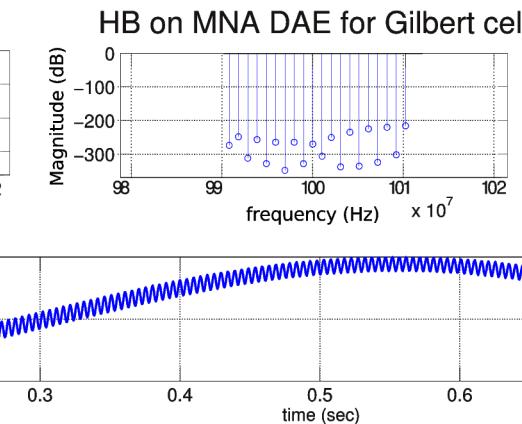
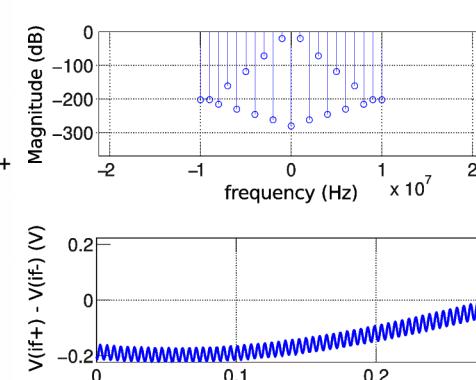
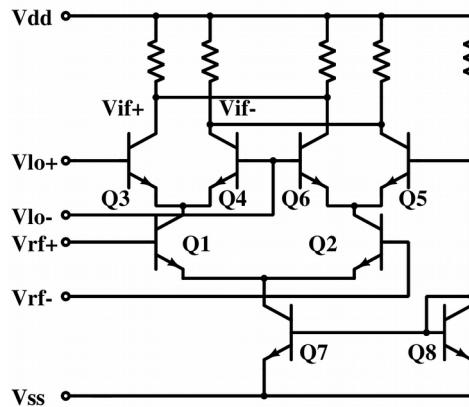
Phase-based TRAN:



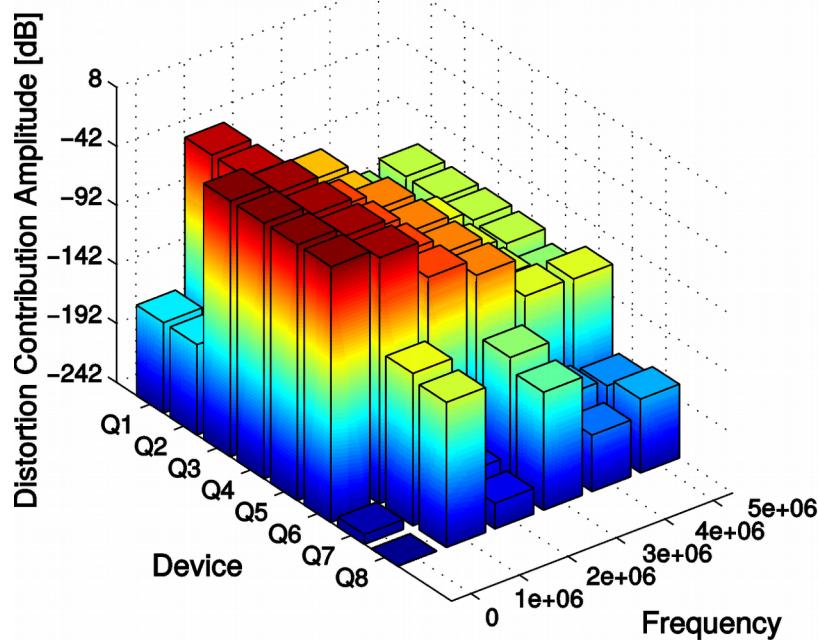
$\Delta\phi(t)$ captures phase response nicely

details: Bhansali/Roychowdhury, "Gen-Adler: the Generalized Adler's equation for injection locking analysis in oscillators". Proc. ASPDAC, 2009.

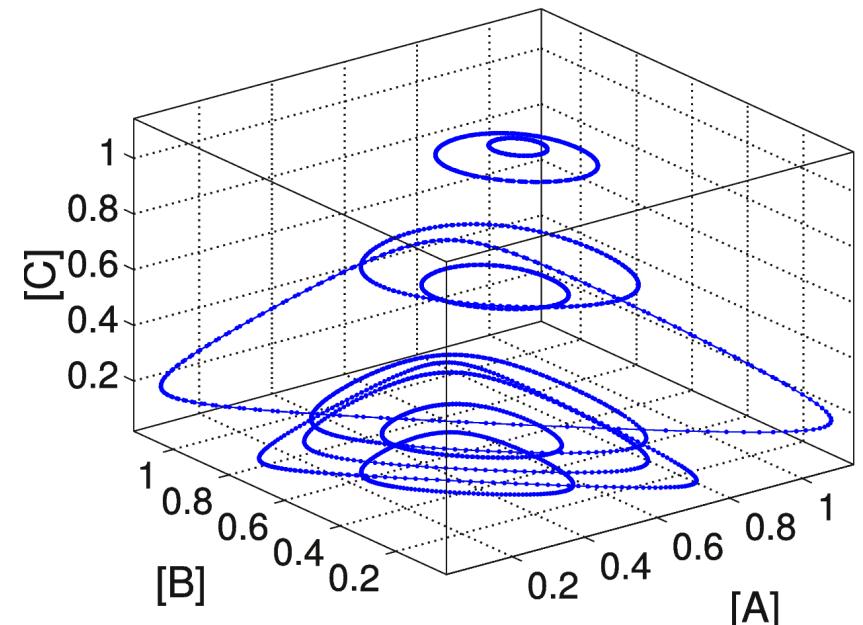
Simulation Algorithms in MAPP: More Examples



Distortion Contribution Analysis on Gilbert cell



3D phase plane plot of RRE
for $A + B \rightarrow 2B$; $B + C \rightarrow 2C$; $C + A \rightarrow 2A$



details: Wu/Roychowdhury, "Efficient per-element distortion contribution analysis via Harmonic Balance adjoints". Proc. CICC 2014.

MAPP: Public Release

- Open Source download: <https://github.com/jaijeet/MAPP>
- License
 - » primary: GPL-v3
 - » alternative licensing available
 - eg, SRC contract terms apply for SRC company use
 - » contributors can specify their own alternative licensing terms for their contributions

MAPP: Features

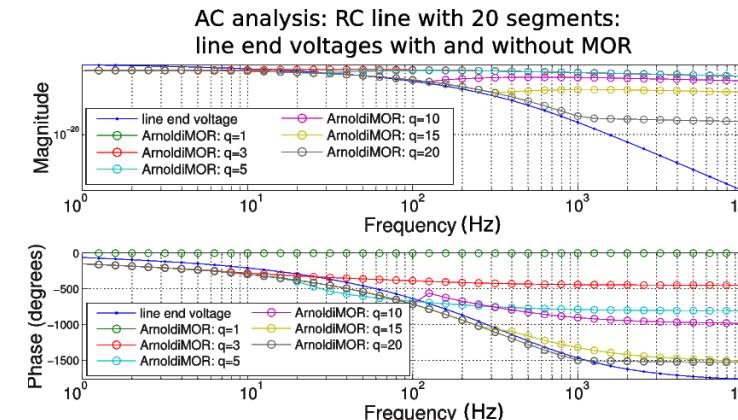
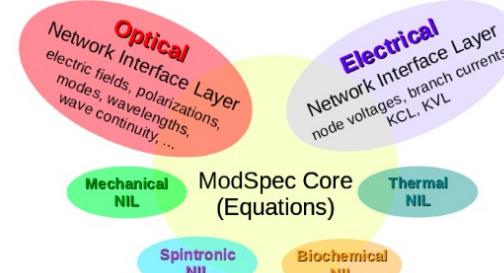
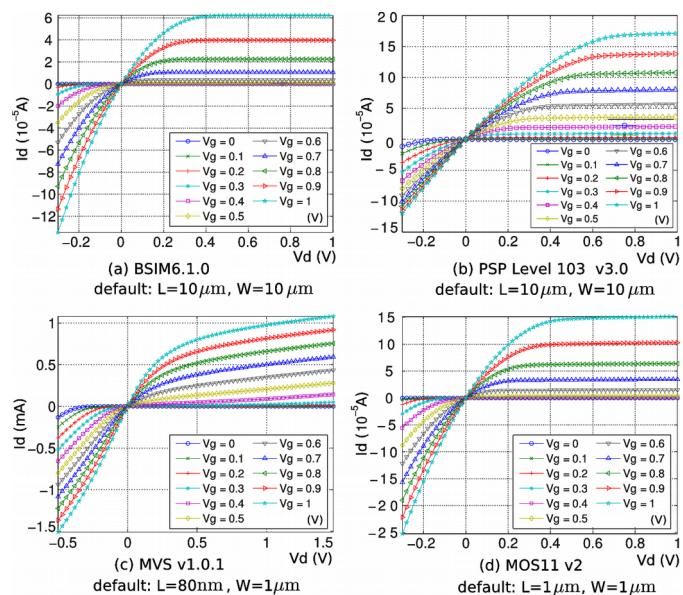
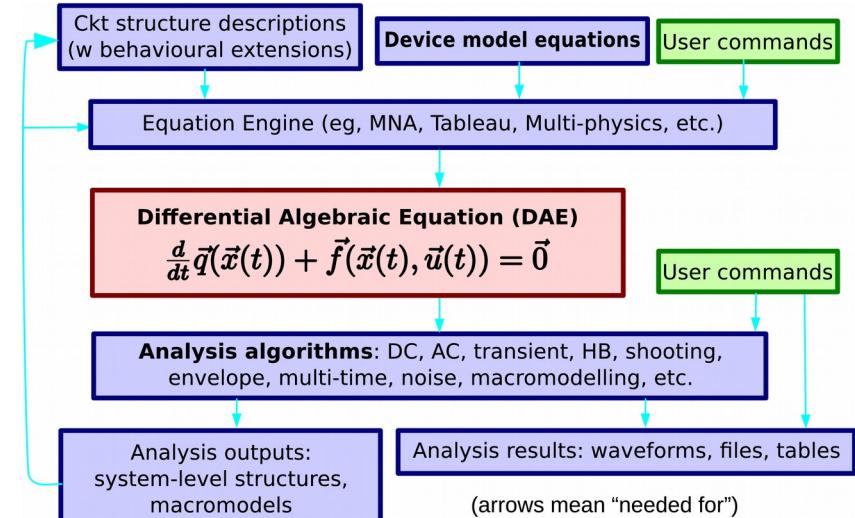
- **Works entirely in MATLAB/Octave**
 - » C++ version to be released
 - » mex interfaces to link C++ devices and circuit DAEs into MATLAB
- **Help system** (start with `help MAPP`)
 - » quick start walk-through
- **Automatic differentiation** (`vecvalder`)
 - » `help MAPPAutodiff`
- **Executable multiphysics device specification** (`ModSpec`)
 - » examples, tutorial: part of help
- **DC, AC, transient analyses**
 - » also noise, homotopy, HB, shooting, PPV, MOR, etc.
(initial version released at PHLOGON.eecs.berkeley.edu)
- **Automated testing system** exercising suite of tests

MAPP: Intended Uses

- Developing simulation-ready device models
 - » including multiphysics devices, network connectivity
- Quickly prototyping new simulation algorithms
 - » hours/days to implement a new analysis
 - assess strengths/limitations before investing resources to implement in “real simulators”
- Learning or teaching modelling/simulation
 - » MATLAB → broadly accessible
 - » help system, tutorials, supporting resources

Summary

```
#ifdef SENSDEBUG
    printf("vd = %.7e \n",vd);
#endif /* SENSDEBUG */
    goto next1;
}
if(ckt->CKTmode & MODEINITSMSIG) {
    vd= *(ckt->CKTstate0 + here->DIOvoltage);
} else if (ckt->CKTmode & MODEINITTRAN) {
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} else if ( (ckt->CKTmode & MODEINITJCT) &&
(ckt->CKTmode & MODETRANOP)
    && (ckt->CKTmode & MODEUIC) ) {
    vd=here->DIOinitCond;
} else if ( (ckt->CKTmode & MODEINITJCT) && here->DIOoff)
{
    vd=0;
} else if ( ckt->CKTmode & MODEINITVCRIT) {
    vd=here->DIOtVcrit;
} else if ( ckt->CKTmode & MODEINITFIX & here->DIOoff) {
    vd=0;
} else {
#ifndef PREDICTOR
    if (ckt->CKTmode & MODEINITPRED) {
```



<https://github.com/jaijeet/MAPP>