

init/limiting

Tianshi Wang

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NR method
the need for
init/limiting
previous
implementations
in MAPP
in SPICE
in Xyce
challenges

Math behind
init/limiting

formula
equiv w/ SPICE
equiv w/ Xyce

Implementation
in MAPP

ModSpec level
DAE level
Analysis level
TODOs

Conclusions

Initialization and Limiting for NR in MAPP

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Overview

1 Background

- the Newton Raphson method
 - the need for init/limiting
- previous implementations of init/limiting
 - in MAPP
 - in SPICE
 - in Xyce

2 Mathematics behind init/limiting

- algebraic function formula with init/limiting
- equivalence with SPICE
- equivalence with Xyce

3 Implementation in MAPP

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- TODOs

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the Newton Raphson method

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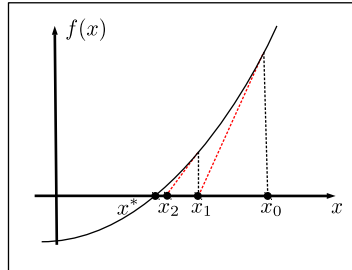
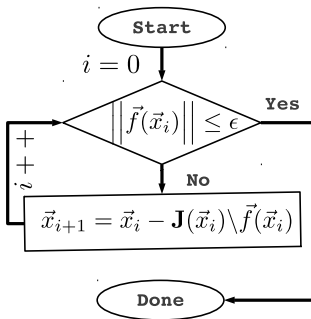
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NR: iterative numerical algorithm to solve $\vec{f}(\vec{x}) = \vec{0}$

- Start with \vec{x}_0 , update \vec{x}_i with derivative information
- $d\vec{x}_i = \vec{x}_{i+1} - \vec{x}_i = -\mathbf{J}(\vec{x}_i) \backslash \vec{f}(\vec{x}_i)$



NR: no guarantee to converge!

the need for init/limiting

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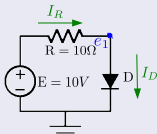
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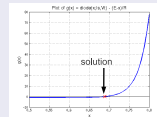
In fact, it is very easy to break NR

simple diode example

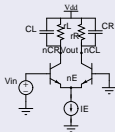


circuit equation: $I_D - I_R = 0$

$$\Rightarrow f(e_1) \triangleq I_S \left(e^{\frac{e_1}{V_T}} - 1 \right) - \frac{E - e_1}{R} = 0$$



BJT differential pair example



won't converge unless given very good initial guess

```
x0 = [5; 3; 3; -0.7; 0.1; -2e-3; 0];  
...  
qss = feval(qss.solve, x0, qss);  
sol = feval(qss.getsolution, qss); % sol close to x0
```

and many more ...

diode mixer (4 diodes), 741 op-amp (~20 BJTs), ...

Previous Implementation in MAPP

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initialization

set \vec{x}_0 to “good” value

e.g.

$x_0 = [5; 3; 3; -0.7; 0.1; -2e-3; 0];$

or

$x_0 = \text{DAE.QSSinitguess}(\text{DAE});$

limiting

directly limit NR update $d\vec{x}_i$

$$d\vec{x}_i = -\mathbf{J}(\vec{x}_i) \backslash \vec{f}(\vec{x}_i)$$

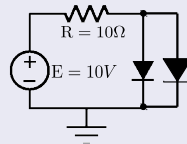
$$d\vec{x}_i = \text{limiting}(d\vec{x}_i, \vec{x}_i)$$

$$\vec{x}_{i+1} = \vec{x}_i + d\vec{x}_i$$

implemented in DAE.NRlimiting

problems

- difficult to write at system-level
 - requires knowing all unk indices/names
 - subject to change with inputs
- how to get it from devices?
- init/limiting for different devices may conflict
 - e.g. two diodes in parallel



init/limiting in SPICE

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NR in SPICE

SPICE uses RHS — circuit (current) right hand side

$$\vec{x}_{i+1} = \mathbf{J}(\vec{x}_i) \backslash \text{RHS}(\vec{x}_i)$$

$$\text{RHS}(\vec{x}_i) \approx -\vec{f}(\vec{x}_i) + \mathbf{J}(\vec{x}_i) \cdot \vec{x}_i$$

When there is no init/limiting:

$$\mathbf{J}(\vec{x}_i) \cdot \vec{x}_{i+1} = \text{RHS}(\vec{x}_i) = -\vec{f}(\vec{x}_i) + \mathbf{J}(\vec{x}_i) \cdot \vec{x}_i$$

$$\vec{x}_{i+1} - \vec{x}_i = -\mathbf{J}(\vec{x}_i) \backslash \vec{f}(\vec{x}_i)$$

J and RHS evaluation in SPICE

- each device contributes to **J/RHS** (based on its bias)
 - \vec{x} contains **node voltages**, but devices use **branch voltages**
- when using init/limiting, device directly/“secretly” changes its bias voltages for its **J/RHS** evaluation
- system-level inconsistency
 - \vec{x} values are not changed uniformly in device evaluation

init/limiting in SPICE: example

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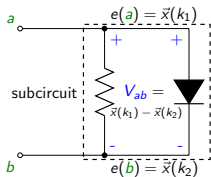
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$$I_R = V_{ab} \cdot G$$

$$\hat{V}_{ab} = \text{pnjlim}(V_{ab}, V_{ab}^{(old)})$$

$$I_D = \text{Diode.Id}(\hat{V}_{ab})$$

$$G_D = \text{dDiode.Id}(\hat{V}_{ab})$$

set up unks/eqns \vec{x}/\vec{f}

calculate bias

calculate $\vec{f}/\text{RHS}/\mathbf{J}$

$$\vec{x} = \begin{bmatrix} \vdots \\ k_1 \\ e(a) \\ k_2 \\ e(b) \\ \vdots \end{bmatrix}$$

$$\vec{f} = \begin{bmatrix} \vdots \\ k_1 \\ \text{KCL}(a) + I_R + I_D \\ k_2 \\ \text{KCL}(b) - I_R - I_D \\ \vdots \end{bmatrix}$$

$$\mathbf{J} = \begin{bmatrix} & k_1 & & k_2 & \\ k_1 & \dots & \frac{d\text{KCL}(a)}{de(a)} + G + G_D & \frac{d\text{KCL}(a)}{de(b)} - G - G_D & \dots \\ k_2 & \dots & \frac{d\text{KCL}(b)}{de(a)} - G - G_D & \frac{d\text{KCL}(b)}{de(b)} + G + G_D & \dots \\ & \vdots & & \vdots & \end{bmatrix}$$

$$\text{RHS} \approx \mathbf{J}\vec{x} - \vec{f}$$

$$I_R \text{ uses } V_{ab}$$

$$I_D \text{ and } G_D \text{ use } \hat{V}_{ab}$$

RHS

$$\begin{bmatrix} \vdots \\ k_1 \\ \text{RHS}(a) + G_D \cdot \hat{V}_{ab} - I_D \\ k_2 \\ \text{RHS}(b) - G_D \cdot \hat{V}_{ab} + I_D \\ \vdots \end{bmatrix}$$

What is RHS ?

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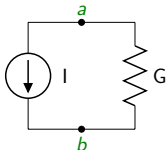
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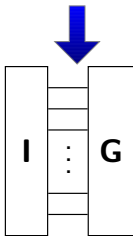
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Linear Circuit:



$$\vec{x} = \begin{bmatrix} e(a) \\ e(b) \end{bmatrix} \quad \mathbf{G} = \begin{bmatrix} +G & -G \\ -G & +G \end{bmatrix}$$

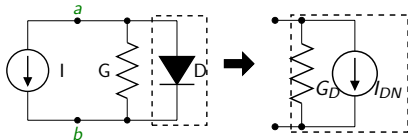
$$\mathbf{G} \cdot \vec{x}^* = \begin{bmatrix} +I \\ -I \end{bmatrix} = \mathbf{I}$$



$$\mathbf{G} \cdot \vec{x}^* = \mathbf{I}$$

current sources

Nonlinear Circuit:



use Norton's Theorem!

$$\mathbf{J} = \begin{bmatrix} +G + G_D & -G - G_D \\ -G - G_D & +G + G_D \end{bmatrix}$$

$$\mathbf{J} \cdot \vec{x}^* = \begin{bmatrix} +I + I_{DN} \\ -I - I_{DN} \end{bmatrix} = \text{RHS}$$

$$\mathbf{J} \cdot \vec{x}^* = \text{RHS}$$

current right hand side

init/limiting in Xyce

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limiting correction in Xyce

$$\begin{aligned}\vec{x}_{k+1} - \vec{x}_k &= \Delta \vec{x}_k^{(total)} &= \Delta \vec{x}_k^{(newton)} + \Delta \vec{x}_k^{(correction)} \\ &= -\mathbf{J}(\vec{x}_k) \backslash \vec{f}(\vec{x}_k) + \Delta \vec{x}_k^{(correction)} \\ &= -\mathbf{J}(\vec{x}_k) \backslash \left[\vec{f}(\vec{x}_k) + \vec{f}_k^{(correction)} \right]\end{aligned}$$

```
bool Instance::loadDAEFVector ()
{
    // 3f5 compatible currents
    // Including derivation of Vd_diff and
    // Limiting Correction
    ...
    // load the voltage limiter vector.
    if ( getDeviceOptions().voltageLimiterFlag )
    {
        double Vd_diff = Vd - Vd_orig;
        double Gd_Jdxdp = 0.0;
        Gd_Jdxdp = -( Gd ) * Vd_diff;
        // Load the dFdxVp vector
        (extData.dFdxVpVectorRawPtr)[li_Neg] += Gd_Jdxdp;
        (extData.dFdxVpVectorRawPtr)[li_Pri] -= Gd_Jdxdp;
    }
    ...
    return true;
}
```

Merits

- SPICE compatible
- can be turned off!

Problems

- physical meaning?
- new device? how?

Challenges with init/limiting Implementations

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- figure out what they really are
- get NR to converge
- easy to explain to device/DAE developers
- easy for users to write
 - avoid system level limiting
- SPICE compatible, but more than SPICE
 - diode-res ModSpec example, limits only junction voltage
- backward compatibility
 - some devices/DAEs don't provide init/limiting information
 - init/limiting can be turned off

Mathematical Expression of init/limiting

init/limiting

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- contains “dangerous” functions (sensitive to their inputs)
 - e.g. $\exp()$

$$\vec{f}(\vec{x}) = \vec{0} \longrightarrow \hat{\vec{f}}(\vec{x}, \vec{xlim}) = \vec{0}$$

- \vec{xlim} : inputs to “dangerous” functions
 - subset of \vec{x} or linear combination of \vec{x}
- $\hat{\vec{f}}$: new function, slightly different from \vec{f}
 - easy to write from \vec{f}
- unknown space enlarged

Mathematical Expression of init/limiting

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$$\hat{f}(\vec{x}, \overrightarrow{xlim}) = \vec{0}$$

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What is \overrightarrow{xlim}

- without init/limiting:

- subset of \vec{x} or linear combination of \vec{x}

$$\overrightarrow{xlim} = \mathbf{xTOxlimMatrix} \cdot \vec{x}$$
$$\hat{f}(\vec{x}, \mathbf{xTOxlimMatrix} \cdot \vec{x}) = \vec{f}(\vec{x})$$

- with initialization:

- directly set to good values

$$\overrightarrow{xlim} = \overrightarrow{\text{initGuess}}$$

- with limiting:

- limited based on old values (last successful iteration)

$$\overrightarrow{xlim} = \text{limiting}(\vec{x}, \overrightarrow{xlimOld})$$

Mathematical Expression of init/limiting

init/limiting

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$$\hat{f}(\vec{x}, \overrightarrow{xlim}) = \vec{0}$$

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- We now know $\vec{x} \rightarrow \overrightarrow{xlim}$

- rewrite \hat{f} as

$$\tilde{f}(\vec{x}) = \hat{f}(\vec{x}, \overrightarrow{xlim}(\vec{x}))$$

- without init/limiting:

$$\tilde{f}(\vec{x}) = \vec{f}(\vec{x})$$

- with init/limiting:

- e.g. with limiting

$$\hat{f}(\vec{x}, \text{limiting}(\vec{x}, \overrightarrow{xlimOld})) = \tilde{f}(\vec{x}, \overrightarrow{xlimOld})$$

- \tilde{f} : a function of \vec{x} and parameters $\overrightarrow{xlimOld}/\overrightarrow{initGuess}$
- converges to the same solution
- has better numerical properties

Mathematical Expression of init/limiting

init/limiting

$$\vec{\tilde{f}}(\vec{x}) = \hat{\vec{f}}(\vec{x}, \overrightarrow{xlim}) = \vec{0}$$

This is the equation we are actually solving

What's J

- $\hat{\vec{f}}$ has multiple derivatives: $\frac{\partial \hat{\vec{f}}}{\partial \vec{x}}$ and $\frac{\partial \hat{\vec{f}}}{\partial \overrightarrow{xlim}}$.

- without init/limiting

$$\frac{d\vec{\tilde{f}}}{d\vec{x}} = \frac{\partial \hat{\vec{f}}}{\partial \vec{x}} + \frac{\partial \hat{\vec{f}}}{\partial \overrightarrow{xlim}} \cdot \mathbf{xTOxlimMatrix} = \frac{d\vec{f}}{d\vec{x}}$$

- with init/limiting

- what's **J** depends on your perspective

- treat \overrightarrow{xlim} as a function of \vec{x}

$$\frac{d\vec{\tilde{f}}}{d\vec{x}} = \frac{\partial \hat{\vec{f}}}{\partial \vec{x}} + \frac{\partial \hat{\vec{f}}}{\partial \overrightarrow{xlim}} \cdot \frac{d\overrightarrow{xlim}}{d\vec{x}}$$

- treat \overrightarrow{xlim} as extra variables ?

- same as no limiting scenario but $\neq \frac{d\vec{f}}{d\vec{x}}$

- SPICE/Xyce's **J**

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- don't want to bother device/DAE users with RHS

- construct RHS from $\hat{f}(\vec{x}, \overrightarrow{xlim})$

$$\text{RHS} = \left(\frac{\partial \hat{f}}{\partial \vec{x}} \cdot \vec{x} + \frac{\partial \hat{f}}{\partial \overrightarrow{xlim}} \cdot \overrightarrow{xlim} \right) - \hat{f}$$

- calculate **J** from $\hat{f}(\vec{x}, \overrightarrow{xlim})$

$$\mathbf{J} = \frac{\partial \hat{f}}{\partial \vec{x}} + \frac{\partial \hat{f}}{\partial \overrightarrow{xlim}} \cdot \mathbf{xTOxlimMatrix}$$

- RHS moved from devices to analyses
 - no MOD.RHS or DAE.RHS in MAPP
 - analyses can construct SPICE-compatible RHS when needed

Compatibility with Xyce

J in Xyce

$$\mathbf{J} = \frac{\partial \hat{\mathbf{f}}}{\partial \vec{x}} + \frac{\partial \hat{\mathbf{f}}}{\partial \overrightarrow{xlim}} \cdot \mathbf{xTOxlimMatrix}$$

same as in SPICE

limiting correction in Xyce

$$\begin{aligned}\vec{x}_{k+1} - \vec{x}_k &= \Delta \vec{x}_k^{(total)} = \Delta \vec{x}_k^{(newton)} + \Delta \vec{x}_k^{(correction)} \\ &= -\mathbf{J}(\vec{x}_k, \overrightarrow{xlim}_k) \setminus \hat{\mathbf{f}}(\vec{x}_k, \overrightarrow{xlim}_k) + \mathbf{J}(\vec{x}_k) \setminus \left[\frac{\partial \hat{\mathbf{f}}}{\partial \overrightarrow{xlim}} \bigg|_{\vec{x}_k, \overrightarrow{xlim}_k} \cdot (\overrightarrow{xlim}_k - \mathbf{xTOxlimMatrix} \cdot \vec{x}_k) \right] \\ &= -\mathbf{J}(\vec{x}_k, \overrightarrow{xlim}_k) \setminus \left[\hat{\mathbf{f}}(\vec{x}_k, \overrightarrow{xlim}_k) + \frac{\partial \hat{\mathbf{f}}}{\partial \overrightarrow{xlim}} \bigg|_{\vec{x}_k, \overrightarrow{xlim}_k} \cdot (\overrightarrow{xlim}_k - \mathbf{xTOxlimMatrix} \cdot \vec{x}_k) \right]\end{aligned}$$

Evidence of Limiting Correction in Other Simulators

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"If limiting occurs, the simulator may need to apply a so-called limiting correction to correct for the fact that currents were not computed at the voltages requested by the simulator. (SPICE itself does not use the currents directly because it solves for the new voltage vector instead of the voltage increment; the so-called SPICE right-hand side does not need a limiting correction.) When correcting a current for limiting, one adds to the current a term composed of [the derivative of the current with respect to the limited voltage multiplied by the difference between the voltage requested by the simulator and that used in the equations.](#)" ¹

"The proposed syntax was tested in [the internal circuit simulator at Analog Devices, ...](#)"

¹Lemaitre, Laurent, et al. "Extensions to Verilog-A to support compact device modeling." Behavioral Modeling and Simulation, 2003. BMAS 2003. Proceedings of the 2003 International Workshop on. IEEE, 2003.

Xyce's Compatibility with SPICE

Why limiting correction is equivalent to SPICE's RHS?
limiting correction:

$$\vec{x}_{k+1} - \vec{x}_k = \Delta \vec{x}_k^{(total)} = \Delta \vec{x}_k^{(newton)} + \Delta \vec{x}_k^{(correction)}$$

$$= -\mathbf{J}(\vec{x}_k, \overrightarrow{xlim_k}) \setminus \left[\hat{\vec{f}}(\vec{x}_k, \overrightarrow{xlim_k}) + \frac{\partial \hat{\vec{f}}}{\partial \overrightarrow{xlim}} \bigg|_{\vec{x}_k, \overrightarrow{xlim_k}} \cdot (\overrightarrow{xlim_k} - \mathbf{xTOxlimMatrix} \cdot \vec{x}_k) \right]$$

$$\Rightarrow \mathbf{J}(\vec{x}_k, \overrightarrow{xlim_k}) \cdot \vec{x}_{k+1} - \left(\frac{\partial \hat{\vec{f}}}{\partial \vec{x}} \bigg|_{\vec{x}_k, \overrightarrow{xlim_k}} + \frac{\partial \hat{\vec{f}}}{\partial \overrightarrow{xlim}} \bigg|_{\vec{x}_k, \overrightarrow{xlim_k}} \cdot \mathbf{xTOxlimMatrix} \right) \cdot \vec{x}_k$$

$$= \mathbf{J}(\vec{x}_k, \overrightarrow{xlim_k}) \cdot (\vec{x}_{k+1} - \vec{x}_k)$$

$$= -\vec{f}(\vec{x}_k, \overrightarrow{xlim_k}) + \left[\frac{\partial \hat{\vec{f}}}{\partial \overrightarrow{xlim}} \bigg|_{\vec{x}_k, \overrightarrow{xlim_k}} \cdot (\overrightarrow{xlim_k} - \mathbf{xTOxlimMatrix} \cdot \vec{x}_k) \right]$$

$$\begin{aligned} \Rightarrow \mathbf{J}(\vec{x}_k, \overrightarrow{xlim_k}) \cdot \vec{x}_{k+1} &= \frac{\partial \hat{\vec{f}}}{\partial \vec{x}} \bigg|_{\vec{x}_k, \overrightarrow{xlim_k}} \cdot \vec{x}_k + \frac{\partial \hat{\vec{f}}}{\partial \overrightarrow{xlim}} \bigg|_{\vec{x}_k, \overrightarrow{xlim_k}} \cdot \overrightarrow{xlim_k} - \hat{\vec{f}}(\vec{x}_k, \overrightarrow{xlim_k}) \\ &= \text{RHS}(\vec{x}_k, \overrightarrow{xlim_k}) \end{aligned}$$

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TODOs

Conclusions

- clean up new versions of ModSpec/DAEAPI and documentation
- xTOxlim or xuTOxlim?
 - debate over the role of u especially in hand-coded DAEs
e.g. allow $q(x, u)$?
- efficiency considerations:
 - e.g. $df = dfdx + dfdxlim * dxlimdx$
potentially calls vecvalder 3 times
(not always, when $xlim = []$, vecvalder is by-passed)
- more thoughts on AFobj
 - why we have it in the first place

Conclusions

init/limiting

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Background

NR method
the need for
init/limiting
previous
implementations
in MAPP
in SPICE
in Xyce
challenges

Math behind
init/limiting

formula
equiv w/ SPICE
equiv w/ Xyce

Implementation
in MAPP

ModSpec level
DAE level
Analysis level
TODOs

Conclusions

- previous implementations of init/limiting
 - in MAPP, SPICE and Xyce
 - their drawbacks
- mathematical formula for init/limiting
 - how to replicate SPICE/Xyce's implementation with it
- implementation in MAPP
 - at ModSpec/DAE/Analyses level
 - how to update your existing ModSpec/DAE/Analyses with init/limiting