

EDA Challenges in Oscillator-based Boolean Computation

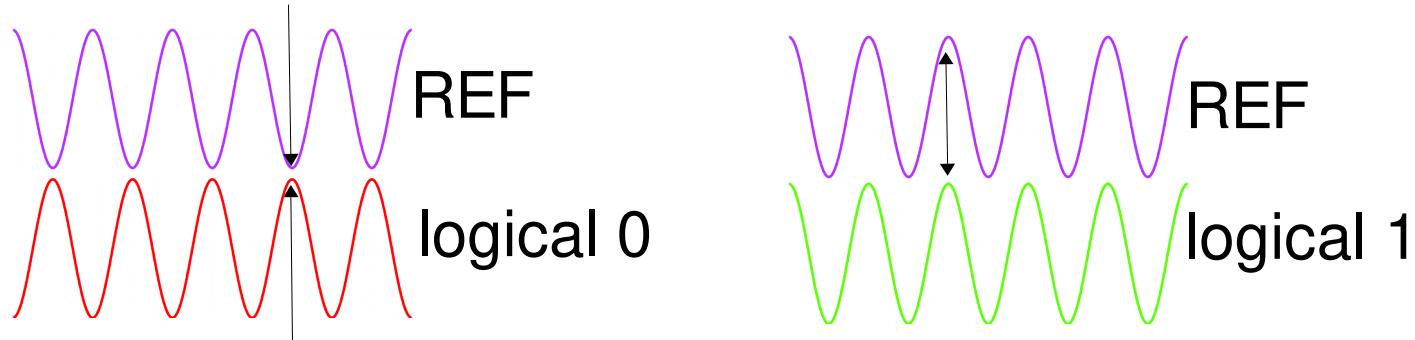
Tianshi Wang

Jaijeet Roychowdhury

Department of Electrical Engineering and Computer Sciences
University of California, Berkeley



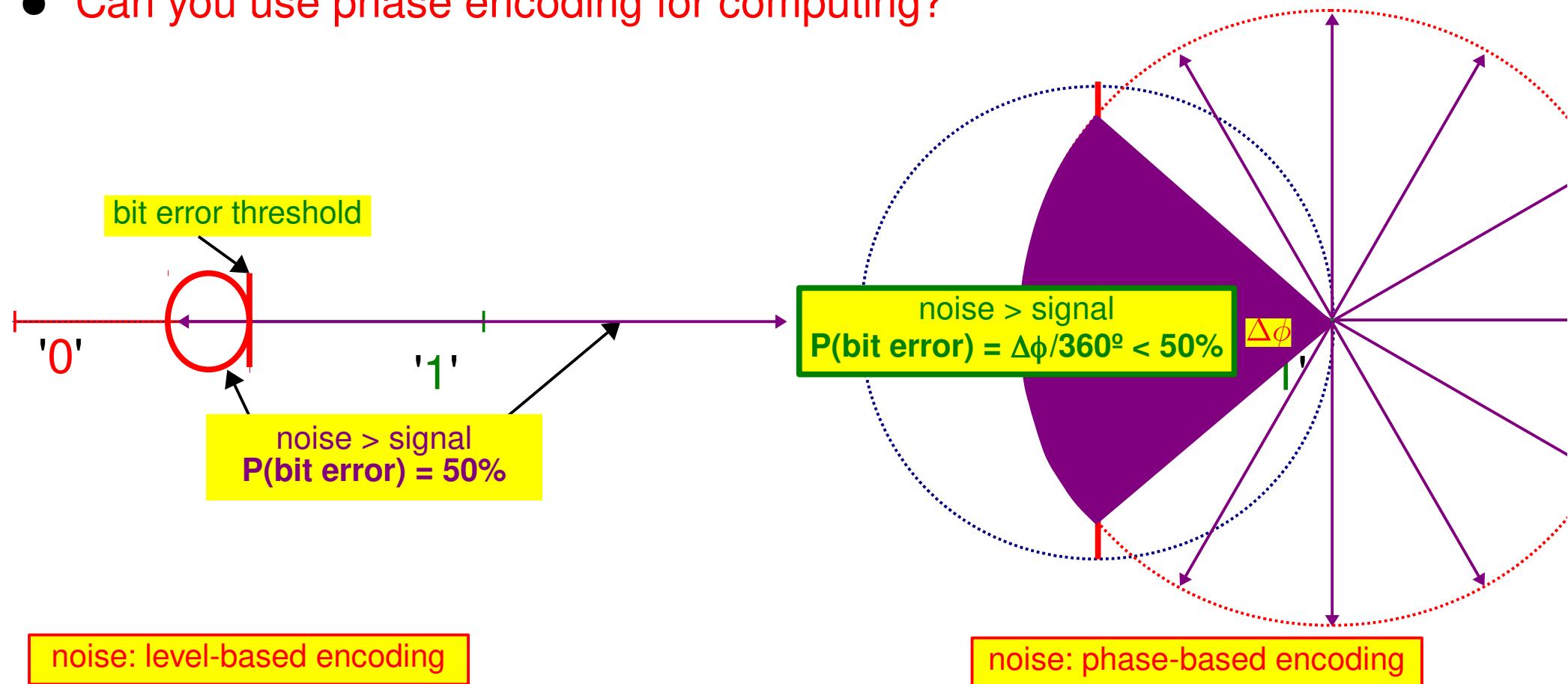
Encoding Bits Using Phase



- Can you use this for computing?
- Even if you can: what is the advantage?

Superior Noise Immunity

- loose analogy: PM/FM vs AM in radio
- **Same reason why the BER of BPSK is superior to that of BASK**
- Can you use phase encoding for computing?



Phase Logic Computers

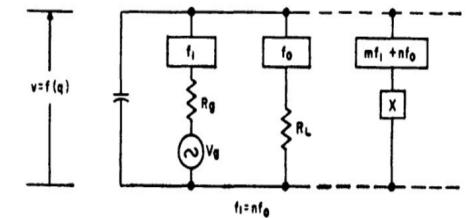
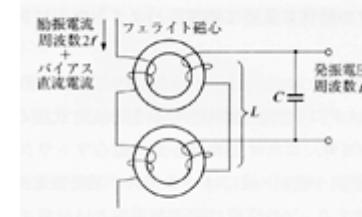
Eiichi Goto, John von Neumann, 1950s and 60s

- “cheap and reliable”
 - » “widely used in Japan”
- not easy to miniaturise
 - » inductors, iron cores
 - » transistors/ICs dominated
 - level-based logic



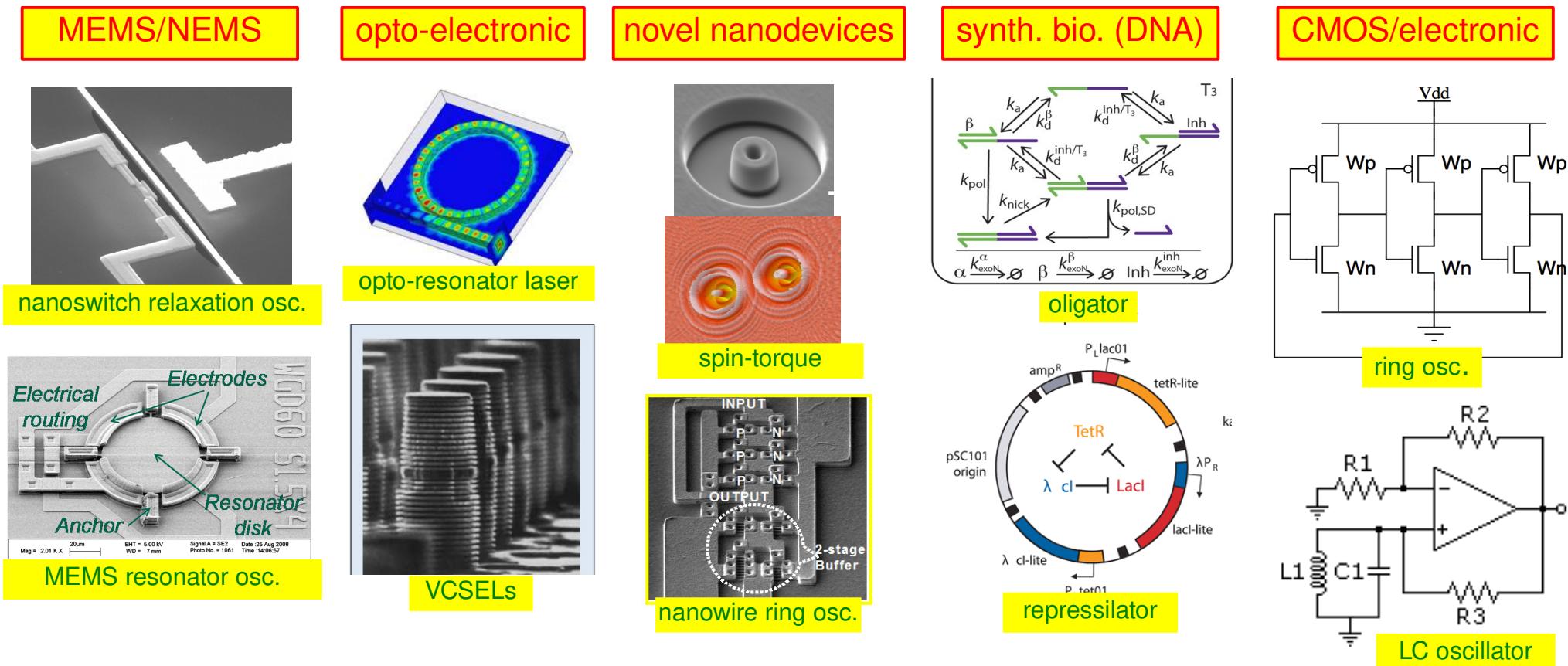
Oi Electric
Parametron X-8-01, 1964
Ferro-Electronic Calculator

Phase Based Logic:
underlying circuitry/components
have been **difficult to miniaturise**
or **impractical for integration**



New Result: (almost) Any Oscillator will Do

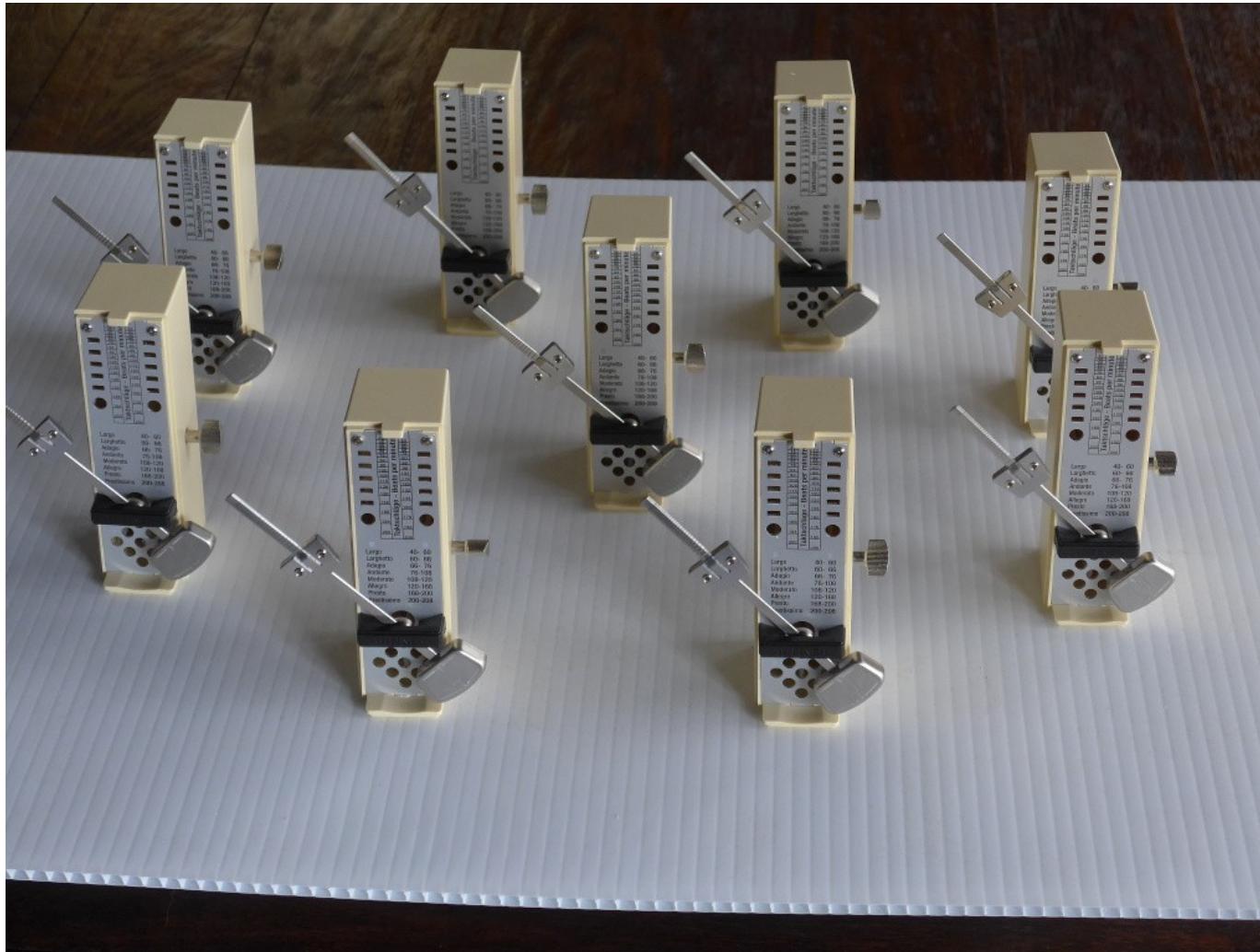
details: Wang/Roychowdhury, "PHLOGON: Phase-based LOGic using Oscillatory Nano-systems". UCNC, 2014.
 Roychowdhury, "Boolean Computation Using Self-Sustaining Nonlinear Oscillators". arXiv, 2014.



many are integrable and nano-scale

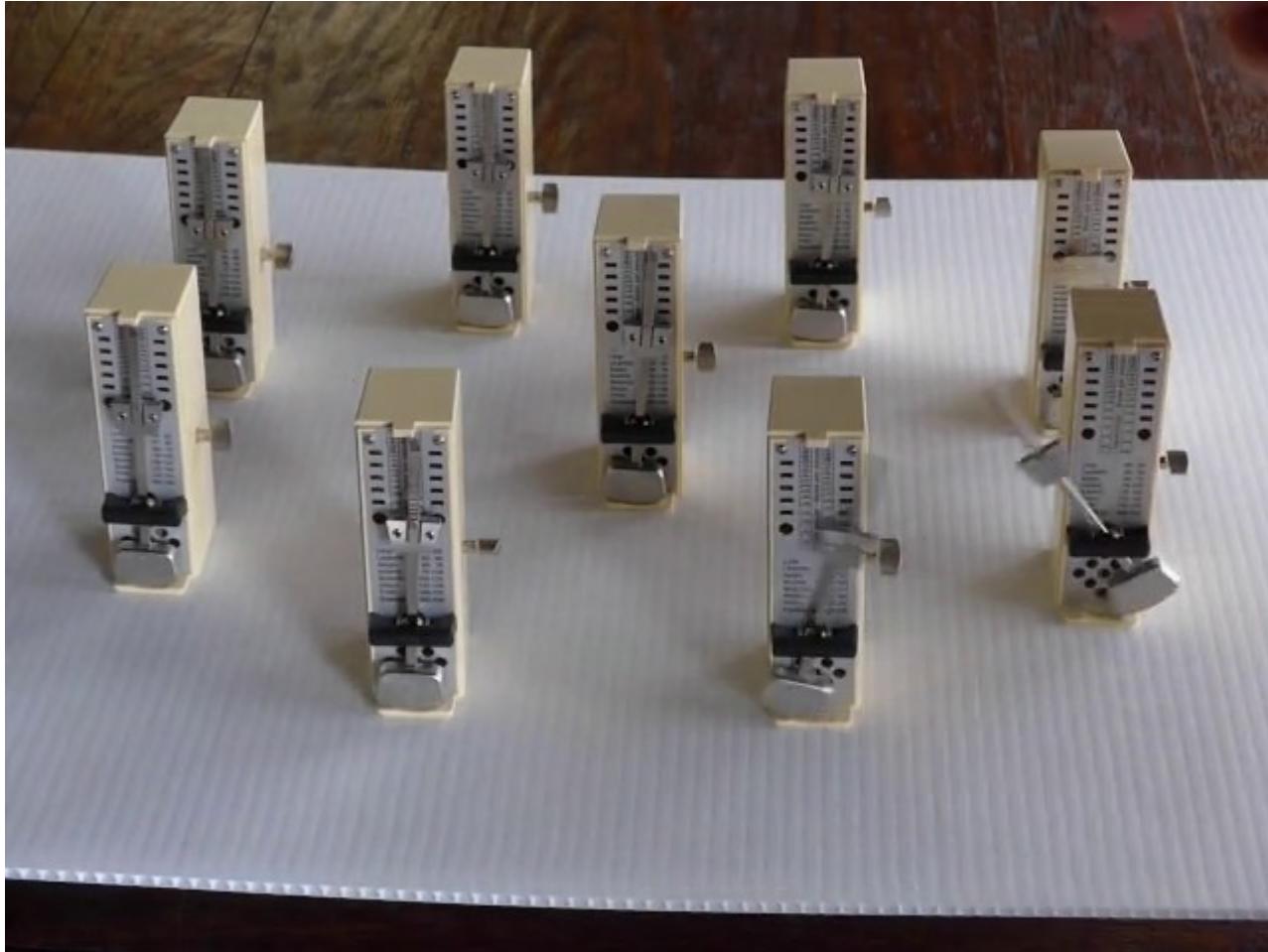
Underlying Mechanism: Injection Locking

- Oscillators can synchronize in phase/frequency



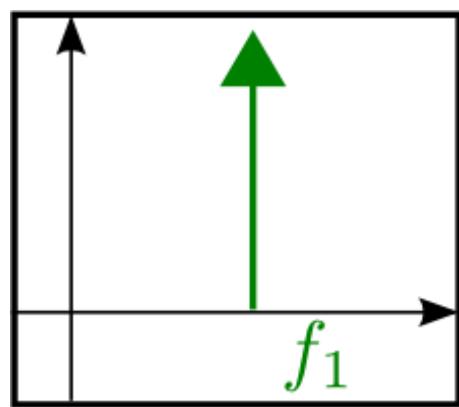
- we use a variant: sub-harmonic injection locking**

- details:** Neogy/Roychowdhury, "Analysis and design of sub-harmonically injection locked

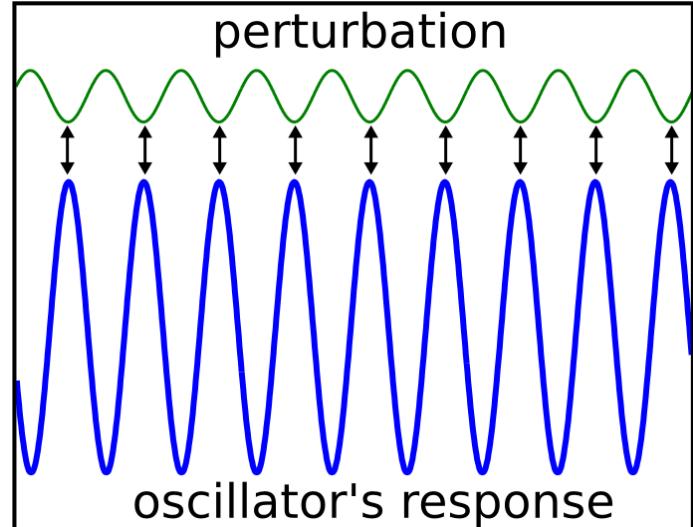


Underlying Mechanism: Injection Locking

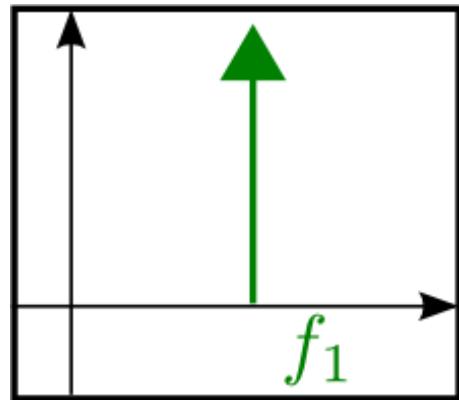
Injection Locking



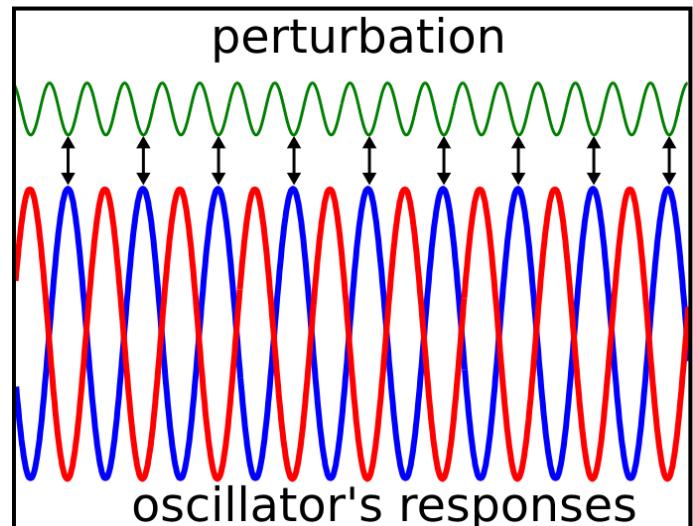
phase lock



Sub-harmonic Injection Locking (SHIL)

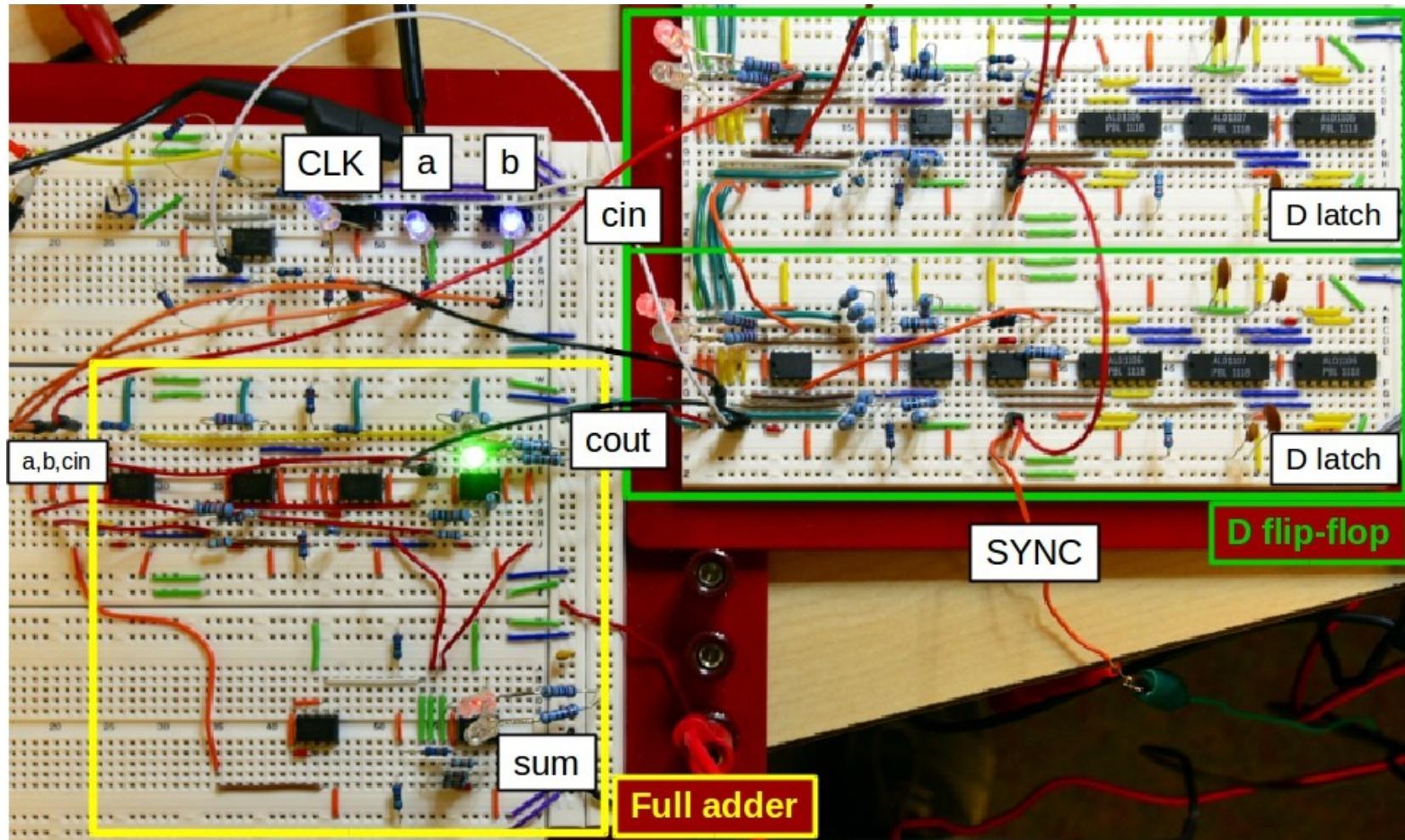


lock 1
180°
phase
shift
lock 2

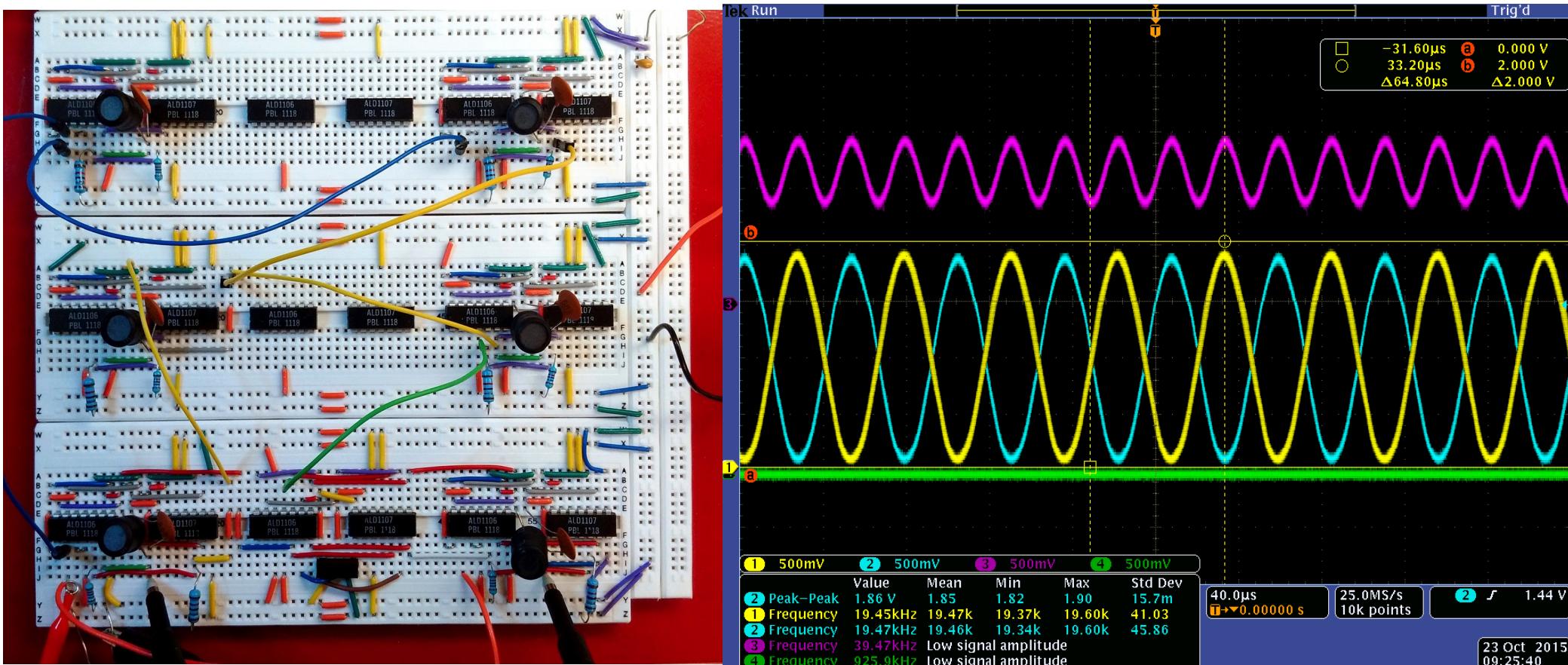


First Phase Logic FSM with Oscillators

- **PHLOGON: PHase LOGic using Oscillatory Nanosystems** using CMOS ring oscillators



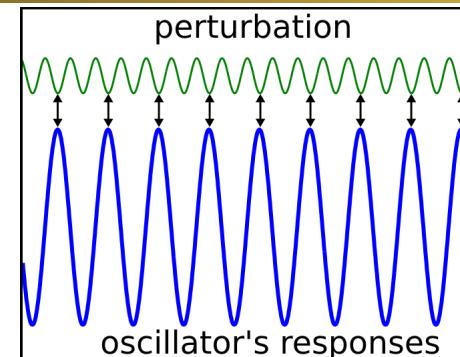
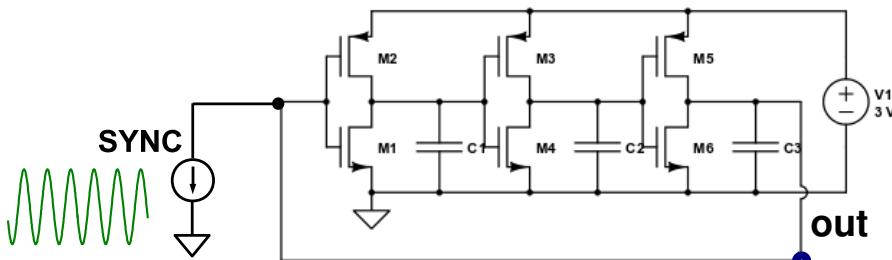
Prototype with CMOS LC Oscillators



What Next?

- **the idea** – oscillator-based Boolean computation
 - **the mechanism** – SHIL
 - **the motivation** – potential noise, energy advantages
 - **the “proof-of-concept” prototypes**
 - **novel “substrates” for computing**
 - **physical design**
- 
- challenges in
modelling & simulation

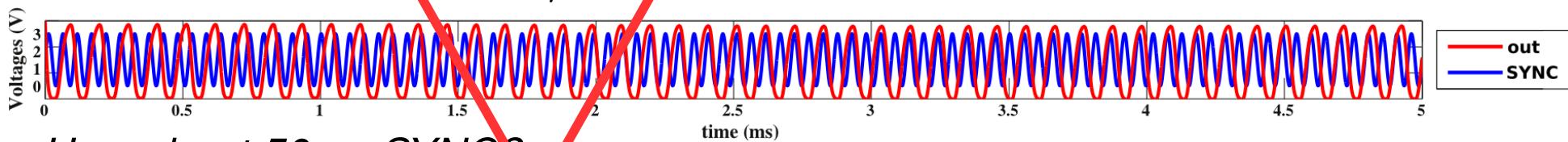
Simulating SHIL of Oscillators



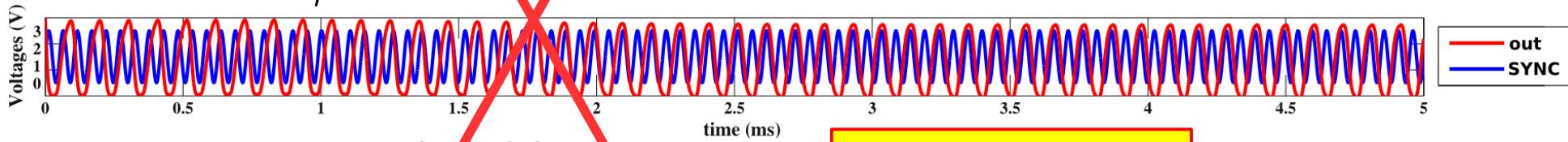
Sub-harmonic
Injection Locking
(SHIL)

Standard SPICE transient simulation

Is SHIL happening with $20 \mu A$ SYNC?

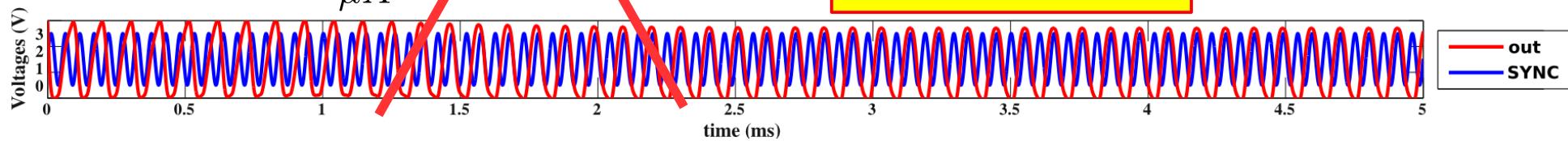


How about $50 \mu A$ SYNC?



How about $100 \mu A$ SYNC?

hard to observe SHIL



inefficient

unbounded error in phase

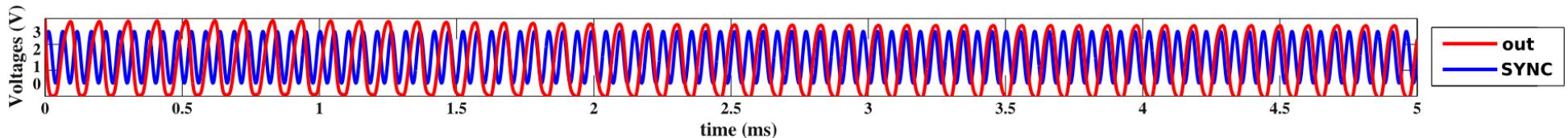
not much insight into design



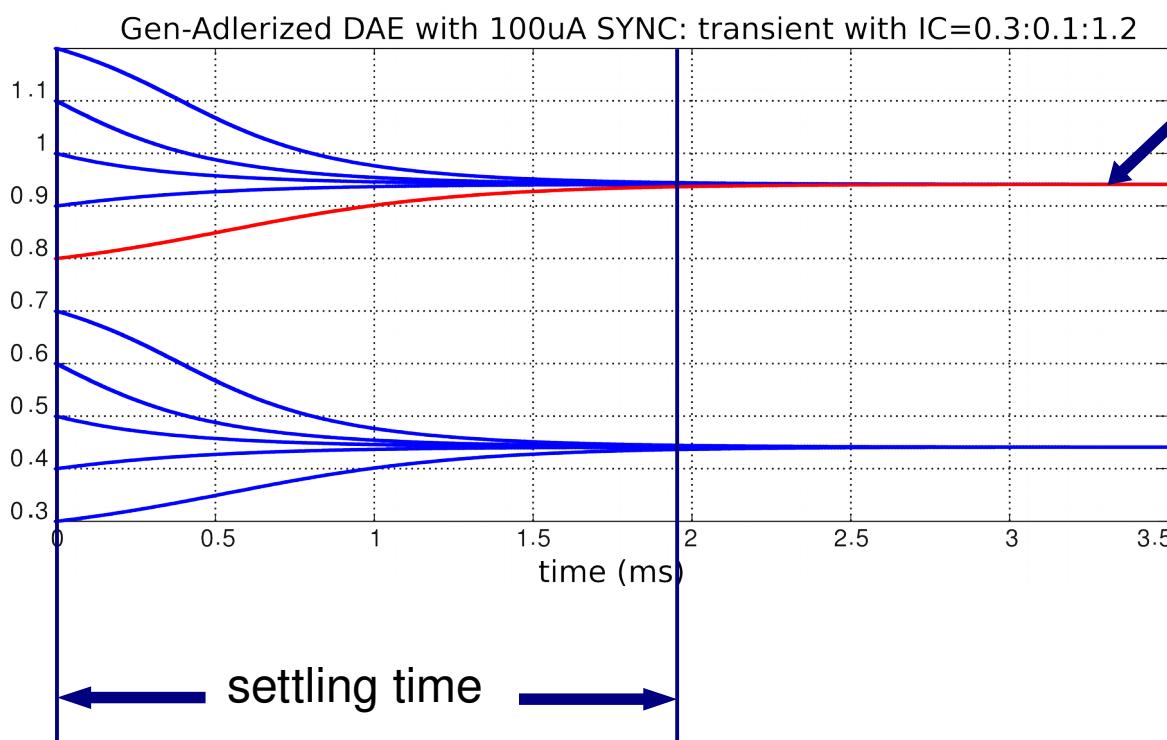
Design tools with phase macromodel analyses

Phase-macromodel-based Analyses

Standard SPICE transient simulation



Phase-based simulation



SHIL occurs: curve “flattens”

“locked phase error”

$$\Delta\phi$$

Generalized Adler's Equation

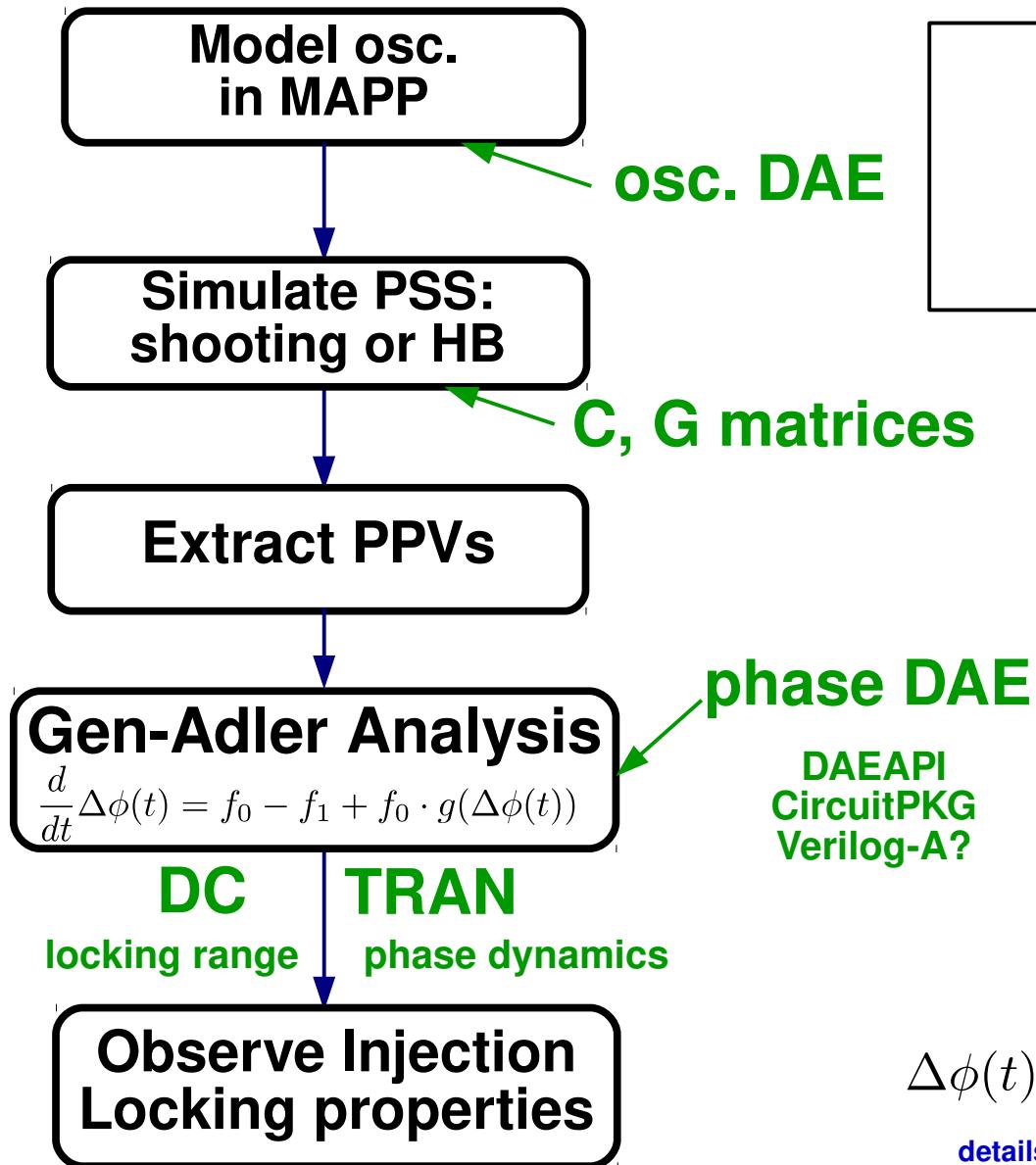
$$\frac{d}{dt}\Delta\phi(t) = f_0 - f_1 + f_0 \cdot g(\Delta\phi(t))$$

$$g(\Delta\phi(t)) = \int_0^1 \vec{v}_1^T(\tau + \Delta\phi(t)) \cdot \vec{b}_1(\tau) d\tau$$

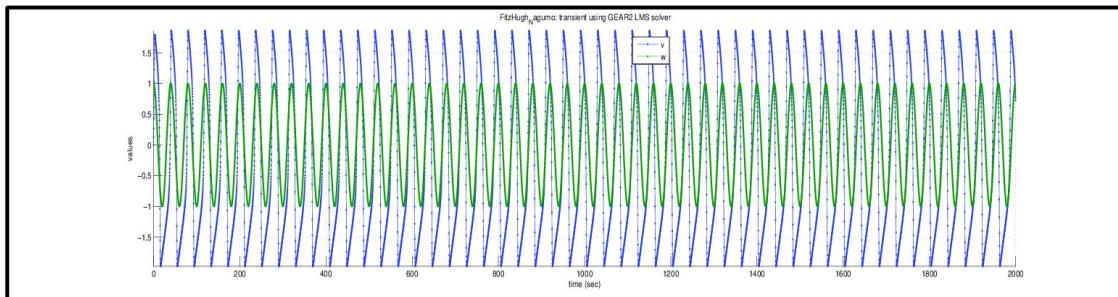
Perturbation Projection Vector (PPV)

details: Bhansali/Roychowdhury, “Gen-Adler: the Generalized Adler's equation for injection locking analysis in oscillators”. Proc. ASPDAC, 2009.

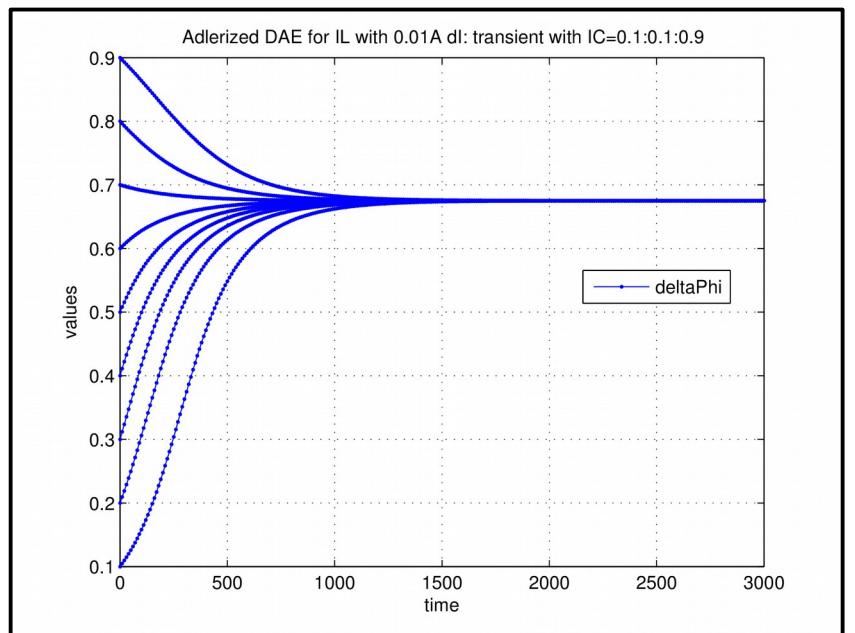
Phase-macromodel-based Analyses



Standard TRAN simulation:



Phase-based TRAN:



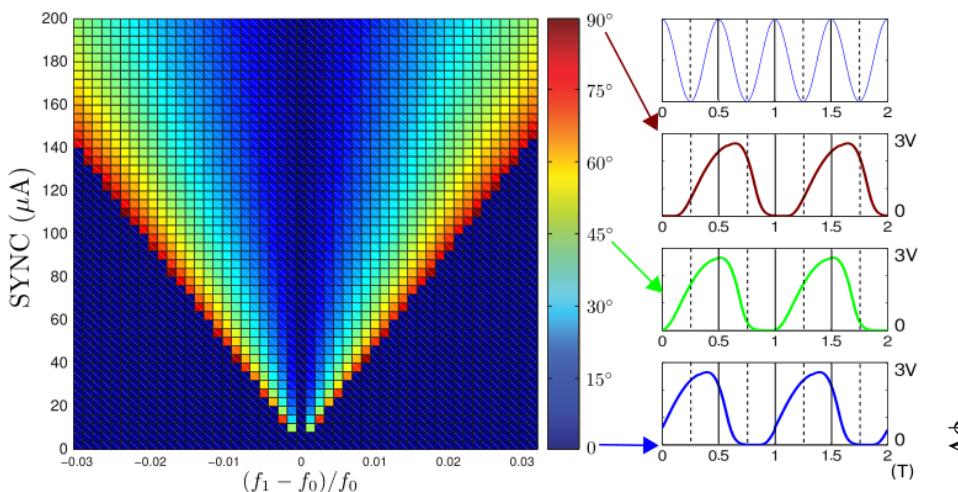
$\Delta\phi(t)$ captures phase response nicely

details: Bhansali/Roychowdhury, “Gen-Adler: the Generalized Adler’s equation for injection locking analysis in oscillators”. Proc. ASPDAC, 2009.

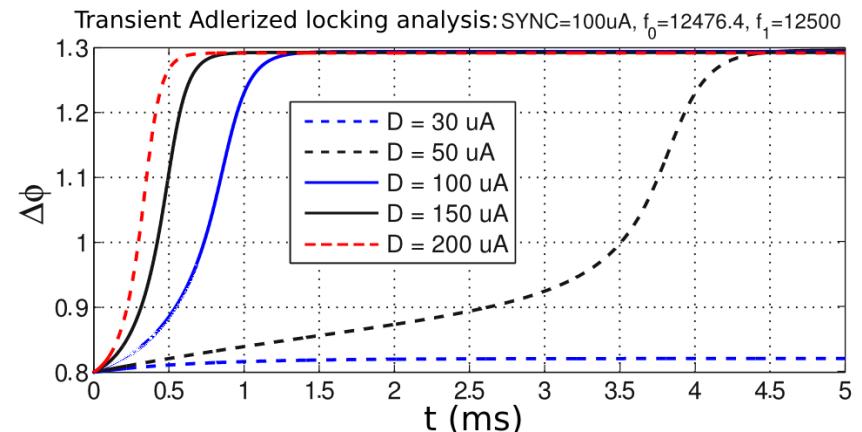
More Capabilities of the Design Tools

details: Wang/Roychowdhury, "Design Tools for Oscillator-based Computing Systems", DAC, 2015.

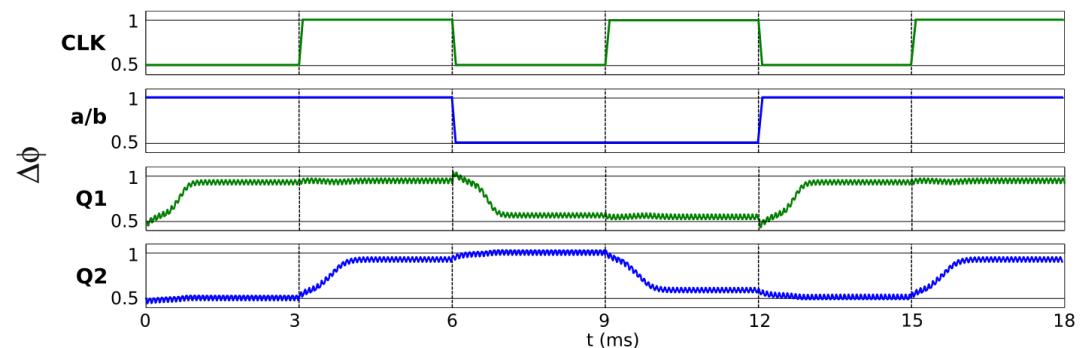
Locked phase error vs.
variations in oscillator
natural frequency



Timing of phase-based D latch



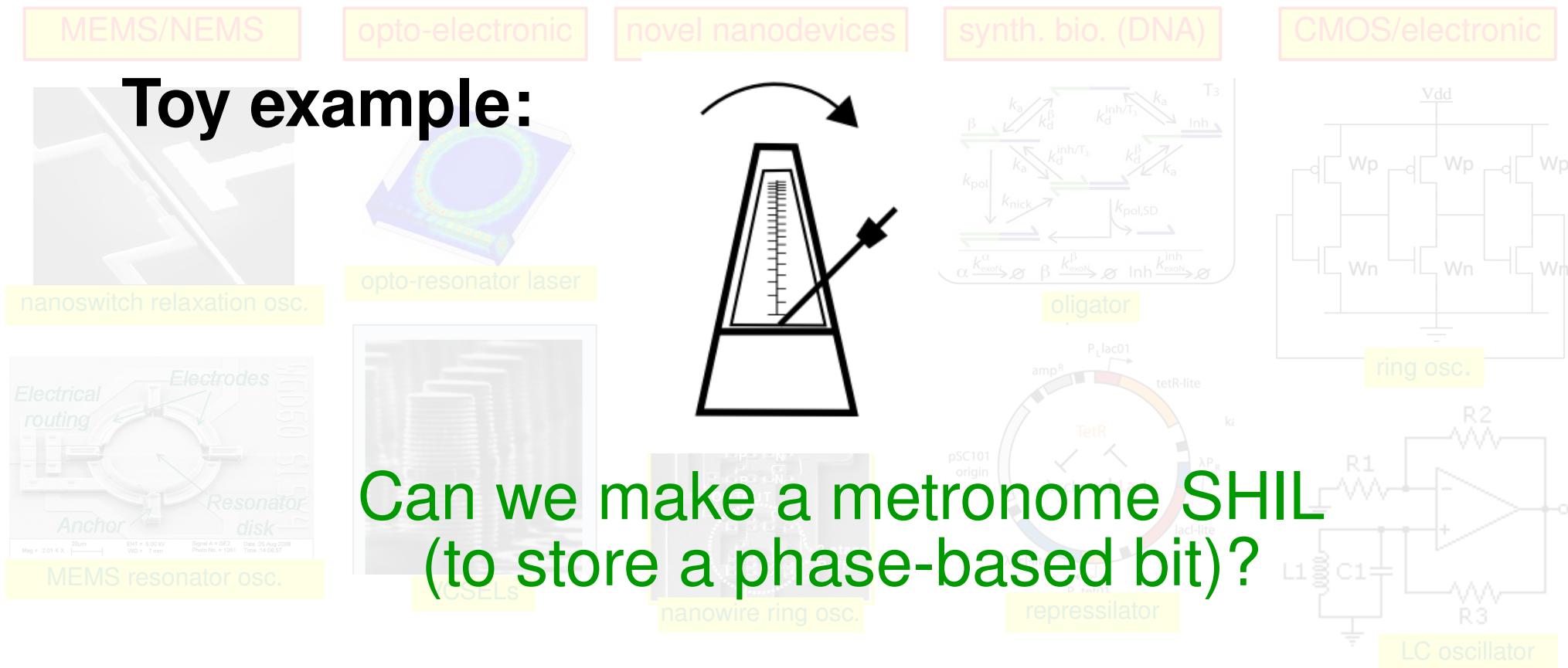
Full system transient
in phase domain



open-source release: PHLOGON.eecs.berkeley.edu

Novel “Substrates” for Computing

- What does it take to explore them?
 - » in simulation at least



Modelling a Metronome

U.S. Patent Jan. 18, 2000 Sheet 3 of 5 6,015,948

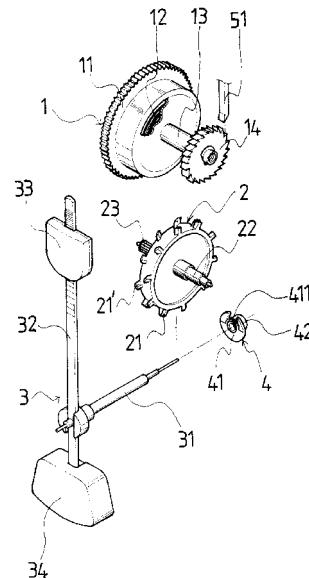
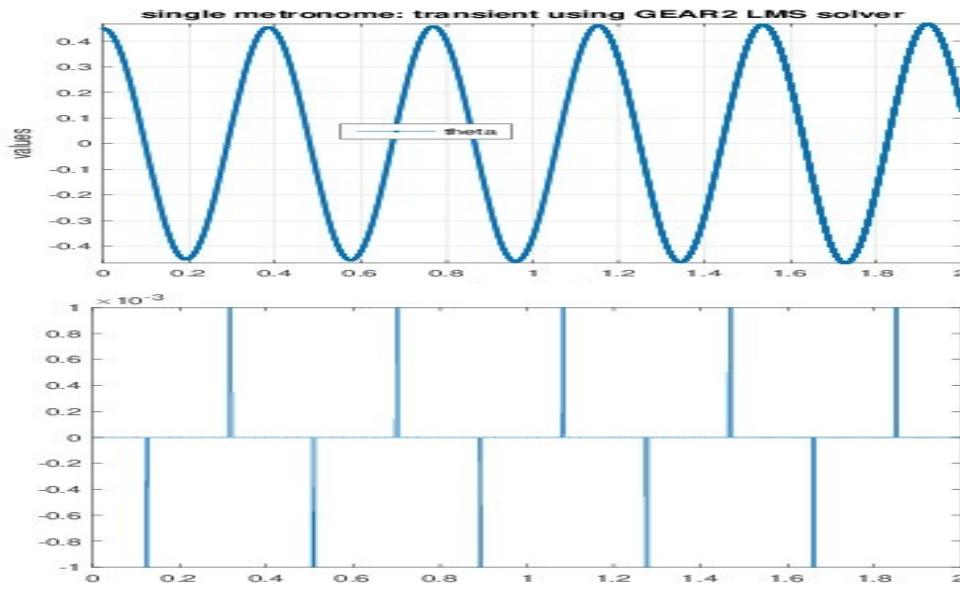


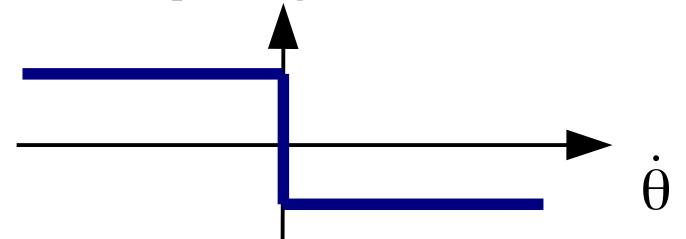
FIG. 4



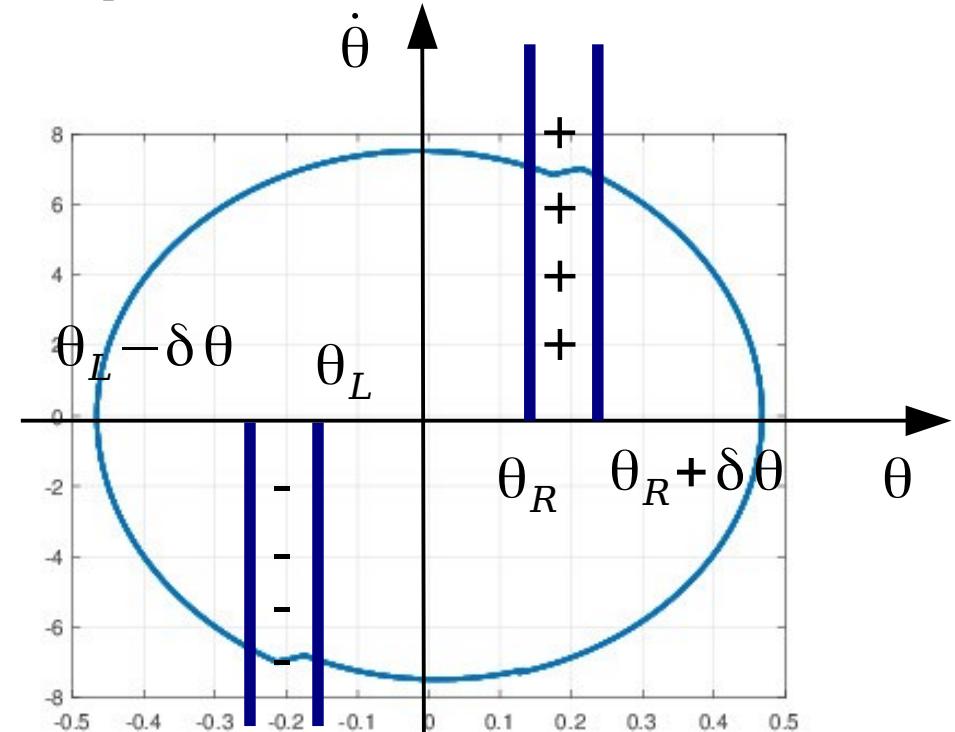
$$\theta \quad \dot{\theta}$$

double-weighted pendulum

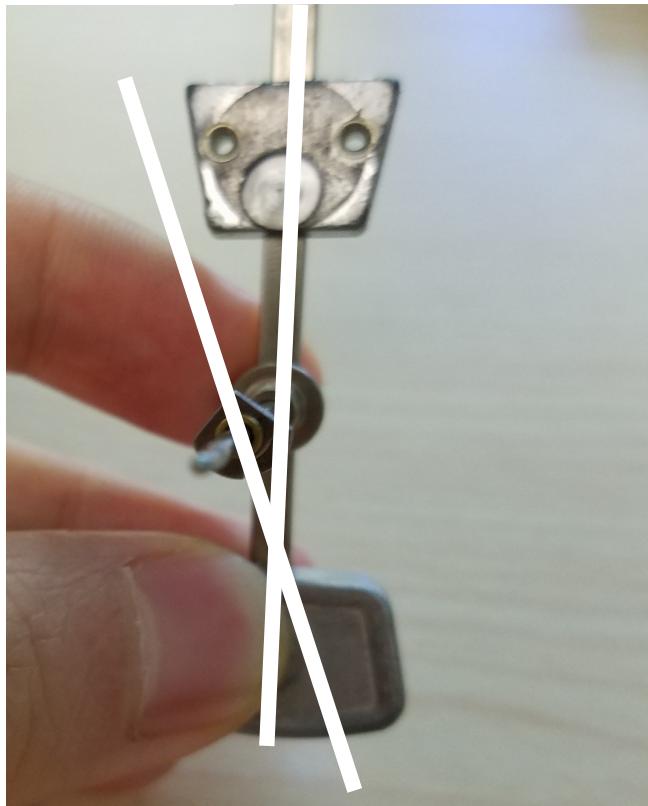
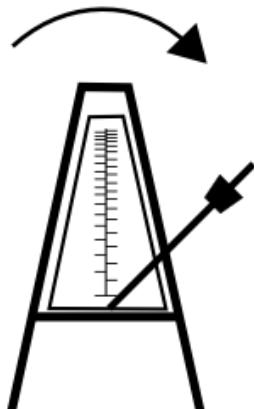
friction damping



escapement mechanism



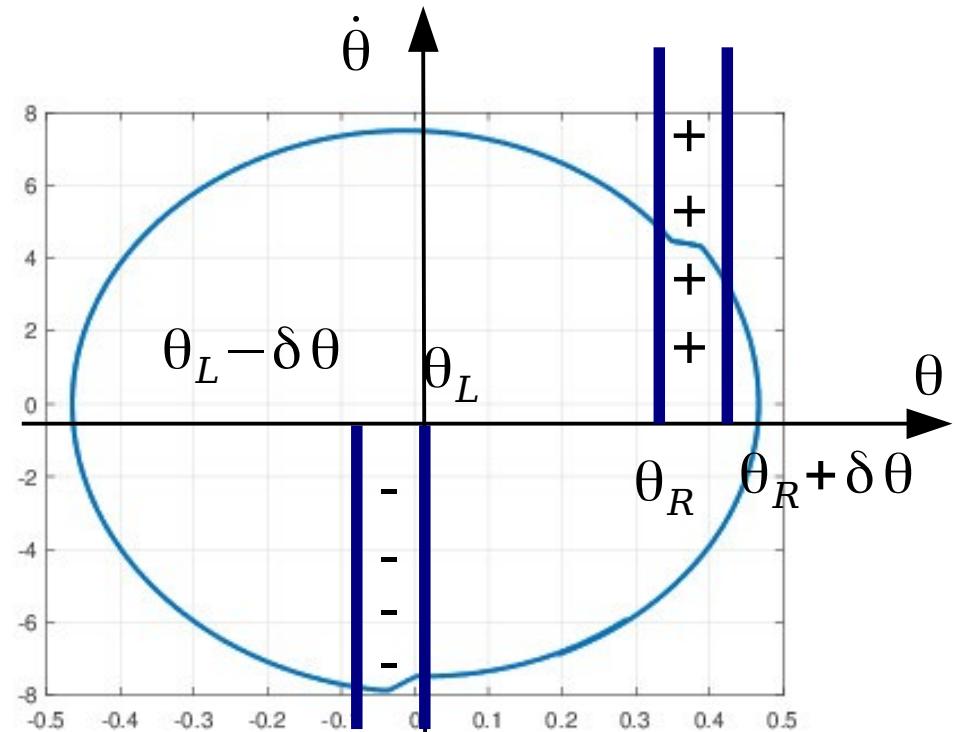
Tweaking a Metronome



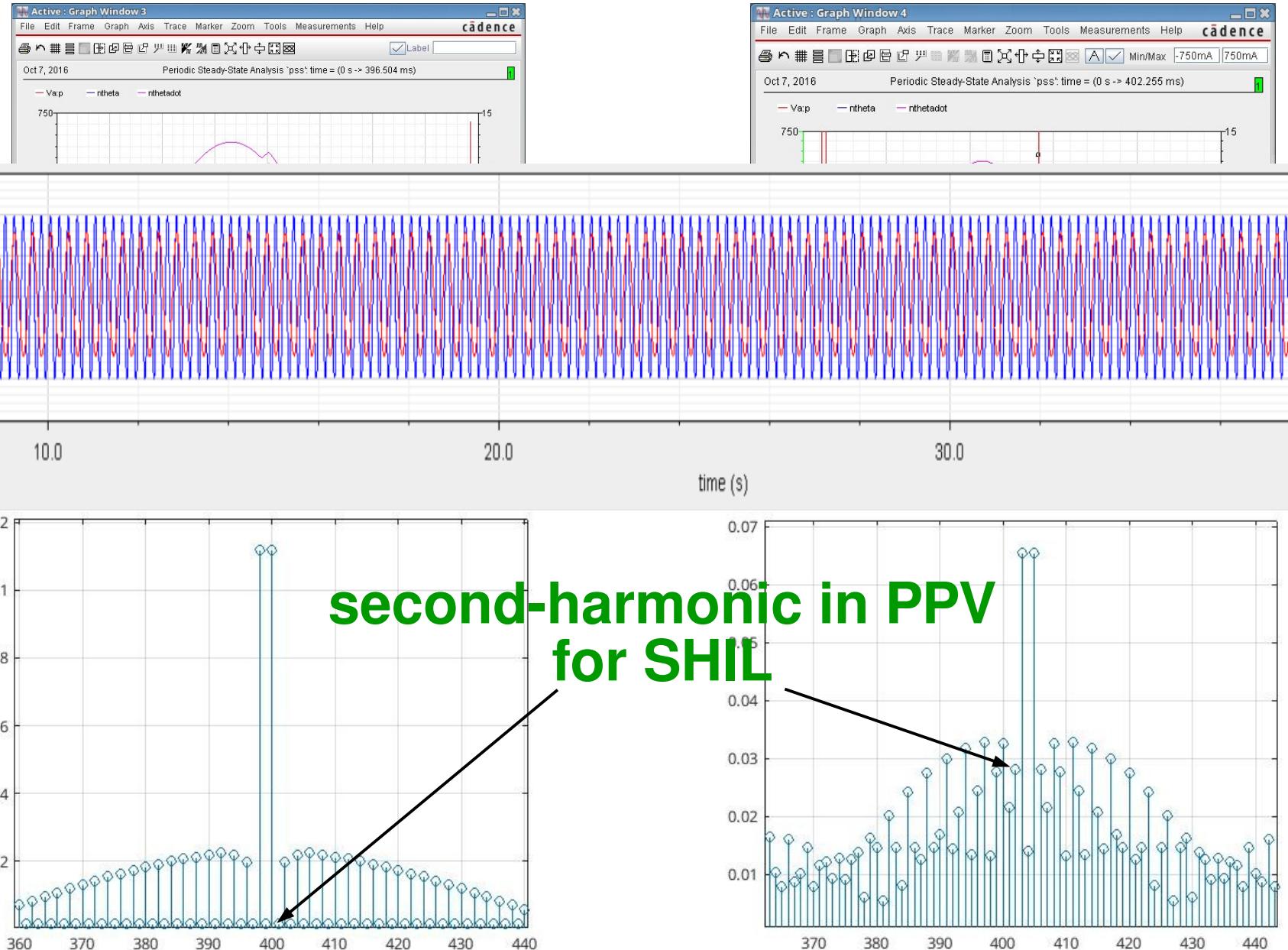
It doesn't SHIL...

no second-harmonic in PPV

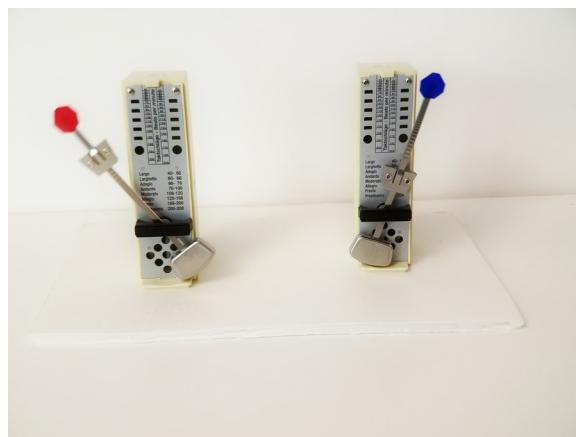
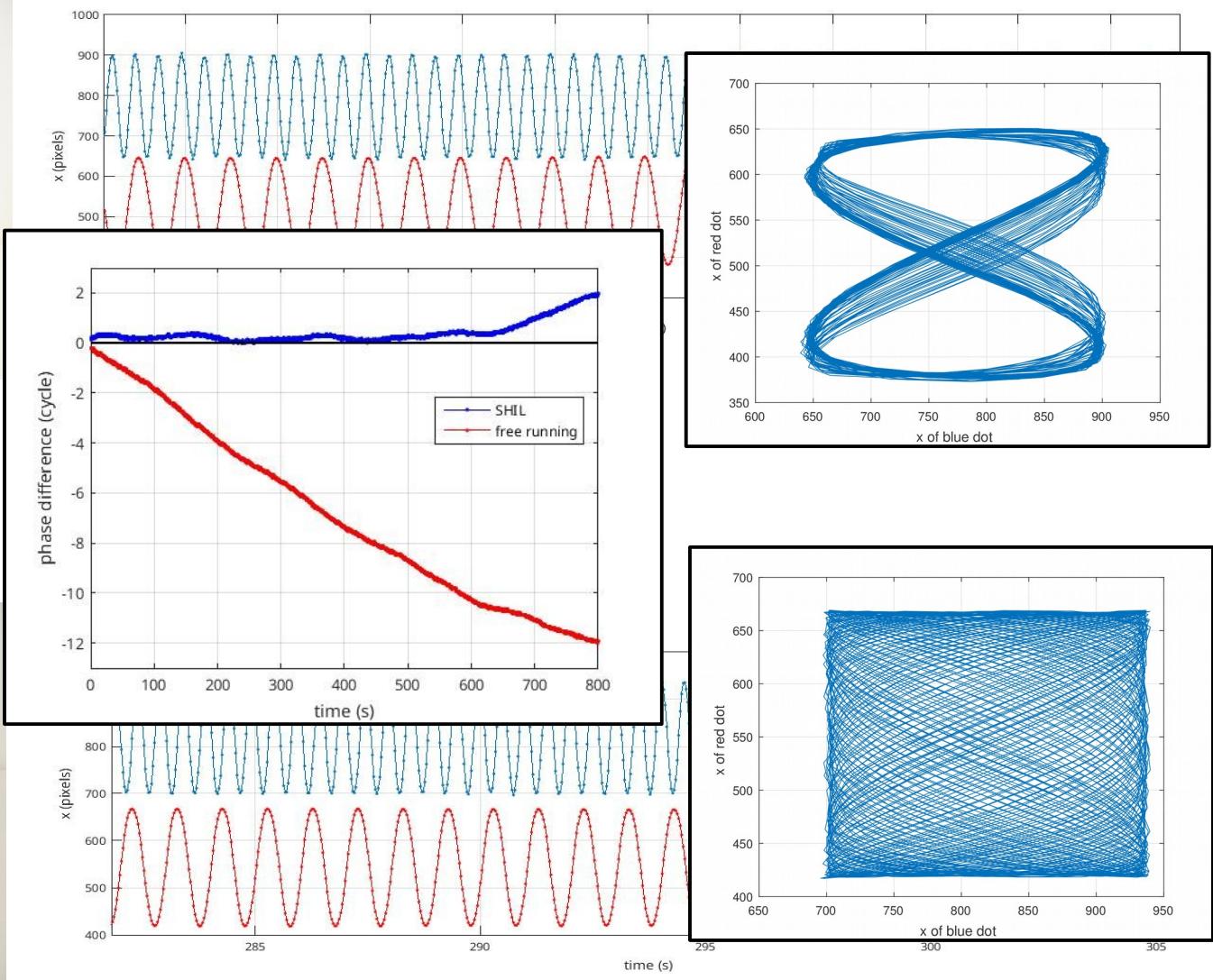
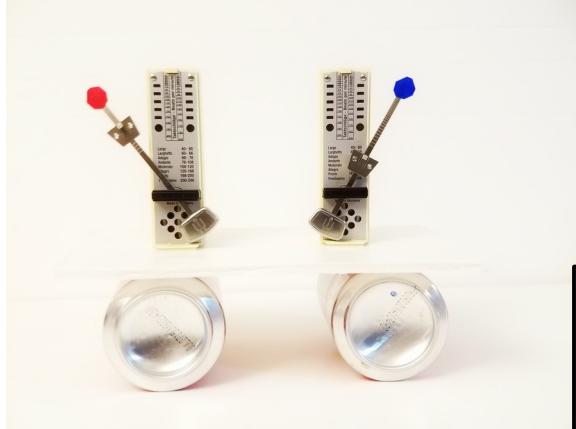
tweak escapement angles



Tweaking a Metronome



SHIL in Metronome

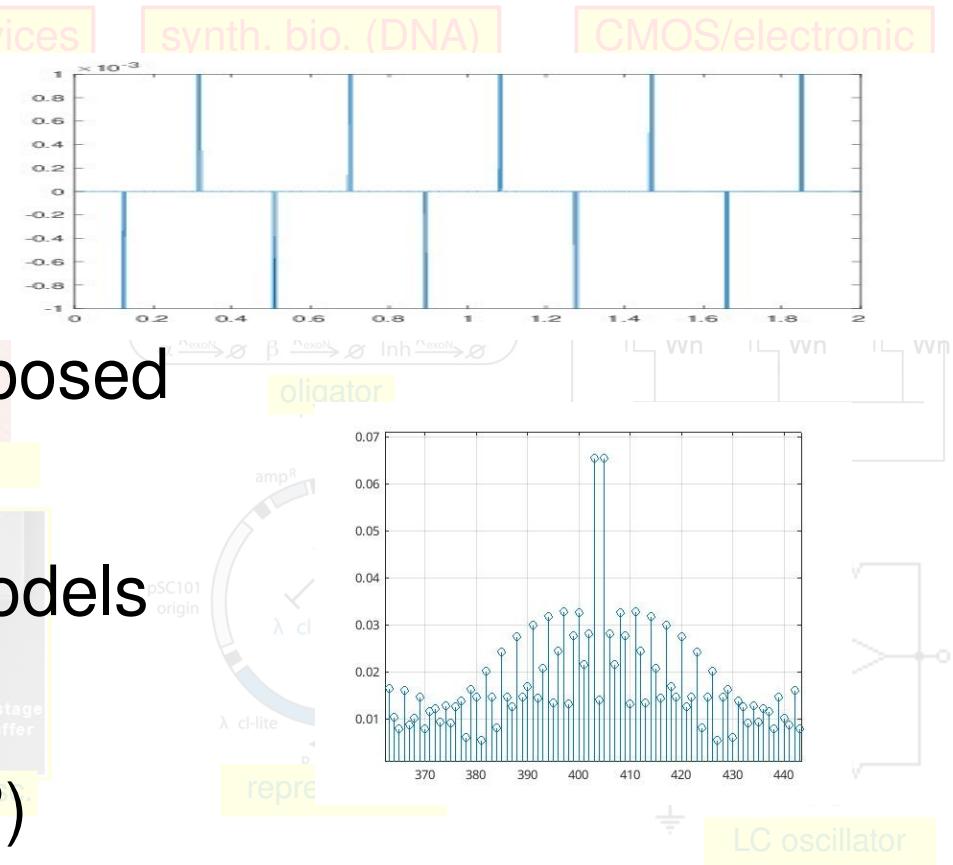


Novel “Substrates” for Computing

- What does it take to explore them?
 - » in simulation at least

- metronome toy example

- » modelling the nonlinearity
- » “simulation-ready”
 - smooth, continuous, well-posed
- » tweak the nonlinearity
 - guided by phase-macromodels
- » system design
 - coupling, flipping the bit (?)



Novel “Substrates” for Computing

- **CMOS oscillators**

- » not novel, but much to be done

- **MEMS oscillators/resonators**

- » Mahboob & Yamaguchi 2011

- » resonate body transistor

- **Spin Torque Nano-oscillators**

- **PCM/RRAM/NCFET relaxation osc.**

- **Optical oscillators/resonators**

- **Biological oscillators**

- » metabolism network, gene regulation, neural network

