

SEMiX® 5

3-Level NPC IGBT-Module

SEMiX305MLI12E4

Features

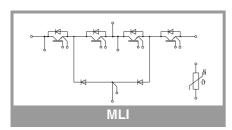
- Solderless assembling solution with PressFIT signal pins and screw power terminals
- IGBT 4 Trench Gate Technology
- V_{CE(sat)} with positive temperature coefficient
- Low inductance case
- Reliable mechanical design with injection moulded terminals and reliable internal connections
- UL recognized file no. E63532
- NTC temperature sensor inside

Remarks*

- Case temperature limited to T_C=125°C max.
- Product reliability results valid for T_j ≤150°C (recommended Tj,op= -40...+150°C)
- IGBT1: outer IGBTs T1 & T4
- IGBT2: inner IGBTs T2 & T3
- Diode1: outer diodes D1 & D4
- Diode2: inner diodes D2 & D3
- Diode5: clamping diodes D5 & D6For storage and case temperature with
- TIM see document "TP(HALA P8)
 SEMiX 5p"

Footnotes

1) Please find further technical information on the SEMIKRON website.



Absolute	Maximum Rating	s		
Symbol	Conditions		Values	Unit
IGBT1				
V _{CES}	T _j = 25 °C		1200	V
Ic		T _c = 25 °C	451	Α
	T _j = 175 °C	T _c = 80 °C	347	Α
I _{Cnom}			300	Α
I _{CRM}	I _{CRM} = 3 x I _{Cnom}		900	Α
V _{GES}			-20 20	V
t _{psc}	$V_{CC} = 800 \text{ V}, V_{GE} \le V_{CES} \le 1200 \text{ V}$	15 V, T _j = 150 °C,	10	μs
Tj			-40 175	°C
IGBT2				
V _{CES}	T _j = 25 °C		1200	V
I _C	T _i = 175 °C	T _c = 25 °C	451	Α
	1, = 173 C	T _c = 80 °C	347	Α
I _{Cnom}			300	Α
I _{CRM}	$I_{CRM} = 3 \times I_{Cnom}$		900	Α
V_{GES}			-20 20	V
t _{psc}	$V_{CC} = 800 \text{ V}, V_{GE} \le V_{CES} \le 1200 \text{ V}$	15 V, T _j = 150 °C,	10	μs
T_j			-40 175	°C
Diode1				
V_{RRM}	T _j = 25 °C		1200	V
I _F	T _ 175 °C	T _c = 25 °C	344	Α
	− T _j = 175 °C	T _c = 80 °C	257	Α
I _{Fnom}			300	Α
I _{FRM}	I _{FRM} = 2 x I _{Fnom}		600	Α
I _{FSM}	10 ms, sin 180°, T _j	= 25 °C	1620	Α
T _j			-40 175	°C
Diode2		<u>.</u>		
V_{RRM}	T _j = 25 °C		1200	V
I _F	T _i = 175 °C	T _c = 25 °C	344	Α
		T _c = 80 °C	257	Α
I _{Fnom}			300	Α
I _{FRM}	I _{FRM} = 2 x I _{Fnom}		600	Α
I _{FSM}	10 ms, sin 180°, T _j	= 25 °C	1620	Α
Tj			-40 175	°C
Diode5				
V_{RRM}	T _j = 25 °C		1200	V
I _F		T _c = 25 °C	344	Α
	T _j = 175 °C	T _c = 80 °C	257	Α
I _{Fnom}		•	300	Α
I _{FRM}	I _{FRM} = 2 x I _{Fnom}		600	Α
I _{FSM}	10 ms, sin 180°, T _j = 25 °C		1620	Α
Tj			-40 175	°C
Module		1		•
I _{t(RMS)}			340	Α
T _{stg}	module without TIN	Л	-40 125	°C
V _{isol}	AC sinus 50Hz, t =	4	4000	V



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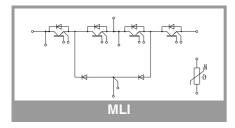
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Characte	ristics					
Symbol	Conditions		min.	typ.	max.	Unit
IGBT1				-71		
V _{CE(sat)}	I _C = 300 A	T _i = 25 °C		1.80	2.05	V
02(041)	V _{GE} = 15 V	T _j = 150 °C		2.20	2.40	V
\ /	chiplevel					
V _{CE0}	chiplevel	T _j = 25 °C T _i = 150 °C		0.80	0.90	V
_	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	$T_i = 150 ^{\circ}\text{C}$ $T_i = 25 ^{\circ}\text{C}$		0.70	0.80	V
r _{CE}	V _{GE} = 15 V chiplevel	$T_i = 25 \text{ C}$ $T_i = 150 \text{ °C}$		3.3 5.0	3.8 5.3	mΩ
$V_{GE(th)}$	$V_{GE} = V_{CE}$, $I_C = 12$ n	,	5	5.8	6.5	mΩ V
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 12$		3	5.0	4	mA
C _{ies}	VGE - 0 V, VCE - 12	f = 1 MHz		18.6		nF
C _{oes}	V _{CE} = 25 V	f = 1 MHz		1.16		nF
C _{res}	$V_{GE} = 0 V$	f = 1 MHz		1.02		nF
Q _G	V _{GE} = - 8 V+ 15 V	1 – 1 1011 12		1700		nC
_	$T_i = 25 ^{\circ}\text{C}$			2.5		Ω
R _{Gint}	$V_{CC} = 600 \text{ V}$	T _i = 150 °C		71		ns
t _{d(on)}	I _C = 300 A	T _i = 150 °C		51		
t _r E _{on}	$V_{GE} = +15/-8 \text{ V}$	T _i = 150 °C		17.4		ns mJ
	$R_{G \text{ on}} = 0.5 \Omega$	T _i = 150 °C		488		
t _{d(off)}	$R_{G \text{ off}} = 1.5 \Omega$ $di/dt_{on} = 5700 \text{ A/}\mu\text{s}$,		148		ns
t _f	$di/dt_{off} = 2300 \text{ A/}\mu\text{s}$	1j = 150 C		140		ns
E _{off}	du/dt = 3500 V/μs	T _j = 150 °C		38.7		mJ
R _{th(j-c)}	per IGBT				0.1	K/W
R _{th(c-s)}	per IGBT (λgrease:	=0.81 W/(m*K))		0.077		K/W
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.037		K/W
IGBT2						
V _{CE(sat)}	I _C = 300 A	T _j = 25 °C		1.80	2.05	V
	V _{GE} = 15 V chiplevel	T _i = 150 °C		2.20	2.40	V
V _{CE0}	criipievei	T _i = 25 °C		0.80	0.90	V
V CE0	chiplevel	T _i = 150 °C		0.70	0.80	V
ros	\/ 15\/	T _i = 25 °C		3.3	3.8	mΩ
r _{CE}	V _{GE} = 15 V chiplevel	T _i = 150 °C		5.0	5.3	mΩ
V _{GE(th)}	$V_{GE} = V_{CE}, I_{C} = 12 \text{ n}$,	5	5.8	6.5	V
_	$V_{GE} = 0 \text{ V}, V_{CE} = 12$		3	3.0	4	mA
I _{CES}	VGE - 0 V, VCE - 12	f = 1 MHz		18.6		nF
C _{oes}	V _{CE} = 25 V	f = 1 MHz		1.16		nF
C _{res}	V _{GE} = 0 V	f = 1 MHz		1.02		nF
	V	1 – 1 1011 12		1700		1
Q _G		V _{GE} = - 8 V+ 15 V				nC
R _{Gint}	$T_j = 25 ^{\circ}\text{C}$ $V_{CC} = 600 ^{\circ}\text{V}$	T 150 °C		2.5		Ω
t _{d(on)}	I _C = 300 A	T _j = 150 °C		116		ns
t _r	$V_{GE} = +15/-8 \text{ V}$	T _j = 150 °C		58		ns
E _{on}	$R_{G \text{ on}} = 0.5 \Omega$	T _j = 150 °C		17.6		mJ
t _{d(off)}	$R_{G \text{ off}} = 1.5 \Omega$	T _j = 150 °C		520		ns
t _f	$di/dt_{on} = 4500 \text{ A/}\mu\text{s}$ $di/dt_{off} = 2100 \text{ A/}\mu\text{s}$			158		ns
E _{off}	$du/dt = 4000 \text{ V/}\mu\text{s}$	T _j = 150 °C		40.6		mJ
R _{th(j-c)}	per IGBT	1			0.1	K/W
R _{th(c-s)}	per IGBT (λgrease:	=0.81 W/(m*K))		0.09		K/W
, ,	per IGBT, pre-appli					
R _{th(c-s)}	material			0.047		K/W



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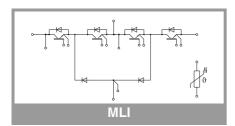
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 ≤150°C (recommended Tj,op=
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- IGBT1: outer IGBTs T1 & T4
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- For storage and case temperature with TIM see document "TP(HALA P8) SEMiX 5p"

Footnotes

1) Please find further technical information on the SEMIKRON website.



Characte	ristics					
Symbol	Conditions		min.	typ.	max.	Unit
Diode1						
$V_F = V_{EC}$	I _F = 300 A	T _j = 25 °C		2.14	2.46	V
	$V_{GE} = 0 V$	T _i = 150 °C		2.07	2.38	V
V _{F0}	chiplevel	T _i = 25 °C		1.30	1.50	V
* FU	chiplevel	T _i = 150 °C		0.90	1.10	V
r _F		T _i = 25 °C		2.8	3.2	mΩ
•	chiplevel	T _i = 150 °C		3.9	4.3	mΩ
I _{RRM}	I _F = 300 A	T _i = 150 °C		306		Α
Q _{rr}	$di/dt_{off} = 4500 \text{ A/}\mu\text{s}$	T _j = 150 °C		46		μC
E _{rr}	$V_{CC} = 600 \text{ V}$ $V_{GE} = +15/-8 \text{ V}$	T _j = 150 °C		22		mJ
R _{th(j-c)}	per diode				0.18	K/W
R _{th(c-s)}	per diode (λgrease	=0.81 W/(m*K))		0.074		K/W
R _{th(c-s)}	per diode, pre-appl material	ied phase change		0.058		K/W
Diode2	•					-11
$V_F = V_{EC}$	I _F = 300 A	T _j = 25 °C		2.14	2.46	V
	V _{GE} = 0 V	T _i = 150 °C		2.07	2.38	V
V_{F0}	chiplevel chiplevel	T _i = 25 °C		1.30	1.50	V
• 10		T _i = 150 °C		0.90	1.10	V
r _F		T _i = 25 °C		2.8	3.2	mΩ
'F	chiplevel	T _i = 150 °C		3.9	4.3	mΩ
I _{RRM}	I _F = 300 A	T: - 150 °C		306		Α
Q _{rr}	di/dt _{off} = 6000 A/μs	T _i = 150 °C		46		μC
E _{rr} 1)	$V_R = 600 \text{ V}$ $V_{GE} = +15/-8 \text{ V}$	T _j = 150 °C		-		mJ
R _{th(j-c)}	per diode				0.18	K/W
R _{th(c-s)}	per diode (λgrease	=0.81 W/(m*K))		0.098		K/W
R _{th(c-s)}	per diode, pre-appl material	ied phase change		0.054		K/W
Diode5						
$V_F = V_{EC}$	I _F = 300 A	T _j = 25 °C		2.14	2.46	V
	chiplevel	T _j = 150 °C		2.07	2.38	V
V _{F0}	chiplevel	T _i = 25 °C		1.30	1.50	V
-10		T _i = 150 °C		0.90	1.10	V
r _F	chiplevel	T _i = 25 °C		2.8	3.2	mΩ
		T _j = 150 °C		3.9	4.3	mΩ
I _{RRM}	I _F = 300 A	T _i = 150 °C		406		Α
Q _{rr}	$di/dt_{off} = 5700 \text{ A/}\mu\text{s}$ $V_R = 600 \text{ V}$	T _j = 150 °C		46		μC
E _{rr}	$V_{GE} = +15/-8 \text{ V}$	T _j = 150 °C		24.2		mJ
R _{th(j-c)}	per diode				0.18	K/W
R _{th(c-s)}	per diode (λgrease=0.81 W/(m*K))			0.109		K/W
R _{th(c-s)}	per diode, pre-applied phase change material			0.081		K/W



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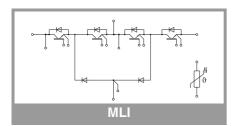
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 For storage and case temperature with TIM see document "TP(HALA P8)

SEMiX 5p" Footnotes

¹⁾ Please find further technical information on the SEMIKRON website.



Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
Module							
L _{sCE1}			27			nΗ	
L _{sCE2}			34			nΗ	
R _{CC'+EE'}	measured between terminal 5 and 1	T _C = 25 °C	0.8			mΩ	
		T _C = 125 °C	1.1			mΩ	
R _{th(c-s)1}	calculated without thermal coupling		0.009			K/W	
R _{th(c-s)2}	including thermal coupling, Ts underneath module (λ _{grease} =0.81 W/ (m*K))		0.014			K/W	
R _{th(c-s)2}	including thermal coupling, Ts underneath module, pre-applied phase change material		0.008			K/W	
Ms	to heat sink (M5)		3		6	Nm	
M _t		to terminals (M6)	3		6	Nm	
						Nm	
w				398		g	
Temperat	ure Sensor						
R ₁₀₀	T _c =100°C (R ₂₅ =5 kΩ)		493 ± 5%			Ω	
B _{100/125}	R _(T) =R ₁₀₀ exp[B _{100/125} (1/T-1/T ₁₀₀)]; T[K];		3550 ±2%			К	

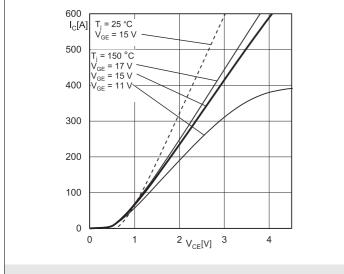


Fig. 1: Typ. IGBT1 output characteristic, incl. $R_{\text{CC}'+\;\text{EE}'}$

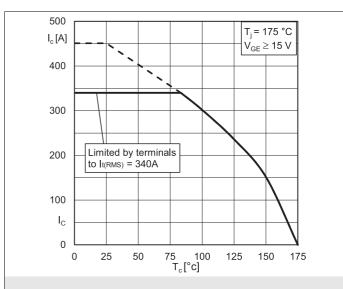


Fig. 2: IGBT1 rated current vs. Temperature I_c=f(T_c)

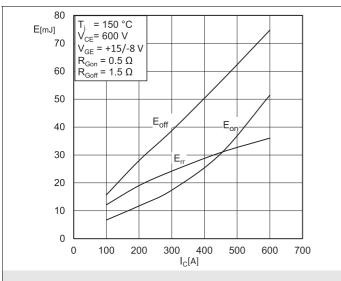


Fig. 3: Typ. IGBT1 & Diode5 turn-on /-off energy = f (I_C)

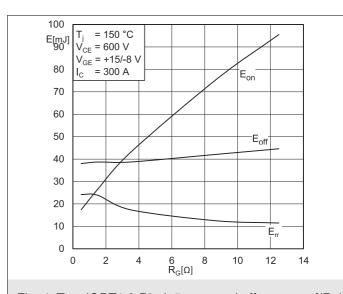


Fig. 4: Typ. IGBT1 & Diode5 turn-on /-off energy = $f(R_G)$

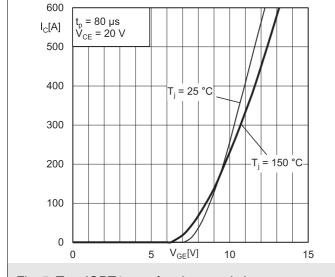


Fig. 5: Typ. IGBT1 transfer characteristic

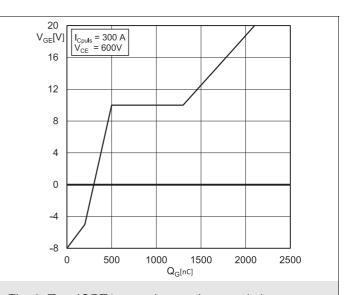
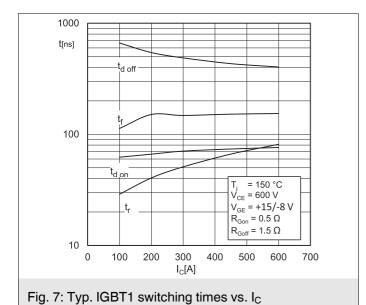


Fig. 6: Typ. IGBT1 gate charge characteristic



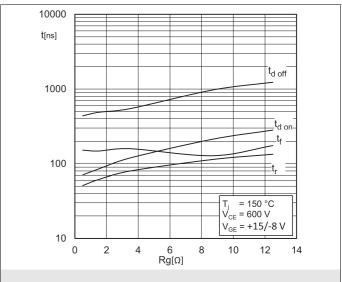


Fig. 8: Typ. IGBT1 switching times vs. gate resistor R_G

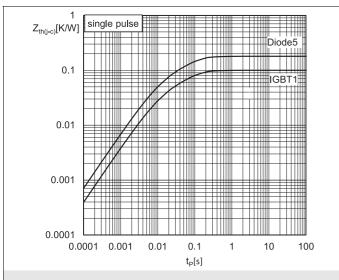


Fig. 9: Transient thermal impedance of IGBT1 & Diode5

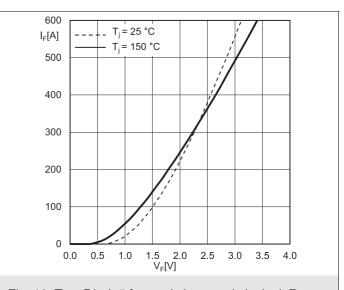


Fig. 10: Typ. Diode5 forward characteristic, incl. $R_{CC'+\,EE'}$

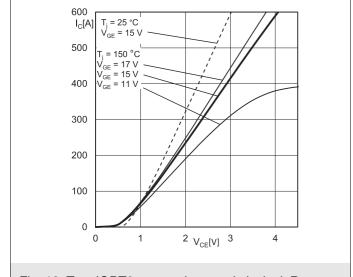


Fig. 13: Typ. IGBT2 output characteristic, incl. R_{CC'+ EE'}

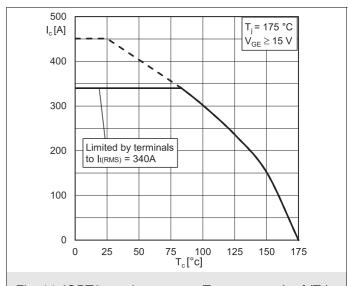


Fig. 14: IGBT2 rated current vs. Temperature I_c= f (T_c)

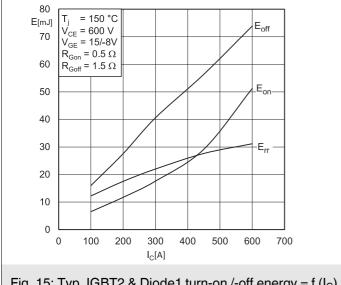


Fig. 15: Typ. IGBT2 & Diode1 turn-on /-off energy = f (I_C)

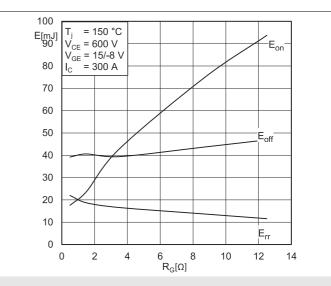


Fig. 16: Typ. IGBT2 & Diode1 turn-on / -off energy = f(R_G)

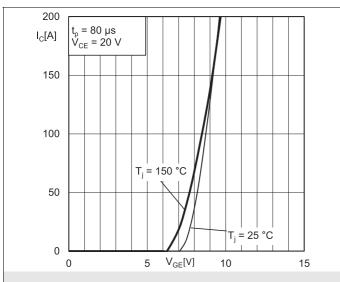


Fig. 17: Typ. IGBT2 transfer characteristic

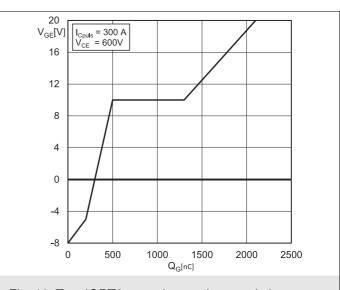


Fig. 18: Typ. IGBT2 gate charge characteristic

10000

t[ns]

1000

100

10

= 150 °C /_{CE} = 600 V /_{GE} = 15/-8 V

2

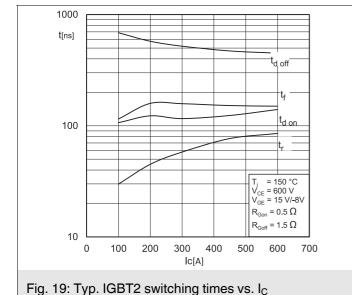


Fig. 20: Typ. IGBT2 switching times vs. gate resistor $R_{\mbox{\scriptsize G}}$

8

 $Rg[\Omega]$

10

t_{d off}

t_{d on}

12

14

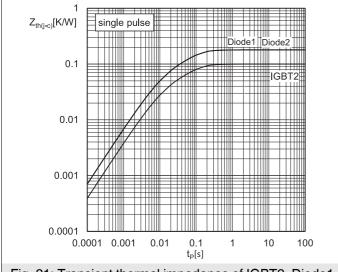


Fig. 21: Transient thermal impedance of IGBT2, Diode1 & Diode2

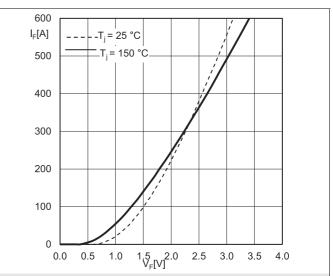
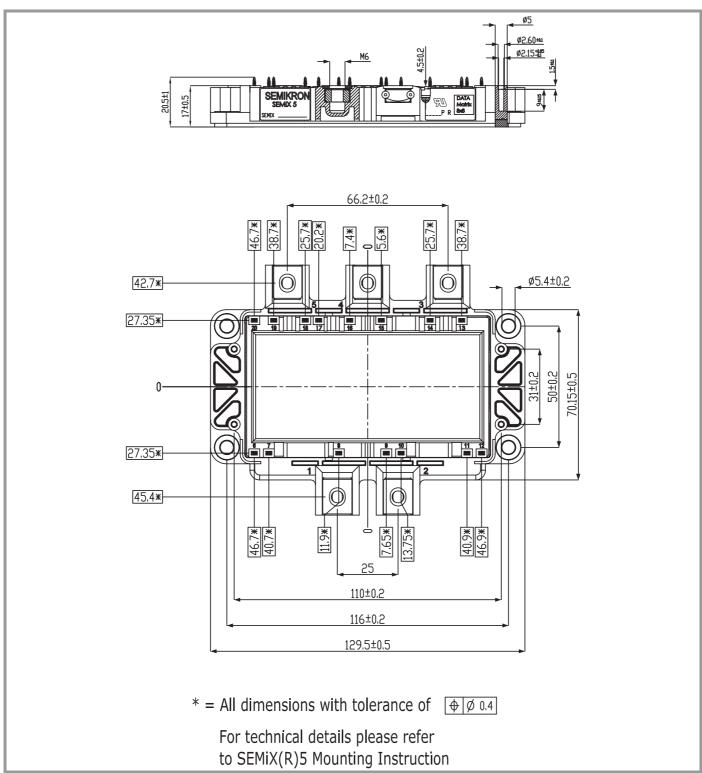
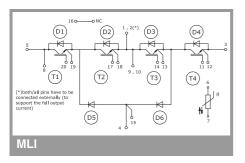


Fig. 22: Typ. Diode1 & Diode2 forward characteristic, incl. $R_{\text{CC'+}\,\text{EE'}}$



SEMiX5p



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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