

SEMITRANS® 3

High Speed IGBT4 Modules

SKM200GB12F4SiC2

Target Data

Features*

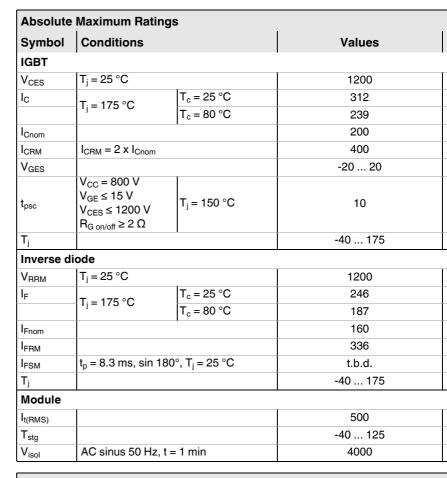
- IGBT4 = 4. Generation Fast Trench (High Speed) IGBT (Infineon)
- · With Silicon Carbide Schottky diodes (ROHM)
- Insulated copper baseplate using DBC Technology (Direct Bonded Copper)
- UL recognized, file no. E63532
- With integrated gate resistor
- For higher switching frequencies

Typical Applications

- · AC inverter drives
- UPS
- · Electronic welders
- DC/DC converters

Remarks

- · Case temperature limited to $T_c = 125^{\circ}C$ max.
- Recommended T_{op} = -40 ... +150°C
- Product reliability results valid for $T_i = 150$ °C



Unit

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Α

Α

Α

Α

V

μs

°C

V

Α

Α

Α

Α

Α

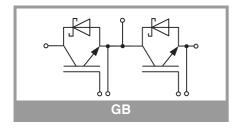
°С

Α

°C

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Characte	eristics					
Symbol	Conditions	min.	typ.	max.	Unit	
IGBT	•					•
V _{CE(sat)}	$I_C = 200 \text{ A}$ $V_{GE} = 15 \text{ V}$ chiplevel	T _j = 25 °C		2.06	2.42	V
		T _j = 150 °C		2.59	2.97	V
V _{CE0}	chiplevel	T _j = 25 °C		1.10	1.28	V
		T _j = 150 °C		0.95	1.13	V
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		4.8	5.7	mΩ
		T _j = 150 °C		8.2	9.2	mΩ
$V_{GE(th)}$	$V_{GE}=V_{CE}$, $I_C=7.6$ mA		5.1	5.8	6.4	V
I _{CES}	V _{GE} = 0 V V _{CE} = 1200 V	T _j = 25 °C			3.2	mA
		T _j = 150 °C		-		mA
C _{ies}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		12.3		nF
C _{oes}		f = 1 MHz		0.81		nF
C _{res}		f = 1 MHz		0.69		nF
Q_{G}	V _{GE} = - 8 V+ 15 V			1134		nC
R _{Gint}	T _j = 25 °C			2.4		Ω
t _{d(on)}	$V_{CC} = 600 \text{ V}$ $I_{C} = 200 \text{ A}$ $V_{GE} = +15/-15 \text{ V}$ $R_{G \text{ on}} = 1 \Omega$ $R_{G \text{ off}} = 1 \Omega$	T _j = 150 °C		t.b.d.		ns
t _r		T _j = 150 °C		t.b.d.		ns
E _{on}		T _j = 150 °C		7		mJ
t _{d(off)}		T _j = 150 °C		t.b.d.		ns
t _f		T _j = 150 °C		t.b.d.		ns
E _{off}		T _j = 150 °C		17		mJ
R _{th(j-c)}	per IGBT				0.115	K/W





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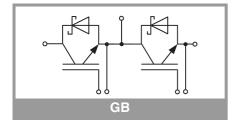
Typical Applications

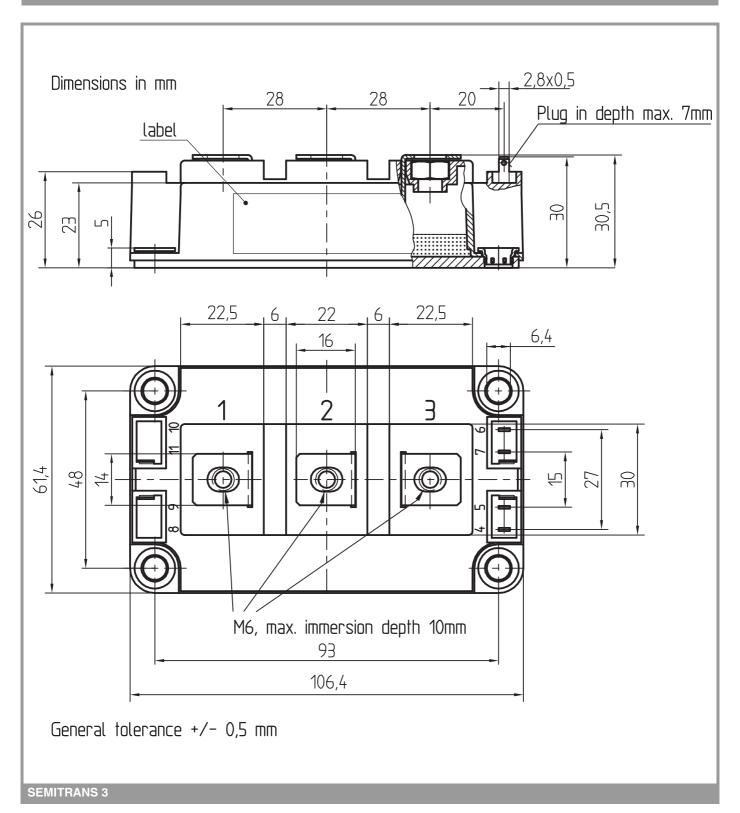
- · AC inverter drives
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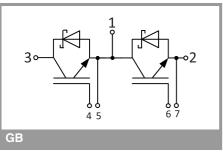
Remarks

- · Case temperature limited to $T_c = 125^{\circ}C$ max.
- Recommended T_{op} = -40 ... +150°C
- · Product reliability results valid for $T_i = 150$ °C

Characteristics										
Symbol	Conditions	min.	typ.	max.	Unit					
Inverse diode										
$V_F = V_{SD}$	I _F = 200 A V _{GE} = 0 V chiplevel	T _j = 25 °C		1.51	1.74	V				
		T _j = 150 °C		2.05	2.40	V				
V _{F0}	chiplevel	T _j = 25 °C		0.95	1.05	V				
		T _j = 150 °C		0.83	0.90	V				
r _F	chiplevel	T _j = 25 °C		2.8	3.4	mΩ				
		T _j = 150 °C		6.1	7.5	mΩ				
C _j	$f = 1 \text{ MHz}, V_R = 80$ parallel to C_{oss}		0.68		nF					
Qc	$V_R = 800 \text{ V}, \text{ di/dt}_{\text{off}} = 500 \text{ A/}\mu\text{s}$			0.53		μC				
R _{th(j-c)}	per diode				0.21	K/W				
Module										
L _{CE}				15		nΗ				
R _{CC'+EE'}	measured per switch	T _C = 25 °C		0.55		mΩ				
		T _C = 125 °C		0.85		mΩ				
Rth _{(c-s)1}	calculated without thermal coupling (λ _{grease} =0.81 W/(m*K))			0.02	0.038	K/W				
Ms	to heat sink M6		3		5	Nm				
M _t		to terminals M6	2.5		5	Nm				
						Nm				
w					325	g				







This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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