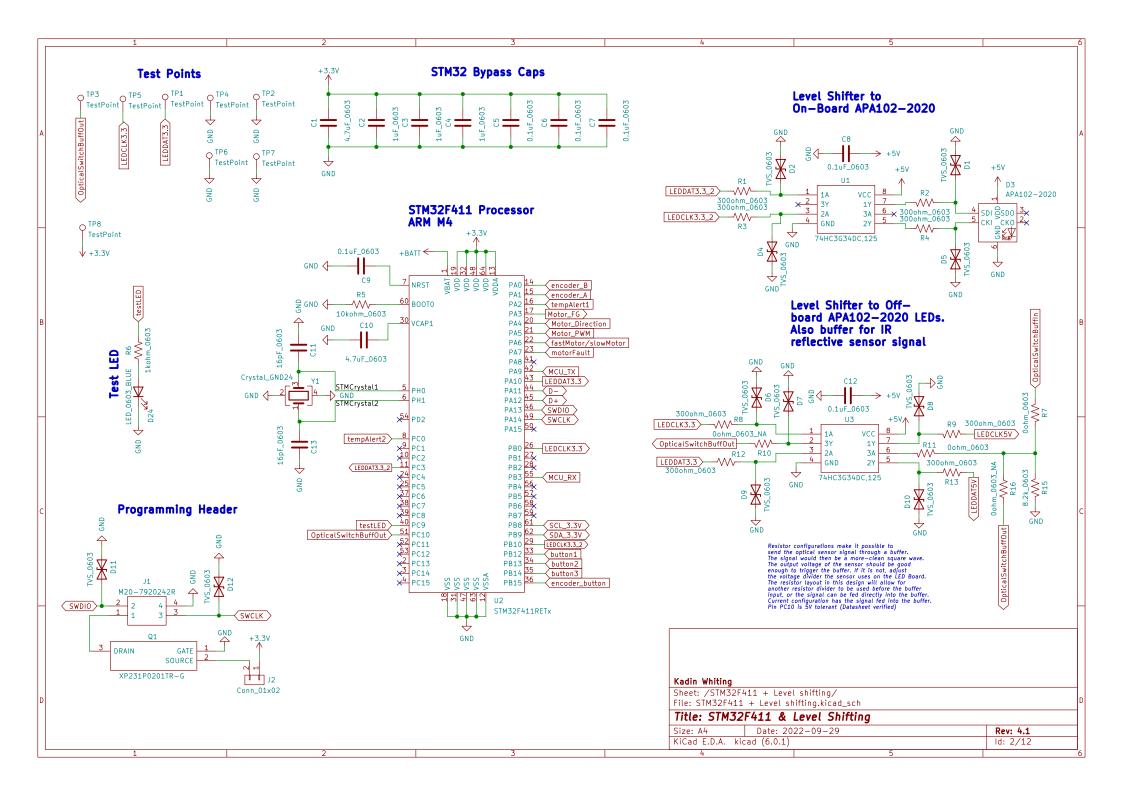
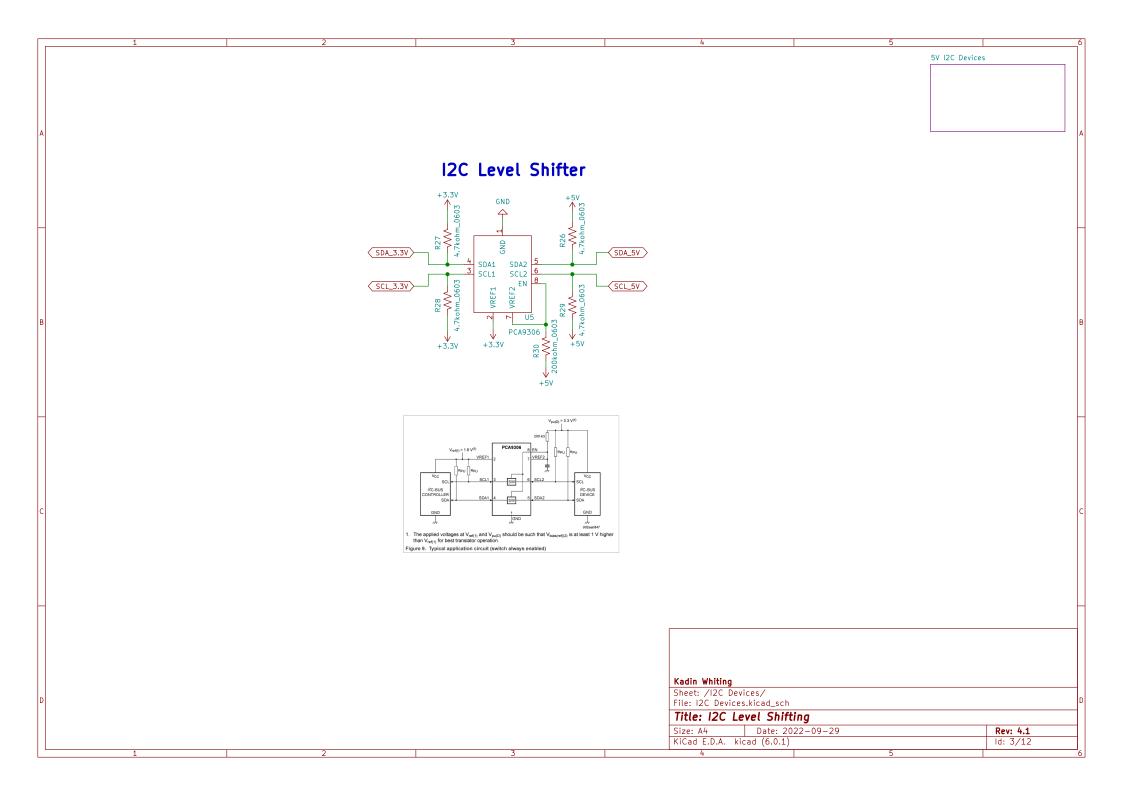
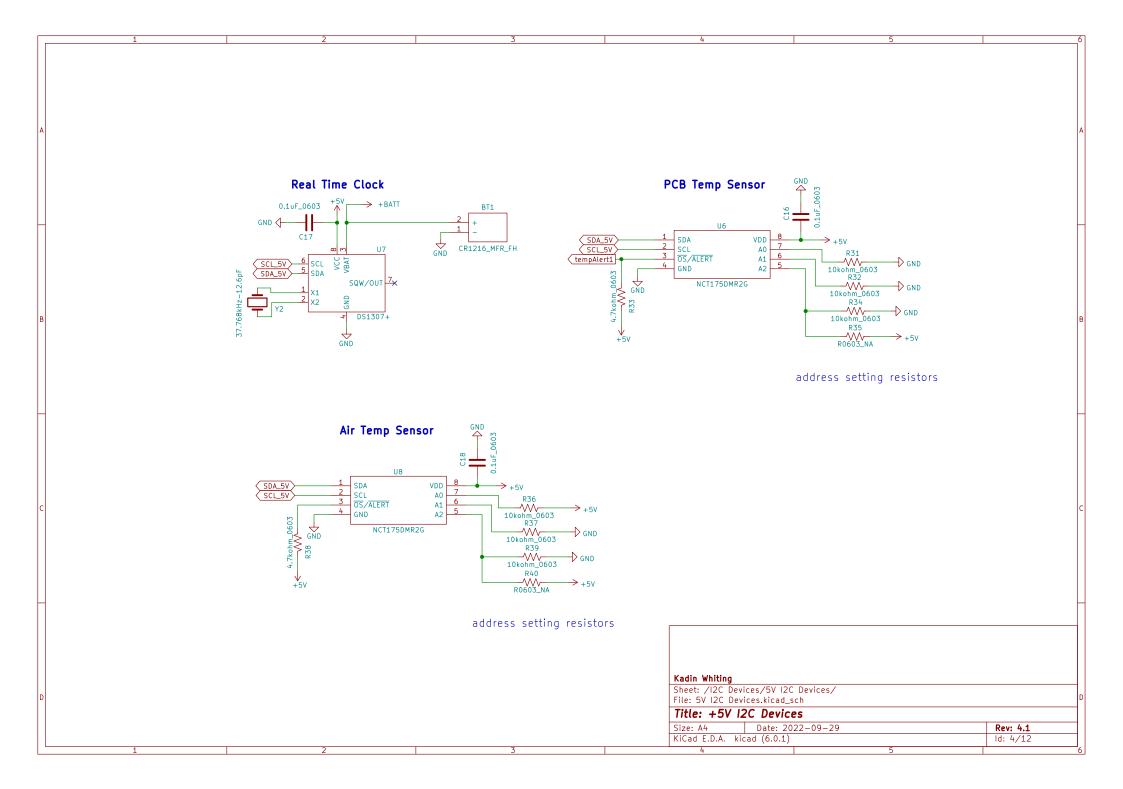
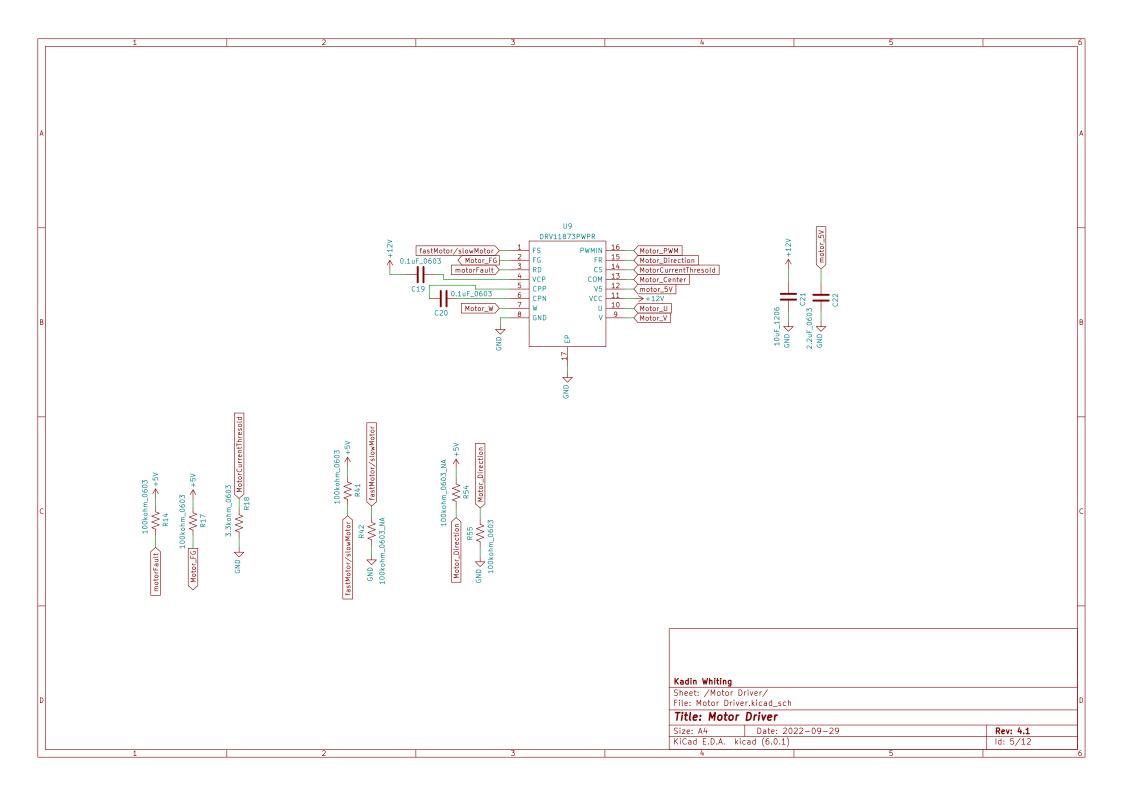
HDD Clock V4.1 This is the schematic for the Driver board of my HDD Persistence of Vision Clock V4.1 STM32F411 + Level shifting If a \* is placed next to a line it indicates it is new to V4.1 Main improvements and included features of this driver board: I2C Devices 1. Powered fully from 12V DC Jack -features a buck converter design capable of 2.4A @ 5V -3.3V LDO capable of 500mA 2. APA102-2020 LFDs instead of WS2812B LFDs -The new LEDs are capable of 25x data speed. —the 2020 package takes up 60% less area than WS2812B LEDs allowing many more to be placed in the Motor Driver same space (V4.0 of the LED board has 116 LEDs). \*-Display line SPI capable of driving LEDs at 50Mhz 3. Integrated 12C PCB Temperature sensors. - Driver Board PCB temp -Ambient air temperature \*routing is improved in V4.1 -LED Board temp (connections for this off-board temp sensor) 4. Multiple light masks and persistence of vision displays. The light mask has a slit from the center of the mask to the outer edge. With this -Method 1: light mask an analog clock can be drawn using persistence of vision. The light mask is arranged in a pattern of a 'Nipkow Disk'. A Nipkow disk is a disk -Method 2: with holes spiraling at a constant rate away from the center of the disk. Back-lighting Interface these holes using persistence of vision should allow for pixels to be drawn using the spinning disk. Using a light mask between LEDs I should be able to create multiple of these displays using the entire circular area of the spinning light mask. If this works it will be a circular mechanical display. I have 2 Nipkow disks designed. An 8 line tall disk and a 12 line tall disk. 6. Encoder and right angle buttons used for user input. LED Board and Motor Connections 7. Wires have been removed from the assembly. -Connections between this driver board and the LED board are made with SMT spring-loaded PCB pins on the driver board and matching pin target pad on the LED board. 8. \*New motor control chip: DRV11873 9. \*New DC jack. Kadin Whiting Sheet: / File: Driver Board HDDCLKV4.1.kicad sch Title: Cover Page Size: A4 Date: 2022-09-29 Rev: 4.1 KiCad E.D.A. kicad (6.0.1) ld: 1/12

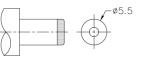








_	1 2 3	4 5	6]
			Power Connectors
	POWER:		
	12V -> 5V -> 3.3V		
4	12/ -> 5/ -> 5.5/		Buck Converter
	12V DC Jack -> 5V Buck Converter -> 3.3V LDO		
	EV UCD 7 7V 100 (		
	5V USB -> 3.3V LDO (no motor power)		
	This version of the HDD Persistence of vision clock takes 12V in from		
1	a DC jack. The motor driver uses the 12V to drive the motor. The 12V		LDO
	is fed into a buck converter circuit. This circuit is capable of		
	2.5A @ 5V output. 5V powers the RTC, temp sensors and LEDs. 5V is fed into a 3.3V regulator capable of 500mA output. 3.3V powers the stm32 ARM		
	processor.		
1			В
			С
$\frac{1}{1}$			
		Kadin Whiting Sheet: /Power/	
		File: Power.kicad_sch	D
		Title: Power Overview           Size: A4         Date: 2022-09-29	Rev: 4.1
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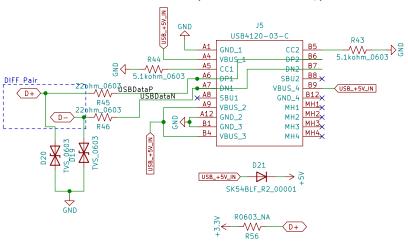


MATING PLUG Jack Insertion Depth: 9.0 mm

GND (

SCHEMATIC	01 03 02
Model	PJ-102A
Center Pin	Ø2.0 mm





## Ordered Cable Polarity

PJ-102A



## Backup Power Input



## Kadin Whiting

Sheet: /Power/Power Connectors/ File: Power Connectors.kicad\_sch

## Title: Power Connectors

Size: A4	Date: 20	22-09-29		Rev: 4.1	
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